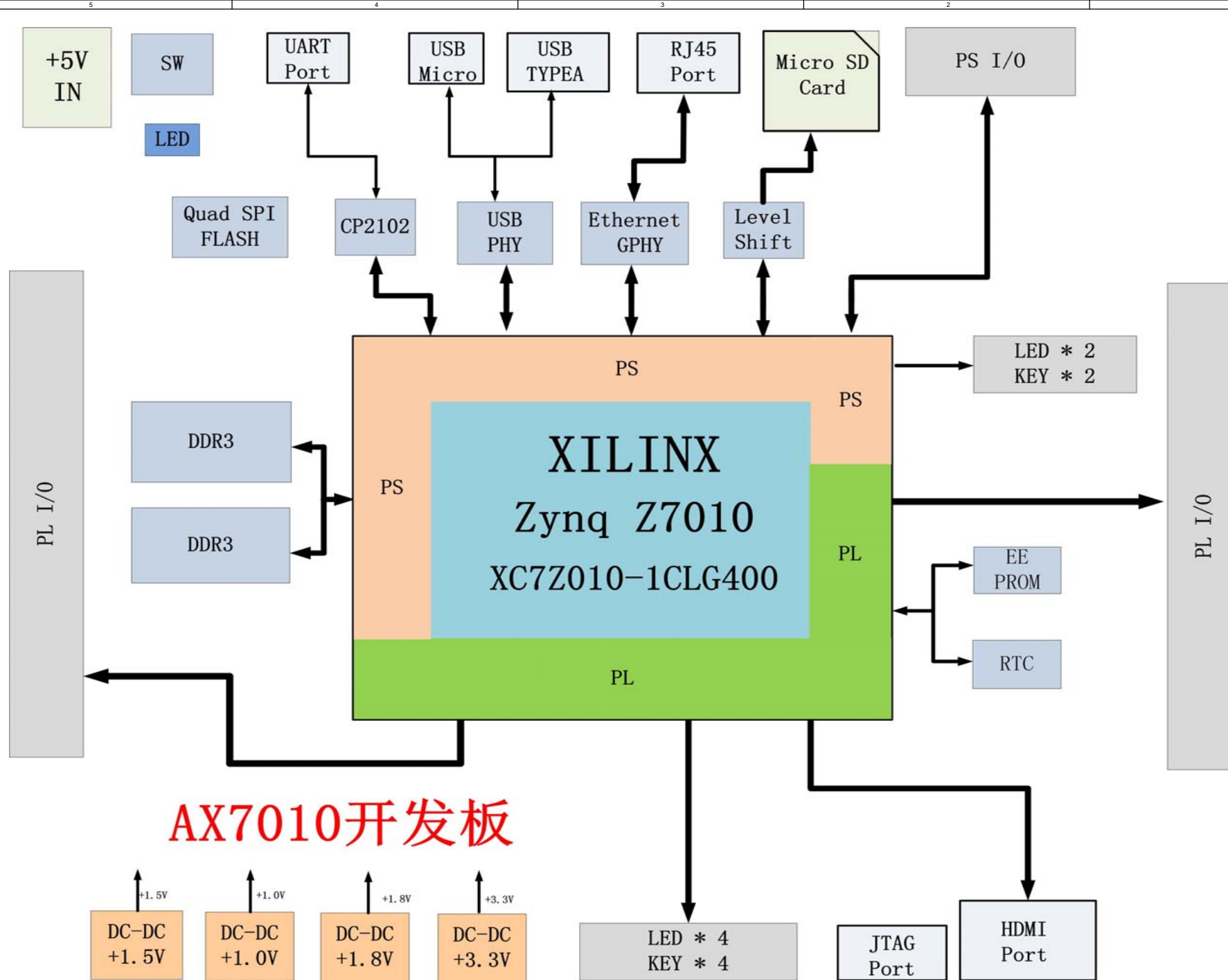


| REV | Description | Date |
|------|---------------|-----------|
| V1.0 | First Release | 2016-7-12 |
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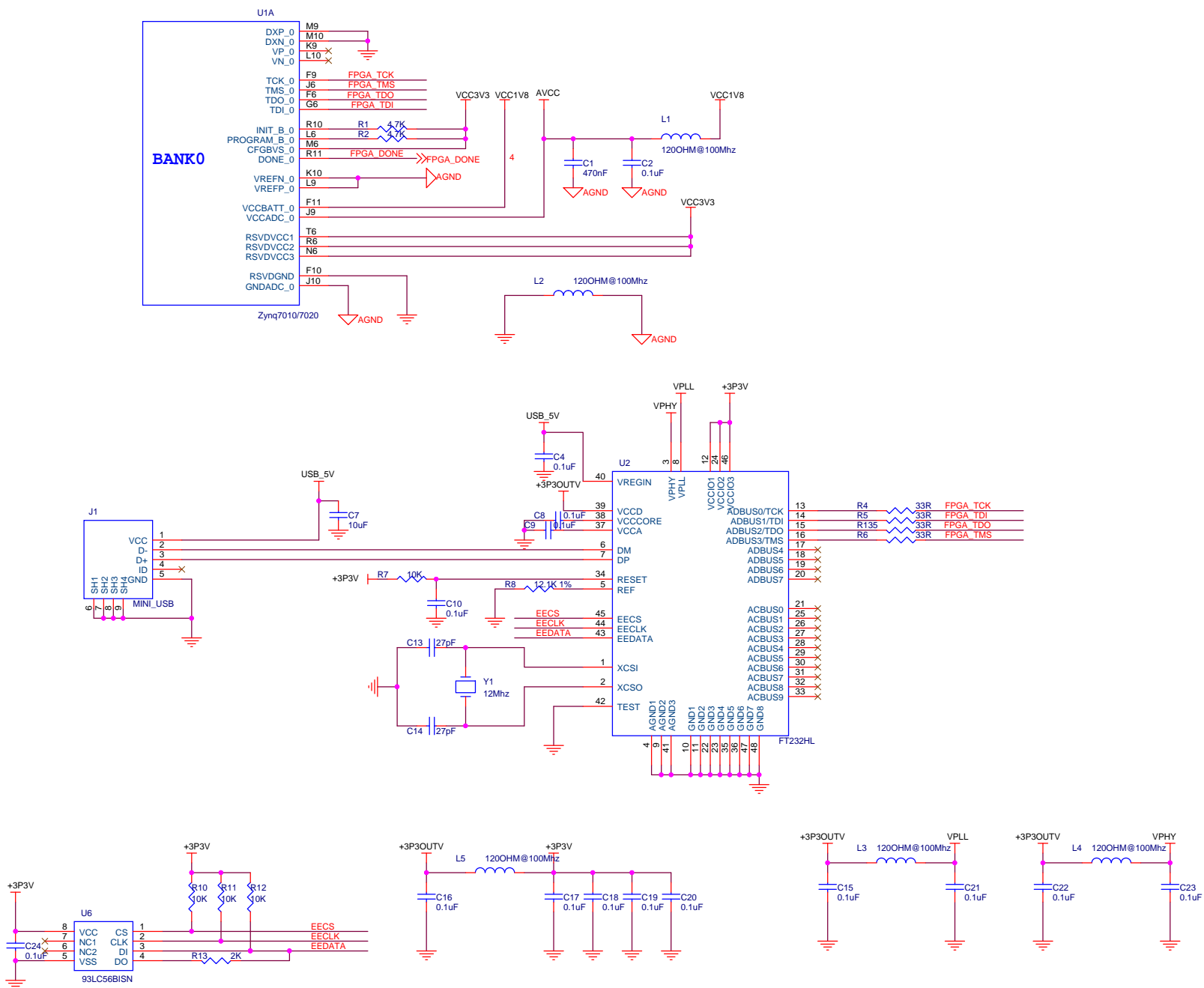
AX7010 Schematics

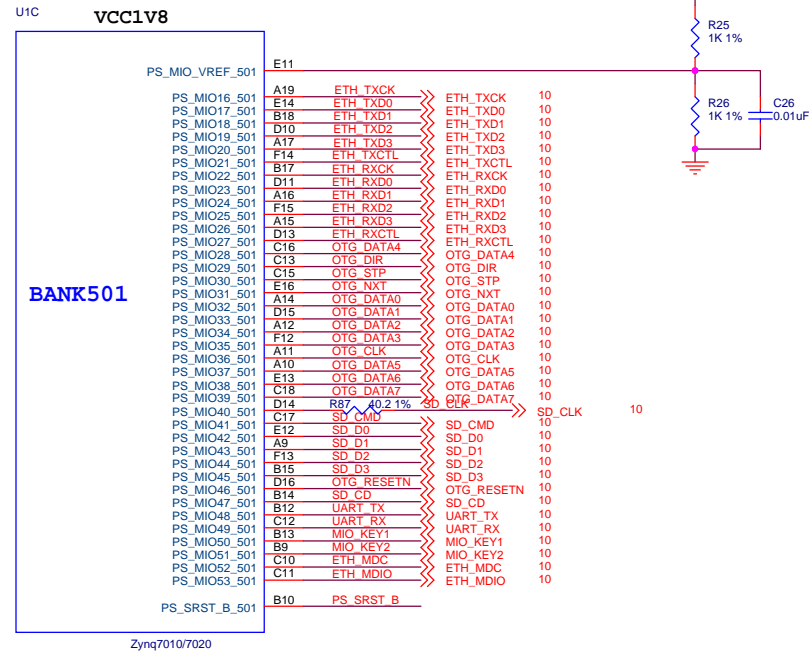
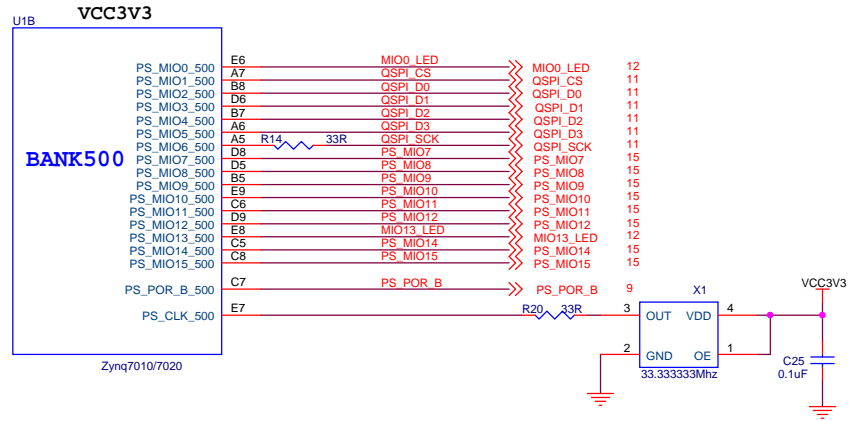
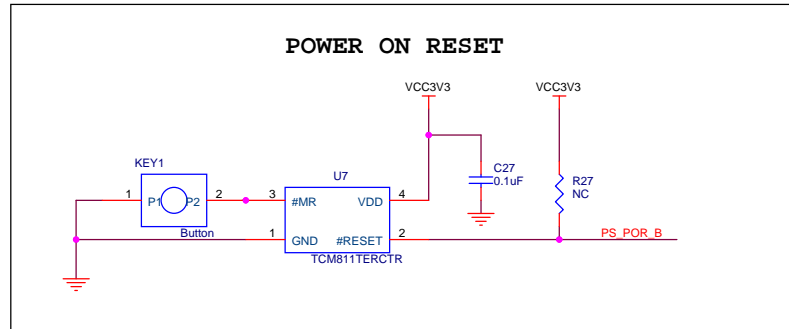
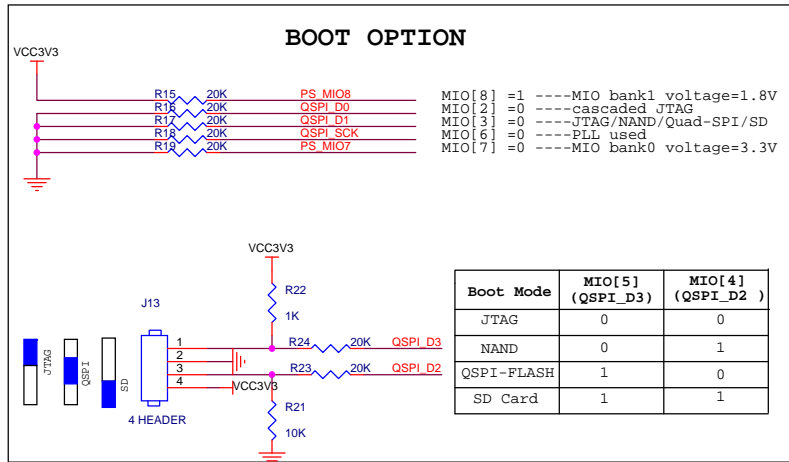
黑金ZYNQ硬件平台

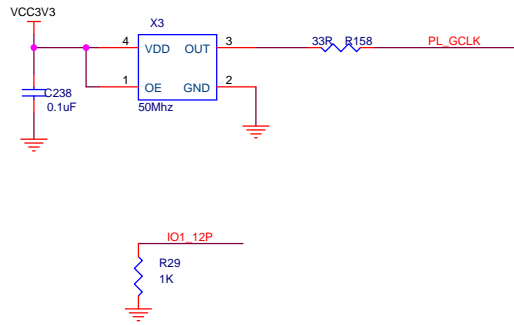
| Page Number | Description |
|-------------|------------------------|
| Page01 | Cover Page |
| Page02 | Block Diagram |
| Page03 | Zynq-7000 JTAG & Bank0 |
| Page04 | Zynq-7000 MIO Config |
| Page05 | Zynq-7000 Bank13-34-35 |
| Page06 | Zynq-7000 Bank502 |
| Page07 | Zynq-7000 Power |
| Page08 | DDR3 |
| Page09 | GPHY |
| Page10 | USB OTG |
| Page11 | FLASH, RTC, EEPROM |
| Page12 | LED, KEY |
| Page13 | UART, SD |
| Page14 | HDMI |
| Page15 | EXTEND IO |
| Page16 | POWER |



AX7010开发板







11 RTC_I2C_SCL
11 EEPROM_I2C_SCL
15 IO1_15P
15 IO1_14N
15 IO1_12N
15 IO1_13P
15 IO1_11N
15 IO1_2P
15 IO1_2N
15 IO1_3P
15 IO1_3N
15 IO1_5P
15 IO1_5N
15 IO1_9P
15 IO1_9N
15 IO1_4P
15 IO1_4N

R19
T19
T11
T10
T12
U12
U13
V13
V12
W13
T14
T15
P14
R14
Y16
Y17
W14
Y14
T16
U17
V15
W15

VCC3V3 BANK34

IO_0_34
IO_25_34
IO_L11P_T1_SRCC_34
IO_L12P_T1_MRCC_34
IO_L12N_T1_MRCC_34
IO_L13P_T2_MRCC_34
IO_L13N_T2_MRCC_34
IO_L14P_T2_SRCC_34
IO_L14N_T2_SRCC_34
IO_L15P_T2_DQS_34
IO_L15N_T2_DQS_34
IO_L16P_T2_34
IO_L16N_T2_34
IO_L17P_T2_34
IO_L17N_T2_34
IO_L18P_T2_34
IO_L18N_T2_34
IO_L19P_T2_34
IO_L19N_T2_34
IO_L20P_T3_34
IO_L20N_T3_34
IO_L21P_T3_DQS_34
IO_L21N_T3_DQS_34
IO_L22P_T3_34
IO_L22N_T3_34
IO_L23P_T3_34
IO_L23N_T3_34
IO_L24P_T3_34
IO_L24N_T3_34

U14
U15
U18
U19
N18
P19
N20
P20
T20
U20
V20
W20
Y18
Y19
V16
W16
R16
R17
T17
R18
V18
W18
W19
N17
P18
P15
P16
IO1_7P
IO1_7N
HDMI_CLK_P
HDMI_CLK_N
HDMI_D2_P
HDMI_D2_N
HDMI_D1_P
HDMI_D1_N
HDMI_D0_P
HDMI_D0_N
HDMI_CEC
HDMI_HPD
HDMI_OUT_EN
EEPROM_I2C_SDA
HDMI_SDA
KEY4
KEY3
HDMI_SCL
IO1_10P
IO1_10N
IO1_1P
IO1_1N
IO1_6P
IO1_6N
IO1_8P
IO1_8N

Zynq7010/7020

VCC3V3 BANK13 (Z7020 Only)

V5
U7
V7
T9
U10
Y7
Y6
Y9
V8
W8
W10
W9
U9
U8
W11
Y11
T5
U5
Y12
Y13
V11
V10
V6
W6
IO_L6N_T0_VREF_13
IO_L11P_T1_SRCC_13
IO_L11N_T1_SRCC_13
IO_L12P_T1_MRCC_13
IO_L12N_T1_MRCC_13
IO_L13P_T2_MRCC_13
IO_L13N_T2_MRCC_13
IO_L14P_T2_SRCC_13
IO_L14N_T2_SRCC_13
IO_L15P_T2_DQS_13
IO_L15N_T2_DQS_13
IO_L16P_T2_13
IO_L16N_T2_13
IO_L17P_T2_13
IO_L17N_T2_13
IO_L18P_T2_13
IO_L18N_T2_13
IO_L19P_T3_13
IO_L19N_T3_VREF_13
IO_L20P_T3_13
IO_L20N_T3_13
IO_L21P_T3_DQS_13
IO_L21N_T3_DQS_13
IO_L22P_T3_13
IO_L22N_T3_13

Zynq7010/7020

15 IO1_17P
15 IO1_17N
15 IO1_16P
15 IO1_16N
15 IO2_12P
15 IO2_12N
15 IO2_13P
15 IO2_13N
15 IO2_1P
15 IO2_1N
15 IO2_6P
15 IO2_6N
15 IO2_11P
15 IO2_11N
15 IO2_5P
15 IO2_5N
15 IO2_8P
15 IO2_8N

G14
J15
C20
B20
B19
A20
E17
D18
D19
D20
E18
E19
F16
F17
M19
M20
M17
M18
L19
L20
K19
J19

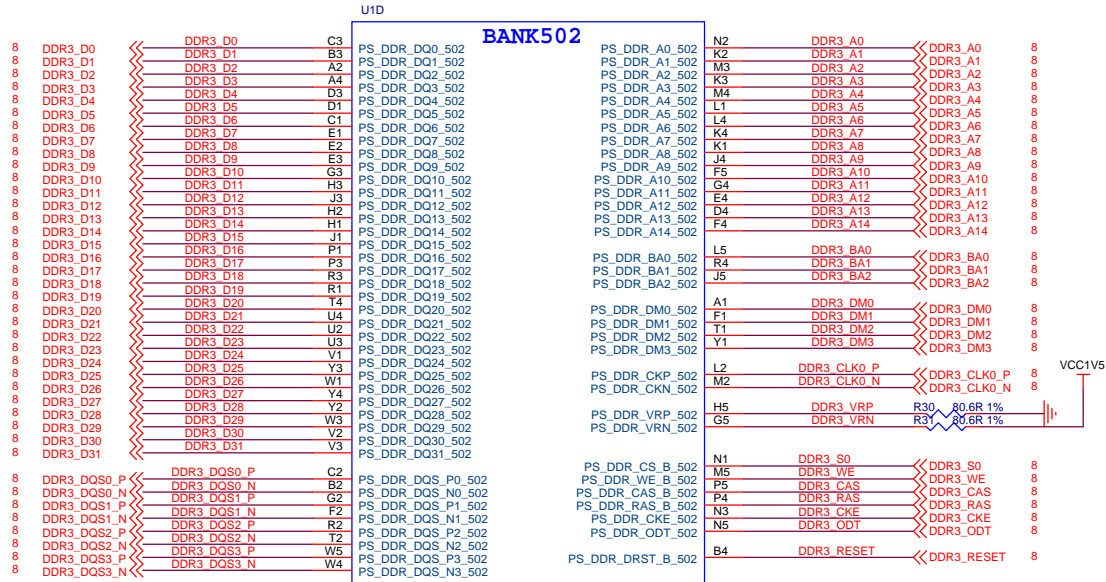
VCCIO BANK35

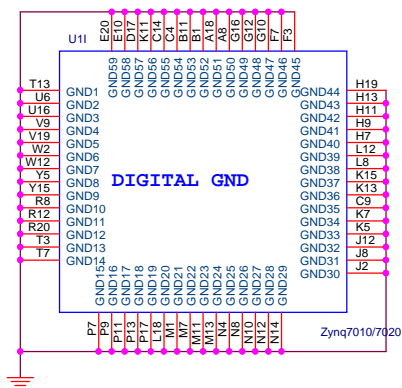
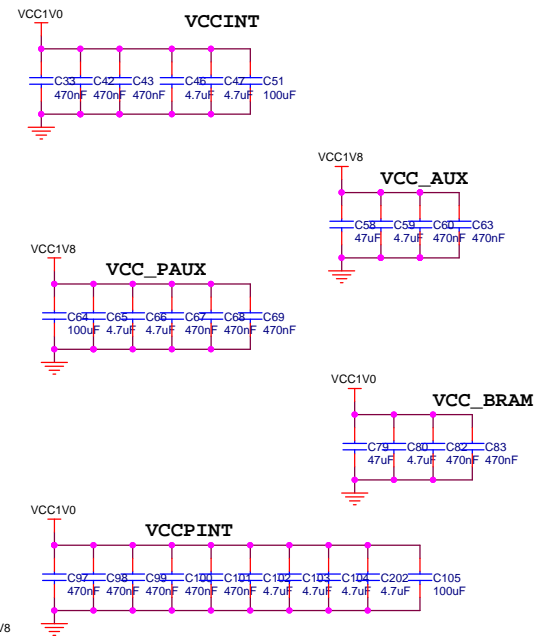
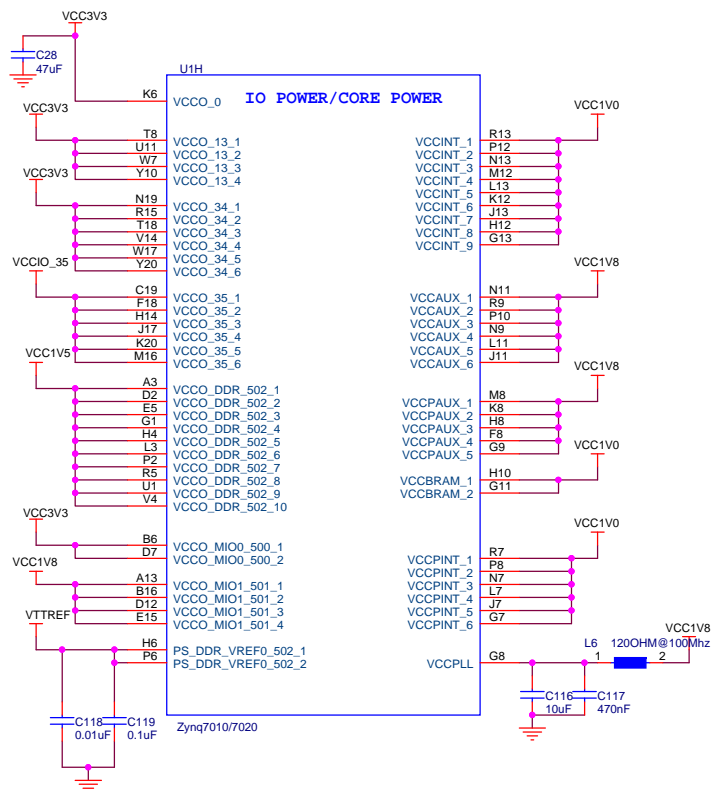
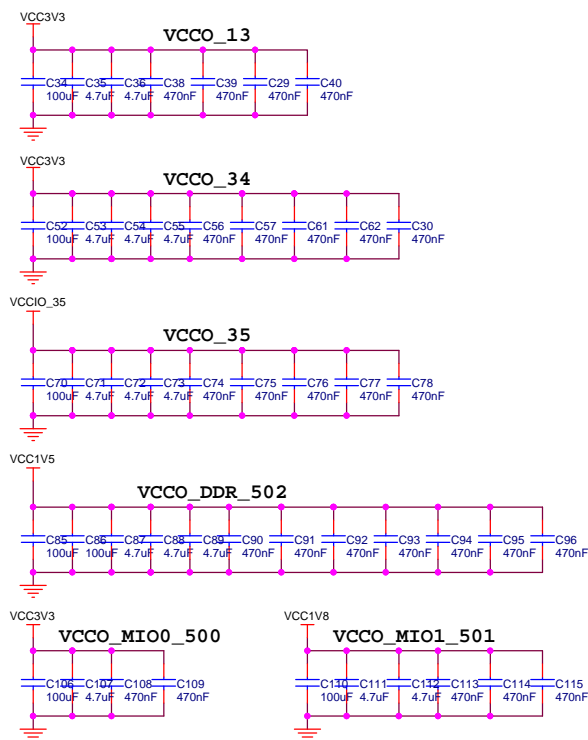
IO_0_35
IO_25_35
IO_L11P_T0_AD0P_35
IO_L11N_T0_AD0N_35
IO_L12P_T0_AD8P_35
IO_L12N_T0_AD8N_35
IO_L3P_T0_DQS_AD1P_35
IO_L3N_T0_DQS_AD1N_35
IO_L4P_T0_35
IO_L4N_T0_35
IO_L5P_T0_AD9P_35
IO_L5N_T0_AD9N_35
IO_L6P_T0_35
IO_L6N_T0_VREF_35
IO_L7P_T1_AD2P_35
IO_L7N_T1_AD2N_35
IO_L8P_T1_AD10P_35
IO_L8N_T1_AD10N_35
IO_L9P_T1_DQS_AD3P_35
IO_L9N_T1_DQS_AD3N_35
IO_L10P_T1_AD11P_35
IO_L10N_T1_AD11N_35

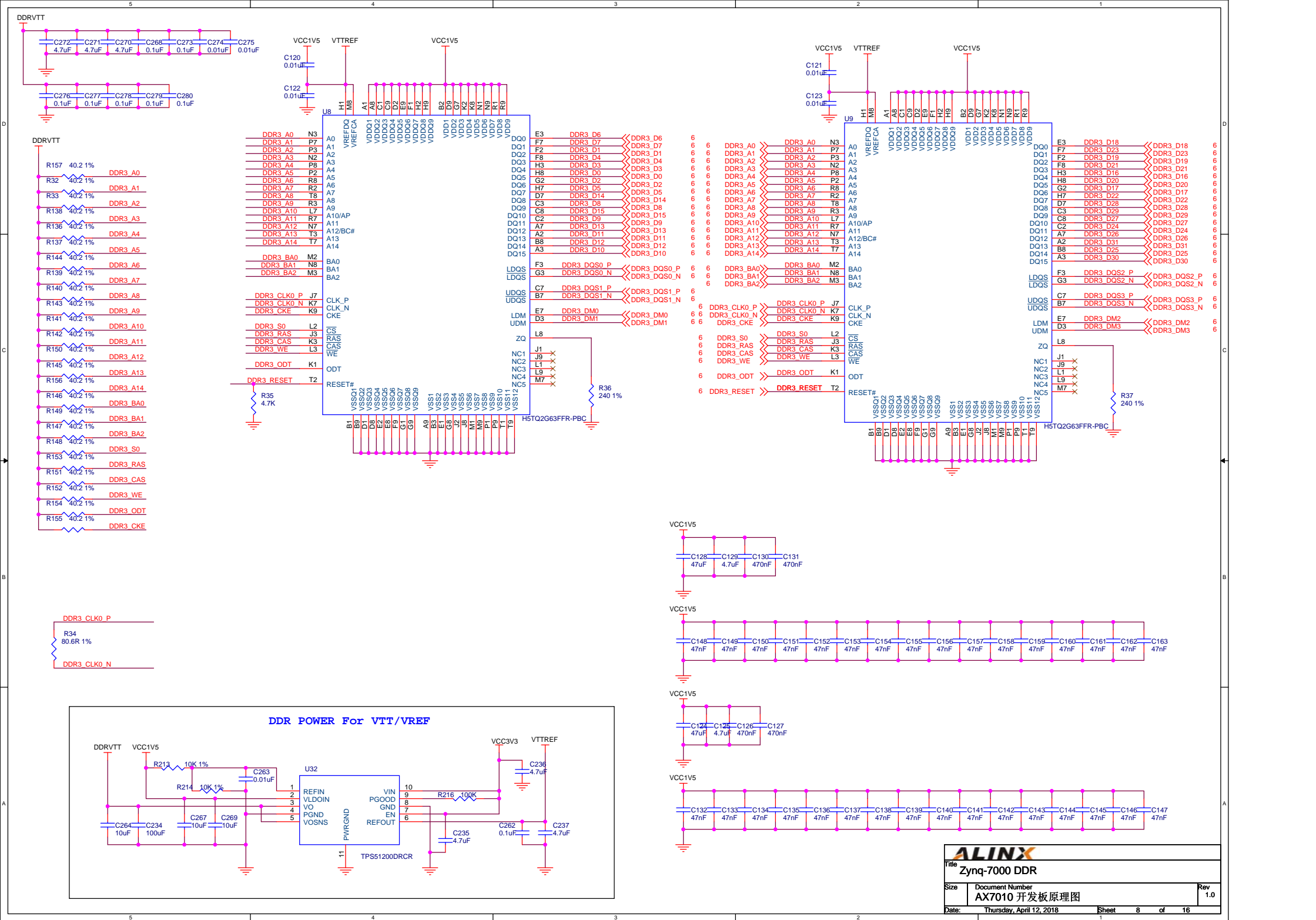
IO_L11P_T1_SRCC_35
IO_L11N_T1_SRCC_35
IO_L12P_T1_MRCC_35
IO_L12N_T1_MRCC_35
IO_L13P_T2_MRCC_35
IO_L13N_T2_MRCC_35
IO_L14P_T2_AD4P_SRCC_35
IO_L14N_T2_AD4N_SRCC_35
IO_L15P_T2_DQS_AD12P_35
IO_L15N_T2_DQS_AD12N_35
IO_L16P_T2_35
IO_L16N_T2_35
IO_L17P_T2_AD5P_35
IO_L17N_T2_AD5N_35
IO_L18P_T2_AD13P_35
IO_L18N_T2_AD13N_35
IO_L19P_T3_35
IO_L19N_T3_VREF_35
IO_L20P_T3_AD6P_35
IO_L20N_T3_AD6N_35
IO_L21P_T3_DQS_AD14P_35
IO_L21N_T3_DQS_AD14N_35
IO_L22P_T3_AD7P_35
IO_L22N_T3_AD7N_35
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IO_L23N_T3_35
IO_L24P_T3_AD15P_35
IO_L24N_T3_AD15N_35

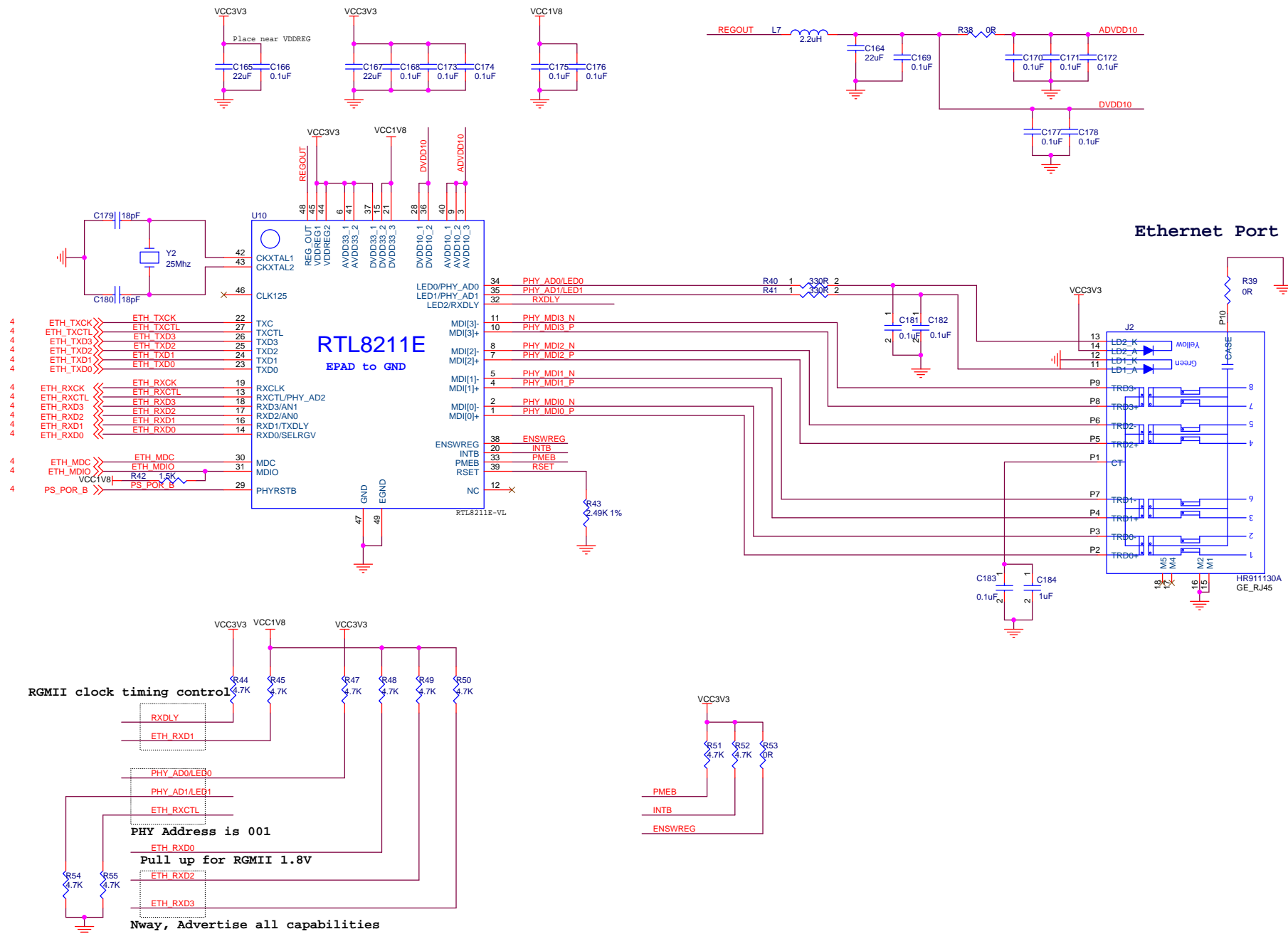
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H16
H17
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H18
F19
F20
G17
G18
J20
H20
G19
G20
H15
G15
K14
J14
N15
N16
L14
L15
M14
M15
K16
J16
IO2_10P
IO2_10N
IO2_7P
IO2_7N
IO2_15P
IO2_15N
IO2_4P
IO2_4N
IO2_2P
IO2_2N
IO2_14P
IO2_14N
IO2_9P
IO2_9N
IO2_3P
IO2_3N
IO2_16P
IO2_16N
IO2_17P
IO2_17N
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KEY2
RTC_I2C_SDA
RTC_RESET
LED1
LED2
LED3
LED4

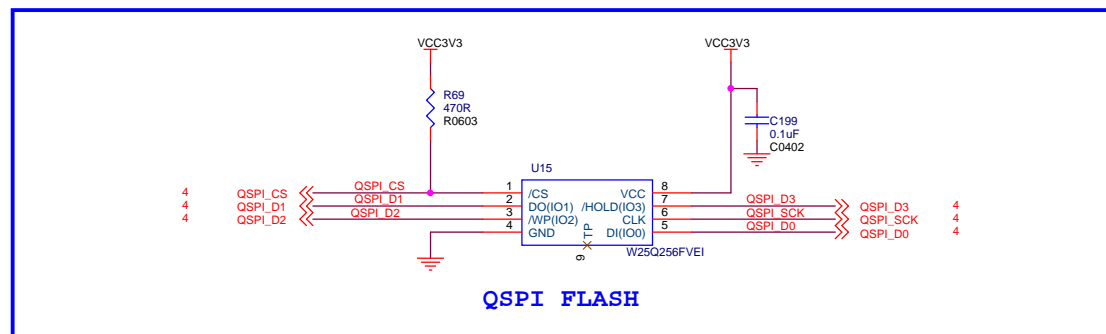
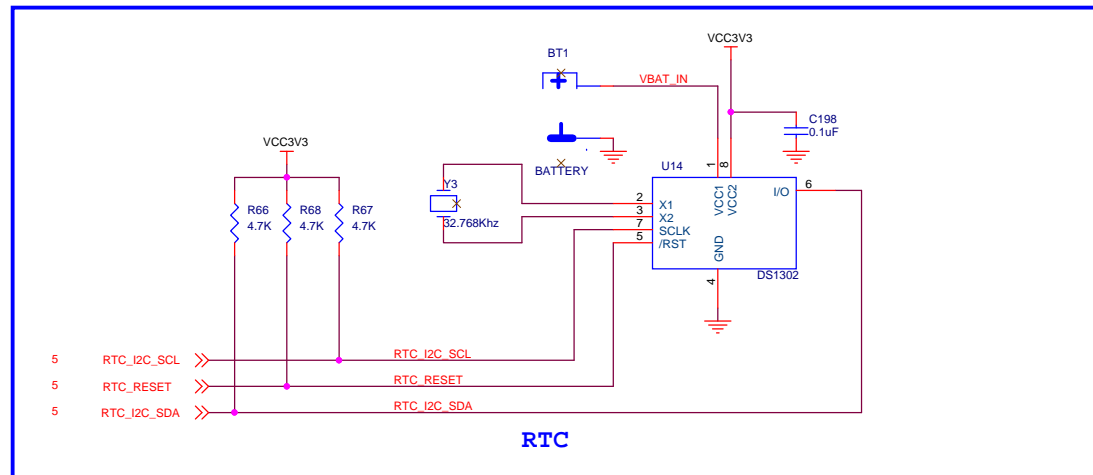
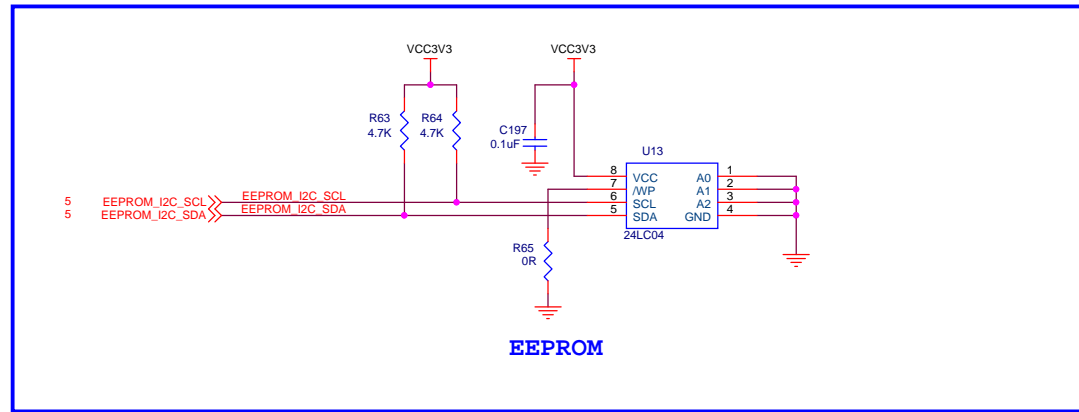
Zynq7010/7020

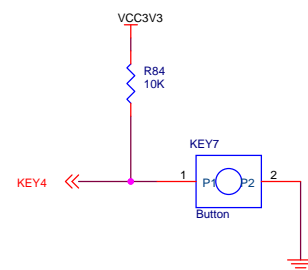
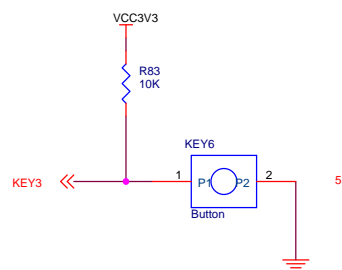
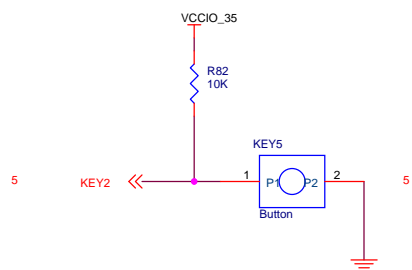
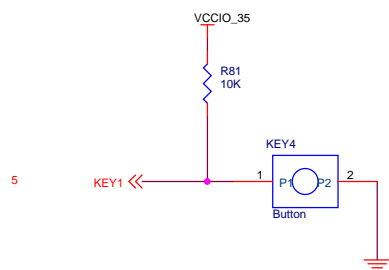
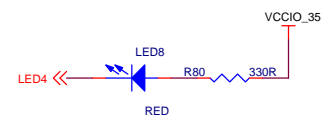
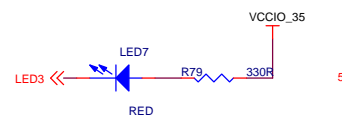
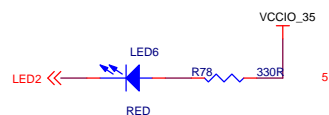
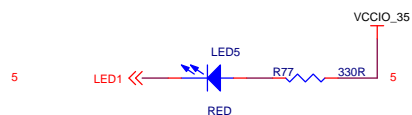
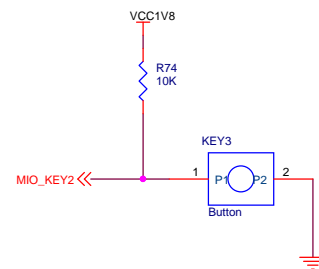
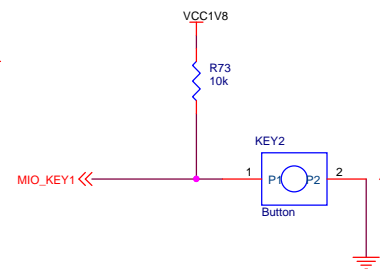
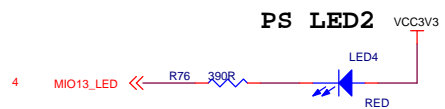
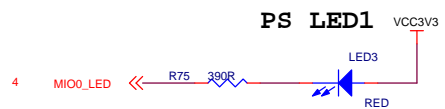
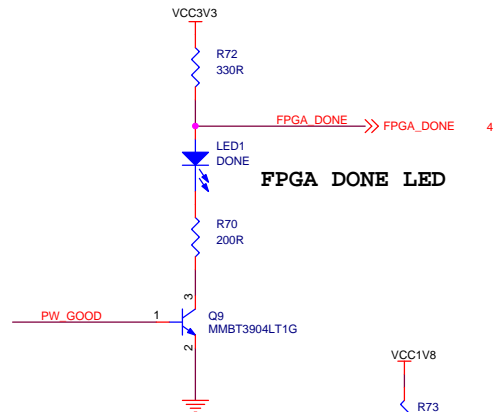
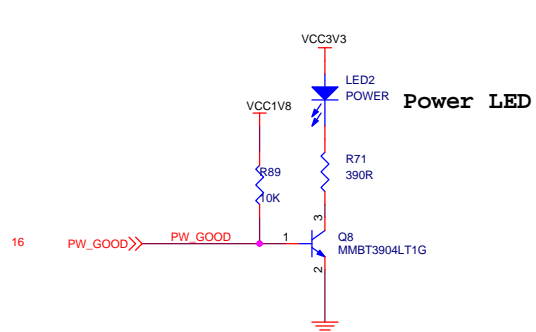


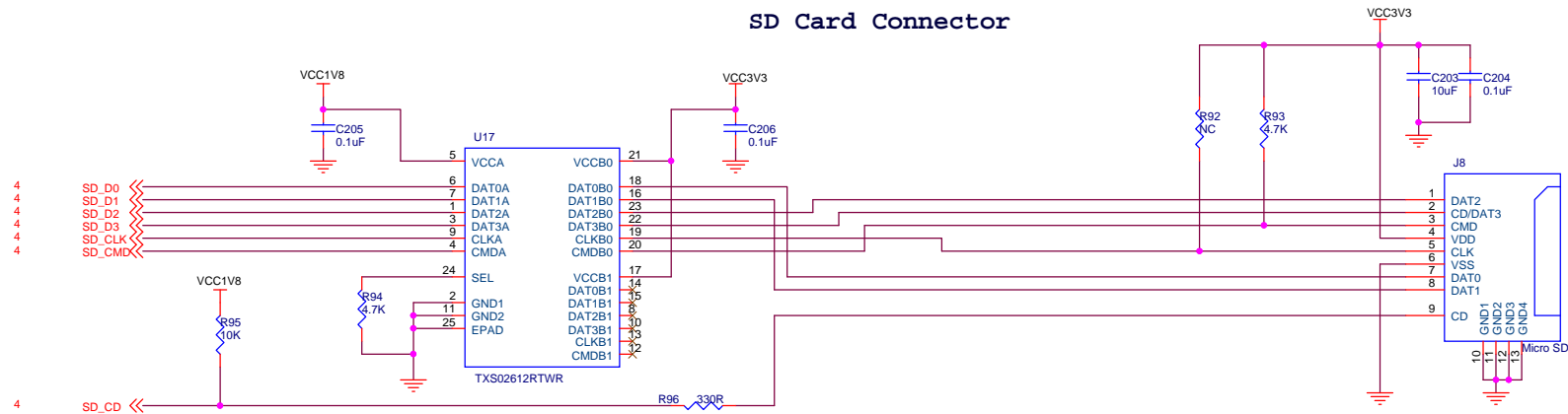
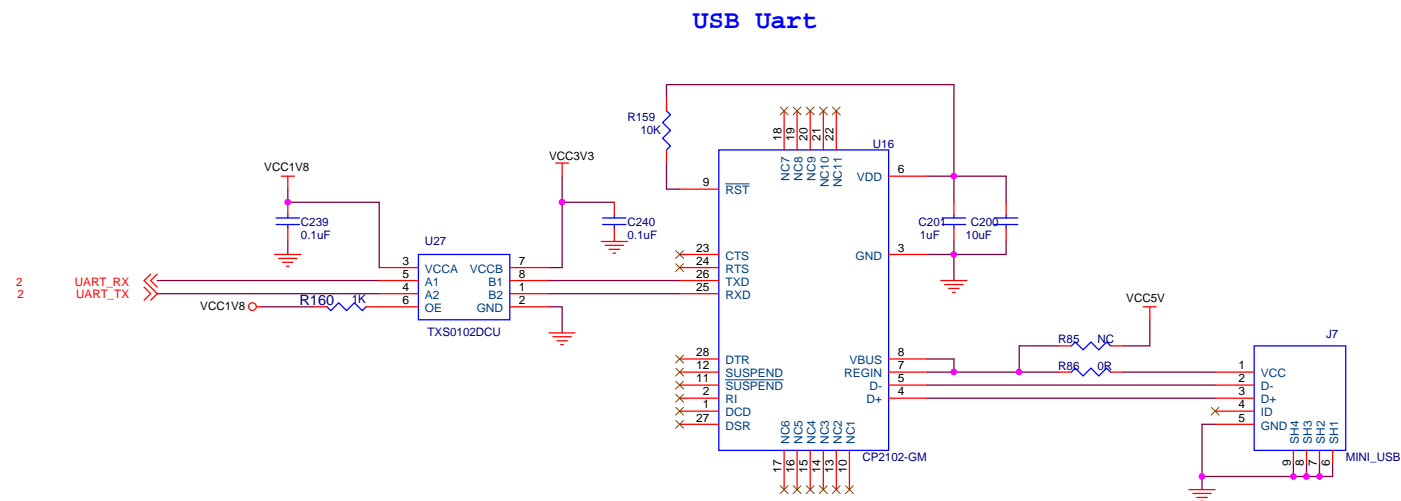


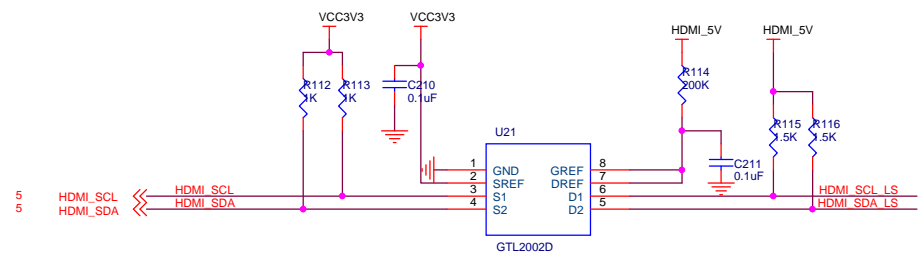
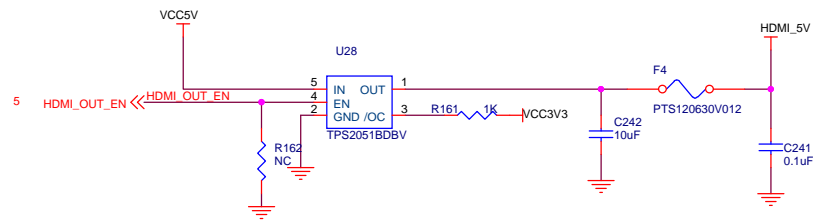
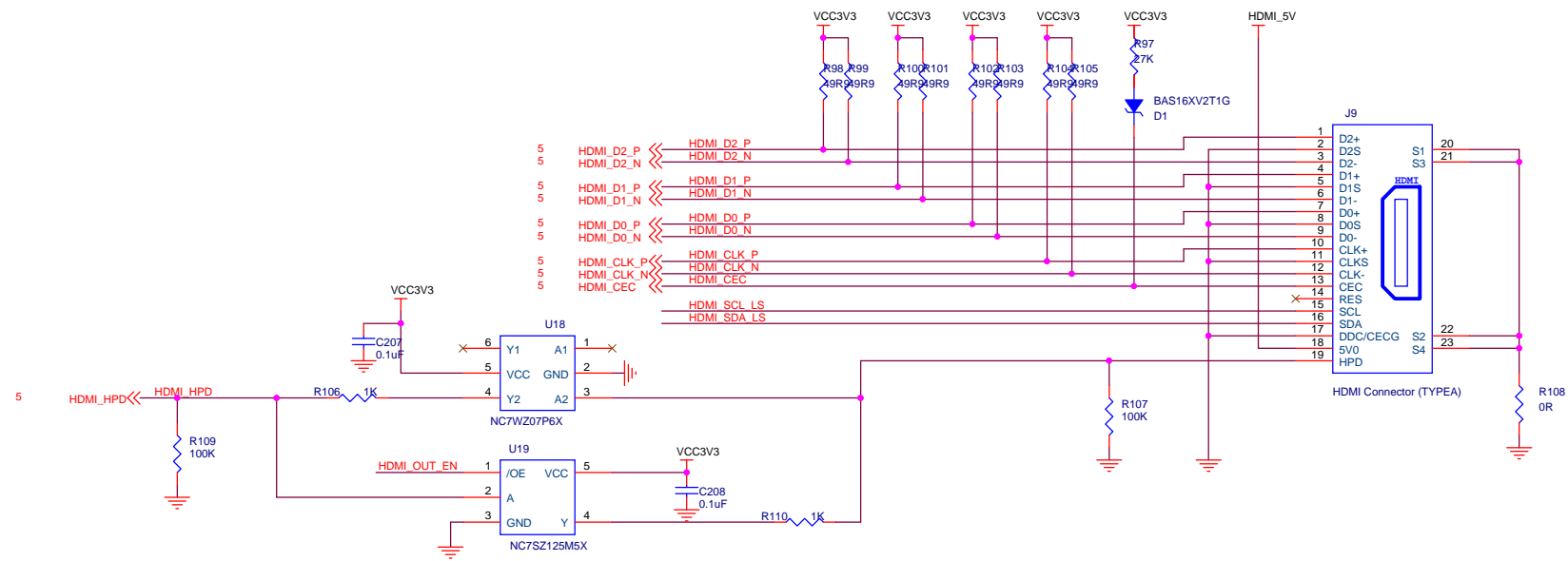




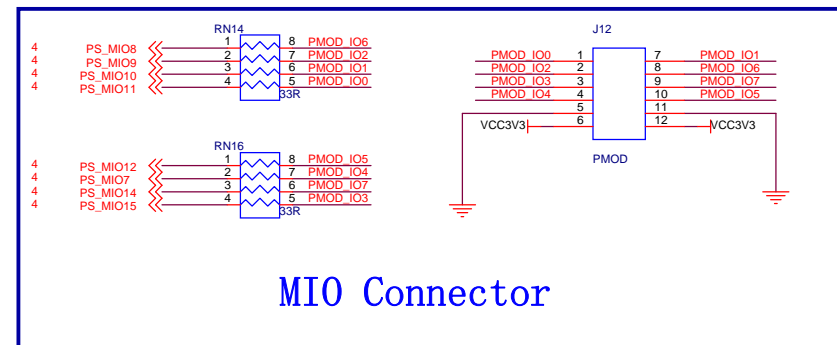
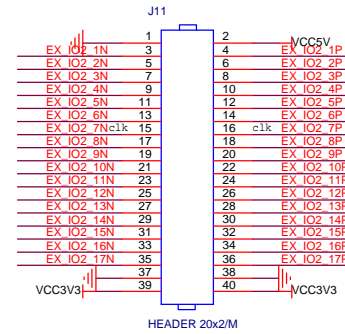
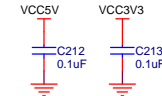
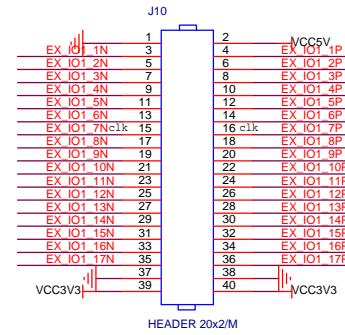






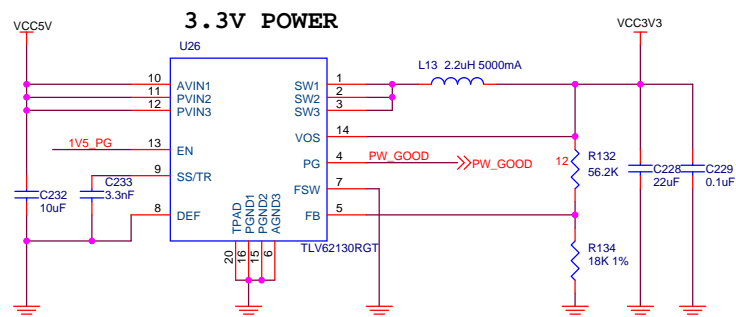
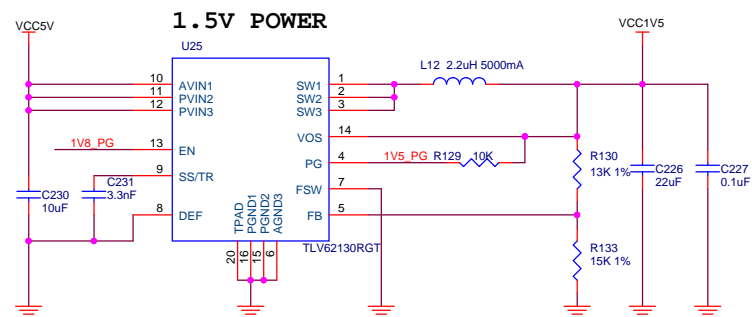
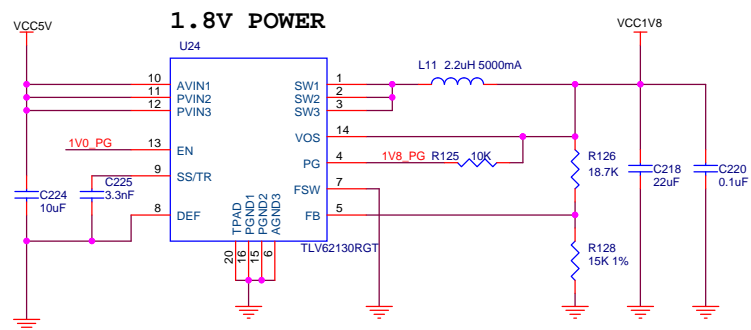
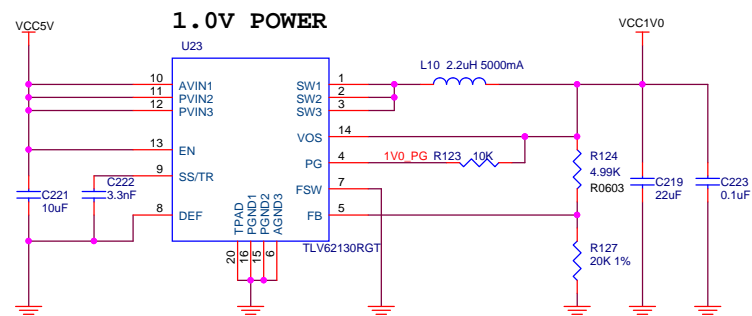
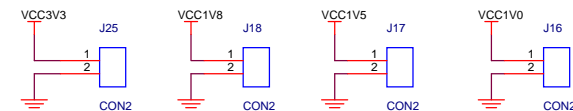
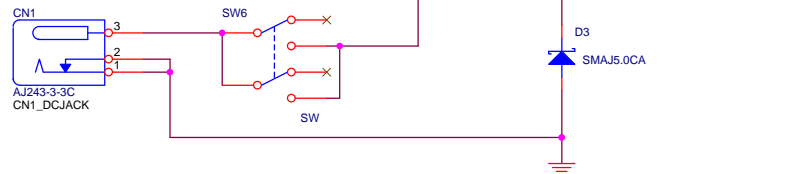


FPGA 40 PIN External IO



MIO Connector

5V/2A AC Adapter



Power On Sequence:

1.0V -> 1.8V -> 1.5 V -> 3.3V -> VCCIO

