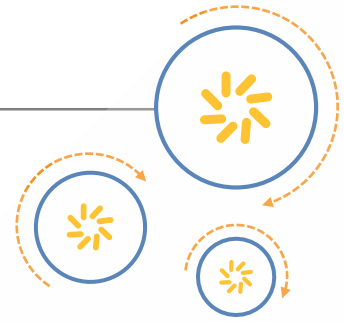




Qualcomm Atheros, Inc.



# IPQ4018/IPQ4028/IPQ4019/IPQ4029 Home Switch Software Development Kit

## User Guide

80-Y9571-7 Rev. B

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Qualcomm Atheros, Inc.  
1700 Technology Drive  
San Jose, CA 95110  
U.S.A.

## Revision history

Revision	Date	Description
A	June 2015	Initial release
B	November 2015	Update <a href="#">2.2.1</a> and <a href="#">5.2.1</a> .

# Contents

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<b>1 Overview .....</b>	<b>6</b>
1.1 Modularity and Hierarchy .....	6
1.2 Flexibility .....	6
1.3 Hardware abstraction .....	6
1.4 Portability .....	6
1.5 Robustness and Reliability .....	7
<b>2 Architecture .....</b>	<b>8</b>
2.1 Basic software architecture .....	8
2.2 Basic software feature .....	9
2.2.1 Port Control .....	9
2.2.2 VLAN .....	10
2.2.3 Port VLAN .....	10
2.2.4 FDB .....	11
2.2.5 ACL .....	11
2.2.6 QoS .....	12
2.2.7 IGMP/MLD .....	12
2.2.8 Leaky .....	13
2.2.9 Mirror .....	13
2.2.10 Rate .....	13
2.2.11 STP .....	13
2.2.12 MIB .....	13
2.2.13 LED .....	13
2.2.14 Misc .....	14
2.2.15 CosMap .....	14
2.2.16 IP .....	15
2.2.17 NAT .....	15
2.2.18 Trunk .....	16
2.2.19 Sec .....	16
2.2.20 Initialization .....	16
<b>3 Building .....</b>	<b>17</b>
3.1 Directory structure .....	17
3.2 Building from the Source .....	18
3.2.1 Options .....	18
3.2.2 Build Target .....	19
<b>4 Porting .....</b>	<b>21</b>
4.1 Initialization .....	21
4.2 Register access .....	21
<b>5 Shell22 .....</b>	<b>22</b>
5.1 Shell basics .....	22
5.2 Detailed commands .....	22
5.2.1 Port control .....	22
5.2.2 VLAN .....	24
5.2.3 Port VLAN .....	25
5.2.4 FDB .....	26

5.2.5 ACL .....	27
5.2.6 QoS .....	27
5.2.7 IGMP/MLD .....	28
5.2.8 Leaky .....	30
5.2.9 Mirror .....	30
5.2.10 Rate .....	30
5.2.11 STP .....	31
5.2.12 MIB .....	31
5.2.13 LED .....	31
5.2.14 CosMap .....	31
5.2.15 Misc .....	32
5.2.16 IP .....	33
5.2.17 NAT .....	35
5.2.18 Trunk .....	36
5.2.19 Security .....	36
5.2.20 Register Access and Debug .....	37
5.2.21 Set Device ID .....	38

# 1 Overview

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The Switch Software Development Kit (SSDK) is a set of drivers to manage Qualcomm® switch. It can be the foundation for customer's application to control the behaviors of the switch or as a reference to build customer's own low-level drivers.

The goals of the design are listed below.

## 1.1 Modularity and Hierarchy

The SSDK is based on a multi-layer architecture in which every layer has its own targets. This architecture ensures the SSDK can scale from small low-end system to future large multi-CPU and distributed system. Customers can select whether including every layer into their own applications or not by simply changing build options to meet their own requirements.

Meanwhile, the SSDK can be partitioned into separate function modules, thus they can only attach required functions into their systems to archive small-footprint implementations for cost sensitive systems.

## 1.2 Flexibility

For running on some UNIX-like Operation System in which operation of the CPU is divided into two distinct modes – Kernel mode and User mode, main modules of the SSDK can be executed in the kernel space or the user space by changing the related option on building. In this way, the SSDK can meet different requirements of customers' various systems.

## 1.3 Hardware abstraction

The SSDK abstracts all hardware details of Qualcomm Atheros switch line by providing consistent APIs to customers, who do not have to understand the implementation and register details and can easily use APIs to build their own applications.

## 1.4 Portability

The SSDK is built upon a System Abstraction Layer (SAL), which abstracts difference between various OSs and CPU architectures and is easily to be ported to a new OS or CPU system. This can be achieved by adhering OS and Board Support Package (BSP) functions to some elaborated SAL APIs.

## 1.5 Robustness and Reliability

The SSDK ensures its production-quality from design phase to release phase. In addition, the SSDK handles possible error states and return a well-defined error code to reduce instability.

## 2 Architecture

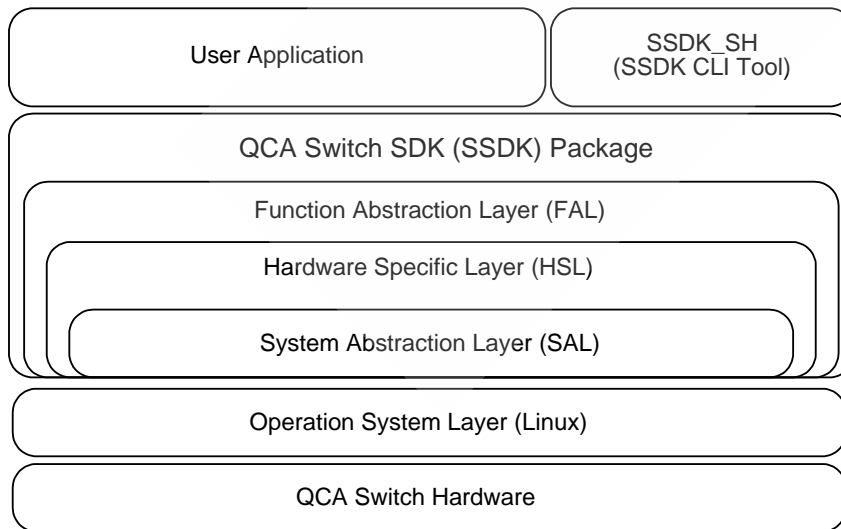
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This chapter gives an overview of the SSDK software architecture and supporting features.

### 2.1 Basic software architecture

The SSDK can be divided into three layers: FAL, HSL and SAL. Function Abstraction Layer (FAL) abstracts the implement details of Qualcomm Atheros switch chips by providing consistent Function APIs (FAPIs) to customers; Hardware Specific Layer (HSL) provides Hardware APIs (HAPIs) to implement functions of specific chip; System Abstraction Layer (SAL) that consists of OS abstraction APIs (OSAPI) and System Driver APIs (SDAPI) are used to abstract the OS and CPU system. In addition, the SSDK includes a CLI-like simple shell to manage the switch and to provide a reference of the usage of the SSDK to customers.

The basic software architecture can be illustrated in the below figure.



FAL is designed for providing unified APIs for Qualcomm Atheros switch line to user applications such as CLI, web and other protocols, for example, IGMP snooping, DHCP relay etc. Customers can build their applications through the well-defined APIs without the deep understanding of internal implementation and register details. Furthermore, because FAL abstracts the differences among Qualcomm Atheros switch products, when porting a system from one chip to another one, customers can build their applications with only slightly changes.

HSL as what implied by its name, is targeted for providing a set of APIs for specific Qualcomm Atheros switch chip, including for example, Isis the name of the switch core for QCA8337. HSL knows the internal structure of the switch and modifies the related registers directly by invoking SDAPIs in the SAL layer. User applications can invoke APIs provide by HSL directly as well. As the result, the SSDK can serve with impressive small foot-print by simply choosing related build options.



SAL consisting of OS abstraction APIs (OSAPI) and System Driver APIs (SDAPI) exists for portability where OSAPIs provide basic APIs such as memory, timer etc. to abstract differences among common OSs as Linux, xBSD and vxWorks, and SDAPIs abstract the details of specific hardware platform by providing MDIO and future possible PCI functions for register access. By this mean, Efforts to porting to a new OS and hardware platform, which can be extremely relieved, are limited to only this layer, although at this time, the SSDK only provides supports for Linux.

Furthermore, the SSDK includes a simple CLI-like SHELL on Linux, which can be referenced as an example for customers' applications, to expedite experience of Qualcomm Atheros switch.

## 2.2 Basic software feature

In most cases, APIs of HSL can be a subset of those of FAL, so we only describe features of FAL layer here. Usually APIs in the same feature set are collected to a file with a meaningful suffix as fal\_acl, fal\_fdb, fal\_igmp, fal\_leaky, fal\_mib, fal\_mirror, fal\_port\_ctrl, fal\_portvlan, fal\_vlan, fal\_qos, fal\_rate, fal\_stp, fal\_misc, fal\_cosmap, fal\_ip, fal\_nat, fal\_trunk and fal\_sec.

### 2.2.1 Port Control

Port control provides APIs for relate port control operations. It supports the following functions:

- Set and get duplex/speed mode on one port
- Set and get ability/status of auto negotiation on one port
- Set and get Atheros header status on one port
- Set and get flow control status on one port
- Set and get power saving status on one port
- Set and get hibernate status on one port
- Set and get congestion drop status on one port
- Run cable diagnostic test on one port
- Set and get 802.3az status on one port
- Set and get medium crossover type on one port
- Check current medium crossover status on one port
- Set and get port prefer medium type on one combo port
- Get current active medium type on one combo port
- Set and get fiber mode on one combo port
- Set and get port local loopback status on one port
- Set and get port remote loopback status on one port
- Set reset status on one port
- Set power off on one port
- Set power on on one port
- Set magic frame MAC address on one port

- Get PHY ID on one port
- Set and get port wake on LAN status
- Set and get interface mode on one combo port
- Check current interface mode status on one combo port

## 2.2.2 VLAN

VLAN provides APIs for operation VLAN entry. It supports the following functions:

- Write VLAN entry in switch chip
- Create delete and update VLAN entry in switch chip
- Next and find VLAN entry in switch chip
- Flush all VLAN entries in switch chip
- Add, deleted and update VLAN port member
- Set and get FID VLAN binding
- Set and get VLAN based source address auto learning status

## 2.2.3 Port VLAN

Port VLAN provides APIs for port-based VLAN feature and QinQ feature based on VLAN translation entry. It supports the following functions:

- Set and get 802.1q VLAN mode on one port
- Set and get egress VLAN mode on one port
- Add, delete, update and get port VLAN member on one port
- Set and get default VLAN id on one port
- Set and get force port default VLAN id status on one port
- Set and get force port-based VLAN status on one port
- Set and get nest VLAN status on one port
- Set and get TPID for nest VLAN on switch chip
- Set and get ingress VLAN mode on one port
- Set and get TLS status on one port
- Set and get priority propagation status on one port
- Set and get default s-vid on one port
- Set and get default c-vid on one port
- Set and get default VRF lookup ID on one port (available in ESS of IPQ4018/IPQ4019/IPQ4028/IPQ4029 only)
- Set and get VLAN propagation status on one port
- Add, delete and get a VLAN translation entry on one port
- Set and get switch QinQ work mode on switch chip

- Set and get QinQ role on one port
- Set and get dot1q work mode on switch chip
- Iterate all VLAN translation entries on one port

The key points for user application to implement QinQ based on VLAN translation entry feature are below:

- Set switch chip QinQ work mode, s-tag mode or c-tag mode.
- Set switch port QinQ work role, core port or edge port.
- Add one or some VLAN translation entries on one port.

For more details, refer to the QinQ application for the QCA switch family.

## 2.2.4 FDB

FDB is also called Address resolution lookup (ARL) Table or MAC Table by other vendors, and provides APIs for maintaining forwarding data base in switch chip. It supports the following functions:

- Add and delete FDB entry. Two more parameters are for the load balance feature in FDB entry for IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS.
- First, next and find FDB entry
- Set and get FDB dynamic learning status on one port
- Set and get FDB aging status on switch chip
- Set and get FDB aging timer on switch chip
- Iterate all FDB entries on switch chip
- Next FDB entries in extend mode
- First FDB entries in extend mode
- Set and get source address learning limit on one port
- Set and get forwarding command for learning limit exceed on one port
- Set and get source address learning limit
- Set and get forwarding command for learning limit exceed
- Add, delete, find and iterate reserved FDB entries

## 2.2.5 ACL

ACL provides APIs for define policy and related actions for specific flow. It supports the following functions:

- Enable and disable ACL engine
- Create and destroy ACL list
- Bind and unbind ACL list on one port
- Add, delete and query ACL rules in a ACL list

The sequence for user applications to implement ACL should as below:

- Create an ACL list
- Add ACL rules to the created ACL list
- Bind the list to a port. The ACL will take effect only when it is bind to port(s)

Since the programming for the ACL engine is complex, contact the local QCA home switch support for further information.

## 2.2.6 QoS

QoS provides APIs for QoS feature. It supports the following functions:

- Set and get the scheduling mode on switch chip
- Set and get port/queue transmission buffer status/number on one port
- Set and get dot1p tag to queue mapping entry on switch chip
- Set and get DSCP to queue mapping entry on switch chip
- Set and get QoS parameters for precedence of queue mapping modes on one port
- Set and get default dot1p tag on one port
- Set and get port receiving buffer number on one port
- Set and get the scheduling mode on one port
- Set and get port default stag priority
- Set and get port default ctag priority
- Set and get queue based QoS remark

## 2.2.7 IGMP/MLD

IGMP provides APIs for IGMP/MLD packets identification and hardware join/leave. It supports the following functions:

- Set and get for IGMP/MLD identification status on one port.
- Set and get IGMP/MLD packet forwarding method when enabling IGMP/MLD identification on switch chip.
- Set and get IGMP/MLD hardware join/leave status on one port.
- Set and get router port(s) for IGMP/MLD hardware join/leave on switch chip.
- Set and get the status for creating/deleting a multicast entry in FDB for hardware join/leave on switch chip.
- Set and get the static status of multicast entry which learned by hardware
- Set and get the leaky status of multicast entry which learned by hardware
- Set and get igmpv3/mldv2 packets hardware acknowledgement status on switch chip
- Set and get the queue status of multicast entry which learned by hardware
- Set and get multicast entry learn limit
- Set and get multicast entry learn limit exceed command

## 2.2.8 Leaky

Leaky provides APIs for leaky function. It supports the following functions:

- Set and get unicast/multicast packets leaky mode (port-based control or FDB-based control)
- Set and get unicast/multicast/ARP packets leaky status on one port

## 2.2.9 Mirror

Mirror provides APIs for mirror feature. It supports the following functions:

- Set and get analyzer port in switch chip for mirror
- Set and get ingress/egress mirror status for one port

## 2.2.10 Rate

Rate provides APIs for rate feature. It supports the following functions:

- Set and get port ingress/port egress/queue egress rate limit status on one port
- Set and get packets type of storm control on one port
- Set and get the rate of storm control on one port
- Set and get the rate of storm control on one port
- Set and get port based policer
- Set and get ACL based policer
- Set and get port based shaper
- Set and get queue based shaper

## 2.2.11 STP

STP provides API for STP feature. It supports the following function:

- Set and get spanning tree state on one port

## 2.2.12 MIB

MIB provides APIs for getting MIB information from switch chip. It supports the following functions:

- Get MIB information on one port
- Set and get MIB status

## 2.2.13 LED

On some Qualcomm switch serials, the LED blinking mode can be controlled by setting control pattern. Usually the LEDs are divided into different groups such as WAN groups and LAN groups for each of which has one or several LEDs depending on by switch chips. Different LEDs in different groups may have different control pattern such as always ON, always OFF, always blinking and blinking depending on port speed, port duplex, etc.

The SSDK provides API to control led blinking pattern for each group. It supports the following function:

- Set and get led control pattern on one led group.

## 2.2.14 Misc

Misc provides APIs for some miscellaneous features. It supports the following functions:

- Set and get ARP status on switch chip
- Set and get forwarding command for unknown source address packets on one port
- Set and get supported max frame size on one chip
- Set and get destination ports for unknown unicast/multicast packets on switch chip
- Set and get CPU port status on switch chip
- Set and get status of PPPoE packets on switch chip
- Set and get status of DHCP packets on one port
- Set and get status of broadcast packets forwarding to CPU on switch chip
- Set and get ARP packets forwarding command on switch chip
- Set and get EAPOL (802.1x) packets forwarding command on switch chip
- Add, delete and get a PPPoE session entry on switch chip
- Set and get EAPOL (802.1x) packets hardware acknowledgement status on one port
- Set and get rip v1 packets hardware acknowledgement status on one port
- Set and get port based ARP request packets hardware acknowledgement status on one port
- Set and get port based ARP ACK packets hardware acknowledgement status on one port
- Add, delete and get PPPoE entries in extend mode

The PPPoE session entry offer capability to forward IP multicast packets encapsulated in PPPoE format to multi switch ports by switch hardware.

## 2.2.15 CosMap

CoSMap provides APIs for cos mapping features. It supports the following functions:

- Set and get DSCP to priority mapping
- Set and get DSCP to drop precedence mapping
- Set and get Dot1p to priority mapping
- Set and get Dot1P to drop precedence mapping (available in IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS only)
- Set and get DSCP to priority mapping for WAN/CPU port (available in IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS only)
- Set and get DSCP to drop precedence mapping for WAN/CPU port (available in IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS only)

- Set and get Dot1p to priority mapping for WAN/CPU port (available in IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS only)
- Set and get Dot1P to drop precedence mapping for WAN/CPU port (available in IPQ4018/IPQ4019/IPQ4028/IPQ4029 ESS only)
- Set and get priority to queue mapping
- Set and get priority to enhanced queue mapping

## 2.2.16 IP

IP provides APIs for IP features. It supports the following functions:

- Add, delete, get and next host entry
- Bind counter and PPPoE session to host entry
- Set and get port based ARP learn flag
- Set and get ARP learn mode
- Set and get IP/ARP source guard mode
- Set and get routing status
- Add, delete and next interface entry
- Set and get IP/ARP source unknown forwarding command
- Set and get IPv6 base address
- Set and get IPv4 host route
- Set and get IPv6 host route
- Set and get IPv4 default route
- Set and get IPv6 default route
- Set and delete IPv4 load balance
- Set and delete IPv6 load balance
- Set and get default flow command

## 2.2.17 NAT

NAT provides APIs for NAT/NAPT features. It supports the following functions:

- Add, delete, get and next NAT entry
- Bind counter to NAT entry
- Add, delete, get and next NAPT entry
- Bind counter to NAPT entry
- Set and get NAT status
- Set and get NAPT status
- Set and get NAT hash flag
- Set and get NAPT hash mode

- Set and get NAT private base address
- Set and get NAT private base address mapping mode
- Add, delete and next public address
- Set and get unknown NAT session forwarding command
- Set and delete flow cookie
- Set and delete flow load balance

## 2.2.18 Trunk

Trunk provides APIs for trunk features. It supports the following functions:

- Set and get trunk group port member information
- Set and get trunk hash mode

## 2.2.19 Sec

Sec provides APIs for Security features. It supports the following function:

- Set and get security check items information

## 2.2.20 Initialization

Initialization provides API for user applications to initialize the SSDK.



# 3 Building

---

This chapter gives an overview of directory architecture of SSDK and demonstrates how to build it in QSDK.

## 3.1 Directory structure

<b>[/]</b>	This is the root directory of the SSDK.
<b>[/app]</b>	This directory contains source code files of nathelper.
<b>[/make]</b>	This directory contains target files which define the components of building.
<b>[/src]</b>	All source code files of the SSDK are kept under this directory.
<b>[/src/api]</b>	This directory contains source code files for API access interface.
<b>[/src/fal]</b>	This directory contains source code files for FAL. FAL can provide unified interfaces and wrap the difference among switch chips. Customers can access these interfaces and needn't care internal details.
<b>[/src/fal_uk]</b>	This directory contains an example for user applications that invoke the SSDK built in kernel mode. It can be built with user applications together.
<b>[/src/hsl]</b>	This directory contains source code files for HSL that provides interfaces for a specific chip.
<b>[/src/hsl/athena]</b>	This directory contains source code files for Athena the switch core name for AR8216.
<b>[/src/hsl/garuda]</b>	This directory contains source code files for Garuda the switch core name for AR8316.
<b>[/src/hsl/phy]</b>	This directory contains source code files for PHY driver.
<b>[/src/hsl/shiva]</b>	This directory contains source code files for Shiva the switch core name for AR8227/AR8228/AR8229.
<b>[/src/hsl/horus]</b>	This directory contains source code files for Horus the switch core name for AR8236.
<b>[/src/hsl/isis]</b>	This directory contains source code files for Isis the switch core name for AR8327/AR8328/AR8325.
<b>[/src/hsl/isisc]</b>	This directory contains source code files for ISISC the switch core name for AR8337.
<b>[/src/hsl/dess]</b>	This directory contains source code files for dess the switch core name for ESS module of IPQ4018/IPQ4019/IPQ4028/IPQ4029 platform.
<b>[/src/shell]</b>	This directory contains source code files all source code files for the SHELL.
<b>[/src/init]</b>	This directory contains source code files for the initialization of the SSDK.
<b>[/src/ref]</b>	This directory contains source code files for reference code.
<b>[/src/sal]</b>	This directory contains source code files for SAL.
<b>[/src/sal/os]</b>	This directory contains source code files for OSAPI.
<b>[/src/sal/sd]</b>	This directory contains source code files for SDAPI.
<b>[/src/util]</b>	This directory contains source code files for utility functions.

<b>[/include]</b>	All header files for the SSDK are kept under this directory.
<b>[/include/api]</b>	This directory contains header files for API access interface declarations.
<b>[/include/common]</b>	This directory contains public header files for the SSDK.
<b>[/include/fal]</b>	This directory contains header files of FAL.
<b>[/include/hsl]</b>	This directory contains header files of HSL.
<b>[/include/hsl/athena]</b>	This directory contains header files for Athena the switch core name for AR8216.
<b>[/include/hsl/garuda]</b>	This directory contains header files for Garuda the switch core name for AR8316.
<b>[/include/hsl/shiva]</b>	This directory contains header files for Shiva the switch core name for AR8227/AR8228/AR8229.
<b>[/include/hsl/horus]</b>	This directory contains header files for Horus the switch core name for AR8236.
<b>[/include/hsl/isis]</b>	This directory contains header files for Horus the switch core name for AR8327/AR8328/AR8325.
<b>[/include/hsl/isisc]</b>	This directory contains header files for Horus the switch core name for AR8337.
<b>[/include/hsl/dess]</b>	This directory contains header files for Horus the switch core name for ESS module of IPQ4018/IPQ4019/IPQ4028/IPQ4029 platform.
<b>[/include/init]</b>	This directory contains header files for the initialization of the SSDK.
<b>[/include/ref]</b>	This directory contains header files for reference code declarations.
<b>[/include/sal]</b>	This directory contains header files for SAL.
<b>[/include/sal/os]</b>	This directory contains header files for OSAPI.
<b>[/include/sal/sd]</b>	This directory contains header files for SDAPI.
<b>[/include/shell]</b>	This directory contains header files for SHELL.

## 3.2 Building from the Source

SSDK can be compiled into two files:

- qca-ssdk.ko: an independent kernel module included all SSDK features.
- ssdk\_sh: an user program provided a cli-like simple shell.

### 3.2.1 Options

Use the following options to satisfy particular requirements and change these options by editing the file config which locates in the root directory:

Switch chip type:

**CHIP\_TYPE = ALL\_CHIP**

FAL included in the SSDK:

**FAL = TRUE**

API\_LOCK all APIs locker with locker or not. If you want all APIs with locker, define **API\_LOCK=TRUE**. Otherwise, define

**API\_LOCK=FALSE**.

Features included in SSDK:

**IN\_ACL=TRUE**  
**IN\_FDB=TRUE**  
**IN\_IGMP=TRUE**  
**IN\_LEAKY=TRUE**  
**IN\_LED=TRUE**  
**IN\_MIB=TRUE**  
**IN\_MIRROR=TRUE**  
**IN\_MISC=TRUE**  
**IN\_PORTCONTROL=TRUE**  
**IN\_PORTVLAN=TRUE**  
**IN\_QOS=TRUE**  
**IN\_RATE=TRUE**  
**IN\_STP=TRUE**  
**IN\_VLAN=TRUE**  
**IN\_REDUCED\_ACL=FALSE**  
**IN\_COSMAP= FALSE**  
**IN\_IP= FALSE**  
**IN\_NAT= FALSE**  
**IN\_TRUNK= FALSE**  
**IN\_SEC= FALSE**  
**IN\_NAT\_HELPER=FALSE**  
**IN\_INTERFACECONTROL=TRUE**  
**IN\_MACBLOCK=FALSE**

For example, set value of IN\_ACL to FALSE if you don't need the ACL feature, then the target of building will exclude the ACL related APIs.

### 3.2.2 Build Target

- Select SSDK in *make menuconfig*

***make menuconfig***

Use the following path to select qca-ssdk module:

Kernel modules → Network Devices → Kmod-qca-ssdk

Then save, exit.

- Building SSDK

- make package/qca-ssdk/install V=s
- Clean SSDK
  - make package/qca-ssdk/clean

# 4 Porting

---

## 4.1 Initialization

SSDK initialization occurs when qca-ssdk.ko is inserted into kernel, and the initialization process is independent of the other modules.

In QSDK, an auto-load ID is specified to determine the loading time of qca-ssdk.ko.

## 4.2 Register access

In QSDK, Linux kernel provides the standard MII bus interface. If switch module connects to CPU through MDIO bus, SSDK uses the interface to access switch registers by a specified bus name.

For example:

```
snprintf(busid, MII_BUS_ID_SIZE, "%s.%d", "mdio-gpio", 0);
miidev = bus_find_device_by_name(&platform_bus_type, NULL, busid);
miibus = dev_get_drvdata(miidev);
miibus->write(bus, phy_addr, QCA_MII_MMD_ADDR, addr);
```

For IPQ4018/IPQ4019/IPQ4028/IPQ4029 SOC, switch module is accessed through AHB bus and SSDK maps physical address and use local bus access mode to access switch registers.

For example:

```
ioremap_nocache(ssdk_dt_global.switchreg_base_addr,
                ssdk_dt_global.switchreg_size);
readl(hw_addr + reg_addr)
writel(reg_val, hw_addr + reg_addr);
```

# 5 Shell

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## 5.1 Shell basics

The SSDK includes a CLI-like switch shell to configure the switch on Linux. Customer can invoke the shell built per chapter 3 by executing the file `ssdk_sh`.

The SSDK provides some useful help mechanisms to facilitate the usage of the Shell. To get help specific to a command mode, a command, a keyword, or an argument, use one of following commands:

- Entering a question mark (?) at the Shell prompt allows you to obtain a list of commands available for each command mode.
- Entering “*abbreviated-command?*” the shell shows a list of commands that begin with a particular character string. (No space between command and question mark.)
- Entering “*Command ?*” to get the keywords or arguments that you must enter next on the command line. (Space between command and question mark.)
- Entering “*abbreviated-command<Tab>*”, the shell helps you to **Completes a partial command name or lists all commands partially matched Help messages of the SSDK** use following conventions:
  - Required command arguments are inside angle brackets (< >).
  - Optional command arguments are in square brackets ([ ]).
  - Alternative keywords are separated by vertical bars (|).
  - The minimum and the maximum of a value range are separated by horizontal line (-).

To quit the Shell, you can enter “*q*” or “*quit*” at the Shell prompt.

## 5.2 Detailed commands

### 5.2.1 Port control

Name	Function	Usage
port duplex get	get duplex mode of a port	<i>port duplex get &lt;port_id&gt;</i>
port duplex set	set duplex mode of a port	<i>port duplex set &lt;port_id&gt; &lt;half   full&gt;</i>
port speed get	get speed mode of a port	<i>port speed get &lt;port_id&gt;</i>
port speed set	set speed mode of a port	<i>port speed set &lt;port_id&gt; &lt;10   100   1000&gt;</i>
port autoAdv get	get auto-negotiation advertisement of a port	<i>port autoAdv get &lt;port_id&gt;</i>

Name	Function	Usage
port autoAdv set	set auto-negotiation advertisement of a port	<i>port autoAdv set &lt;port_id&gt; &lt;cap_bitmap&gt;</i>
port autoNeg get	get auto-negotiation status of a port	<i>port autoNeg get &lt;port_id&gt;</i>
port autoNeg enable	enable auto-negotiation of a port	<i>port autoNeg enable &lt;port_id&gt;</i>
port autoNeg restart	restart auto-negotiation process of a port	<i>port autoNeg restart &lt;port_id&gt;</i>
port header set	set Atheros header/tag status of a port	<i>port header set &lt;port_id&gt; &lt;enable   disable&gt;</i>
port header get	get Atheros header/tag status of a port	<i>port header get &lt;port_id&gt;</i>
port txhdr set	set Atheros header/tag status of a port	<i>port txhdr set &lt;port_id&gt; &lt;noheader onlymanagement allframe&gt;</i>
port txhdr get	get Atheros header/tag status of a port	<i>port txhdr get &lt;port_id&gt;</i>
port rxhdr set	set Atheros header/tag status of a port	<i>port rxhdr set &lt;port_id&gt; &lt;noheader onlymanagement allframe&gt;</i>
port rxhdr get	get Atheros header/tag status of a port	<i>port rxhdr get &lt;port_id&gt;</i>
port flowCtrl set	set flow control status of a port	<i>port flowCtrl set &lt;port_id&gt; &lt;enable   disable&gt;</i>
port flowCtrl get	get flow control status of a port	<i>port flowCtrl get &lt;port_id&gt;</i>
port powersave set	set power saving status of a port	<i>port powersave set &lt;port_id&gt; &lt;enable   disable&gt;</i>
port powersave get	get power saving status of a port	<i>port powersave get &lt;port_id&gt;</i>
port hibernate set	set hibernate status of a port	<i>port hibernate set &lt;port_id&gt; &lt;enable   disable&gt;</i>
port hibernate get	get hibernate status of a port	<i>port hibernate set &lt;port_id&gt;</i>
port cdt run	run cable diagnostic test of a port	<i>port cdt run &lt;port_id&gt; &lt;mdi_pair&gt;</i>
port congedrop set	set congestion drop status	<i>port congedrop set &lt;port_id&gt; &lt;enable   disable&gt;</i>
port congedrop get	get congestion drop status	<i>port congedrop set &lt;port_id&gt;</i>
port ieee8023az set	set 802.3 az status of a port	<i>port ieee8023az set &lt;port_id&gt; &lt;enable disable&gt;</i>
port ieee8023az get	get 802.3 az status of a port	<i>port ieee8023az get &lt;port_id&gt;</i>
port crossover set	set crossover of a port	<i>port crossover set &lt;port_id&gt; &lt;auto mdi mdix&gt;</i>
port crossover get	get crossover of a port	<i>port crossover get &lt;port_id&gt;</i>
port crossover status check	show current crossover status of a port	<i>port crossover status &lt;port_id&gt;</i>
port prefer medium set	set prefer medium of a combo port	<i>port preferMedium set &lt;port_id&gt; &lt;copper fiber&gt;</i>
port prefer medium get	get prefer medium of a combo port	<i>port preferMedium get &lt;port_id&gt;</i>
Port current medium get	get port current medium type	<i>port mediumType get &lt;port_id&gt;</i>
port fiber mode set	set port fiber mode of a combo port	<i>port fiberMode set &lt;port_id&gt; &lt;100fx 1000bx&gt;</i>
port fiber mode get	get port fiber mode of a combo port	<i>port fiberMode get &lt;port_id&gt;</i>
port local loopback set	set port local loopback of a port	<i>port localLoopback set &lt;port_id&gt; &lt;enable disable&gt;</i>
port local loopback get	get port local loopback of a port	<i>port localLoopback get &lt;port_id&gt;</i>

Name	Function	Usage
port remote loopback set	set port remote loopback of a port	<i>port remoteLoopback set &lt;port_id&gt; &lt;enable/disable&gt;</i>
port remote loopback get	get port remote loopback of a port	<i>port remoteLoopback get</i>
port reset	set port reset of a port	<i>port reset set &lt;port_id&gt;</i>
port power off	set port power off of a port	<i>port poweroff set &lt;port_id&gt;</i>
port power on	set port power on of a port	<i>port poweron set &lt;port_id&gt;</i>
port magic frame address set	set port magic frame address of a port	<i>port magicFrameMac set &lt;port_id&gt; &lt;mac_address&gt;</i>
port magic frame address get	get port magic frame address of a port	<i>port magicFrameMac get &lt;port_id&gt;</i>
port phy id get	get port PHY ID of a port	<i>port phyId get &lt;port_id&gt;</i>
port wol set	set port WOL status of a port	<i>port wolstatus set &lt;port_id&gt; &lt;enable/disable&gt;</i>
port wol get	get port WOL status of a port	<i>port wolstatus get &lt;port_id&gt;</i>
port interface mode set	set port interface mode of a combo port	<i>port interfaceMode set &lt;port_id&gt; &lt;psgmii_baset/psgmii_bx1000/psgmii_fx100/psgmii_amdet/sgmii_baset&gt;</i>
port interface mode get	get port interface mode of a combo port	<i>port interfaceMode get &lt;port_id&gt;</i>
port interface mode status get	get port current interface mode of a combo port	<i>port interfaceMode status &lt;port_id&gt;</i>

## 5.2.2 VLAN

Name	Function	Usage
vlan entry create	create a VLAN entry	<i>vlan entry create &lt;vlan_id&gt;</i>
vlan entry del	delete a VLAN entry	<i>vlan entry del &lt;vlan_id&gt;</i>
vlan entry update	update port member of a VLAN entry	<i>vlan entry update &lt;vlan_id&gt; &lt;member_bitmap&gt; &lt;0&gt;</i>
vlan entry find	find a VLAN entry by VLAN ID	<i>vlan entry find &lt;vlan_id&gt;</i>
vlan entry next	find next VLAN entry by VLAN ID	<i>vlan entry next &lt;vlan_id&gt;</i>
vlan entry append	append a VLAN entry	<i>vlan entry append</i>
vlan entry show	show whole VLAN entries	<i>vlan entry show</i>
vlan entry flush	flush all VLAN entries	<i>vlan entry flush</i>
vlan fid set	set VLAN entry FID	<i>vlan fid set &lt;vlan_id&gt; &lt;fid&gt;</i>
vlan fid get	get VLAN entry FID	<i>vlan fid get &lt;vlan_id&gt;</i>
vlan member add	add VLAN entry member	<i>vlan member add &lt;vlan_id&gt; &lt;port_id&gt; &lt;unmodified/untagged/tagged&gt;</i>
vlan member del	delete VLAN entry member	<i>vlan member del &lt;vlan_id&gt; &lt;port_id&gt;</i>
vlan learnsts set	set VLAN entry learn status	<i>vlan learnsts set &lt;vlan_id&gt; &lt;enable/disable&gt;</i>
vlan learnsts get	get VLAN entry learn status	<i>vlan learnsts get &lt;vlan_id&gt;</i>



## 5.2.3 Port VLAN

Name	Function	Usage
portVlan ingress get	get ingress VLAN mode of a port	<i>portVlan ingress get &lt;port_id&gt;</i>
portVlan ingress set	set ingress VLAN mode of a port	<i>portVlan ingress set &lt;port_id&gt; &lt;disable   secure   check   fallback&gt;</i>
portVlan egress get	get egress VLAN mode of a port	<i>portVlan egress get &lt;port_id&gt;</i>
portVlan egress set	set egress VLAN mode of a port	<i>portVlan egress set &lt;port_id&gt; &lt;unmodified   untagged   tagged&gt;</i>
portVlan member add	add a member to the port based VLAN of a port	<i>portVlan member add &lt;port_id&gt; &lt;memport_id&gt;</i>
portVlan member del	delete a member from the port based VLAN of a port	<i>portVlan member del &lt;port_id&gt; &lt;memport_id&gt;</i>
portVlan member update	update members of the port based VLAN of a port	<i>portVlan member update &lt;port_id&gt; &lt;port_bitmap&gt;</i>
portVlan member get	get members of the port based VLAN of a port	<i>portVlan member get &lt;port_id&gt;</i>
portVlan defaultVid get	get default VLAN ID of a port	<i>portVlan defaultVid get &lt;port_id&gt;</i>
portVlan defaultVid set	set default VLAN ID of a port	<i>portVlan defaultVid set &lt;port_id&gt; &lt;vid&gt;</i>
portVlan forceVid set	set VLAN ID enforcement status of a port	<i>portVlan forceVid set &lt;port_id&gt; &lt;enable   disable&gt;</i>
portVlan forceVid get	get VLAN ID enforcement status of a port	<i>portVlan forceVid get &lt;port_id&gt;</i>
portVlan forceMode set	set port based VLAN enforcement status of a port	<i>portVlan forceMode set &lt;port_id&gt; &lt;enable   disable&gt;</i>
portVlan forceMode get	get port based VLAN enforcement status of a port	<i>portVlan forceMode get &lt;port_id&gt;</i>
portVlan nestVlan set	set nest VLAN status of a port	<i>portVlan nestVlan set &lt;port_id&gt; &lt;enable   disable&gt;</i>
portVlan nestVlan get	get nest VLAN status of a port	<i>portVlan nestVlan get &lt;port_id&gt;</i>
portVlan sVlanTPID set	set service VLAN TPID	<i>portVlan sVlanTPID set &lt;tpid&gt;</i>
portVlan sVlanTPID get	get service VLAN TPID	<i>portVlan sVlanTPID get</i>
portVlan invlan set	set port invlan mode	<i>portVlan invlan set &lt;port_id&gt; &lt;admit_all admit_tagged admit_untagged&gt;</i>
portVlan invlan get	get port invlan mode	<i>portVlan invlan get &lt;port_id&gt;</i>
portVlan tlsMode set	set TLS mode	<i>portVlan tlsMode set &lt;port_id&gt; &lt;enable disable&gt;</i>
portVlan tlsMode get	get TLS mode	<i>portVlan tlsMode get &lt;port_id&gt;</i>
portVlan priPropagation set	set priority propagation	<i>portVlan priPropagation set &lt;port_id&gt; &lt;enable disable&gt;</i>
portVlan priPropagation get	get priority propagation	<i>portVlan priPropagation get &lt;port_id&gt;</i>
portVlan defaultSVid set	set default SVID	<i>portVlan defaultSVid set &lt;port_id&gt; &lt;vlan_id&gt;</i>
portVlan defaultSVid get	get default SVID	<i>portVlan defaultSVid get &lt;port_id&gt;</i>

Name	Function	Usage
portVlan defaultCvid set	set default CVID	<i>portVlan defaultCvid set &lt;port_id&gt; &lt;vlan_id&gt;</i>
portVlan defaultCvid get	get default CVID	<i>portVlan defaultCvid get &lt;port_id&gt;</i>
portVlan vlanPropagation set	set VLAN propagation	<i>portVlan vlanPropagation set &lt;port_id&gt; &lt;disable clone replace&gt;</i>
portVlan vlanPropagation get	get VLAN propagation	<i>portVlan vlanPropagation get &lt;port_id&gt;</i>
portVlan translation add	add VLAN translation	<i>portVlan translation add &lt;port_id&gt;</i>
portVlan translation del	del VLAN translation	<i>portVlan translation del &lt;port_id&gt;</i>
portVlan translation get	get VLAN translation	<i>portVlan translation get &lt;port_id&gt;</i>
portVlan qinqMode set	set qinq mode	<i>portVlan qinqMode set &lt;ctag/stag&gt;</i>
portVlan qinqMode get	get qinq mode	<i>portVlan qinqMode get</i>
portVlan qinqRole set	set qinq role	<i>portVlan qinqRole set &lt;port_id&gt; &lt;edge/core&gt;</i>
portVlan qinqRole get	get qinq role	<i>portVlan qinqMode get &lt;port_id&gt;</i>
portVlan translation iterate	iterate VLAN translation tables	<i>portVlan translation iterate &lt;port_id&gt; &lt;iterator&gt;</i>
*portvlan vrf_id set	set port VRF ID	<i>portVlan vrf_id set &lt;port_id&gt; &lt;vrf_id&gt;</i>
*portvlan vrf_id get	get port VRF ID	<i>portVlan vrf_id get &lt;port_id&gt;</i>

1. \* Available in ESS of IPQ4018/IPQ4019/IPQ4028/IPQ4029 only.

## 5.2.4 FDB

Name	Function	Usage
fdb entry add	add a FDB entry	<i>fdb entry add</i>
fdb entry flush	flush all FDB entries	<i>fdb entry flush &lt;0:dynamic only 1:dynamic and static&gt;</i>
fdb entry show	show whole FDB entries	<i>fdb entry show</i>
fdb portEntry flush	flush all FDB entries by a port	<i>fdb portEntry flush &lt;port_id&gt; &lt;0:dynamic only 1:dynamic and static&gt;</i>
fdb entry del	delete a FDB entry	<i>fdb entry del</i>
fdb firstEntry find	find the first FDB entry	<i>fdb firstEntry find</i>
fdb nextEntry find	find next FDB entry	<i>fdb nextEntry find</i>
fdb entry find	find a FDB entry	<i>fdb entry find</i>
fdb portLearn set	set FDB entry learning status of a port	<i>fdb portLearn set &lt;port_id&gt; &lt;enable   disable&gt;</i>
fdb portLearn get	get FDB entry learning status of a port	<i>fdb portLearn get &lt;port_id&gt;</i>
fdb ageCtrl set	set FDB entry aging status	<i>fdb ageCtrl set &lt;enable   disable&gt;</i>
fdb ageCtrl get	get FDB entry aging status	<i>fdb ageCtrl get</i>
fdb ageTime set	set FDB entry aging time	<i>fdb ageTime set &lt;time:s&gt;</i>
fdb ageTime get	get FDB entry aging time	<i>fdb ageTime get</i>
fdb entry iterate	iterate all FDB entries	<i>fdb entry iterate &lt;iterator&gt;</i>
fdb ptlearnlimit set	set port FDB entry learn limit	<i>fdb ptlearnlimit set &lt;port_id&gt; &lt;enable disable&gt; &lt;limitcounter&gt;</i>

Name	Function	Usage
fdb ptlearnlimit get	get port FDB entry learn limit	<i>fdb ptlearnlimit get &lt;port_id&gt;</i>
fdb ptlearnexceedcmd set	set port forwarding command when exceed learn limit	<i>fdb ptlearnexceedcmd set &lt;port_id&gt; &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
fdb ptlearnexceedcmd get	get port forwarding command when exceed learn limit	<i>fdb ptlearnexceedcmd get &lt;port_id&gt;</i>
fdb learnlimit set	set FDB entry learn limit	<i>fdb learnlimit set &lt;enable/disable&gt; &lt;limitcounter&gt;</i>
fdb learnlimit get	get FDB entry learn limit	<i>fdb ptlearnlimit get</i>
fdb learnexceedcmd set	set forwarding command when exceed learn limit	<i>fdb ptlearnexceedcmd set &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
fdb learnexceedcmd get	get FDB entry learn limit	<i>fdb learnexceedcmd get</i>
fdb learnexceedcmd get	get FDB entry learn limit	<i>fdb learnexceedcmd get</i>
fdb resventry add	add a reserve FDB entry	<i>fdb resventry add</i>
fdb resventry del	delete a reserve FDB entry	<i>fdb resventry del</i>
fdb resventry find	find a reserve FDB entry	<i>fdb resventry find</i>
fdb resventry iterate	iterate all reserve FDB entries	<i>fdb resventry iterate &lt;iterator&gt;</i>
fdb resventry show	show whole reserve FDB entries	<i>fdb resventry show</i>
fdb ptLearnstatic set	set FDB entry learning static status of a port	<i>fdb ptLearnstatic set &lt;port_id&gt; &lt;enable/disable&gt;</i>
fdb ptLearnstatic get	get FDB entry learning static status of a port	<i>fdb ptLearnstatic get &lt;port_id&gt;</i>

## 5.2.5 ACL

Name	Function	Usage
acl list create	create an ACL list	<i>acl list create &lt;list_id&gt; &lt;priority&gt;</i>
acl list destroy	destroy an ACL list	<i>acl list destroy &lt;list_id&gt;</i>
acl list bind	bind an ACL list to a port	<i>acl list bind &lt;list_id&gt; &lt;0-0:direction&gt; &lt;0-0:objtype&gt; &lt;objindex&gt;</i>
acl list unbind	unbind an ACL list from a port	<i>acl list unbind &lt;list_id&gt; &lt;0-0:direction&gt; &lt;0-0:objtype&gt; &lt;objindex&gt;</i>
acl rule add	add ACL rules to an ACL list	<i>acl rule add &lt;list_id&gt; &lt;rule_id&gt; &lt;rule_nr&gt;</i>
acl rule delete	delete ACL rules from an ACL list	<i>acl rule delete &lt;list_id&gt; &lt;rule_id&gt; &lt;rule_nr&gt;</i>
acl rule query	query an ACL rule	<i>acl rule query &lt;list_id&gt; &lt;rule_id&gt;</i>
acl status set	set status of ACL engine	<i>acl status set &lt;enable   disable&gt;</i>
acl status get	get status of ACL engine	<i>acl status get</i>
acl udfprofile set	set port UDF profile	<i>acl udfprofile set &lt;port_id&gt; &lt;l2/l2snap/l3/l3plus/l4&gt; &lt;offset&gt; &lt;length&gt;</i>
acl udfprofile get	get port UDF profile	<i>acl udfprofile get &lt;port_id&gt; &lt;l2/l2snap/l3/l3plus/l4&gt;</i>

## 5.2.6 QoS

Name	Function	Usage
qos qTxBufSts set	set queue Tx buffer counting status of a port	<b>qos qTxBufSts set</b> <port_id> <enable   disable>
qos qTxBufSts get	get QoS queue Tx buffer counting status of a port	<b>qos qTxBufSts get</b> <port_id>
qos qTxBufNr set	set queue Tx buffer number	<b>qos qTxBuf set</b> <port_id> <queueid:0-3> <number>
qos qTxBufNr get	get queue Tx buffer number	<b>qos qTxBuf get</b> <port_id> <queueid:0-3>
qos ptTxBufSts set	set port Tx buffer counting status of a port	<b>qos ptTxBufSts set</b> <port_id> <enable   disable>
qos ptTxBufSts get	set port Tx buffer counting status of a port	<b>qos ptTxBufSts get</b> <port_id>
qos ptTxBufNr set	set port Tx buffer number	<b>qos ptTxBufNr set</b> <port_id> <number>
qos ptTxBufNr get	get port Tx buffer number	<b>qos ptTxBufNr get</b> <port_id>
qos ptMode set	set QoS mode of a port	<b>qos ptMode set</b> <port_id> <da   up   dscp   flow> <enable   disable>
qos ptMode get	get QoS mode of a port	<b>qos ptMode get</b> <port_id> <da   up   dscp   flow>
qos ptModePri set	set the priority of QoS modes of a port	<b>qos ptModePri set</b> <port_id> <da   up   dscp   flow> <priority:0-3>
qos ptModePri get	get the priority of QoS modes of a port	<b>qos ptModePri get</b> <port_id> <da   up   dscp   flow>
qos ptschMode set	set port traffic scheduling mode	<b>qos ptschMode set</b> <port_id> <sp/wrr/mix/mixplus> <q0,q1,q2,q3>
qos ptschMode get	get port traffic scheduling mode	<b>qos ptschMode get</b> <port_id> <sp/wrr/mix/mixplus> <q0,q1,q2,q3>
qos ptRxBufNr set	set port Rx buffer number	<b>qos ptRxBufNr set</b> <port_id> <number:0-60>
qos ptRxBufNr get	get port Rx buffer number	<b>qos ptRxBufNr get</b> <port_id>
qos ptDefaultSpri set	set default stag priority for received frames of a port	<b>qos ptDefaultSpri set</b> <port_id> <spri:0-7>
qos ptDefaultSpri get	get default stag priority for received frames of a port	<b>qos ptDefaultSpri get</b> <port_id>
qos ptDefaultCpri set	set default ctag priority for received frames of a port	<b>qos ptDefaultCpri set</b> <port_id> <spri:0-7>
qos ptDefaultCpri get	get default ctag priority for received frames of a port	<b>qos ptDefaultCpri get</b> <port_id>
qos ptQuRemark set	set egress queue based remark	<b>qos ptQuRemark set</b> <port_id> <queue_id> <table_id> <enable/disable>
qos ptQuRemark get	get egress queue based remark	<b>qos ptQuRemark get</b> <port_id> <queue_id>

## 5.2.7 IGMP/MLD

Name	Function	Usage
igmp mode set	set IGMP/MLD snooping status of a port	<b>igmp mode set</b> <port_id> <enable   disable>
igmp mode get	get port IGMP/MLD snooping status	<b>igmp mode get</b> <port_id>
igmp cmd set	set IGMP/MLD frames forwarding command	<b>igmp cmd set</b> <forward   drop   cpycpu   rdtcpu>

Name	Function	Usage
igmp cmd get	get IGMP/MLD frames forwarding command	<i>igmp cmd get</i>
igmp portJoin set	set IGMP/MLD hardware joining status	<i>igmp portJoin set &lt;port_id&gt; &lt;enable   disable&gt;</i>
igmp portJoin get	get IGMP/MLD hardware joining status	<i>igmp portJoin get &lt;port_id&gt;</i>
igmp portLeave set	set IGMP/MLD hardware leaving status	<i>igmp portLeave set &lt;port_id&gt; &lt;enable   disable&gt;</i>
igmp portLeave get	get IGMP/MLD hardware leaving status	<i>igmp portLeave get &lt;port_id&gt;</i>
igmp rp set	set IGMP/MLD router ports	<i>igmp rp set port_bit_map</i>
igmp rp get	get IGMP/MLD router ports	<i>igmp rp get</i>
igmp createStatus set	set IGMP/MLD ability for creating entry	<i>igmp createStatus set &lt;enable   disable&gt;</i>
igmp createStatus get	get IGMP/MLD ability for creating entry	<i>igmp createStatus get</i>
igmp static set	set IGMP/MLD static status for creating entry	<i>igmp static set &lt;enable/disable&gt;</i>
igmp static get	get IGMP/MLD static status for creating entry	<i>igmp static get</i>
igmp leaky set	set IGMP/MLD leaky status for creating entry	<i>igmp leaky set &lt;enable/disable&gt;</i>
igmp leaky get	get IGMP/MLD leaky status for creating entry	<i>igmp leaky get</i>
igmp version3 set	set IGMP v3/MLD v2 status for creating entry	<i>igmp version3 set &lt;enable/disable&gt;</i>
igmp version3 get	get IGMP v3/MLD v2 status for creating entry	<i>igmp version3 get</i>
igmp queue set	set IGMP/MLD queue status for creating entry	<i>igmp queue set &lt;enable/disable&gt;</i>
igmp queue get	get IGMP/MLD queue status for creating entry	<i>igmp queue get</i>
igmp ptlearnlimit set	set port Multicast entry learn limit	<i>igmp ptlearnlimit set &lt;port_id&gt; &lt;enable/disable&gt; &lt;limitcounter&gt;</i>
igmp ptlearnlimit get	get port Multicast entry learn limit	<i>igmp ptlearnlimit get &lt;port_id&gt;</i>
igmp ptlearnexceedcmd set	set port forwarding command when exceed multicast learn limit	<i>igmp ptlearnexceedcmd set &lt;port_id&gt; &lt;forward drop cpcpu rdtcpu&gt;</i>
igmp ptlearnexceedcmd get	get port forwarding command when exceed multicast learn limit	<i>igmp ptlearnexceedcmd get &lt;port_id&gt;</i>

## 5.2.8 Leaky

Name	Function	Usage
leaky ucMode set	set unicast packets leaky mode	<i>leaky ucMode set &lt;port   fdb&gt;</i>
leaky ucMode get	get unicast packets leaky mode	<i>leaky ucMode get</i>
leaky mcMode set	set multicast packets leaky mode	<i>leaky mcMode set &lt;port   fdb&gt;</i>
leaky mcMode get	get multicast packets leaky mode	<i>leaky mcMode get</i>
leaky arp set	set ARP packets leaky mode	<i>leaky arp set &lt;port_id&gt; &lt;enable   disable&gt;</i>
leaky arp get	get ARP packets leaky mode	<i>leaky arp get &lt;port_id&gt;</i>
leaky ptUcMode set	set unicast packets leaky status of a port	<i>leaky ptUcMode set &lt;port_id&gt; &lt;enable   disable&gt;</i>
leaky ptUcMode get	get unicast packets leaky status of a port	<i>leaky ptUcMode get &lt;port_id&gt;</i>
leaky ptMcMode set	set multicast packets leaky status of a port	<i>leaky ptMcMode set &lt;port_id&gt; &lt;enable   disable&gt;</i>
leaky ptMcMode get	get multicast packets leaky status of a port	<i>leaky ptMcMode get &lt;port_id&gt;</i>

## 5.2.9 Mirror

Name	Function	Usage
mirror analyPt set	set mirror analysis port	<i>mirror analyPt set &lt;port_id&gt;</i>
mirror analyPt get	get mirror analysis port	<i>mirror analyPt get</i>
mirror ptIngress set	set ingress mirror status of a port	<i>mirror ptIngress set &lt;port_id&gt; &lt;enable   disable&gt;</i>
mirror ptIngress get	get ingress mirror status of a port	<i>mirror ptIngress get &lt;port_id&gt;</i>
mirror ptEgress set	set egress mirror status of a port	<i>mirror ptEgress set &lt;port_id&gt; &lt;enable   disable&gt;</i>
mirror ptEgress get	get egress mirror status of a port	<i>mirror ptEgress get &lt;port_id&gt;</i>

## 5.2.10 Rate

Name	Function	Usage
rate portpolicer set	set port policer	<i>rate portpolicer set &lt;port_id&gt;</i>
rate portpolicer get	get storm control rate	<i>rate portpolicer get &lt;port_id&gt;</i>
rate portshaper set	set port egress shaper	<i>rate portshaper set &lt;port_id&gt; &lt;enable/disable&gt;</i>
rate portshaper get	get port egress shaper	<i>rate portshaper get &lt;port_id&gt;</i>
rate queueshaper set	set queue egress shaper	<i>rate queueshaper set &lt;port_id&gt; &lt;queue_id&gt; &lt;enable/disable&gt;</i>
rate queueshaper get	get queue egress shaper	<i>rate queueshaper get &lt;port_id&gt; &lt;queue_id&gt;</i>
rate aclpolicer set	set ACL policer	<i>rate aclpolicer set &lt;policer_id&gt;</i>

Name	Function	Usage
rate aclpolicer get	get ACL policer	<i>rate aclpolicer get &lt;policer_id&gt;</i>

## 5.2.11 STP

Name	Function	Usage
stp ptState set	set STP state of a port	<i>stp ptState set st_id &lt;port_id&gt; &lt;disable   block   listen   learn   forward&gt;</i>
stp portState get	get STP state of a port	<i>stp ptState get st_id &lt;port_id&gt;</i>

## 5.2.12 MIB

Name	Function	Usage
mib statistics get	get statistics information of a port	<i>mib statistics get &lt;port_id&gt;</i>
mib status set	get MIB status	<i>mib status set &lt;enable   disable&gt;</i>
mib status get	get MIB status	<i>mib status set</i>

## 5.2.13 LED

Name	Function	Usage
led ctrlpattern set	set LED control pattern	<i>led ctrlpattern set &lt;group_id&gt; &lt;led_id&gt;</i>
led ctrlpattern get	get LED control pattern	<i>led ctrlpattern get &lt;group_id&gt; &lt;led_id&gt;</i>

## 5.2.14 CosMap

Name	Function	Usage
cosmap dscp2pri set	set DSCP to priority map table	<i>cosmap dscp2pri set &lt;dscp&gt; &lt;priority&gt;</i>
cosmap dscp2pri get	get DSCP to priority map table	<i>cosmap dscp2pri get &lt;dscp&gt;</i>
cosmap dscp2dp set	set DSCP to DP map table	<i>cosmap dscp2dp set &lt;dscp&gt; &lt;dp&gt;</i>
cosmap dscp2dp get	get DSCP to DP map table	<i>cosmap dscp2dp get &lt;dscp&gt;</i>
cosmap up2pri set	set dot1p to priority map table	<i>cosmap up2pri set &lt;up&gt; &lt;priority&gt;</i>
cosmap up2pri get	set dot1p to priority map table	<i>cosmap up2pri get &lt;up&gt;</i>
cosmap up2dp set	set dot1p to DP map table	<i>cosmap up2dp set &lt;up&gt; &lt;dp&gt;</i>
cosmap up2dp get	set dot1p to DP map table	<i>cosmap up2dp get &lt;up&gt;</i>
*cosmap dscp2ehpri set	set DSCP to priority map table for 0, 5, 6 ports	<i>cosmap dscp2ehpri set &lt;dscp&gt; &lt;priority&gt;</i>
*cosmap dscp2ehpri get	get DSCP to priority map table for 0, 5, 6 ports	<i>cosmap dscp2ehpri get &lt;dscp&gt;</i>
*cosmap dscp2ehdp set	set DSCP to DP map table for 0, 5, 6 ports	<i>cosmap dscp2ehdp set &lt;dscp&gt; &lt;dp&gt;</i>
*cosmap dscp2ehdp get	get DSCP to DP map table for 0, 5, 6 ports	<i>cosmap dscp2ehdp get &lt;dscp&gt;</i>

Name	Function	Usage
*cosmap up2ehpri set	set dot1p to priority map table for 0, 5, 6 ports	<b>cosmap up2ehpri set &lt;up&gt; &lt;priority&gt;</b>
*cosmap up2ehpri get	set dot1p to priority map table for 0, 5, 6 ports	<b>cosmap up2ehpri get &lt;up&gt;</b>
*cosmap up2ehdp set	set dot1p to DP map table for 0, 5, 6 ports	<b>cosmap up2ehdp set &lt;up&gt; &lt;dp&gt;</b>
*cosmap up2ehdp get	set dot1p to DP map table for 0, 5, 6 ports	<b>cosmap up2ehdp get &lt;up&gt;</b>
cosmap pri2q set	set priority to queue mapping	<b>cosmap pri2q set &lt;priority&gt; &lt;queueid&gt;</b>
cosmap pri2q get	get priority to queue mapping	<b>cosmap pri2q get &lt;priority&gt;</b>
cosmap pri2ehq set	set priority to enhanced queue mapping	<b>cosmap pri2ehq set &lt;priority&gt; &lt;queueid&gt;</b>
cosmap pri2ehq get	get priority to enhanced queue mapping	<b>cosmap pri2ehq get &lt;priority&gt;</b>
cosmap egRemark set	set egress remark table	<b>cosmap egRemark set &lt;tableid&gt;</b>
cosmap egRemark get	get egress remark table	<b>cosmap egRemark get &lt;tableid&gt;</b>

1. \* Available in ESS of IPQ4018/IPQ4019/IPQ4028/IPQ4029 only.

## 5.2.15 Misc

Name	Function	Usage
misc frameMaxSize set	set the maximal received frame size of the device	<b>misc frameMaxSize set &lt;size:byte&gt;</b>
misc frameMaxSize get	get the maximal received frame size of the device	<b>misc frameMaxSize get</b>
misc ptUnkUcFilter set	set flooding status of unknown unicast frames	<b>misc ptUnkUcFilter set &lt;port_id&gt; &lt;enable / disable&gt;</b>
misc ptUnkUcFilter get	get flooding status of unknown unicast frames	<b>misc ptUnkUcFilter get &lt;port_id&gt;</b>
misc ptUnkMcFilter set	set flooding status of unknown multicast frames	<b>misc ptUnkMcFilter set &lt;port_id&gt; &lt;enable / disable&gt;</b>
misc ptUnkMcFilter get	get flooding status of unknown multicast frames	<b>misc ptUnkMcFilter get &lt;port_id&gt;</b>
misc cpuPort set	set CPU port status	<b>misc cpuPort set &lt;enable / disable&gt;</b>
misc cpuPort get	get CPU port status	<b>misc cpuPort get</b>
misc PppoeCmd set	set PPPoE frames forwarding command	<b>misc PppoeCmd set &lt;forward / rdtcpu&gt;</b>
misc PppoeCmd get	get PPPoE frames forwarding command	<b>misc PppoeCmd get</b>
misc Pppoe set	set PPPoE frames hardware identification status	<b>misc Pppoe set &lt;enable / disable&gt;</b>
misc Pppoe get	get PPPoE frames hardware identification status	<b>misc Pppoe get</b>
misc ptDhcp set	set DHCP frames hardware identification status	<b>misc ptDhcp set &lt;port_id&gt; &lt;enable / disable&gt;</b>
misc ptDhcp get	get DHCP frames hardware identification status	<b>misc ptDhcp get &lt;port_id&gt;</b>



Name	Function	Usage
misc arpcmd set	set ARP packets forwarding command	<i>misc arpcmd set &lt;forward  cpycpu rdtcpu&gt;</i>
misc arpcmd get	get ARP packets forwarding command	<i>misc arpcmd get</i>
misc eapolcmd set	set EAPOL packets forwarding command	<i>misc eapolcmd set &lt; cpycpu rdtcpu&gt;</i>
misc eapolcmd get	set EAPOL packets forwarding command	<i>misc eapolcmd get</i>
misc eapolstatus set	set EAPOL frames hardware identification status	<i>misc eapolstatus set &lt;port_id&gt; &lt;enable/disable&gt;</i>
misc eapolstatus get	get EAPOL frames hardware identification status	<i>misc eapolstatus get &lt;port_id&gt;</i>
misc rip set	set RIP packets hardware identification status	<i>misc rip set &lt;enable/disable&gt;</i>
misc rip get	get RIP packets hardware identification status	<i>misc rip get</i>
misc ptarpreq set	set ARP request packets hardware identification status	<i>misc ptarpreq set &lt;port_id&gt; &lt;enable/disable&gt;</i>
misc ptarpreq get	get ARP request packets hardware identification status	<i>misc ptarpreq get &lt;port_id&gt;</i>
misc ptarpack set	set ARP ACK packets hardware identification status	<i>misc ptarpack set &lt;port_id&gt; &lt;enable/disable&gt;</i>
misc ptarpack get	get ARP ACK packets hardware identification status	<i>misc ptarpack get &lt;port_id&gt;</i>
misc extendpppoe add	add a PPPoE session entry	<i>misc extendpppoe add</i>
misc extendpppoe del	delete a PPPoE session entry	<i>misc extendpppoe del</i>
misc extendpppoe get	get a PPPoE session entry	<i>misc extendpppoe get</i>
misc glomacaddr set	set the global MAC address	<i>misc glomacaddr set</i>
misc glomacaddr get	get the global MAC address	<i>misc glomacaddr get</i>
misc lldp set	set LLDP frames hardware identification status	<i>misc lldp set</i>
misc lldp get	get LLDP frames hardware identification status	<i>misc lldp get</i>
misc framecrc set	set frame CRC reserve status	<i>misc framecrc set</i>
misc framecrc get	get frame CRC reserve status	<i>misc framecrc get</i>

## 5.2.16 IP

Name	Function	Usage
ip hostentry add	add host entry	<i>ip hostentry add</i>
ip hostentry del	delete host entry	<i>ip hostentry del &lt;del_mode&gt;</i>
ip hostentry get	get host entry	<i>ip hostentry get &lt;get_mode&gt;</i>
ip hostentry next	next host entry	<i>ip hostentry next &lt;next_mode&gt;</i>
ip hostentry show	show whole host entries	<i>ip hostentry show</i>

Name	Function	Usage
ip hostentry bindcnt	bind counter to host entry	<i>ip hostentry bindcnt &lt;host entry id&gt; &lt;cnt id&gt; &lt;enable/disable&gt;</i>
ip hostentry bindpppoe	bind PPPoE to host entry	<i>ip hostentry bindpppoe &lt;host entry id&gt; &lt;pppoe id&gt; &lt;enable/disable&gt;</i>
ip ptarplearn set	set port ARP learn flag, bit[0] requests bit[1] acknowledgement	<i>ip ptarplearn set &lt;port_id&gt; &lt;flag&gt;</i>
ip ptarplearn get	get port ARP learn flag, bit[0] requests bit[1] acknowledgement	<i>ip ptarplearn get &lt;port_id&gt;</i>
ip arplearn set	set ARP learn mode	<i>ip arplearn set &lt;learnlocal/learnall&gt;</i>
ip arplearn get	get ARP learn mode	<i>ip arplearn get &lt;port_id&gt;</i>
ip ptipsrcguard set	set IP source guard mode	<i>ip ptipsrcguard set &lt;port_id&gt; &lt;mac_ip/mac_ip_port/mac_ip_vlan/mac_ip_port_vlan/no_guard&gt;</i>
ip ptipsrcguard get	get IP source guard mode	<i>ip ptipsrcguard get &lt;port_id&gt;</i>
ip ptarpsrcguard set	set ARP source guard mode	<i>ip ptarpsrcguard set &lt;port_id&gt; &lt;mac_ip/mac_ip_port/mac_ip_vlan/mac_ip_port_vlan/no_guard&gt;</i>
ip ptarpsrcguard get	get ARP source guard mode	<i>ip ptarpsrcguard get &lt;port_id&gt;</i>
ip routestatus set	set IP route status	<i>ip routestatus set &lt;enable/disable&gt;</i>
ip routestatus get	get IP route status	<i>ip routestatus get</i>
ip intfentry add	add interface MAC address	<i>ip intfentry add &lt;enable/disable&gt;</i>
ip intfentry del	delete interface MAC address	<i>ip intfentry del</i>
ip intfentry add	add interface MAC address	<i>ip intfentry add &lt;enable/disable&gt;</i>
ip intfentry show	IP INTF entry show	<i>ip intfentry show</i>
ip ipunksrc set	set IP unknown source command	<i>ip ipunksrc set &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
ip ipunksrc get	get IP unknown source command	<i>ip ipunksrc get</i>
ip arpunksrc set	set ARP unknown source command	<i>ip arpunksrc set &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
ip arpunksrc get	get ARP unknown source command	<i>ip arpunksrc get</i>
ip ip6baseaddr set	set IP6 base address	<i>ip ip6baseaddr set &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
ip ip6baseaddr get	get IP6 base address	<i>ip ip6baseaddr get</i>
ip vrfbaseaddr set	Set IP4 base address	<i>ip vrfbaseaddr set &lt;ip_addr&gt;</i>
ip vrfbaseaddr get	Get IP4 base address	<i>ip vrfbaseaddr get</i>
ip defaultroute set	Set default route	<i>ip defaultroute set</i>
ip defaultroute get	Get default route	<i>ip defaultroute get</i>
ip hosttroute set	Set host route	<i>ip hostroute set</i>

Name	Function	Usage
ip hosttroute get	Get host route	<i>ip hosttroute get</i>
ip rfsip4 set	Set IP4 load balance	<i>ip rfsip4 set</i>
ip rfsip4 get	Get IP4 load balance	<i>ip rfsip4 get</i>
ip rfsip6 set	Set IP6 load balance	<i>ip rfsip6 set</i>
ip rfsip6 get	Get IP6 load balance	<i>ip rfsip6 get</i>
ip defaultflowcmd set	Set default flow command	<i>ip defaultflowcmd set &lt;vrf id&gt; &lt;lan2lan/wan2lan/lan2wan/wan2wan&gt; &lt;forward/drop/rdtcpu/admit_all&gt;</i>
ip defaultflowcmd get	Get default flow command	<i>ip defaultflowcmd set &lt;vrf id&gt; &lt;lan2lan/wan2lan/lan2wan/wan2wan&gt;</i>
ip defaulttrtflowcmd set	Set default flow command	<i>ip defaulttrtflowcmd set &lt;vrf id&gt; &lt;lan2lan/wan2lan/lan2wan/wan2wan&gt; &lt;forward/drop/rdtcpu/admit_all&gt;</i>
ip defaulttrtflowcmd get	Get default flow command	<i>ip defaulttrtflowcmd get &lt;vrf id&gt; &lt;lan2lan/wan2lan/lan2wan/wan2wan&gt;</i>

## 5.2.17 NAT

Name	Function	Usage
nat natentry add	add NAT entry	<i>nat natentry add</i>
nat natentry del	delete NAT entry	<i>nat natentry del &lt;del_mode&gt;</i>
nat natentry get	get NAT entry	<i>nat natentry get &lt;get_mode&gt;</i>
nat natentry show	show whole NAT entries	<i>nat natentry show</i>
nat natentry bindcnt	bind counter to NAT entry	<i>nat natentry bindcnt &lt;nat entry id&gt; &lt;cnt id&gt; &lt;enable/disable&gt;</i>
nat naptentry add	add NAPT entry	<i>nat naptentry add</i>
nat naptentry del	delete NAPT entry	<i>nat naptentry del &lt;del_mode&gt;</i>
nat naptentry get	get NAPT entry	<i>nat naptentry get &lt;get_mode&gt;</i>
nat naptentry show	show whole NAPT entries	<i>nat naptentry show</i>
nat naptentry bindcnt	bind counter to NAPT entry	<i>nat naptentry bindcnt &lt;napt entry id&gt; &lt;cnt id&gt; &lt;enable/disable&gt;</i>
nat natstatus set	set NAT status	<i>nat natstatus set &lt;enable/disable&gt;</i>
nat natstatus get	get NAT status	<i>nat natstatus get</i>
nat naptstatus set	set NAPT status	<i>nat naptstatus set &lt;enable/disable&gt;</i>
nat naptstatus get	get NAPT status	<i>nat naptstatus get</i>
nat nathash set	set NAT hash mode	<i>nat nathash set &lt;flag&gt;</i>
nat nathash get	get NAT hash mode	<i>nat nathash get</i>
nat naptmode set	set NAPT mode	<i>nat naptmode set &lt;fullcone/strictcone/portstrict/synmatric&gt;</i>
nat naptmode get	get NAPT mode	<i>nat naptmode get</i>
nat prvbaseaddr set	set NAT PRV base address	<i>nat prvbaseaddr set &lt;ip4 addr&gt;</i>
nat prvbaseaddr get	get NAT PRV base address	<i>nat prvbaseaddr get</i>

Name	Function	Usage
nat prvaddrmode set	set NAT PRV address map mode	<i>nat prvaddrmode set &lt;enable/disable&gt;</i>
nat prvaddrmode get	get NAT PRV address map mode	<i>nat prvaddrmode get</i>
nat pubaddr add	add PUB address	<i>nat pubaddr add &lt;enable/disable&gt;</i>
nat pubaddr del	delete PUB address	<i>nat pubaddr del &lt;del_mode&gt;</i>
nat pubaddr show	show whole PUB address entries	<i>nat pubaddr show</i>
nat natunksess set	set NAT unknown session command	<i>nat natunksess set &lt;forward/drop/cpycpu/rdtcpu&gt;</i>
nat natunksess get	get NAT unknown session command	<i>nat natunksess get</i>
nat flowentry add	add flow entry	<i>nat flowentry add</i>
nat flowentry del	delete flow entry	<i>nat flowentry del &lt;del_mode&gt;</i>
nat flowentry get	get flow entry	<i>nat flowentry get &lt;get_mode&gt;</i>
nat flowentry show	show whole flow entries	<i>nat flowentry show</i>
nat flowentry bindcnt	bind counter to flow entry	<i>nat flowentry bindcnt &lt;napt entry id&gt; &lt;cnt id&gt; &lt;enable/disable&gt;</i>

## 5.2.18 Trunk

Name	Function	Usage
trunk group set	set trunk group member information	<i>trunk group set &lt;trunk_id&gt; &lt;disable/enable&gt; &lt;port_bitmap&gt;</i>
trunk group get	get trunk group member information	<i>trunk group get &lt;trunk_id&gt;</i>
trunk hashmode set	set trunk hash mode	<i>trunk hashmode set &lt;hash_mode&gt;</i>
trunk hashmode get	get trunk hash mode	<i>trunk hashmode get &lt;trunk_id&gt;</i>

## 5.2.19 Security

Name	Function	Usage
sec mac set	set MAC part security filter	<i>sec mac set &lt;resv_vid/invalid_src_addr&gt; &lt;value&gt;</i>
sec mac get	get MAC part security filter	<i>sec mac get &lt;resv_vid/invalid_src_addr&gt;</i>
sec ip set	set IP related security filter	<i>sec ip set &lt;invalid_ver/same_addr/ttl_change_status/ttl_val&gt; &lt;value&gt;</i>
sec ip get	get IP related security filter	<i>sec ip get &lt;invalid_ver/same_addr/ttl_change_status/ttl_val&gt;</i>
sec ip4 set	set IPv4 related security filter	<i>sec ip4 set &lt;invalid_hl/hdr_opts/invalid_df/frag_offset_min_len/frag_offset_min_size/frag_offset_max_len/invalid_frag_offset/invalid_sip/invalid_dip/invalid_checksum/invalid_pl/df_clear_status/ipid_random_status&gt; &lt;value&gt;</i>

Name	Function	Usage
sec ip4 get	get IPv4 related security filter	<b>sec ip4 get</b> <b>&lt;invalid_hl/hdr_opts/invalid_df/frag_offset_min_len/frag_offset_min_size/frag_offset_max_len/invalid_frag_offset/invalid_sip/invalid_dip/invalid_chksum/invalid_pl/df_clear_status/ipid_random_status&gt;</b>
sec ip6 set	set IPv6 related security filter	<b>sec ip6 set &lt;invalid_dip/invalid_sip/invalid_pl&gt; &lt;value&gt;</b>
sec ip6 get	get IPv6 related security filter	<b>sec ip6 get &lt;invalid_dip/invalid_sip/invalid_pl&gt;</b>
sec tcp set	set TCP related security filter	<b>sec tcp set</b> <b>&lt;blat/invalid_hl/min_hdr_size/invalid_syn/su_block/sp_block/sap_block/xmas_scan/null_scan/sr_block/sf_block/sar_block/rst_scan/rst_with_data/fa_block/pa_block/ua_block/invalid_chksum/invalid_urgptr/invalid_opts&gt; &lt;value&gt;</b>
sec tcp get	get TCP related security filter	<b>sec tcp get</b> <b>&lt;blat/invalid_hl/min_hdr_size/invalid_syn/su_block/sp_block/sap_block/xmas_scan/null_scan/sr_block/sf_block/sar_block/rst_scan/rst_with_data/fa_block/pa_block/ua_block/invalid_chksum/invalid_urgptr/invalid_opts&gt;</b>
sec udp set	set UDP related security filter	<b>sec udp set &lt;blat/invalid_len/invalid_chksum&gt; &lt;value&gt;</b>
sec udp get	get UDP related security filter	<b>sec udp get &lt;blat/invalid_len/invalid_chksum&gt;</b>
sec icmp4 set	set ICMP4 related security filter	<b>sec icmp4 set &lt;ping_pl_exceed/ping_frag/ping_max_pl&gt; &lt;value&gt;</b>
sec icmp4 get	get ICMP4 related security filter	<b>sec icmp4 get &lt;ping_pl_exceed/ping_frag/ping_max_pl&gt;</b>
sec icmp6 set	set ICMP6 related security filter	<b>sec icmp6 set &lt;ping_pl_exceed/ping_frag/ping_max_pl&gt; &lt;value&gt;</b>
sec icmp6 get	get ICMP6 related security filter	<b>sec icmp6 get &lt;ping_pl_exceed/ping_frag/ping_max_pl&gt;</b>

## 5.2.20 Register Access and Debug

Name	Function	Usage
debug phy get	read PHY register	<b>debug phy get &lt;ph_id&gt; &lt;reg_addr&gt;</b>
debug phy set	write PHY register	<b>debug phy set &lt;ph_id&gt; &lt;reg_addr&gt; &lt;value&gt;</b>
debug reg get	read switch register	<b>debug reg get &lt;reg_addr&gt;</b>
debug reg set	write switch register	<b>debug reg set &lt;reg_addr&gt; &lt;value&gt;</b>
debug entry get	read switch register entry	<b>debug entry get &lt;entry_name&gt;</b>
debug entry set	write switch register entry	<b>debug entry set &lt;entry_name&gt;</b>
debug field get	read switch register field	<b>debug field get &lt;field_name&gt;</b>
debug field set	write switch register field	<b>debug field set &lt;field_name&gt;</b>
debug aclList dump	dump all ACL lists	<b>debug aclList dump</b>
debug aclRule dump	dump all ACL rules	<b>debug aclRule dump</b>

## 5.2.21 Set Device ID

Name	Function	Usage
device id set	set device ID	<i><b>device id set &lt;dev_id&gt;</b></i>