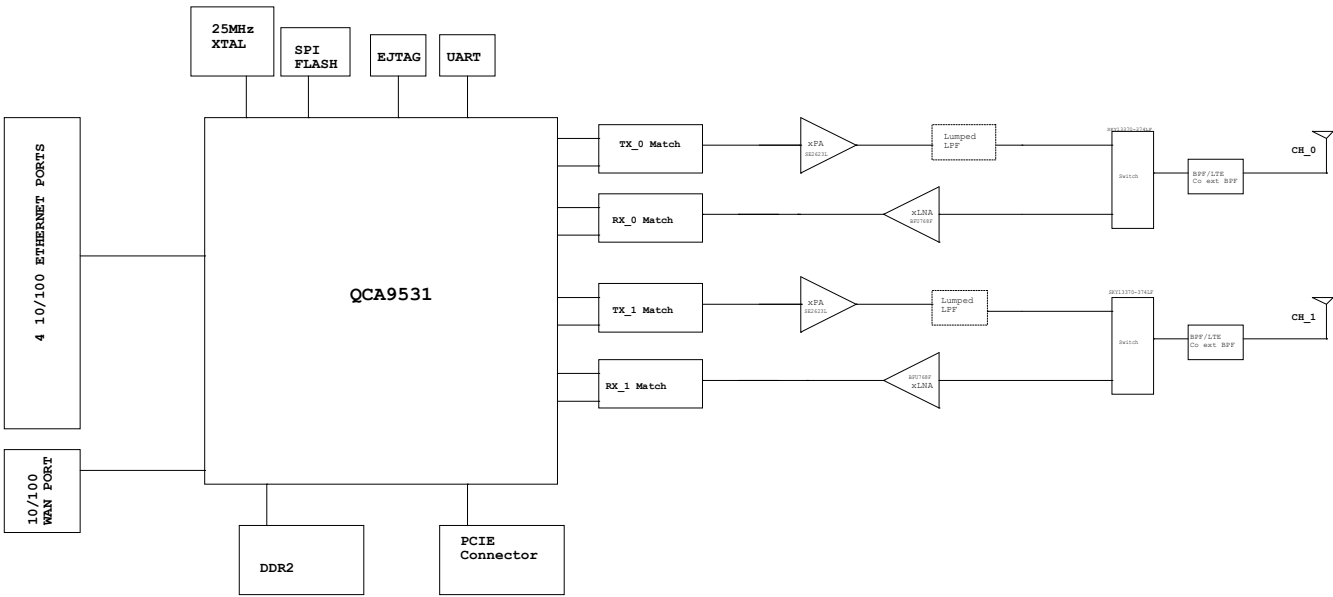


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DATE	REVISION NUMBER	INITIALS	DESCRIPTION
13th May 2014	LD20-Y7242-4_RevA	NK&SG	1-Derived from LD20-Y7242_RevB 2-02-2014-02000001 to 12000 and 03 to 1000

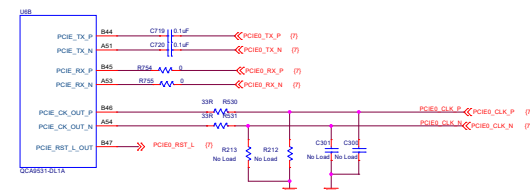
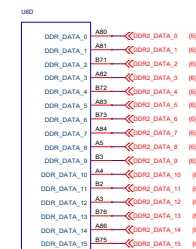
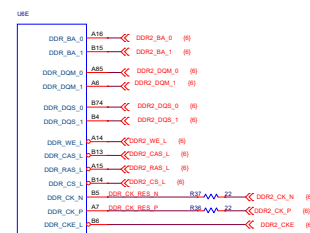
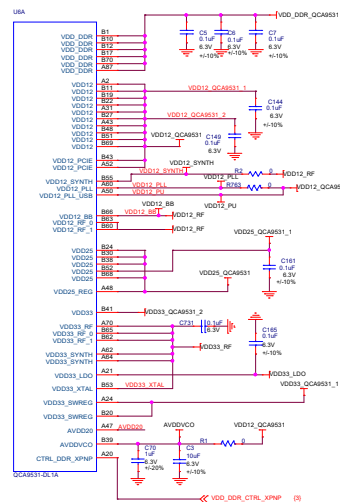
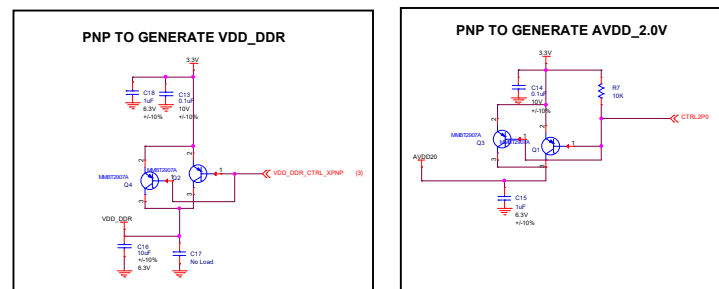
CONTRACT NO.		QUALCOMM Atheros, Inc. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A.			
DRAWN BY Fajana & Nagendra	DATE 13th May 2014	TITLE			
ENGINEER		SCHEMATIC			
PROJECT ENGINEER		CCA, AP147, QC-A931 BASED 3X 802.11B/G/N AP W/ 10/100 PORTS(4+1)PCIE COM_5MHZ 128M DDR 128M NOR			
QUALITY ASSURANCE					
CONFIGURATION					
SIZE D		DRAWING NO LD20-Y7242-5	REV A		
DESIGN ACTIVITY APPROVAL					
DESIGN APPROVAL		SCALE NONE	RELEASE DATE 13th May 2014	SHEET 1 of 10	

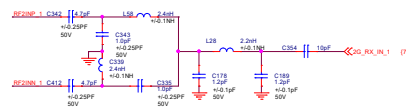
BLOCK DIAGRAM



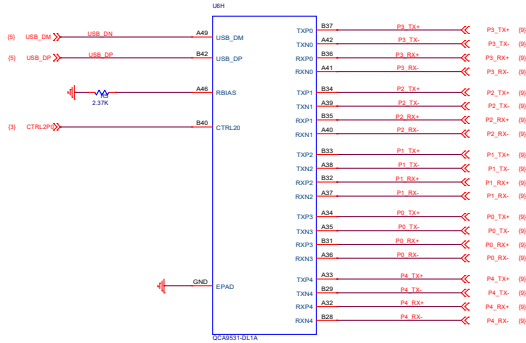
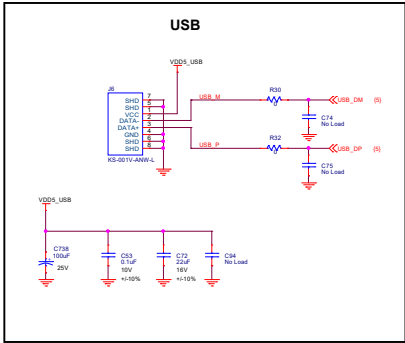
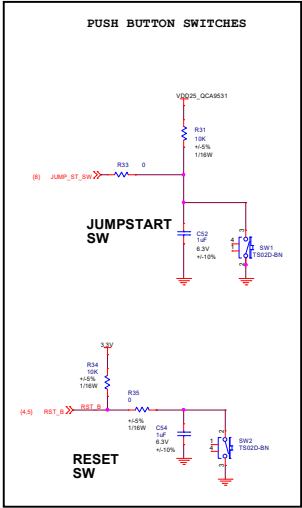
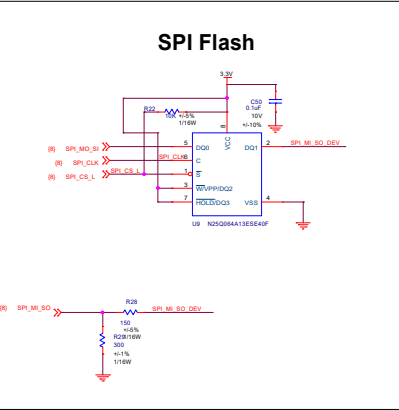
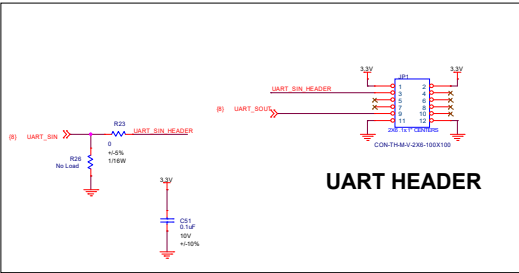
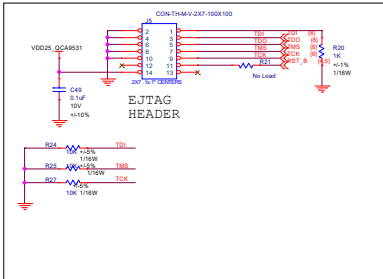
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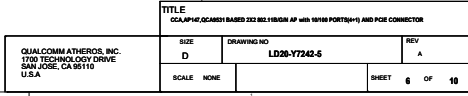
TITLE			
QCA9531, QCA9531 based 2x2 MIMO 802.11n AP with 10/100 Ethernet and PCIE connector			
QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A.	SIZE	DRAWING NO.	REV.
	D	LD28-Y7242-5	A
SCALE: NONE		SHEET 2 OF 10	



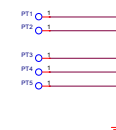
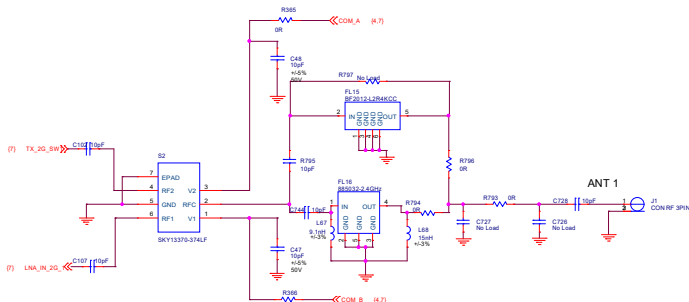
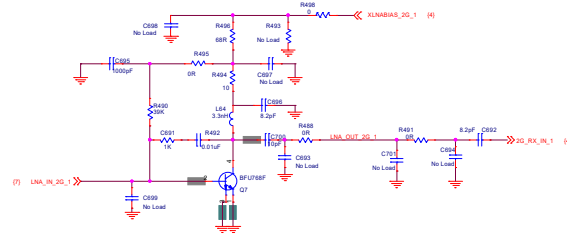
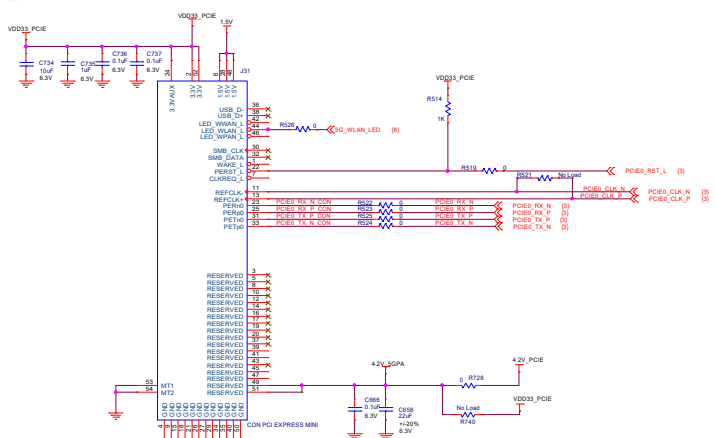
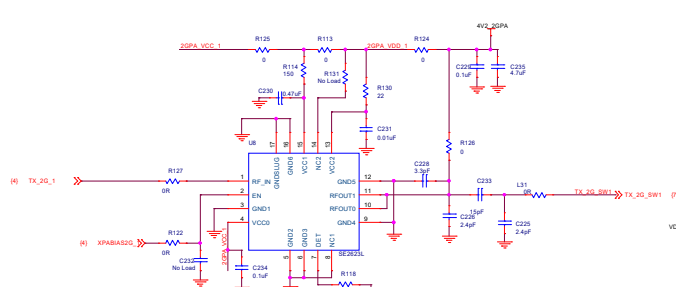


		TITLE	
		QUALCOMM/DCARH1 BASED 203 MLI 1150M AP with 10/100 PORTS(+/-) and POE CONNECTOR	
QUALCOMM Atheros, Inc. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A	SIZE	DRAWING NO	REV
	D	LD20-Y742-S	A
	SCALE NONE		SHEET 4 OF 10



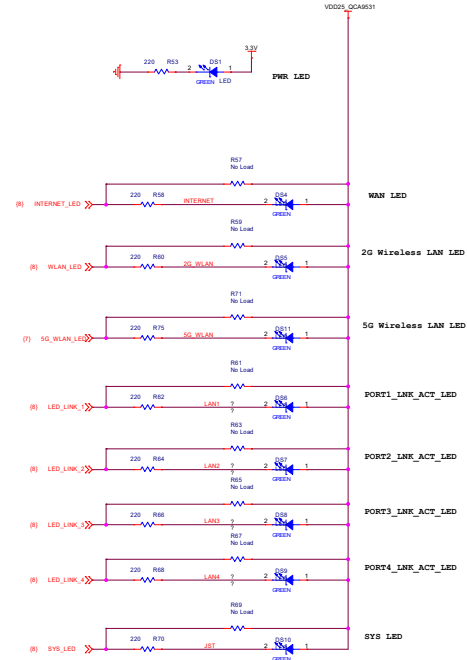
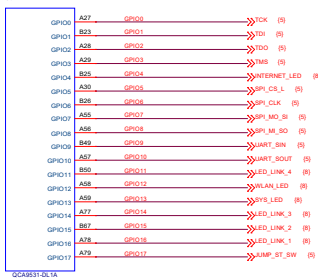


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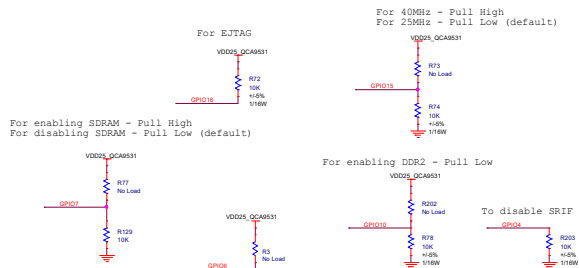


QUALCOMM ATHEROS, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A.		TITLE	
		OCA/PHD/CCASBI BASED 232 BELL/180M AP with 18/18 PORTS(H+I) AND POE CONNECTOR	
		SIZE D	DRAWING NO LD26-Y742-6
		REV A	
SCALE NONE			SHEET 7 OF 10

USF

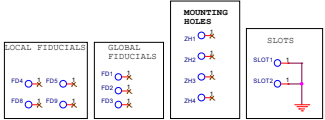
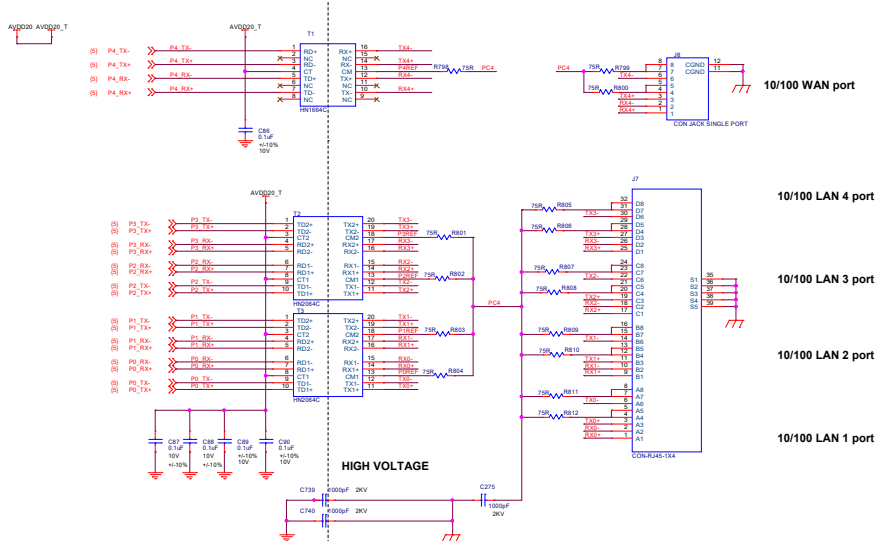
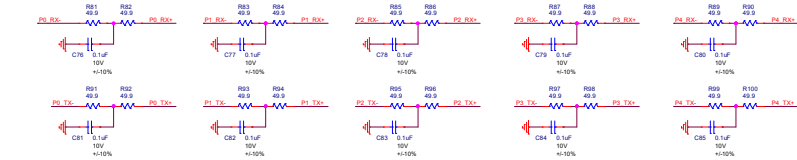


Config Strap Settings



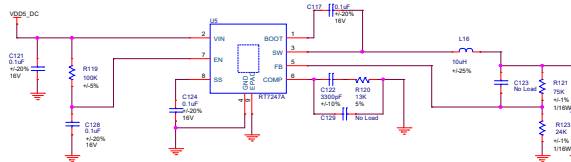
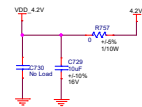
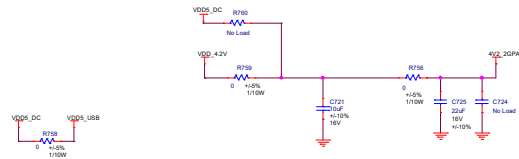
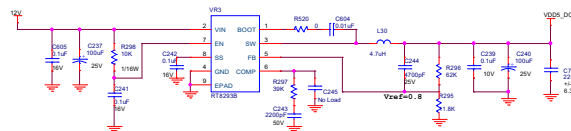
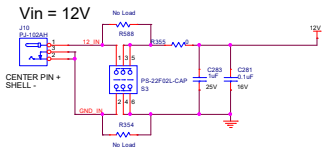
-Variant Name-

TITLE		
QUALCOMM Atheros, Inc. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110		
SIZE	DRAWING NO	REV
D	LD28-Y7242-5	A
SCALE	NONE	SHEET 8 OF 10

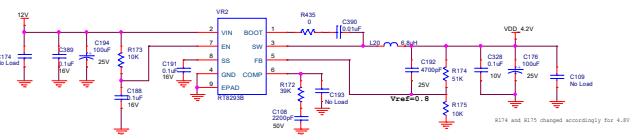
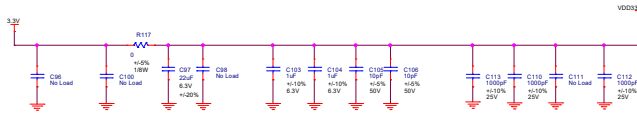


TITLE			
CAL-PAF-SCABH BASED 32 M11800H AP with 10/100 PORTS and 10/100 LAN PCIE CONNECTOR			
QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110	SIZE	DRAWING NO	REV
	D	LD28-Y7242-5	A
SCALE NONE		SHEET 9 OF 10	

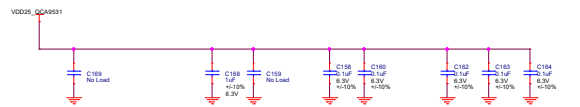
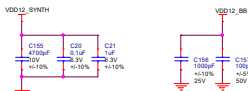
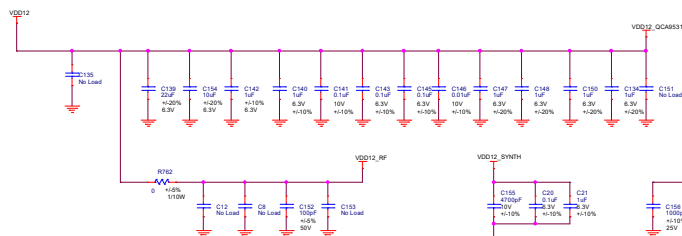
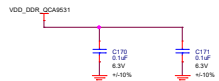
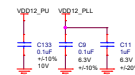
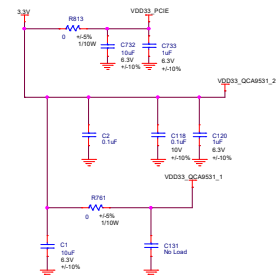
POWER SUPPLY, REGULATORS & DECOUPLING



Notes:
 $V_{OUT} = 0.8 (1 + R_{top}/R_{bot}) V$
 $V_{out} = 0.8 (1 + 75/24) = 3.3 V$



R174 and R175 changed accordingly for 4.0%



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«Variant Name»

QUALCOMM Atheros, Inc. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A		TITLE CCAPI4672CABIN1 BASED 32x36 1810IN AP with 18108 PORTS(4-3) AND PCIe CONNECTOR	
		SIZE D	DRAWING NO LD28-7724-5
		REV A	
		SCALE NONE	SHEET 10 OF 10