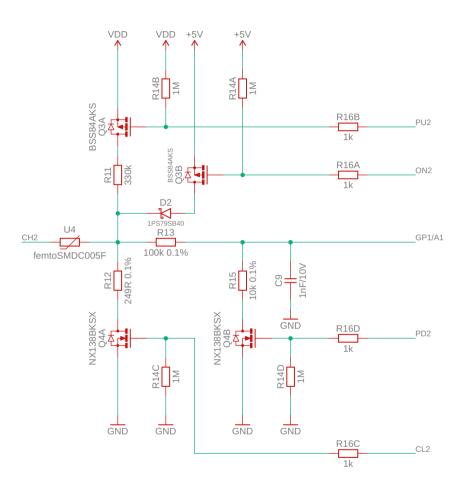


CHANNEL 1

VDD VDD • +5∨ **^** R8B 1M R8A 1M BSS84AKS Q1A R10B 1k PU1 BSS84 Q1B ON1 R5 D1 1PS79SB40 R7 100k 0.1% GP0/A0 CH1 femtoSMDC005F 10k 0.1% R6 NX138BKSX Q2A NX138BKSX Q2B GND R10D 1k PD1 R8D 1M GND GND GND GND R10C 1k CL1

CHANNEL 2





CHESTER-X0

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CHANNEL 3

VDD VDD • +5∨ **^** R20A 1M BSS84AKS Q5A R22B 1k PU3 BSS84 Q5B R17 ON3 D3 1PS79SB40 R19 100k 0.1% СНЗ GP2/A2 femtoSMDC005F 10k 0.1% R18 NX138BKSX Q6A NX138BKSX Q6B GND R22D 1k PD3

GND

GND

R20D 1M

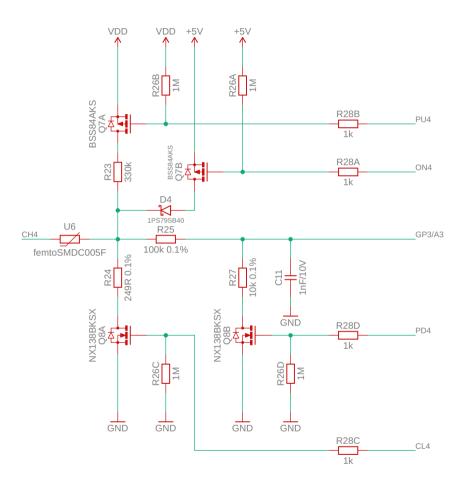
GND

R22C 1k

CL3

GND

CHANNEL 4





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