littleBits General Fab Notes UNLESS OTHERWISE NOTED IN DATA

- 1. Overall thickness 1/16th of an inch (62.5mil)
- Slots on the two edges of the PCB must not have fillet. OVER-ROUTE SLOTS PER GERBER USING 1MM BIT
- 3. Lead free processing
- 4. Fabricate in accordance with IPC-6011 and IPC-6012 Type 2,

 Class 2 CURRENT REVISION Acceptability IAW IPC-A-600 CURRENT REVISION
- 5. Material FR-4
- 6. 1oz copper outer layers
- 7. HASL, ENIG or Silver finish acceptable
- 8. Tent vias top side
- 9. LPI soldermask white both sides (white to match golden sample).

NOTE: Soldermask recommended PNs:

MFG: SZRD (http://www.szrd.com) PN: white oil H-8100

MFG: SZRD (http://www.szrd.com) PN: PSR 4000 LEW1

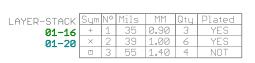
MFG: Taiyo PN: PSR-4000 WT02

Silkscreen purple epoxy ink both sides:

Color: R (120), G (43), B (143): Pantone 267.

Please compare soldermask and legend color to golden sample provided Non-conforming color or color variation will result in board rejection.

- 10. Panelize and retain per assembly shop requirements. minimum 0.2in rail around array, minimum 3 fiducials and v-score, no mouse bites, minimum space between boards 0.5in all directions.
- 11. 100% electrical continuity testing required.
- 12. No x-outs allowed.
- 13. No added marks or changes of any kind without written customer approval.
- 14. Checkplot and array approval required before production.
- 15. Silk screen text size and thickness to match provided artwork size +/- .1mm







littleBits, Inc. 60 East 11th Street New York, NY, 1003, USA phone: 917-464-4577

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1mm DIA. ROUTER PATH