

St. Joseph's College of Engineering
Department of Computer Science & Engineering

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Department of Information Technology

Assignment –V

CS6303 - Computer Architecture

Unit 5

Part – A

1. What is principle of locality?
2. Define Hit and Miss.
3. Distinguish between memory mapped I/O and I/O mapped I/O.
4. What is Direct mapped cache?
5. Mention the advantages of USB.
6. What is meant by vectored interrupt?
7. Compare Static RAM and Dynamic RAM.
8. What is SCSI?

Part - B

1. Discuss DMA controller with block diagram.
2. Explain the need for cache memory and discuss the different types of mapping functions with necessary block diagram.
3. Discuss the steps involved in the address translation of virtual memory with necessary block diagram.
4. What is an interrupt? Explain the different types of interrupts and the different ways of handling the interrupts.