

St.Joseph's College of Engineering  
Department of CSE & IT  
CS6303 Computer Architecture  
Assignment II

PART-A

1. What is carry save addition?
2. Define Booth's Algorithm.
3. What is arithmetic overflow?
4. What is guard bit? What are the ways to truncate guard bits?
5. Draw the format for floating point number.
6. State the rule for floating point addition.
7. Write Restoring and Non-Restoring division algorithm.
8. What is Subword parallelism?

PART-B

1. Explain with a diagram the design of a fast multiplier using carry save adder circuit.
2. Explain in detail about the carry look ahead adder.
3. State the restoring division technique.
4. Give the block diagram for a floating point adder and subtractor unit and discuss its operations.