**ASSIGNMENT-1**

**EC6504-MICROPROCESSOR AND MICROCONTROLLER**

**UNIT-I**
**PART-A**
1.What are assembler directives? Give example.
2.What are the advantages of the segmented memory scheme in 8086?
3.How is the physical address generated in 8086? (or) How 16 bit address is converted into 20 bit address in 8086? (Nov/Dec 2013)
4.Draw the PSW format for 8086
5. Give the operation of CBW and TEST instructions of 8086. (Nov/Dec 2013)
6.What is meant by a vectored interrupt? (May/Jun 2014)
7. What are the addressing modes of sequence control transfer instructions in 8086? Give example.
8.Define a MACRO.
**PART-B**
1. Explain the internal architecture of 8086 and explain the functions of each block in detail. (Nov/Dec 2013)
2.Describe the addressing modes of 8086 with examples from instruction set of 8086.
3.Draw and discuss the interrupt structure of 8086.
4.Explain the different instruction used for input and output operation in I/O mapped I/O mode of 8086