

Heater & Fan Control

Htr_Fan.sch

Stepper Driver

Stepper_Drv.sch

Headers & Power

Headers.sch

http://www.ohwr.org/attachments/2388/cern_ohLv_1_2.txt

CERN OSH License 1.2

Think3DPrint3D, Escher 3D

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Title: Duex5

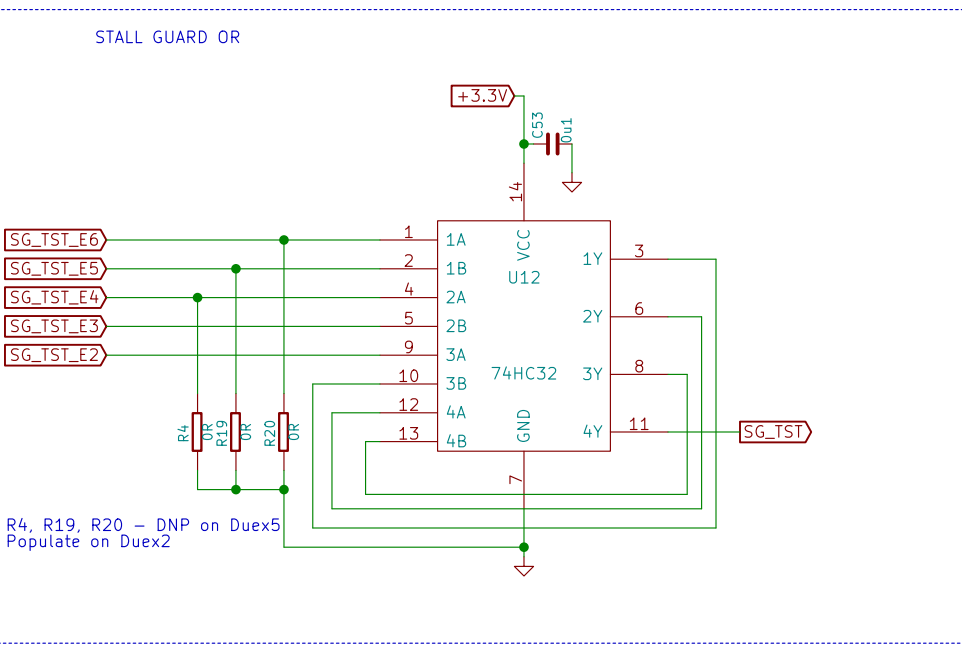
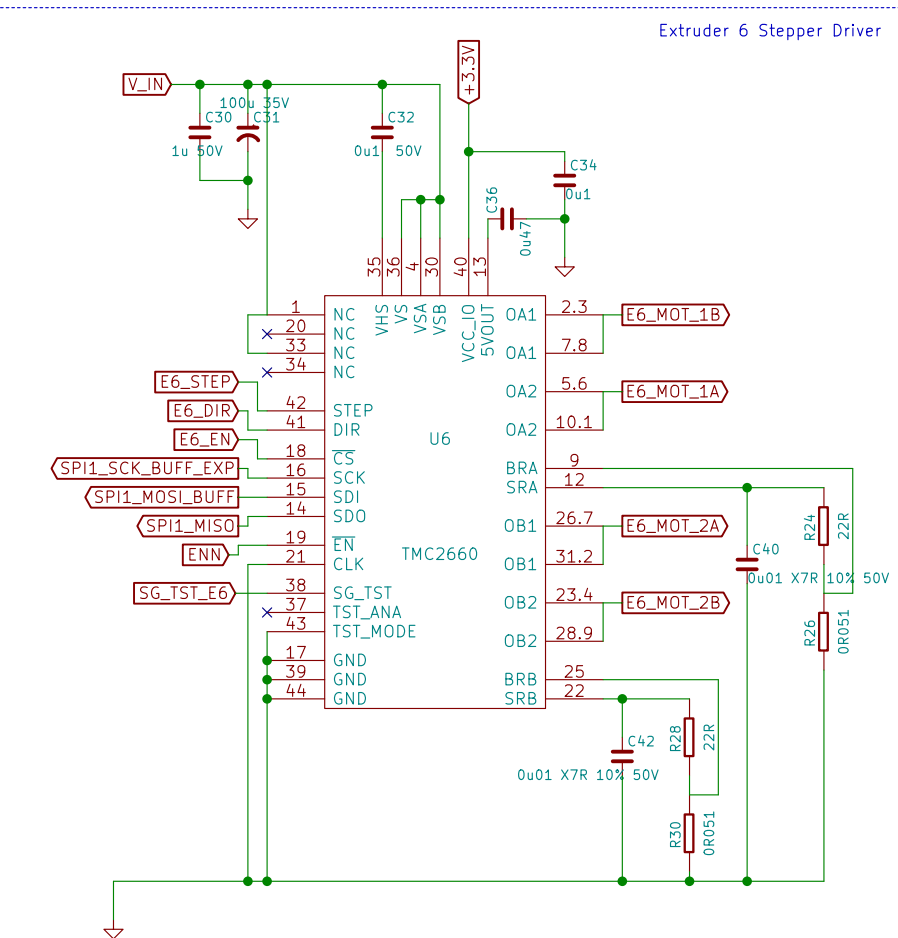
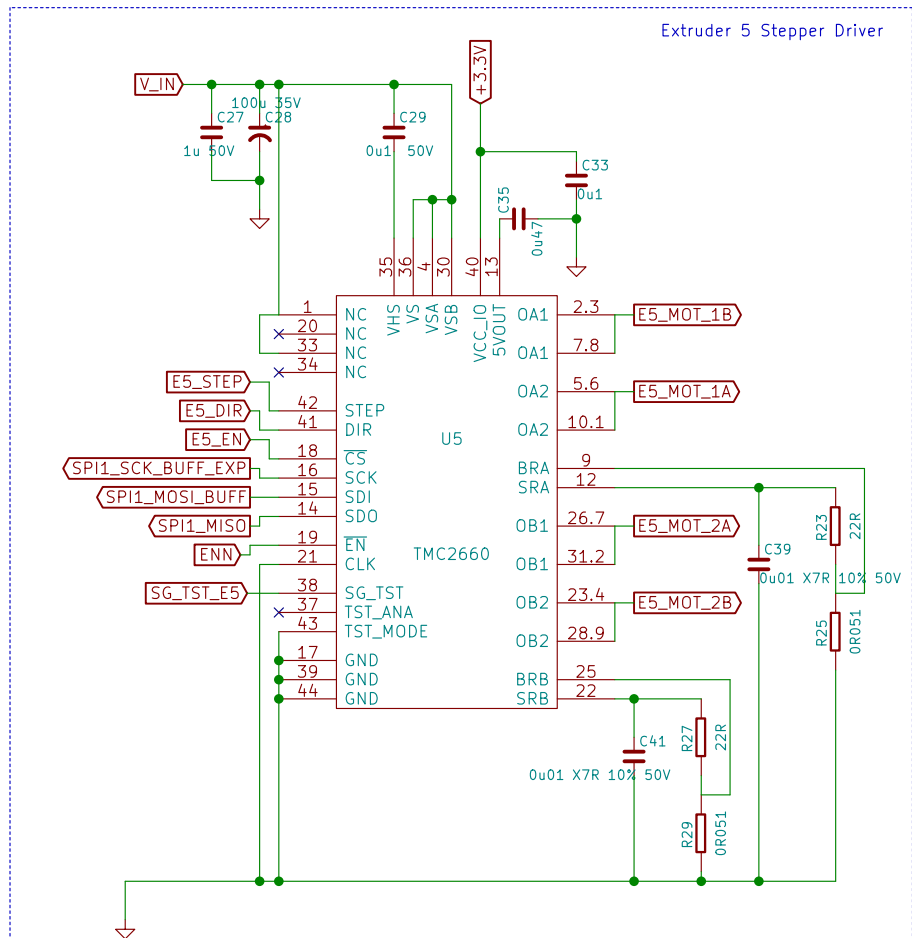
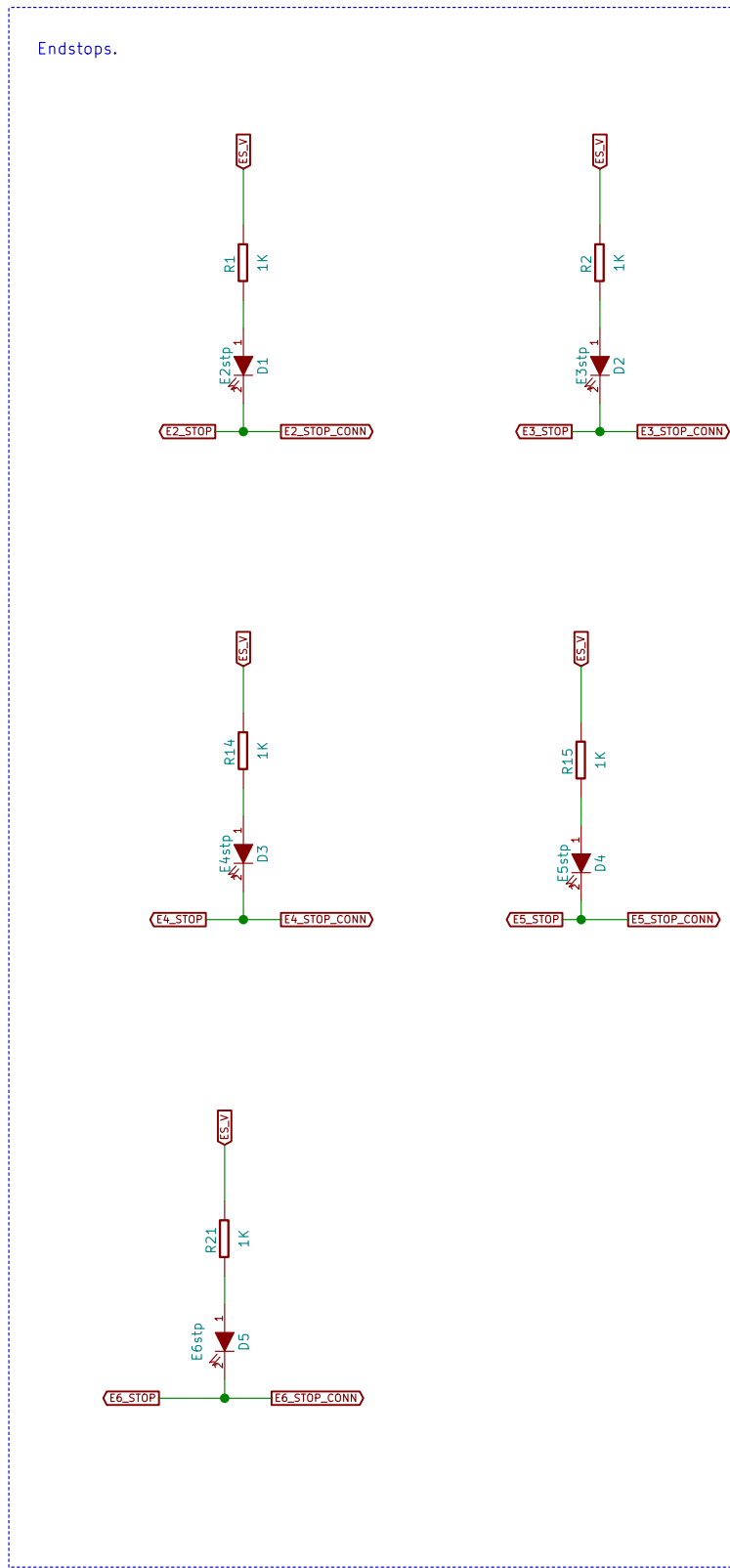
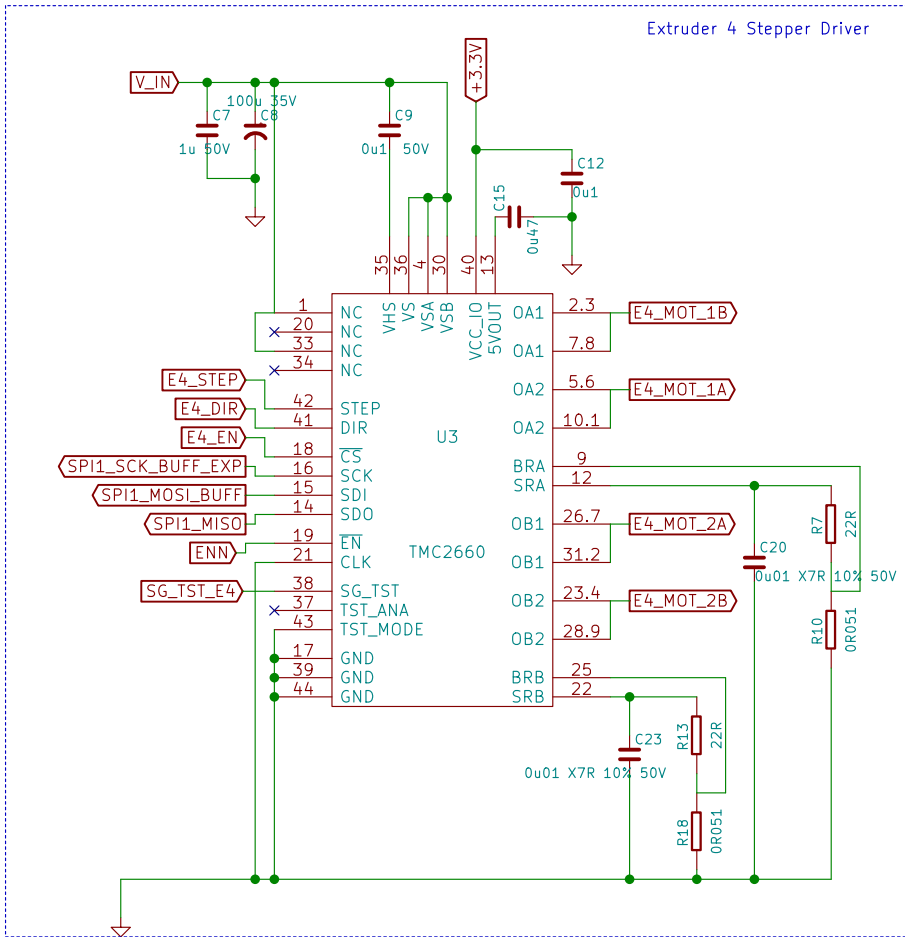
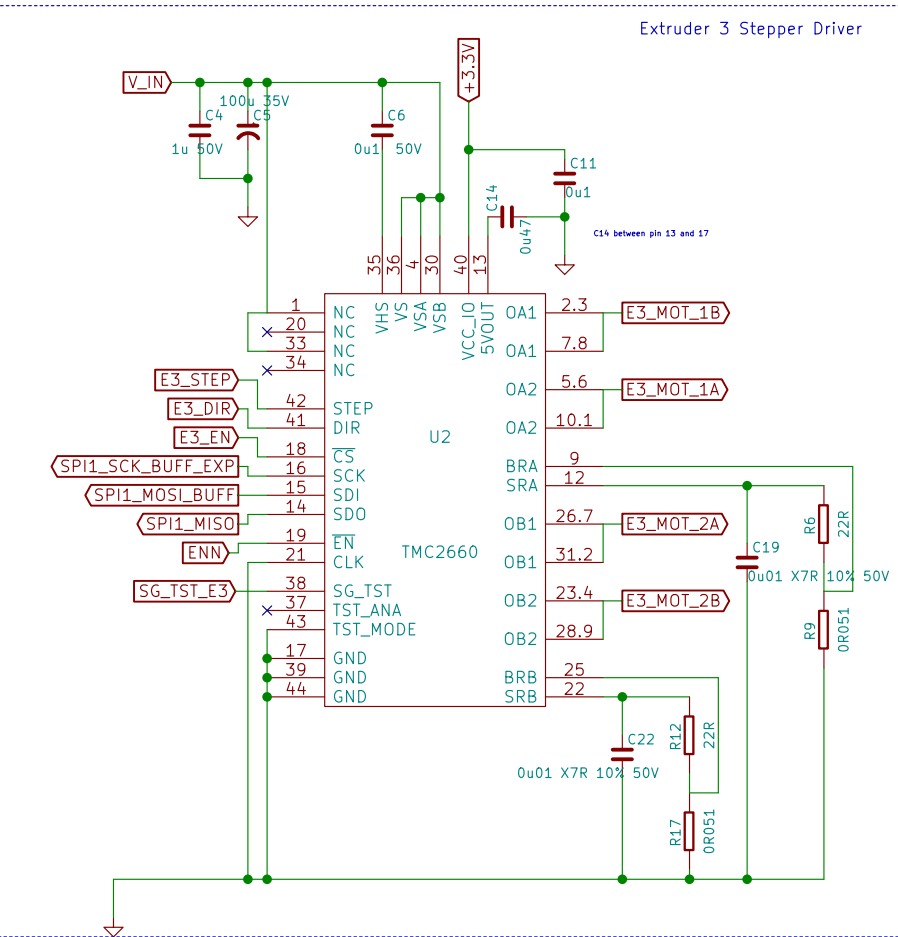
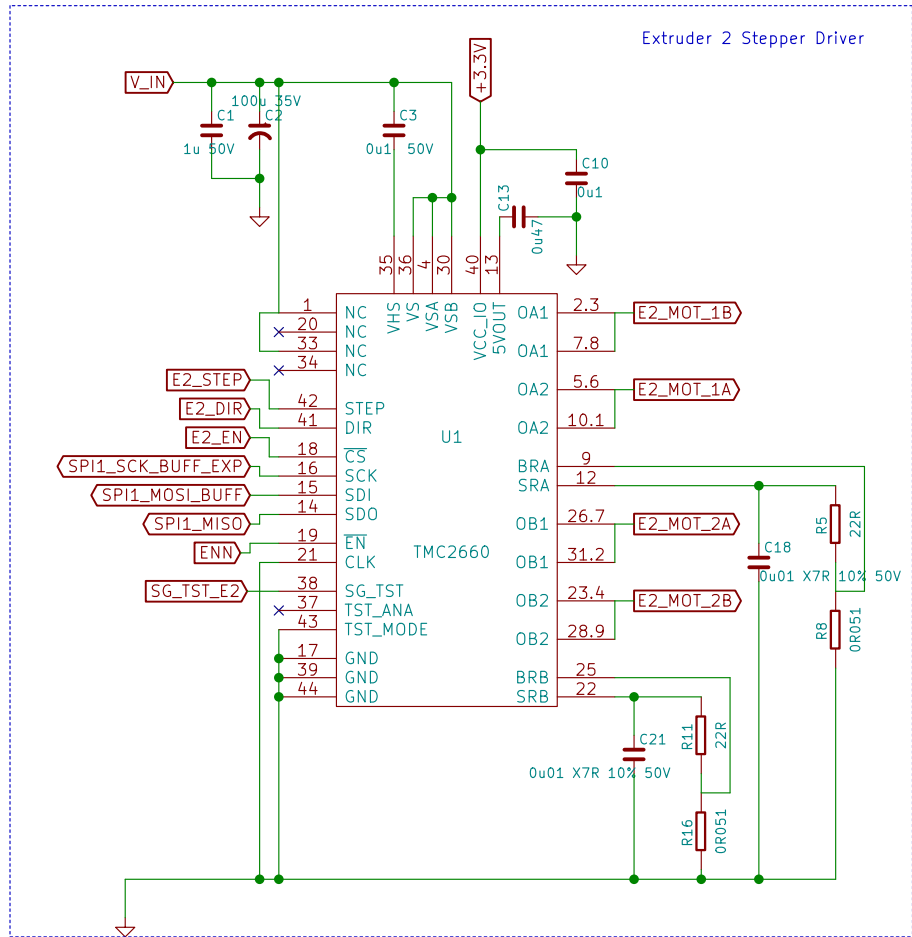
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Date: 2016-11-26

Rev: 0.7

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Think3DPrint3D, Escher 3D

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Size: A2 Date: 2016-11-26

Rev: 0.7

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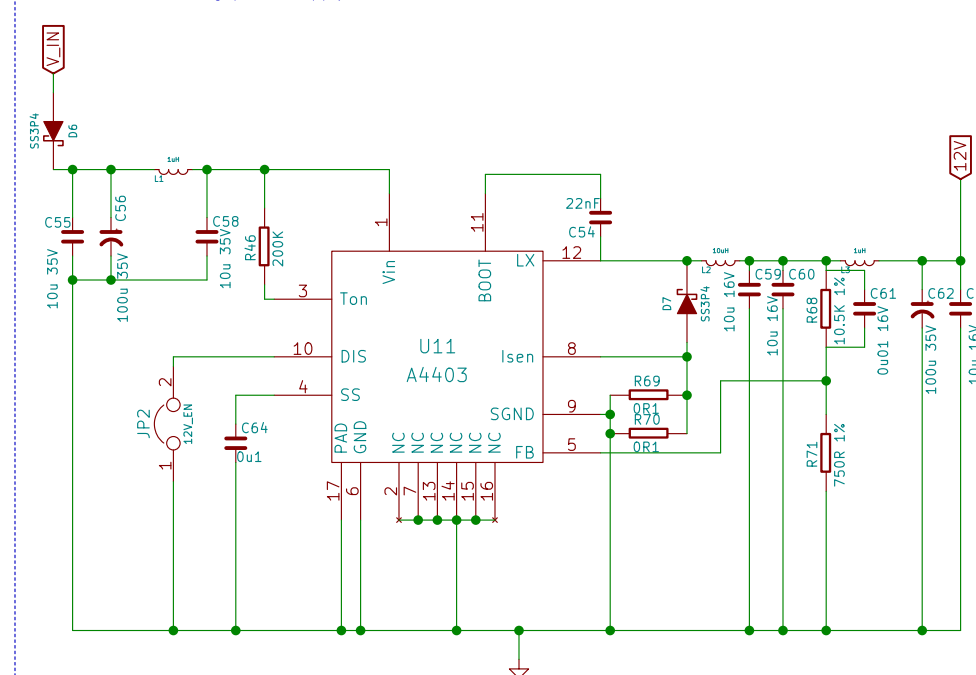
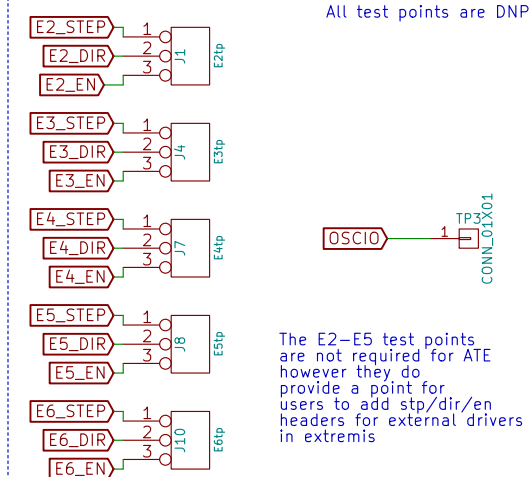
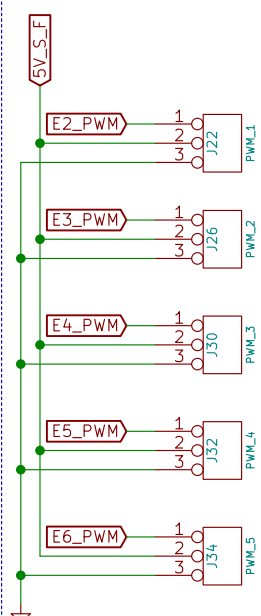
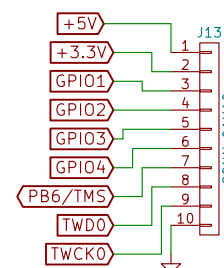
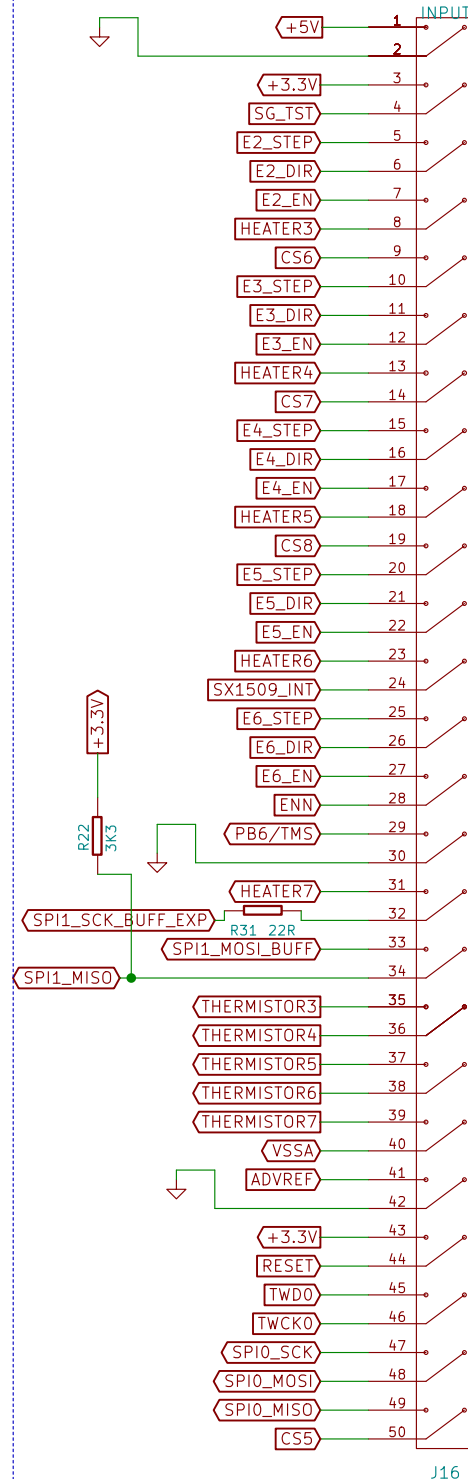
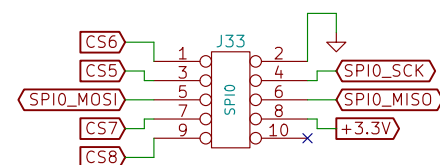
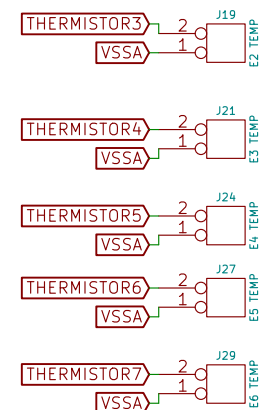
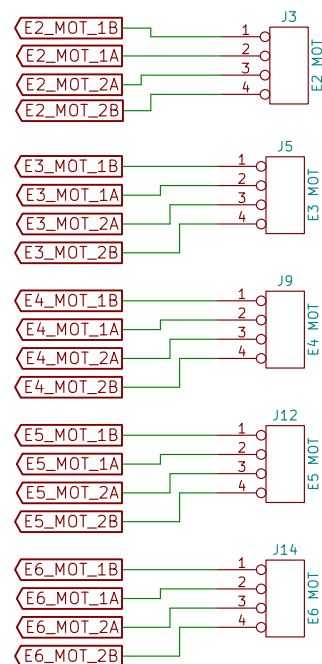
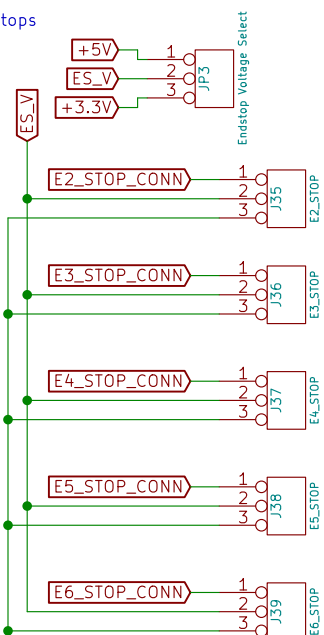
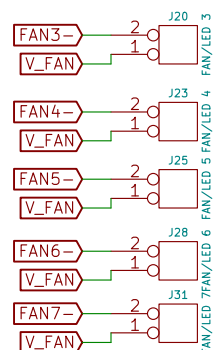
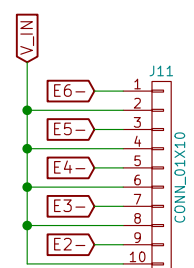
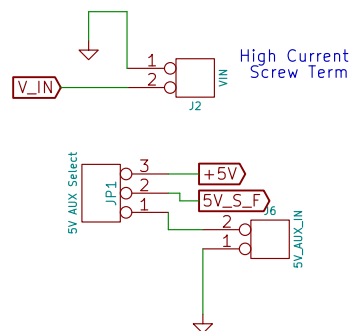


Diagram illustrating a 16-bit bus structure. The bus is divided into four 4-bit segments. The first segment contains FID M9, FID M1, FID M2, and FID M3. The second segment contains MOUNTING M4, MOUNTING M5, MOUNTING M6, and MOUNTING M7. The third segment contains MOUNTING M8. The fourth segment contains LOGO M8. The bus is labeled 16-bit at the bottom.

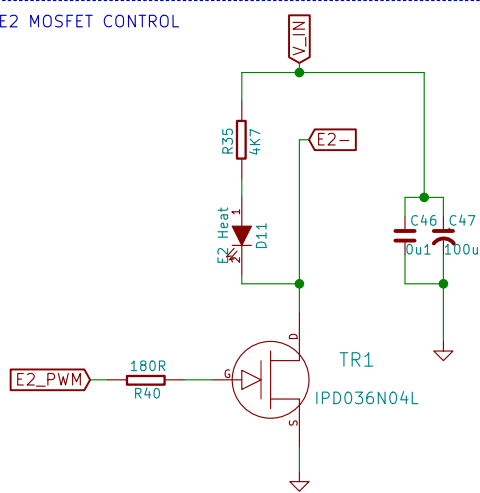
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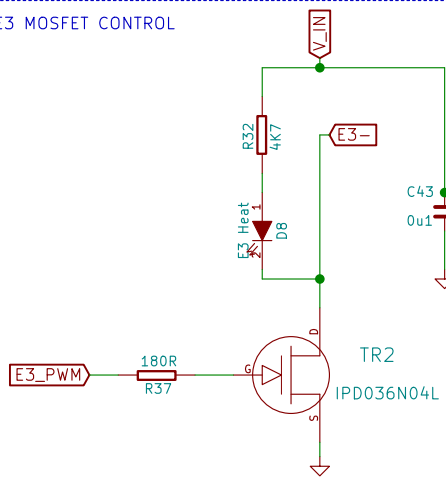
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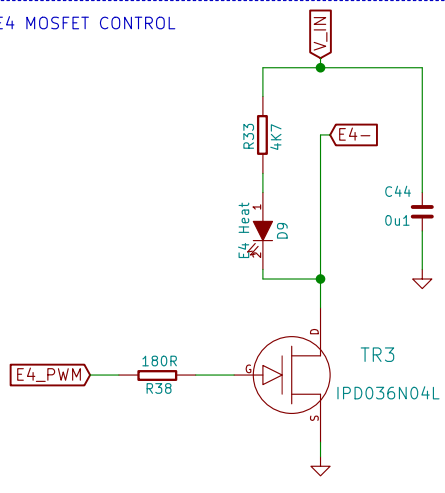
E2 MOSFET CONTROL



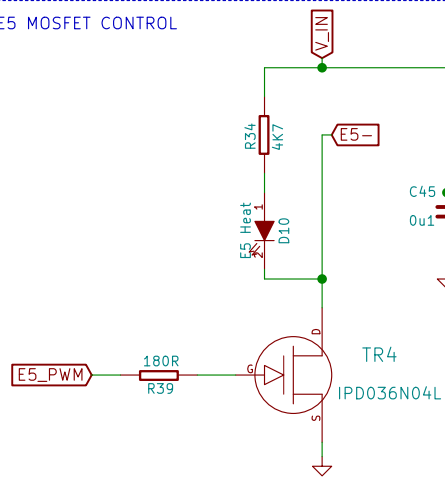
E3 MOSFET CONTROL



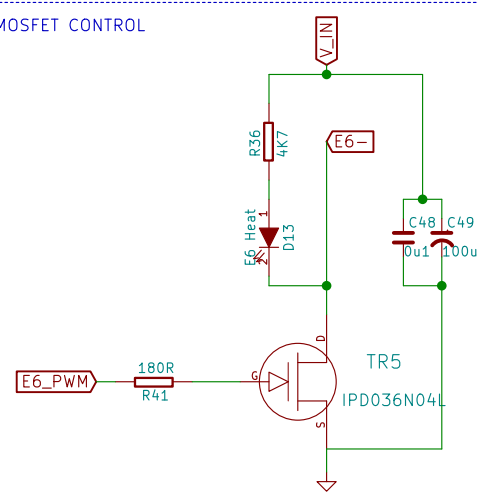
E4 MOSFET CONTROL



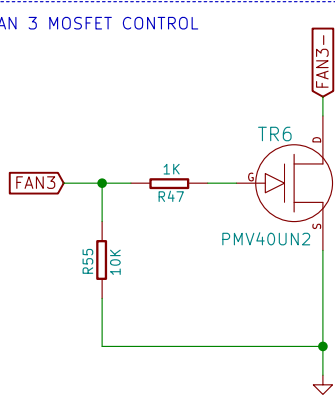
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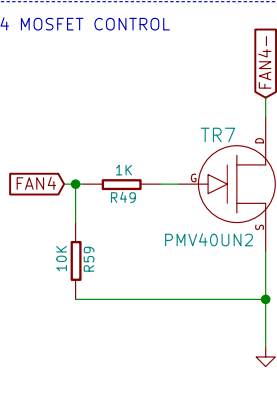
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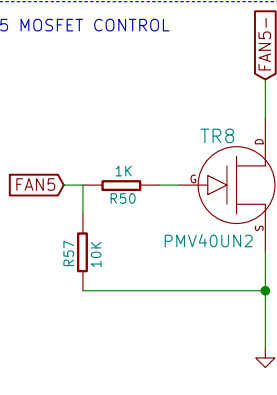
FAN 3 MOSFET CONTROL



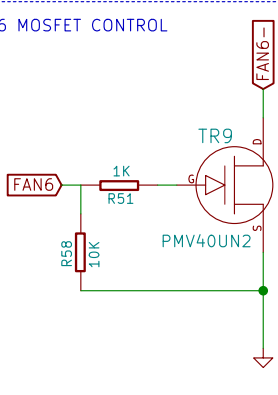
FAN 4 MOSFET CONTROL



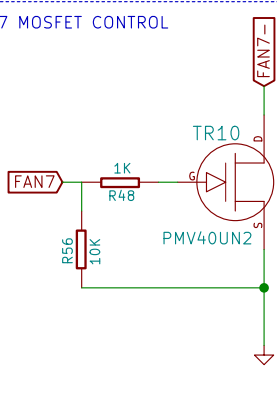
FAN 5 MOSFET CONTROL



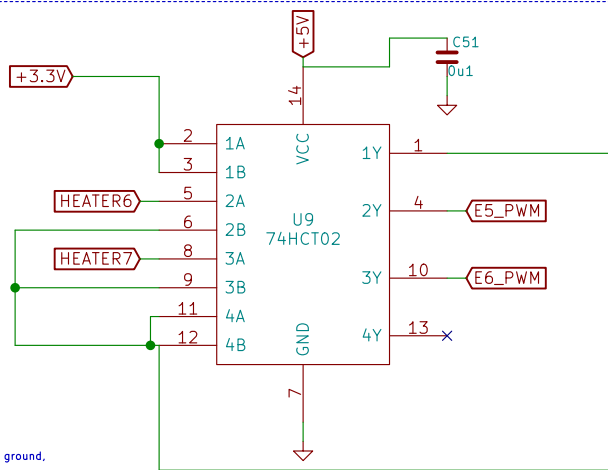
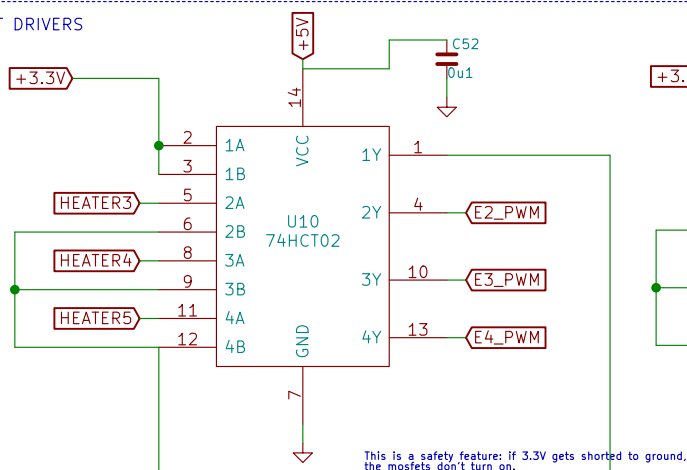
FAN 6 MOSFET CONTROL



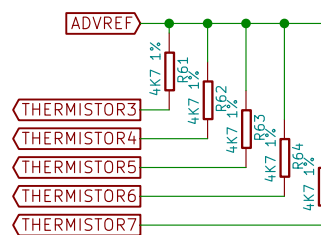
FAN 7 MOSFET CONTROL



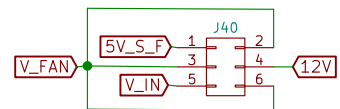
POWER MOSFET DRIVERS



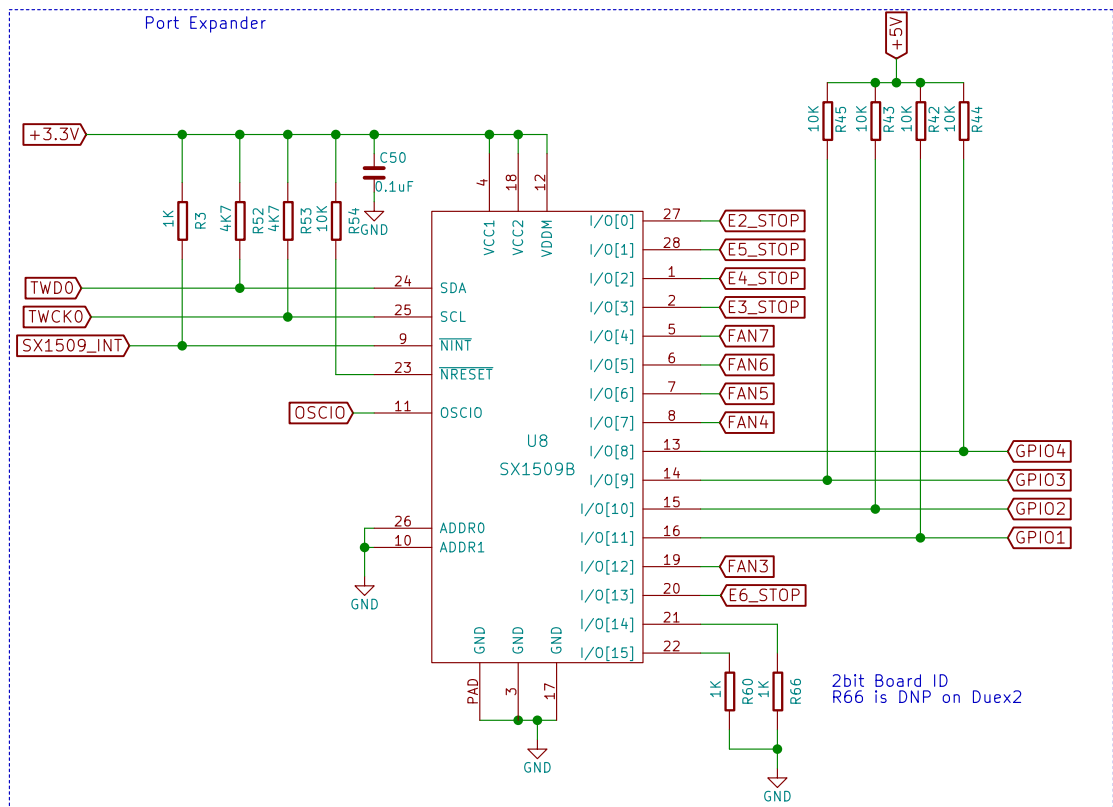
Thermistor Pullups



FAN Voltage Select



Port Expander



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