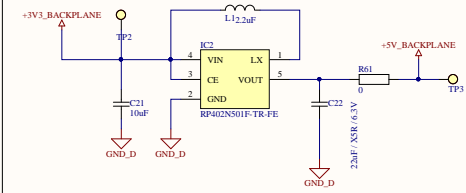
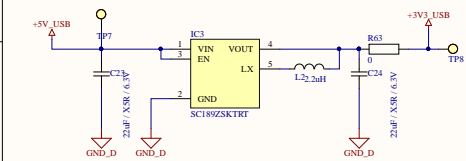




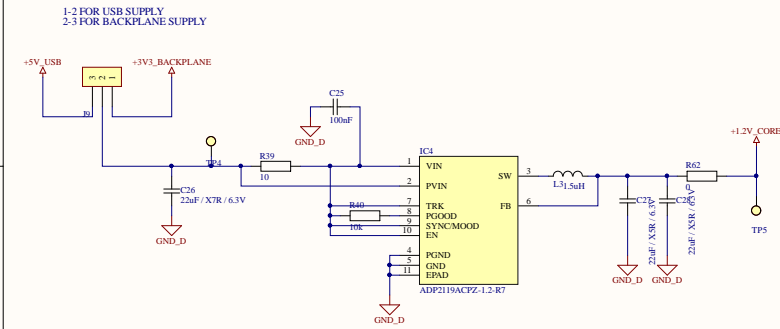
5V FROM 3V3 BACKPLANE



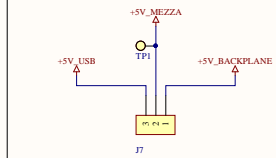
3V3 FROM 5V USB



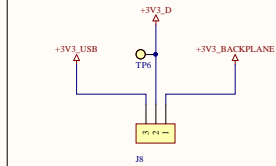
1V2 FPGA CORE SUPPLY



5V SUPPLY SELECTOR



3V3 SUPPLY SELECTOR



FPGA_POETIC_V1_01.PrjPcb

Supply_0_V1_01.SchDoc

Revision : V1.1

Sheet 5 of 6

Hes·so VALAIS WALLIS

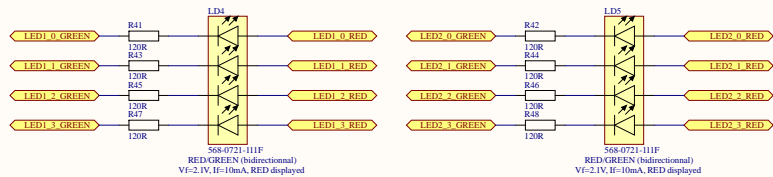
Date : 02.06.2021



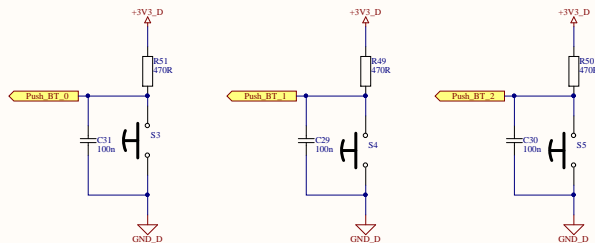
Design by : Jean Nanchen

R:\Diploma\TD2021\SYND\jean.nanchen\Hardware\FPGA_Board\V1_01\Supply_0_V1_01.SchDoc

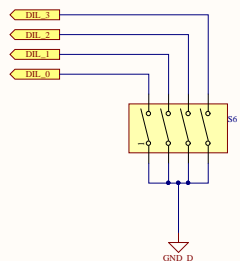
BI-COLOR LED



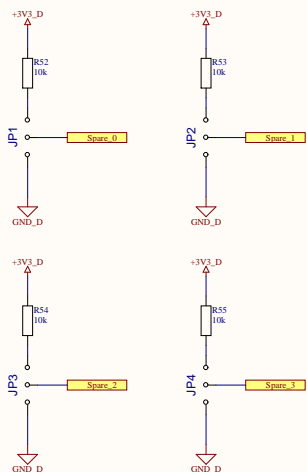
PUSH BUTTONS



DIL SWITCH 4 POS



SPARE PULL UP/DOWN HEADER



FPGA_POETIC_V1_01.PrjPcb

UserInterface_0_V1_01.SchDoc

Revision : V1.1

Sheet 6 of 6

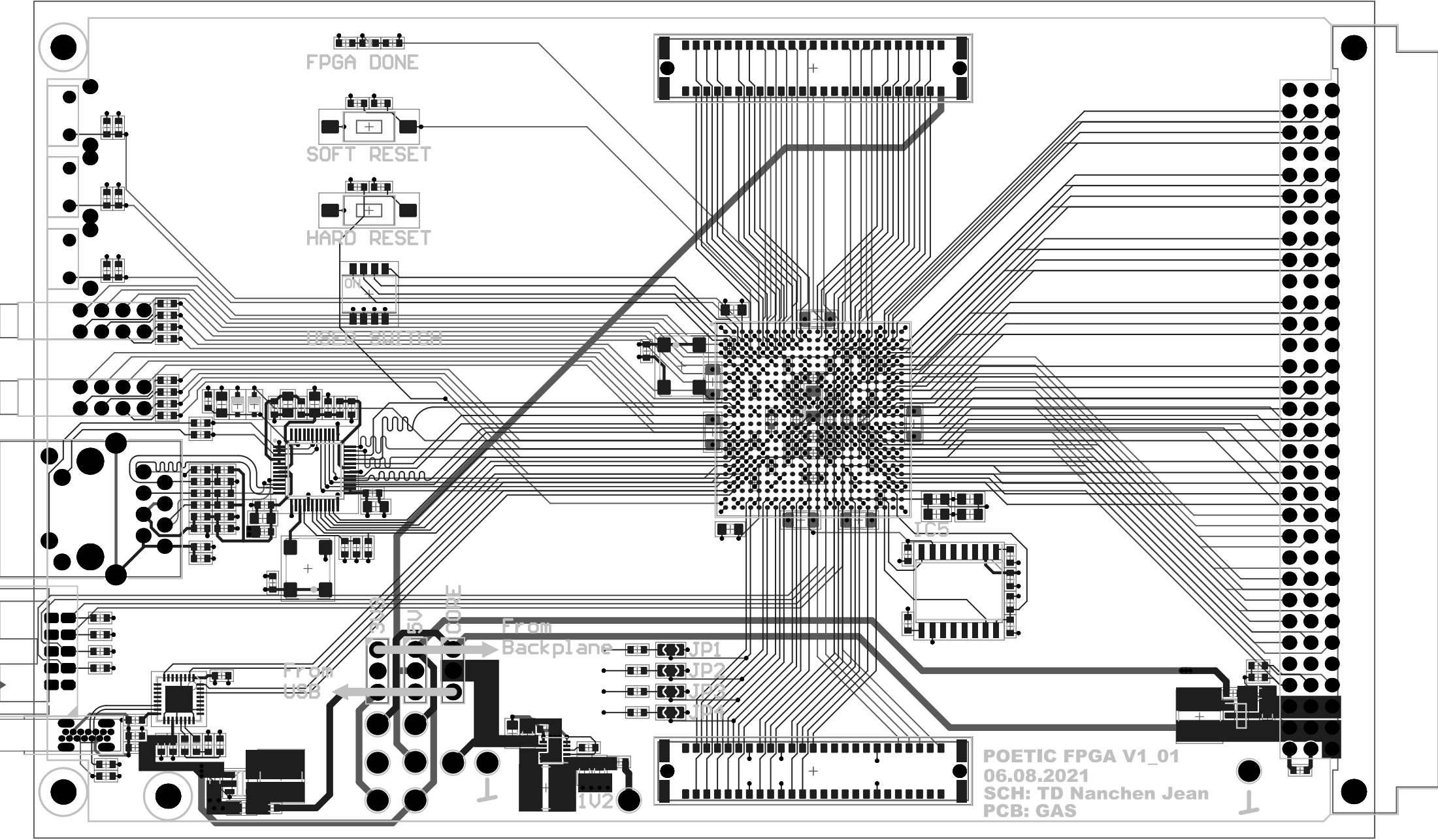
Hes·so VALAIS WALLIS

Date : 02.06.2021



Design by : Jean Nanchen

R:\Diploma\TD2021\SYND\jean.nanchen\Hardware\FPGA_Board\V1_01\UserInterface_0_V1_01.SchDoc



POETIC FPGA V1_01
06.08.2021
SCH: TD Nanchen Jean
PCB: GAS