

30V, 18A DUAL N-CHANNEL POWER MOSFET

GENERAL DESCRIPTION

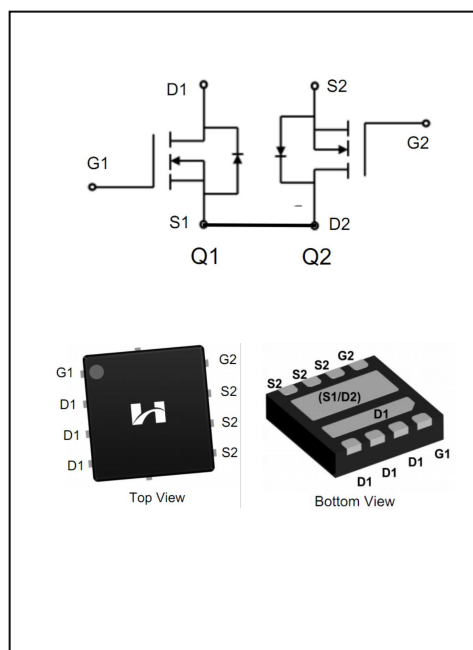
The SFN0318T2 uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety applications.

Features

- ◆ $V_{DS}=30V, I_D=18A$
- ◆ $R_{DS(on)}$
TYP: $8.2m\Omega @ V_{GS}=10V$

Applications

- ◆ Power faction correction (PFC)
- ◆ Switched mode power supplies (SMPS)
- ◆ Uninterruptible power supply (UPS)
- ◆ LED lighting power



ORDERING INFORMATION

Part No.	Package	Marking	Material	Packing
SFN0318T2	DFN3*3-8L	SFN0318T2	Pb Free	Reel

ABSOLUTE MAXIMUM RATINGS (T_J=25°C unless otherwise noted)

Characteristics	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current	I _D	T _C = 25°C 18	A
		T _C = 100°C 13.5	
Drain Current Pulsed(Note 1)	I _{DM}	70	A
Power Dissipation(T _C =25°C) -Derate above 25°C	P _D	24	W
Operation Junction Temperature Range	T _J	-55~+150	°C
Storage Temperature Range	T _{stg}	-55~+150	°C
Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	TL	300	°C

THERMAL CHARACTERISTICS

Characteristics	Symbol	MAX	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}	4.2	°C/W
Thermal Resistance, Junction-to-Ambient	R _{θJA}	43.5	°C/W

ELECTRICAL CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Off Characteristics						
Drain -Source Breakdown Voltage	B _V DSS	V _{GS} =0V, I _D =250μA	30	--	--	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	--	--	100	nA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =20V, V _{DS} =0V	--	--	100	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =-20V, V _{DS} =0V	--	--	-100	
On Characteristics						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D =250μA	1.0	1.2	1.8	V
Static Drain- Source On State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =4A	--	11.5	16.5	mΩ
		V _{GS} =10V, I _D =8A	--	8.2	10.5	
Dynamic Characteristics						
Input Capacitance	C _{iSS}	V _{DS} =15V V _{GS} =0V f=1.0MHZ	--	1850	--	pF
Output Capacitance	C _{oSS}		--	220	--	
Reverse Transfer Capacitance	C _{rSS}		--	150	--	
Switching Characteristics						
Turn-on Delay Time	t _{d(on)}	V _{DD} =20V, R _G =10Ω I _D =10A (Note 2.3)	--	8.5	--	ns
Turn-on Rise Time	t _r		--	65	--	
Turn-off Delay Time	t _{d(off)}		--	89	--	
Turn-off Fall Time	t _f		--	66	--	
Total Gate Charge	Q _g	V _{DS} =24V, I _D =10A V _{GS} =10V (Note 2.3)	--	19	--	nC
Gate-Source Charge	Q _{gs}		--	5.5	--	
Gate-Drain Charge	Q _{gd}		--	6.0	--	

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Characteristics	Symbol	Test conditions	Min.	Typ.	Max.	Unit
Continuous Source Current	I_S	Integral Reverse P-N Junction Diode in the MOSFET	--	--	18	A
Pulsed Source Current	I_{SM}		--	--	70	
Diode Forward Voltage	V_{SD}	$I_S=8A, V_{GS}=0V$	--	0.8	1.4	V
Reverse Recovery Time	T_{rr}	$I_F=8A$ $dI_F/dt=100A/\mu S$	--	17.5	--	ns
Reverse Recovery Charge	Q_{rr}		--	0.01	--	μC

1. Pulse width limited by maximum junction temperature

2. Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

3. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

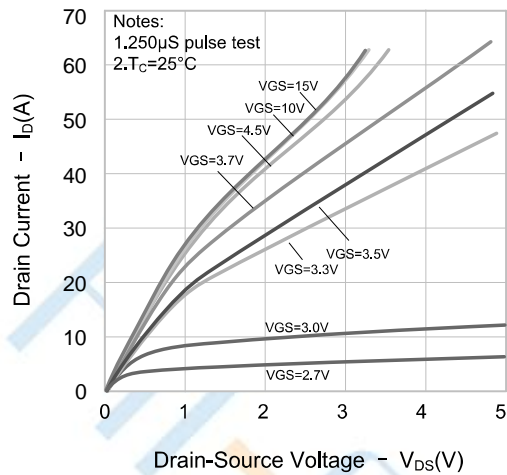


Figure 2. Transfer Characteristics

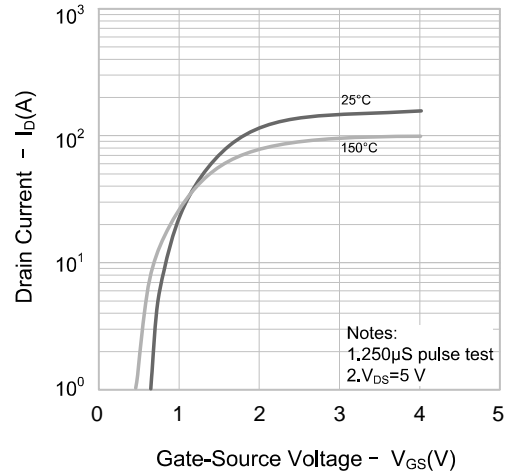


Figure 3. On-Resistance Variation vs. Drain Current

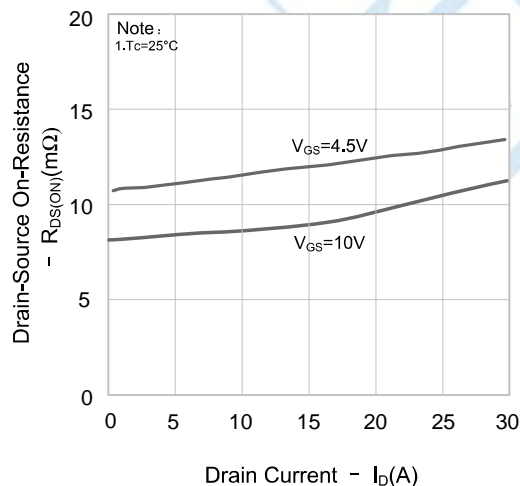


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

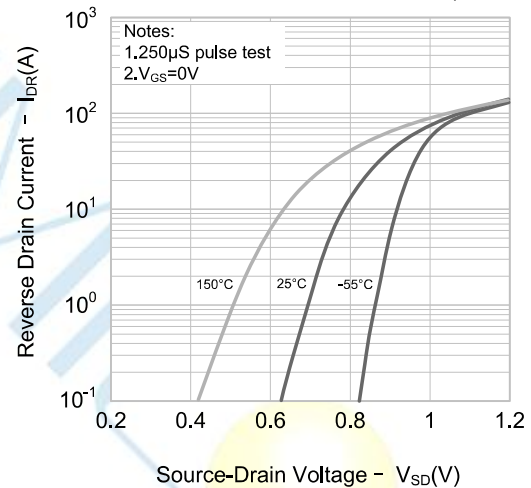


Figure 5. Capacitance Characteristics

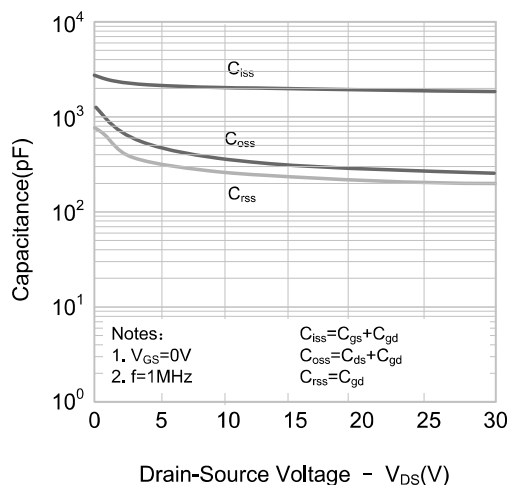
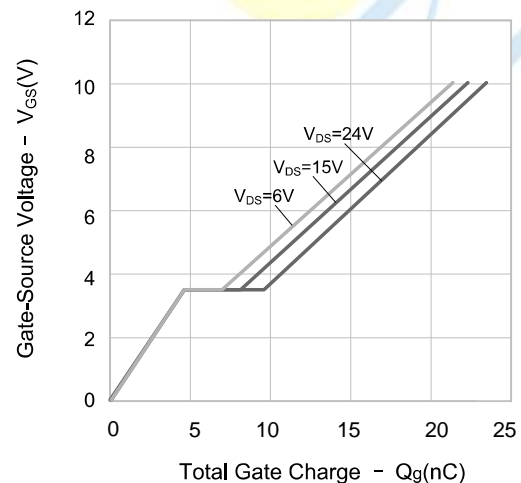


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics

Figure 7. Breakdown Voltage Variation vs Temperature

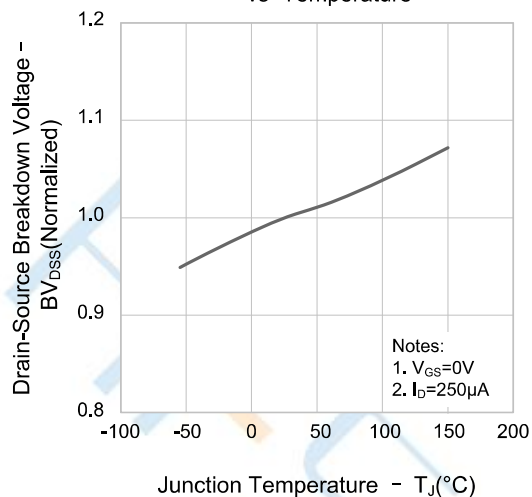


Figure 8. On-resistance Variation vs Temperature

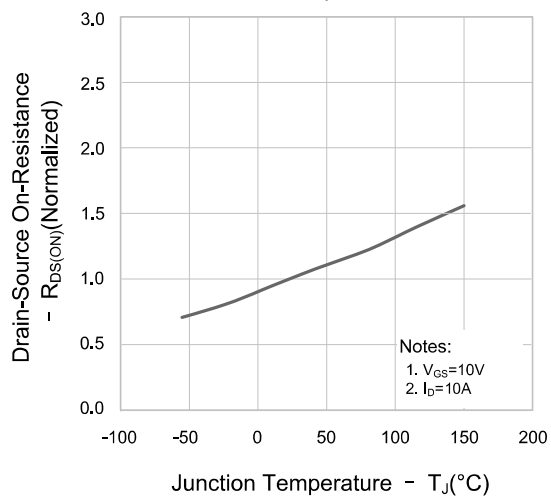
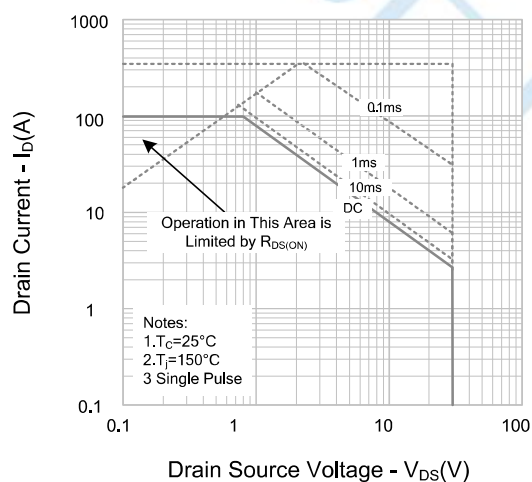
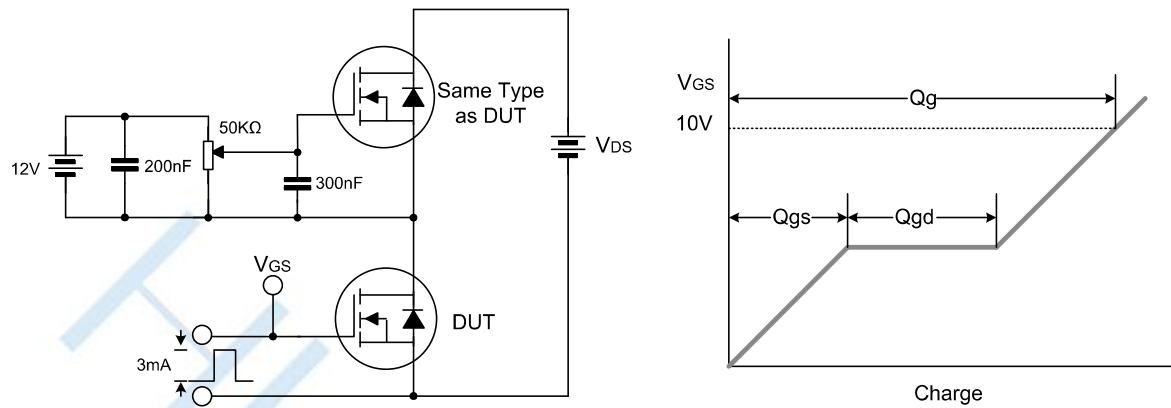


Figure 9. Max. Safe Operating Area

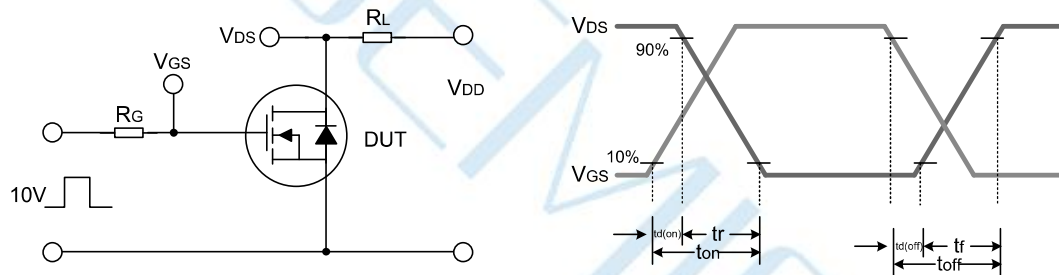


Test Circuit

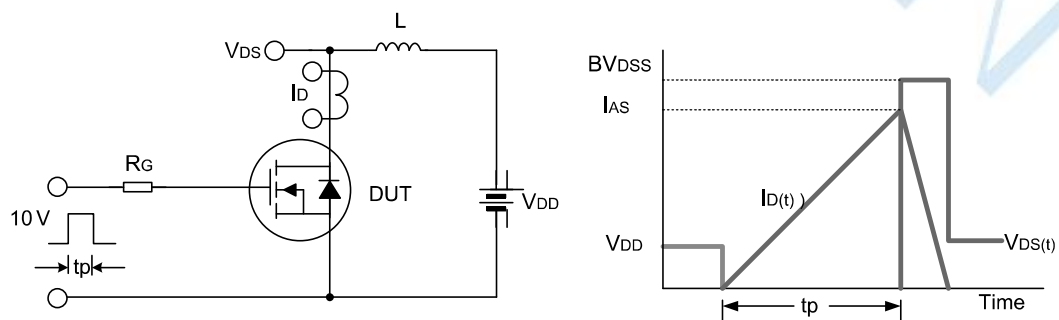
Gate Charge Test Circuit & Waveform



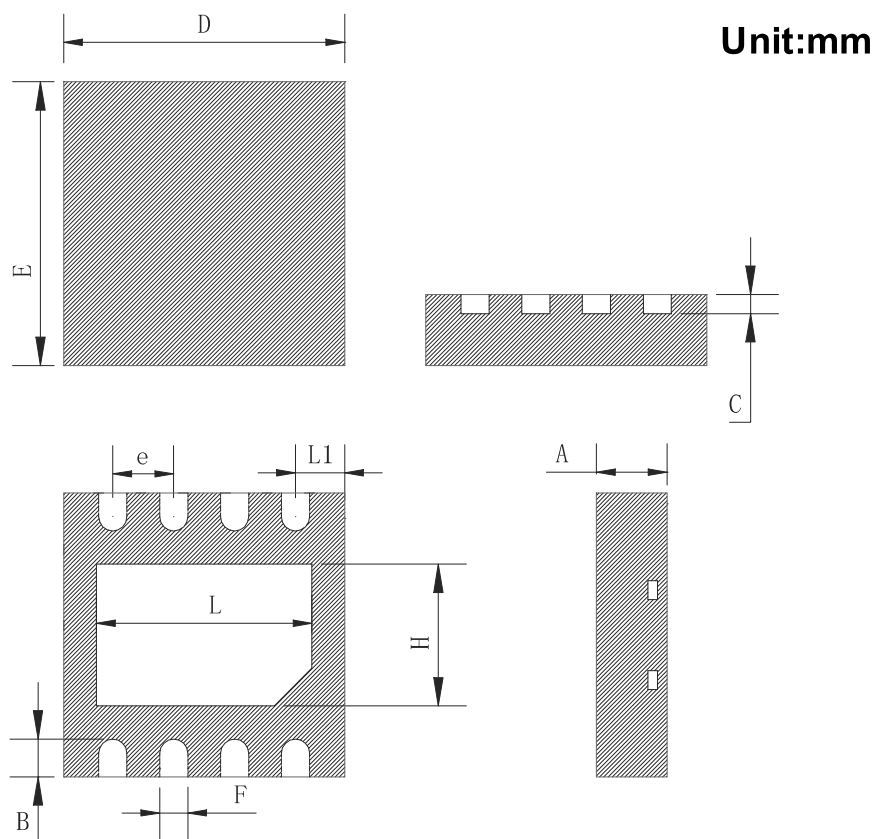
Resistive Switching Test Circuit & Waveform



Undamped Inductive Switching Test Circuit & Waveform



Package Dimensions of DFN3*3-8L



Symbol	Min	Typ	Max
A	0.70	0.75	0.80
B	0.35	0.40	0.45
C	0.153	0.203	0.253
D	2.90	3.00	3.10
E	2.90	3.00	3.10
e	0.60	0.65	0.70
F	0.25	0.30	0.35
H	1.40	1.50	1.60
L	2.20	2.30	2.40
L1	0.425	0.525	0.625

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