SMX **Instruction Cache** Warp Scheduler Warp Scheduler Warp Scheduler Warp Scheduler Dispatch Dispatch Dispatch Dispatch Dispatch Dispatch Dispatch Dispatch Register File (65,536 x 32-bit GK110) | (131,072 x32-bit GK210) 1 Core Core Core DP Unit Core Core Core Core Core Core SFU Core Core Core LD/ST SFU DP Unit LD/ST SFU Core Core Core DP Unit Core Core Core SFU Core Core Core Core Core Core LD/ST LD/ST SFU Core Core Core DP Unit Core Core Core DP Unit Core Core Core DP Unit Core Core Core LD/ST SFU LD/ST SFU Core Core Core Core Core Core **DP Unit** Core Core Core DP Unit Core Core Core LD/ST SFU Core Core Core Core Core Core **DP Unit** LD/ST Core Core OP Unit Core Core Core LD/ST SFU Core Core Core DP Unit Core Core Core DP Unit LD/ST SFU Core Core Core DP Unit Core Core Core LD/ST SFU SFU SFU Core Core Core Core LD/ST Core Core Core LD/ST Core Core LD/ST SFU SFU Core Core Core Core DP Unit Core Core LD/ST Core LD/ST Core DP Unit Core Core Core Core Core SFU Core Core Core Core Core LD/ST SFU Core Core Core Core Core LD/ST SFU Core Core Core DP Unit Core Core Core LD/ST SFU **DP Unit** Core Core Core DP Unit LD/ST SFU Core Core Core DP Unit Core Core Core LD/ST SFU Core Core Core Core Core Core DP Unit Core Core Core Core Core Core **DP Unit** LD/ST SFU LD/ST SFU Core Core Core DP Unit Core Core Core LD/ST SFU Core DP Unit Core Core Core DP Unit SFU Core Core Core Core Core LD/ST Core Core Core SFU Core DP Unit Core Core LD/ST Core Core Core Core LD/ST SFU Core Core Core Core Core LD/ST SFU Core Core DP Unit Core LD/ST SFU Core Core Core Core Core Core LD/ST SFU Core Core Core DP Unit Core Core Core DP Unit LD/ST SFU Interconnect Network (64 KB Shared Memory / L1 Cache GK110) | (128 KB Shared Memory / L1 Cache GK210) 48 KB Read-Only Data Cache Tex Tex