

USRP based X-band Digital Beam Forming Synthetic Aperture Imaging Radar



Presenter: P. Stenger: Northrop Grumman

Technical Team: M. Blue: Northrop Grumman,

M. Urdareanu, G. Steans, N. Henry, T. Lewis: FAMU-FSU College of Eng.



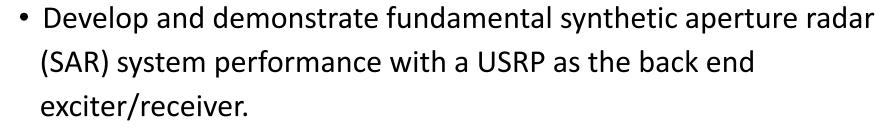
- Project goals
- System concept and aperture design
 - Conversion to X-band
 - Electronics module
- USRP waveform generation and reception
- MIMO and TDM timing and control
- Measurement scene
- Measurements produced by USRP
- Signal post processing and calibration
- Image formation
- Summary highlights and future work







Project Goals



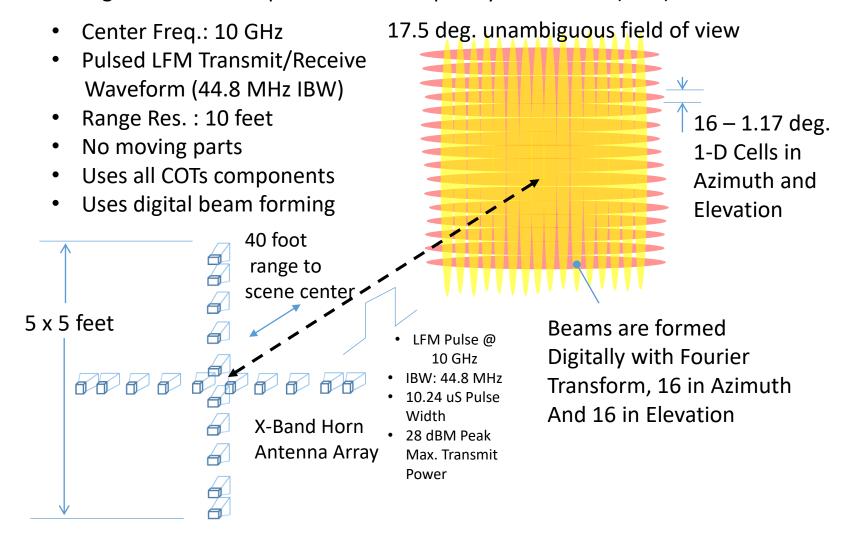


- Demonstrate implementation of several key radar techniques
 - Virtual element antenna aperture formation
 - Multiple Input Multiple Output (MIMO) techniques
 - Time Domain Multiplexing (TDM) of radar waveform between elements
 - Up-conversion of USRP carrier to X-band for better resolution in area limited platforms
 - Digital Beam Forming (DBF) for image formation. No moving parts.
 - Mitigate transmit leakage while receiving radar return
 - Use USRP for digital pulse compression and as many signal processing functions as practical given academic semester time constraints
 - Use only COTs components

Imaging Radar Operational Concept

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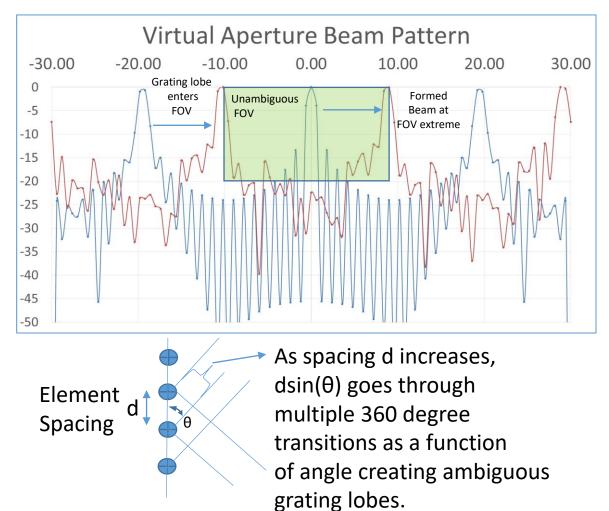
- Union of the orthogonal 1-D images forms 2-D cross-range image of scene
- Down-range resolved with pulsed linear frequency modulated (LFM) waveform





Sixteen Synthetic Aperture 1-D Virtual Elements at 3λ spacing formed for each axis

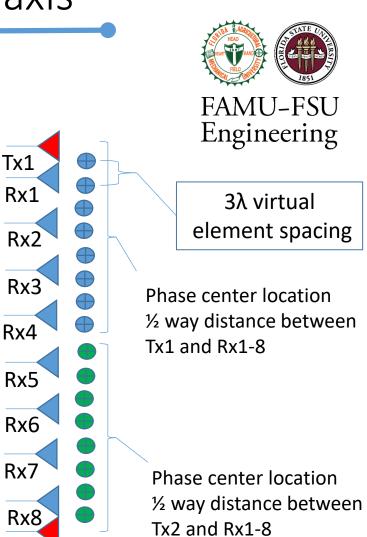
Unambiguous field of view of +/- 8.75 deg., 1.17 deg. Spatial resolution



Generates 16 phase centers Tx1/Rx1 Tx1/Rx2 Tx1/Rx3 Tx1/Rx4 Tx1/Rx8 Tx2/Rx1 Tx2/Rx2 Tx2/Rx3 Tx2/Rx4 Tx2/Rx8

Tx1

Tx2



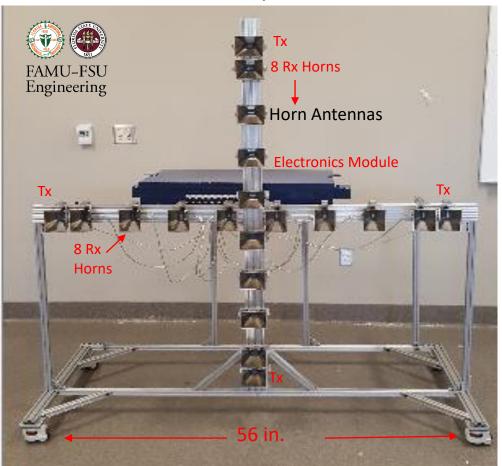
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Aperture and Component Module Hardware Photos



All COTs component used

Antenna Aperture



USRP Interconnections with Electronics module and PC

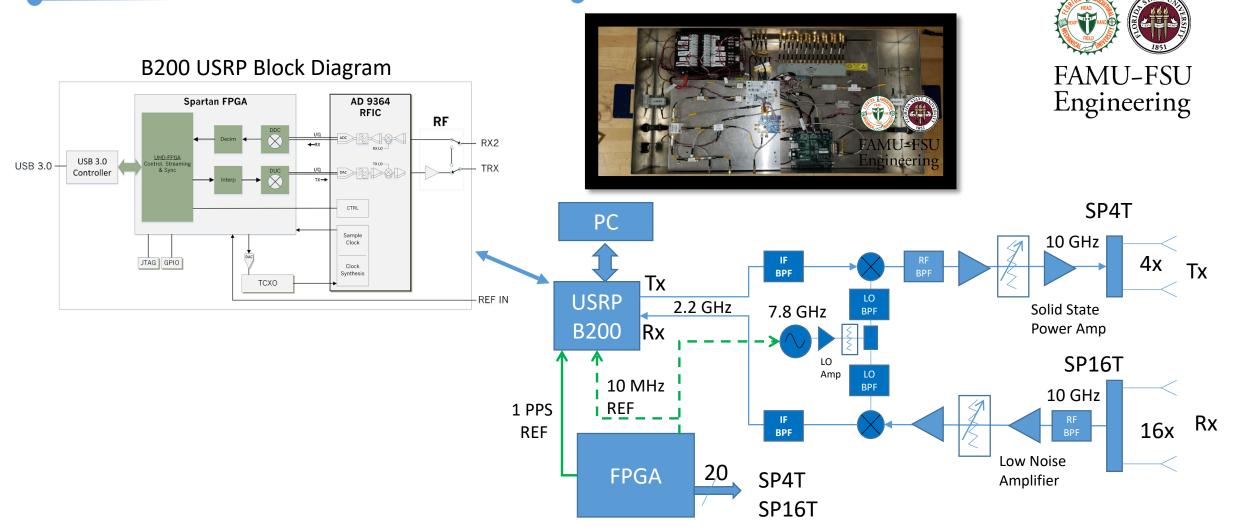




All Commercial Components Used In Electrical Design



Electronic Component Module





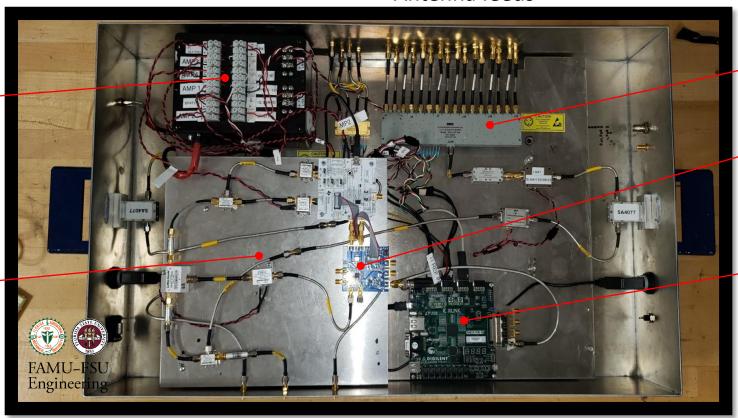
Electronics Module Detail

Hardline coax cable interconnects components

Antenna feeds

Custom
Power
supply

X-band UP/DOWN Converter section





SP16T RF Switch

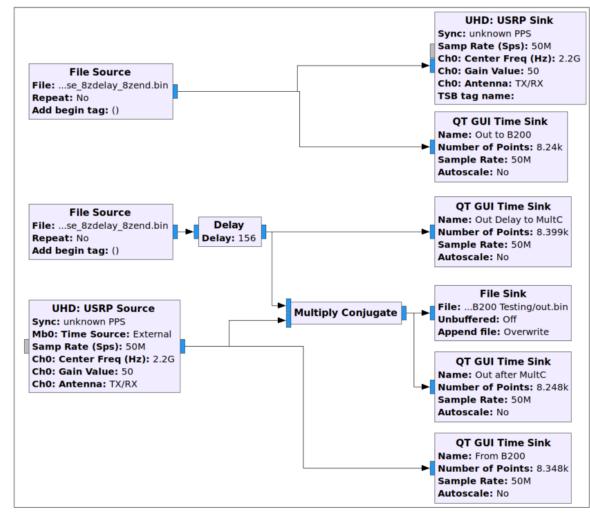
Synthesized LO Source

FPGA timing Board



Pulsed FMCW Flow Graph

- Fs = 50 MHz sample rate
- 2.2 GHz IF
- 44.8 MHz IBW LFM
- Digital Pulse Compression
- 8,192 complex samples define 16 pulse data collect
- Non-streaming
 - Tried but could not eliminate dropouts at the 50MHz sample rate over USB 3.0

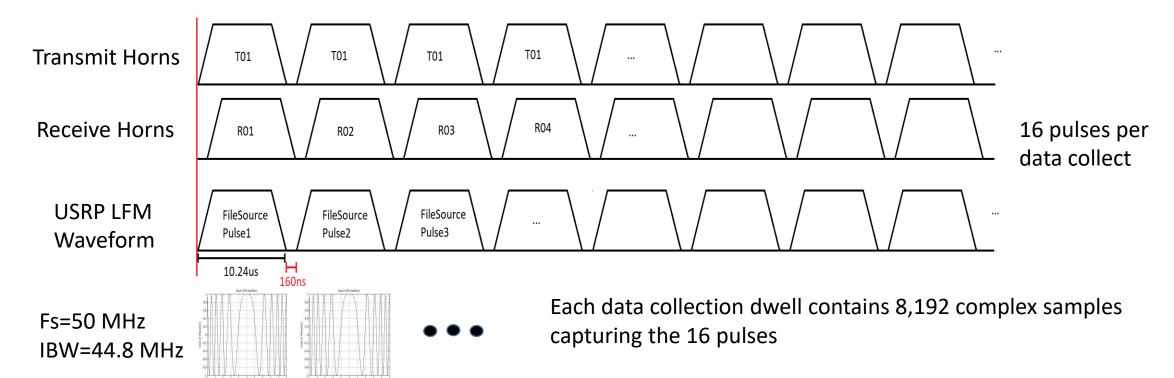




Timing Critical Between USRP Waveform and FPGA Discrete Switching Signals

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- FPGA generates discrete signals for MIMO operation
- Delay in USRP output relative to 1PPS ref signal was a challenge to synchronize switching MIMO elements

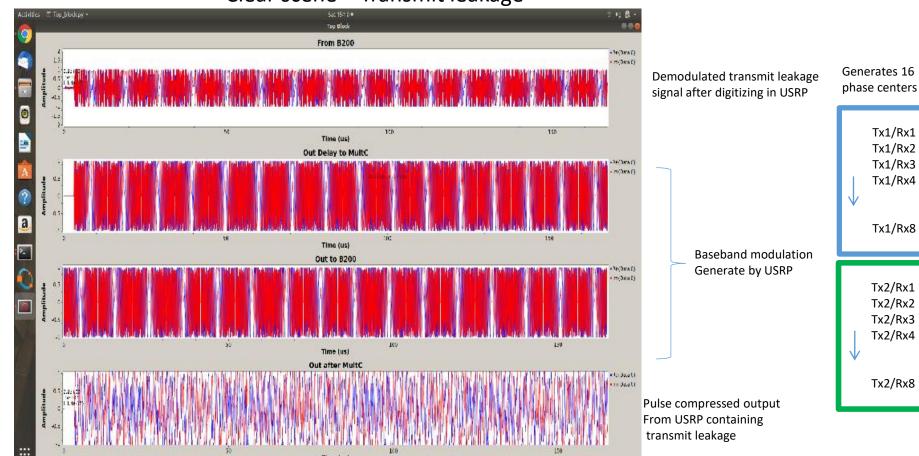


Full Thread Radar Performance GRC Output



• 16 LFM pulses form one coherent data collect interval

Clear scene + Transmit leakage





Tx1/Rx1
Tx1/Rx2
Tx1/Rx3
Tx1/Rx4

Rx2

Phase center location
½ way distance between
Tx1 and Rx1-8

Rx4

Rx5

Rx8

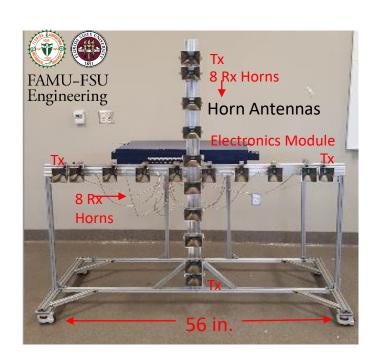
Phase center location ½ way distance between Tx2 and Rx1-8

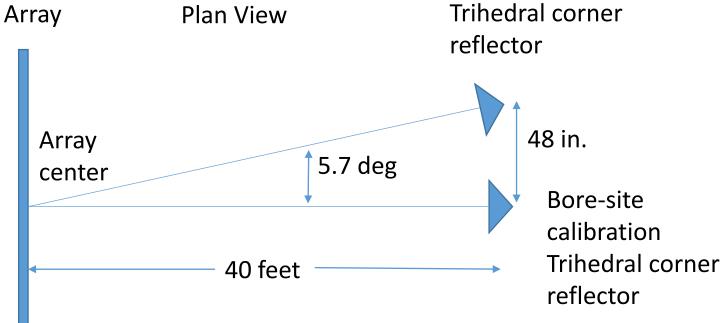
Simple Scene With Corner Reflectors Used to Verify Fundamental System Performance



 Corner reflectors at same range with minimal scattering variables in scene







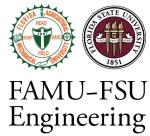
Test



Scene Viewed From Aperture

- Trihedral reflectors used for broad angle response avoiding precision alignment challenges
- Flat plate or dihedral reflector specular response requires careful alignment







Displayed data from the USRP in the GRC

Sample Rate: 50M

File Sink

File: ...B200 Testing/out.bin

QT GUI Time Sink

Number of Points: 8.248k Sample Rate: 50M Autoscale: No

QT GUI Time Sink Name: From B200 Number of Points: 8.348k

Sample Rate: 50M

Autoscale: No

Append file: Overwrite

Name: Out after MultC

Autoscale: No

Unbuffered: Off

Multiply Conjugate -

Add begin tag: ()

Sync: unknown PPS

Samp Rate (Sps): 50M

Ch0: Gain Value: 50

Ch0: Antenna: TX/RX

UHD: USRP Source

Mb0: Time Source: External

Ch0: Center Freq (Hz): 2.2G

Sixteen LFM pulses at key points in the signal thread

Clear scene + Transmit leakage signal after digitizing in USRP **UHD: USRP Sink** Sync: unknown PPS Samp Rate (Sps): 50M Ch0: Center Freq (Hz): 2.2G Ch0: Gain Value: 50 File Source Ch0: Antenna: TX/RX File: ...se 8zdelay 8zend.bin TSB tag name: Repeat: No Add begin tag: () **OT GUI Time Sink** Name: Out to B200 Number of Points: 8.24k Sample Rate: 50M Autoscale: No **QT GUI Time Sink** File Source Name: Out Delay to MultC Delay File: ...se 8zdelay 8zend.bin Number of Points: 8.399k **Delay:** 156 Repeat: No

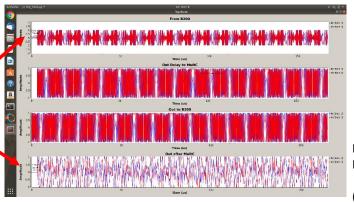
Demodulated transmit leakage



Baseband modulation Generate by USRP

Pulse compressed output From USRP containing transmit leakage

Two Corner Reflectors (CRs) + Transmit leakage



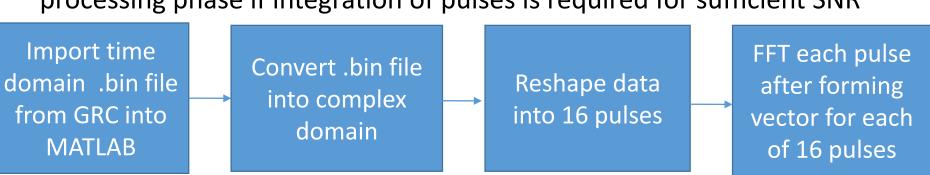
Demodulated return Signal after digitizing in USRP. CR returns adding in with transmit leakage.

Pulse compressed output From USRP containing transmit leakage and CR returns (the input to MATLAB processing)

Processing Flow in MATLAB After Digital Pulse Compression in USRP

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- Complex baseband data is obtained from the USRP
- Starting phase is different for each data collect requiring post processing phase if integration of pulses is required for sufficient SNR





Extract complex value for each of 16 pulses at selected FFT range bin

Apply calibration to 16 complex values from boresite data

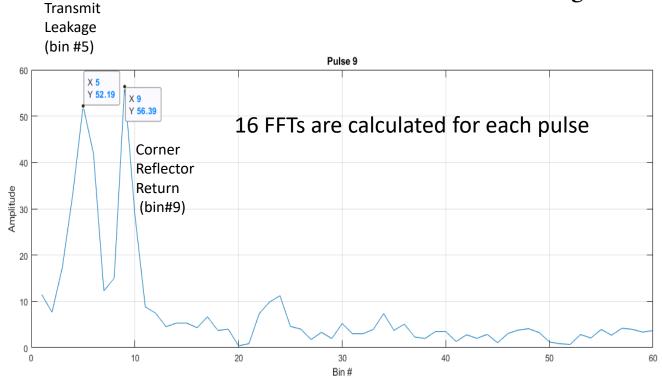
Perform 16 point spatial DFT

Time Domain FFT of Each Transmit Receiver Pair Pulse Resolves Range of Corner Reflector



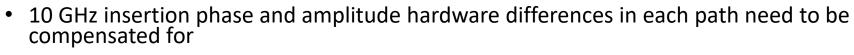


- Complex signal values of desired bin #9 are obtained for image formation.
 - Sufficient bandwidth (BW) of 44.8 MHz in each pulse enables mitigating transmit leakage
 - Resolution of 1/2BW = 11
 nS or around 10 feet
 - Note from data (9-5)*10 feet = 40 feet which is range difference between transmit leakage and corner reflector range



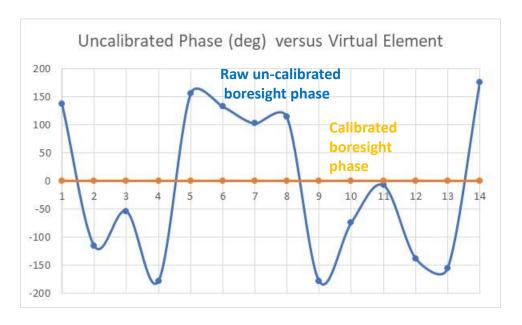
Calibration of Single Corner Reflector Boresight Measurement Needed







- Multiply by measured boresight conjugate and normalizing amplitude forces boresight to amplitude of '1' and phase of 0 deg. This calibration is applied to all data.
- Elements 1 and 16 (closest to the transmit elements) suffered to much transmit leakage not allowing receive measurement



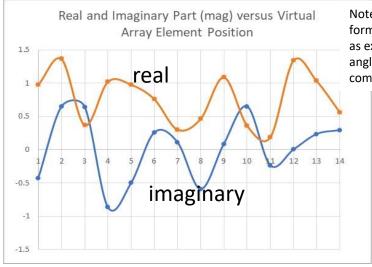
- Un-calibrated response symmetry indicates hardware insertion phase similar between paths
- Antenna aperture was mechanical aligned to corner reflector
- Alignment slightly off since symmetric around 6:7 rather than 7:8.

Final Spatial Beam Forming DFT Resolves Corner Reflectors in Cross-range

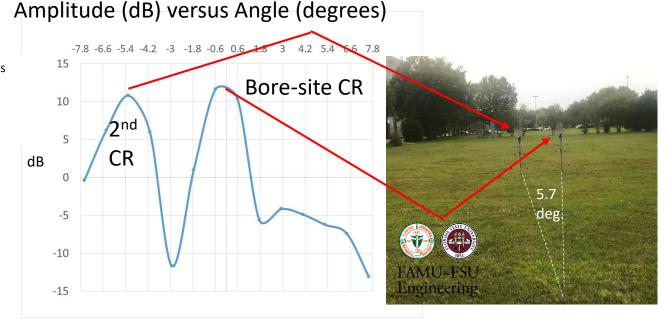
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- Measured results of final spatial FFT beam forming code with Taylor
 weighting producers angle of arrival in image plane for both reflectors in scene
- 2nd CR is measure at -5.4 degrees off bore-site. This was very close to the off bore-site mechanical angle of -5.7 degrees.

Complex values after calibration applied to two corner reflector measured data



Note: real and imaginary parts form sinusoidal response as expected representing angle of arrival difference compared to boresight





Summary Highlights and Future Work

- Commercial USRP RF carrier was sufficiently phase stable to do digital beam forming
- Streaming was not possible at the 50 MHz sample rate over USB 3.0
- USRP phase starts at different value during each data collect requiring phase alignment in post processing if pulse integration required for SNR improvement
- Sufficient IBW needed to separate transmit leakage from minimum range.
- Isolating transmit element to nearest receive element so that receive path can linearly receive return signal
- Delay in USRP output relative to 1PPS signal from FPGA could be mitigated by RF detecting coupled off path and using that as a reference for FPGA switching
- Collect data over additional RF frequencies with same IBW and stitch together to increase range resolution
- Move signal processing functions from MATLAB into the custom GRC modules

