

A naive proposal for a scalable modular planar Hultipole RF trap

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In this proposal we are trying to figure out how to design and manufacture multipole RF trap chips as a brick for larger trap constructions of higher geometric and topological complexity. To accomplish the fiddly part of attain a concerted multifold of kind of synchronized RF sub-traps on those ceramics substrate chips (we'll call them bricks for pun reasons) we are using a hybrid PLL approach to experiment with different synchronisation strategies. That means every brick has its own PLL-ed HF trap drivers. To get rid of bulky cavity or helical resonators, we are testing the feasibility of using piezo resonators - in our case simply off-the-shelf quartz resonators. The Ion Traps itself are Hilbert Curve shaped rails. We use this approach for two primary reasons. The first reason is, the fractal nature of the hilbert curve allows a higher density of ions on the planar area. The second reason is, even the topology is linear or circular it is possible to have convinient interaction distances even though the linear distance on the curve is > 1. To change the direct 2D grid neighbourhood it's only a shift operation on the circular QCCD register aka the trap. That should lead to easy options for surface code error correction and stuff like that.

1 The Trap

The trap is a circular rail planar trap folded in the shape of a Hilbert Curve. We are using Lindenmayer Systems to generate those geometries. To visualize the Idea have a look at this hacky Python snippet:

- 1.1 Semi-linear configuration along Hilbert Curves
- 2 The Brick
- 3 The Chamber
- 4 Connecting Chambers
- 5 CMOS Image Sensor App Board