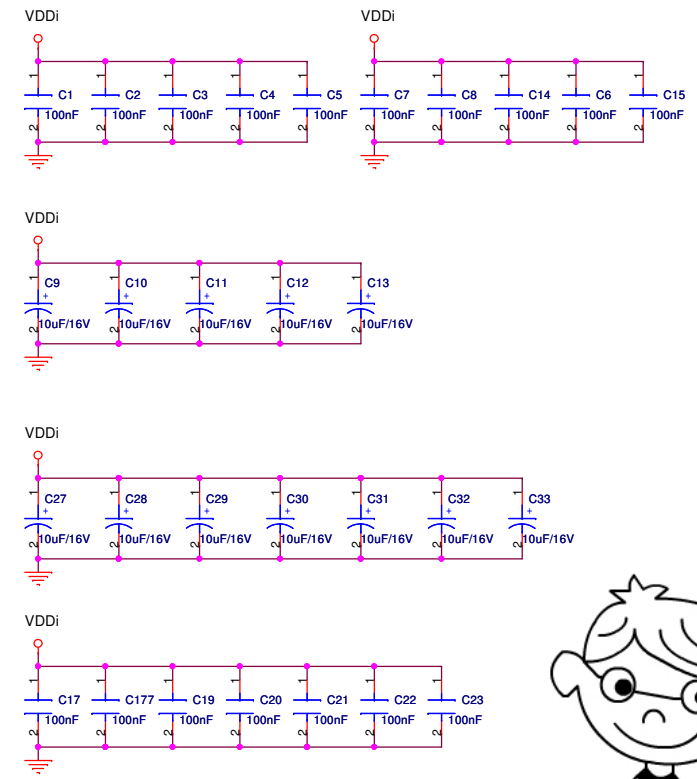
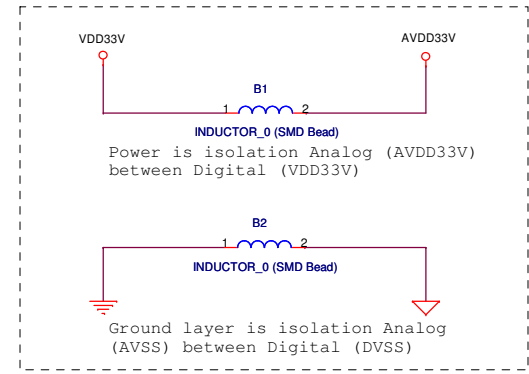
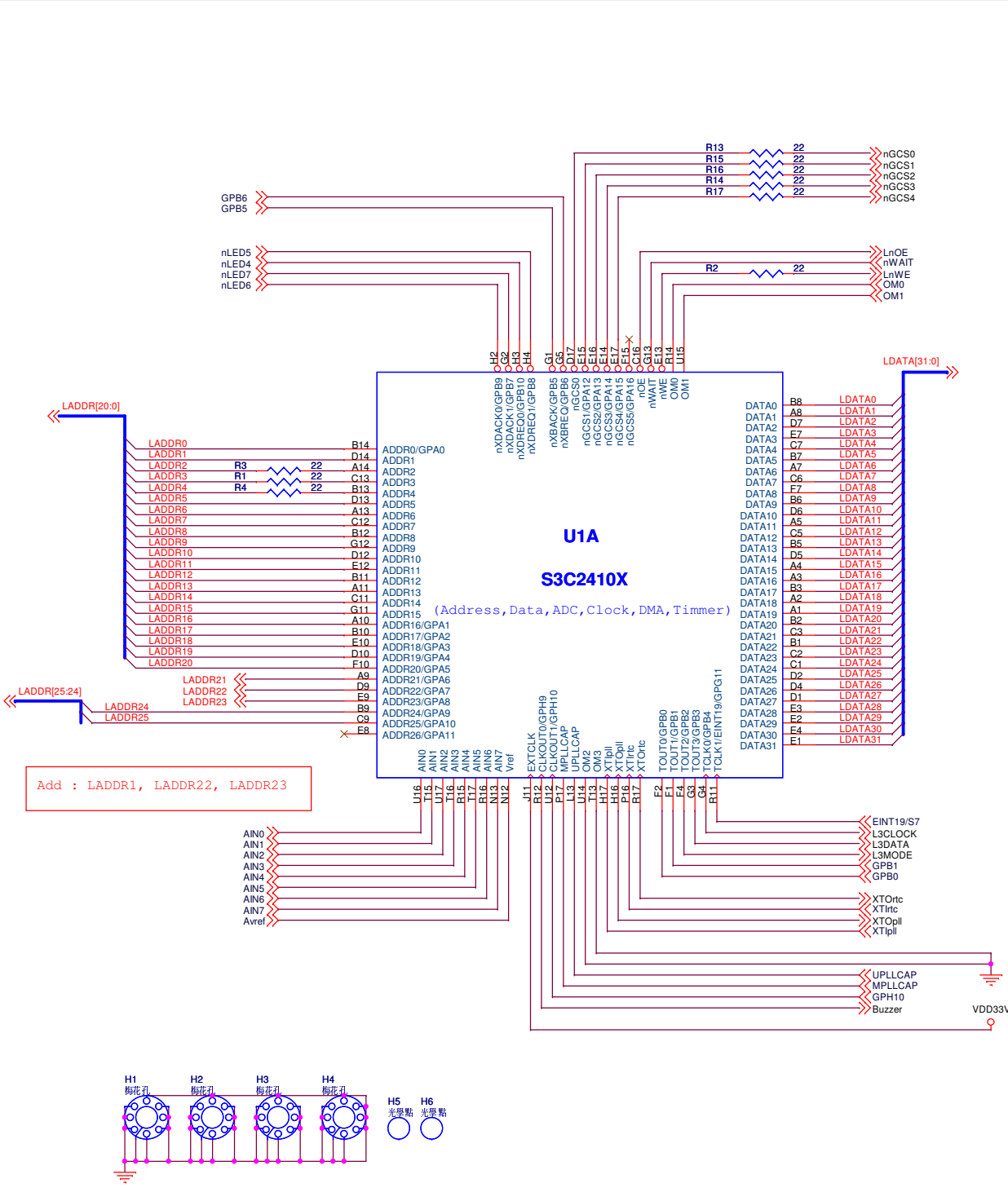


Jollen-Kit! Pro. JK2410_2008 Board (S3C2410 Reference Board)

PCB Revision	Date	Description
		<p>The PCB Artwork guide for S3C2410X</p> <ul style="list-style-type: none"> - The recommended land open size is 0.39 ~ 0.41mm diameter on the bottom of the BGA. (Pattern 0.1mm, Space 0.12mm, Escape Via pad 0.4mm/Drill 0.2mm) - All the SDRAM signals(nSCS,nSRAS,nSCAS,nWE,DQMn,SCKE,SCLK,BAn,ADDR,DATA) have to be similar length. By our lab test result, this PCB routing method has enhanced the SDRAM I/O voltage margin up to 2.5V - Power signal (GND, 1.8V, 3.3V) should be reinforced as soon as possible. Also, the bypass capacitor has to be nearest to the power pads. - All the memory signals are simulated at 35pF load. So, all capacitance including the board parasitic should be smaller than 35pF. The parasitic capacitance of the S3C2410 is typically 5pF. <p>Update Version Y2008 :</p> <ul style="list-style-type: none"> (1)Add : LED4, LED5, LED6, LED7 (2)Remove : J1, J2 (3)Change CON12 from "Female" to "Male" (4)Add System Bus : J10, Bus control : J11, Using pitch 2.0 (5)IIC signal open (6)Add Buzzer signal (7)電解電容改成鉅質電容, package refer to SBC2410 (8)Power Switch : 改成 和SBC2410一樣, package refer to SBC2410 (9)RJ45 改了

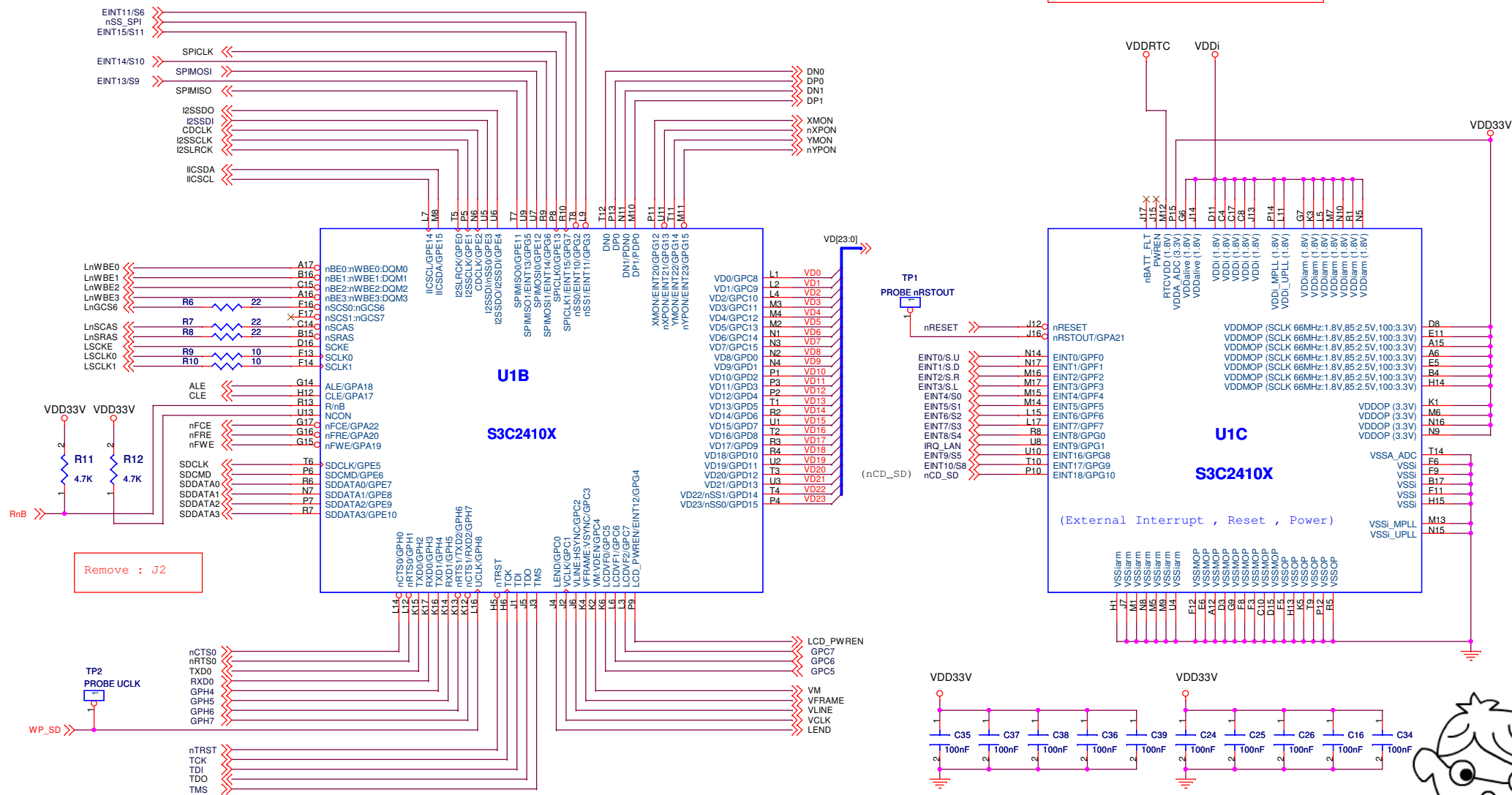


Title			
JK2410			
Size	Document Number	Revision History	
A3			Rev 1.3
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Title	JK2410	
Size	Document Number	Rev
A3	MCU (ADDR/DATA/ADC/CLOCK/TIMMER/INT/RESET/POWER)	1.3
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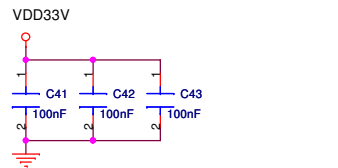
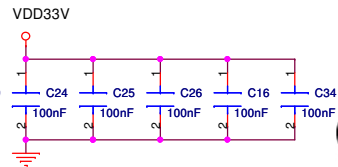
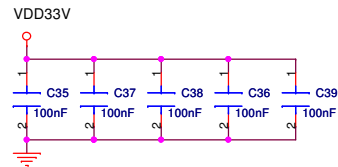
Remove : J1, R5
Terminated : U1C / J17



Remove : J2

TP1
PROBE nRSTOUT

(nCD_SD)
nCD_SD

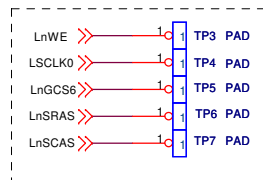


Address Step J2 (NCON)	NAND Flash Configuration
0	512 Byte/Page , 3 Step Addressing
1	512 Byte/Page , 4 Step Addressing

All these bypass Caps must be placed near each VDD33V pads of S3C2410X,U1

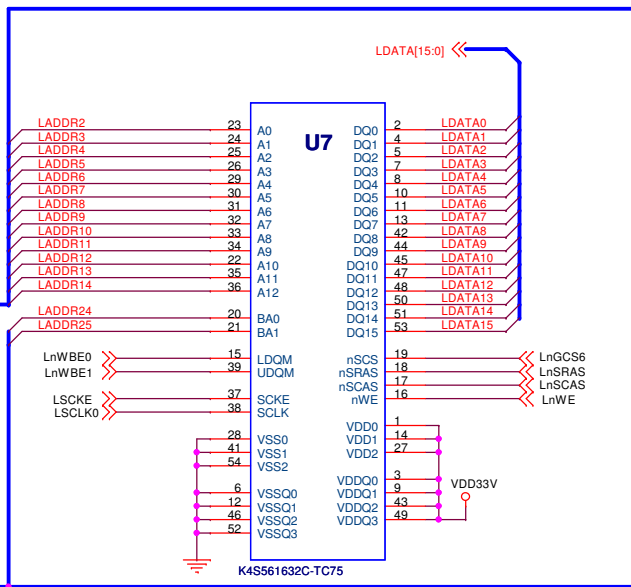


http://www.jollen.org/		
Title	JK2410	
Size A3	Document Number MCU (LCD/JTAG/UART/USB/NAND/MMC/UART/USB/SP/MEMORY)	Rev 1.3
Date:	Thursday, August 21, 2008	Sheet 3 of 13

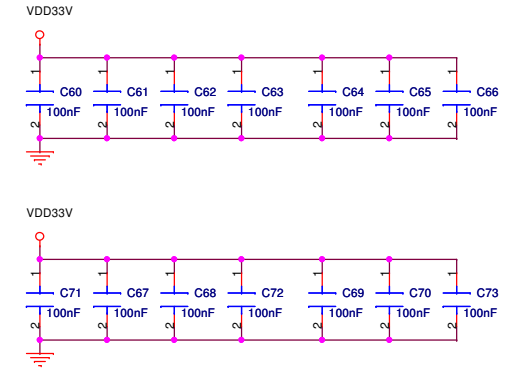
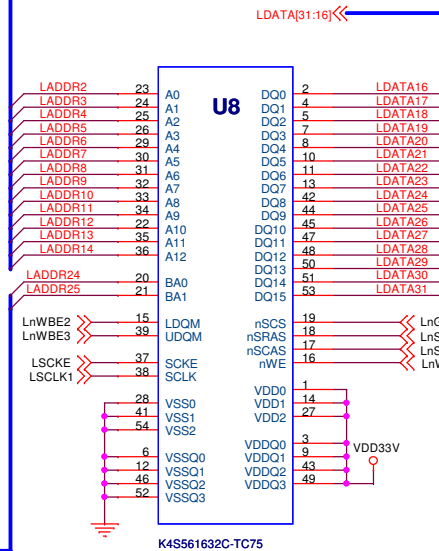


LADDR[14:2] >>

LADDR[25:24] >>

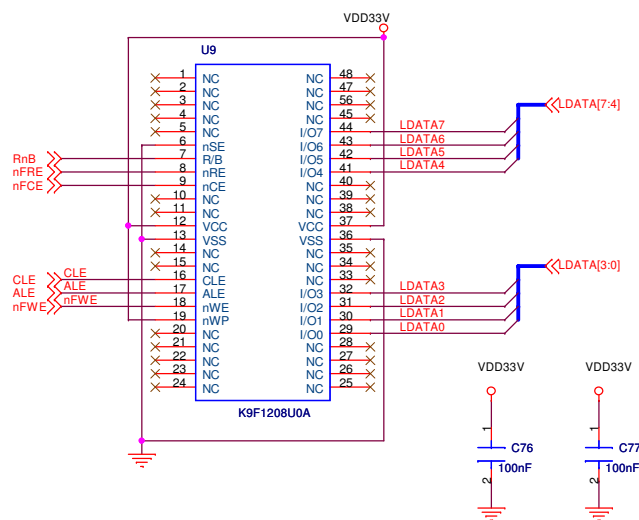


SDRAM, 32MB X 2

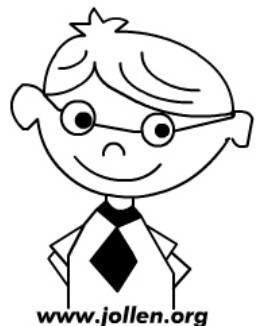
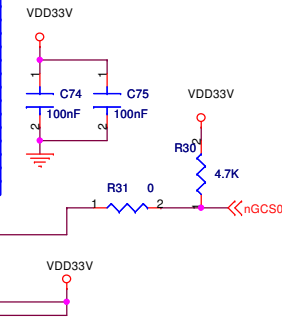
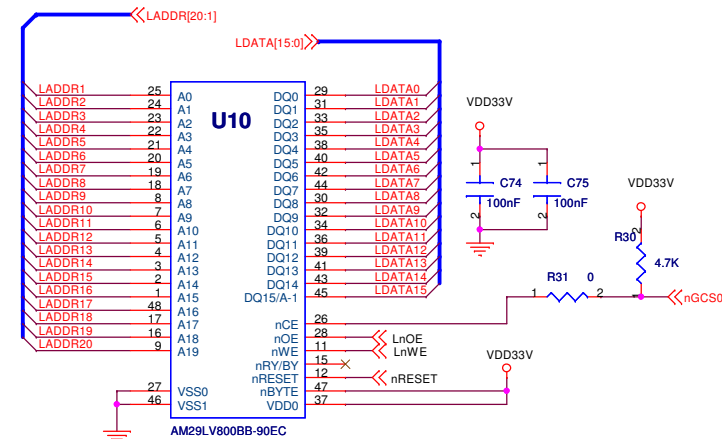


All these bypass Caps must be placed near each VDD33V pads of U7,U8

SAMSUNG NAND Flash, 64MB

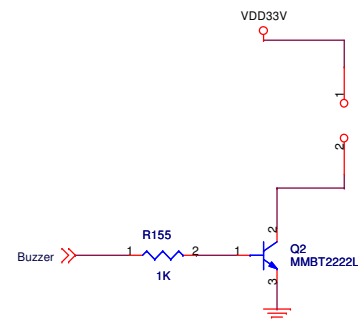
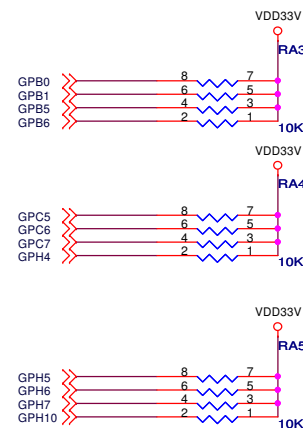
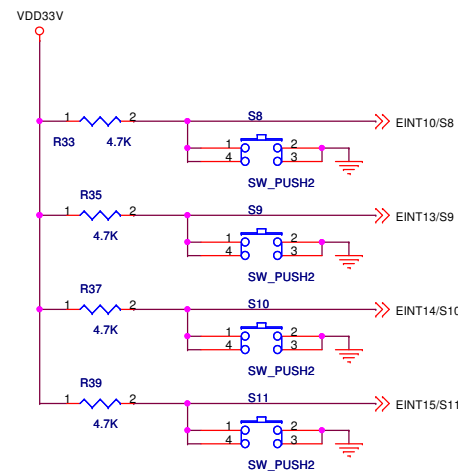
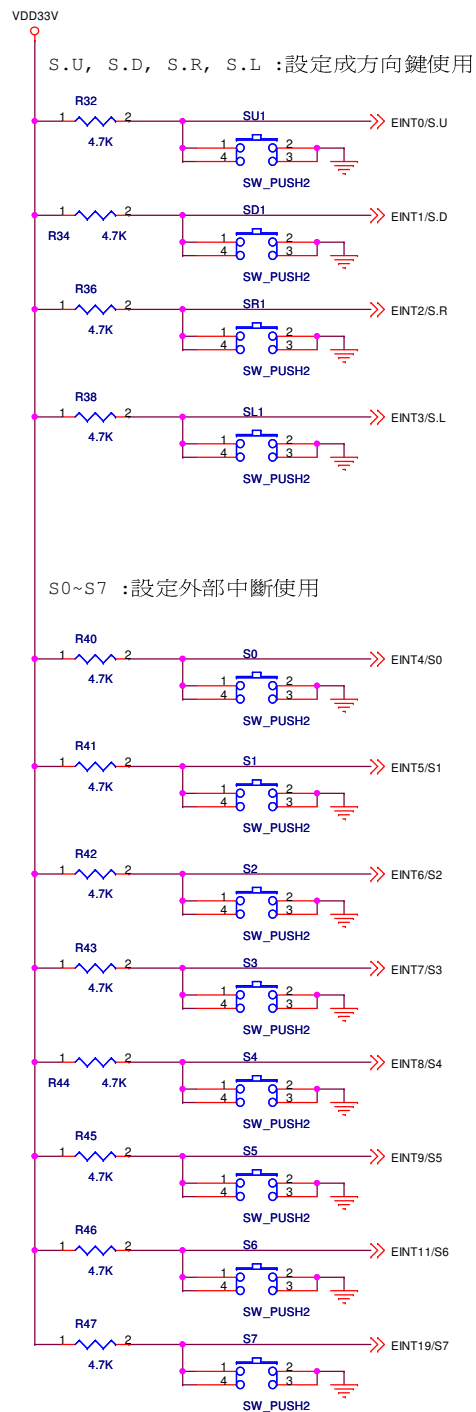


AMD NOR Flash, 1MB



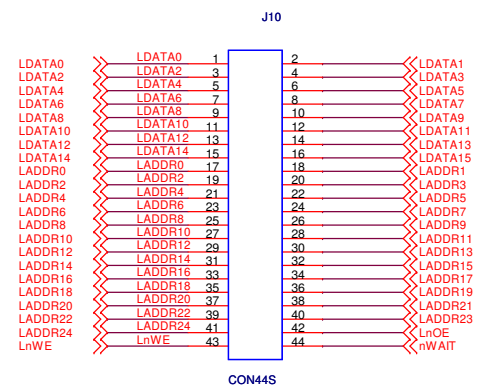
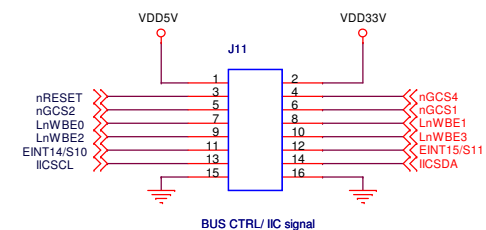
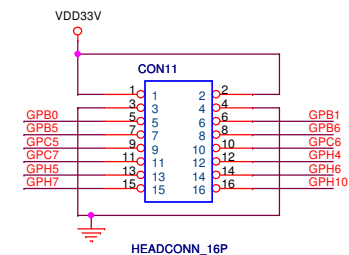
<http://www.jollen.org/>

Title	JK2410	Rev	1.3
Size	A3	Document Number	Memory area
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Add : CON16, Q2, R155

GPIOs for programming.



System Bus

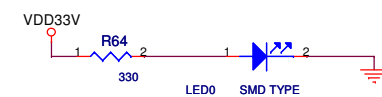
Add : J10, J11

<http://www.jollen.org/>

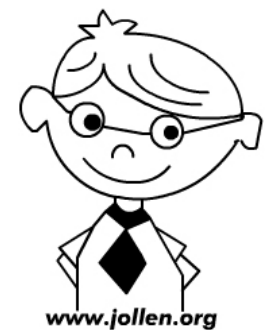
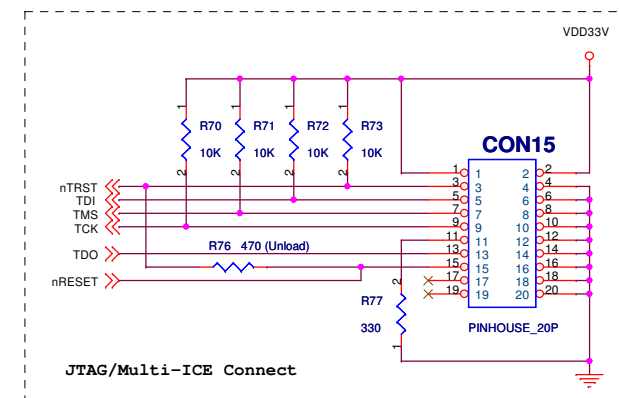
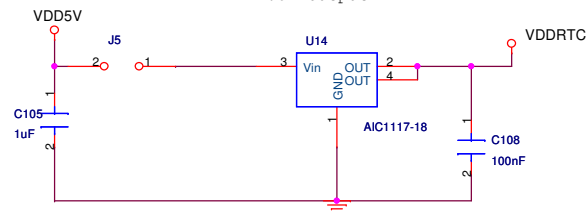
Title			JK2410
Size	Document Number		Rev
A3	GPIO, System Bus		1.3
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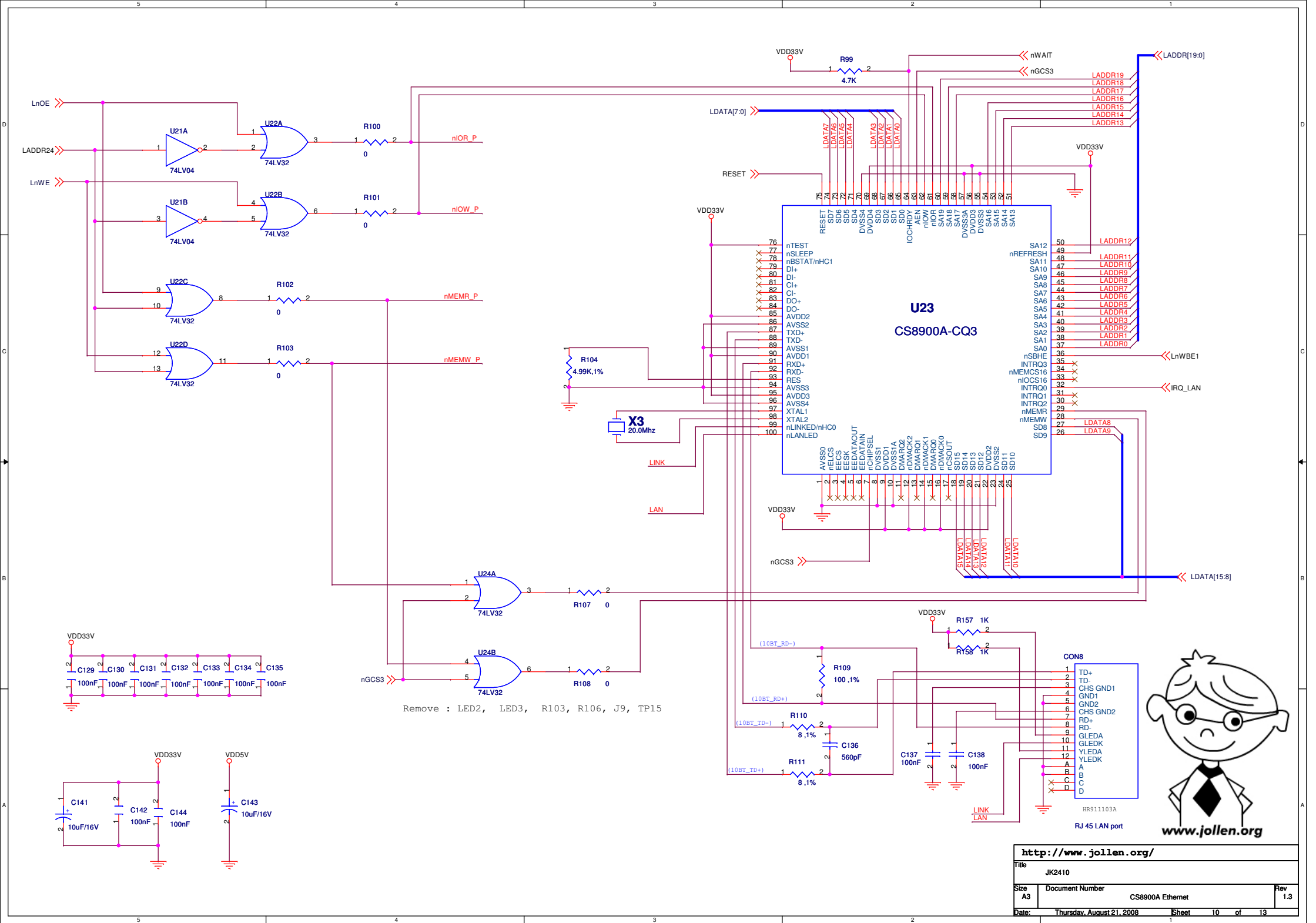


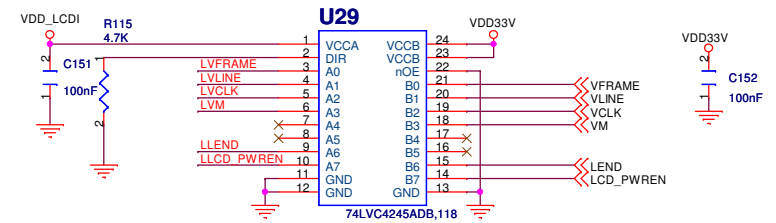
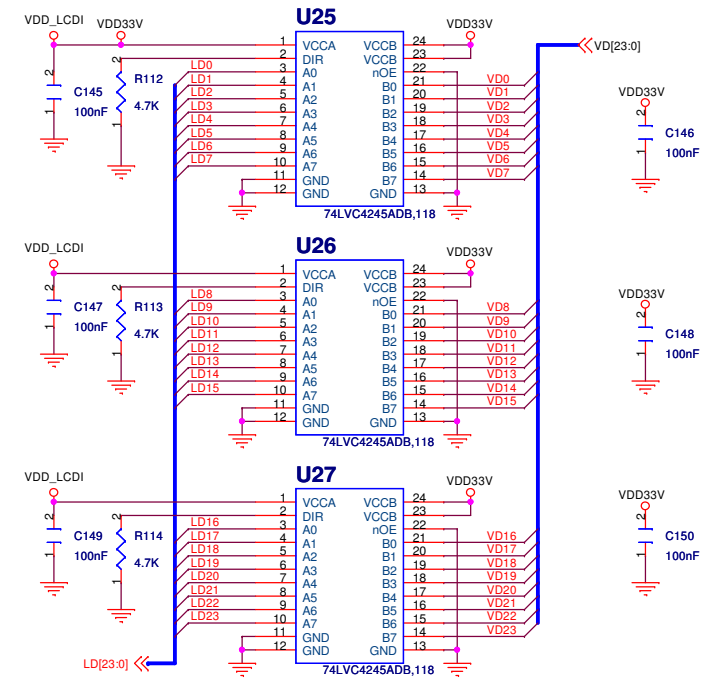
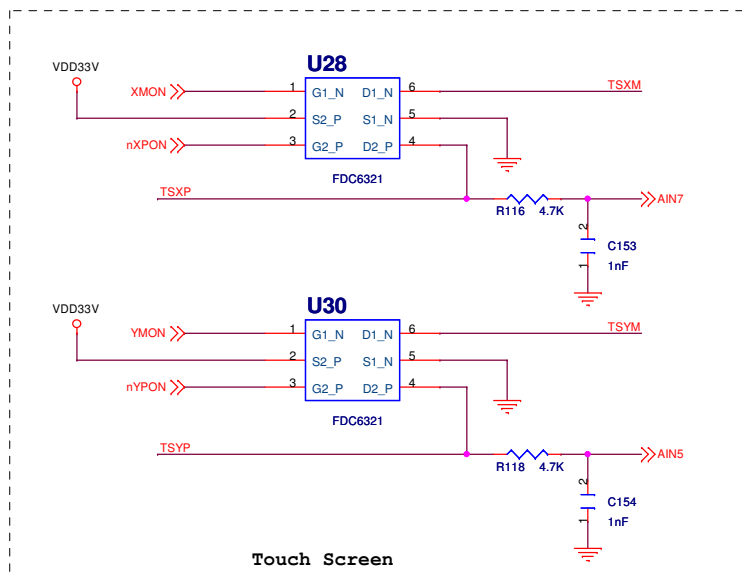
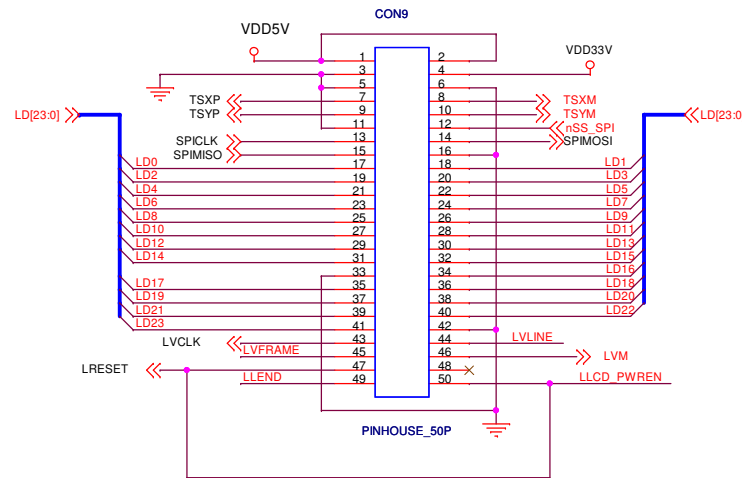
www.jollen.org



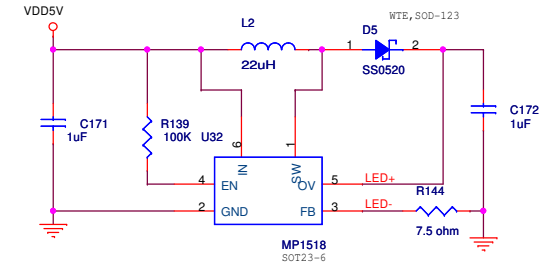
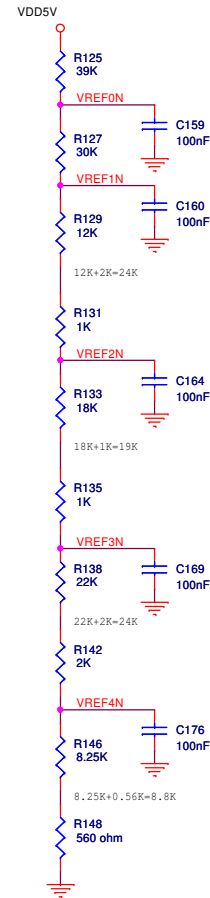
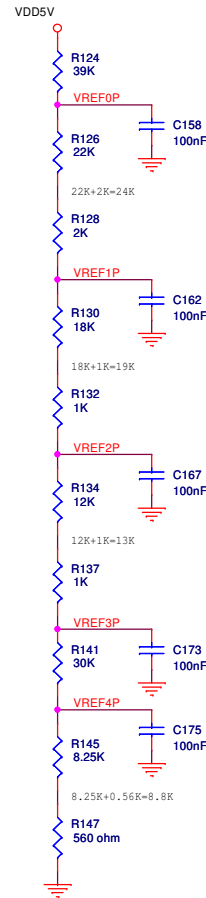
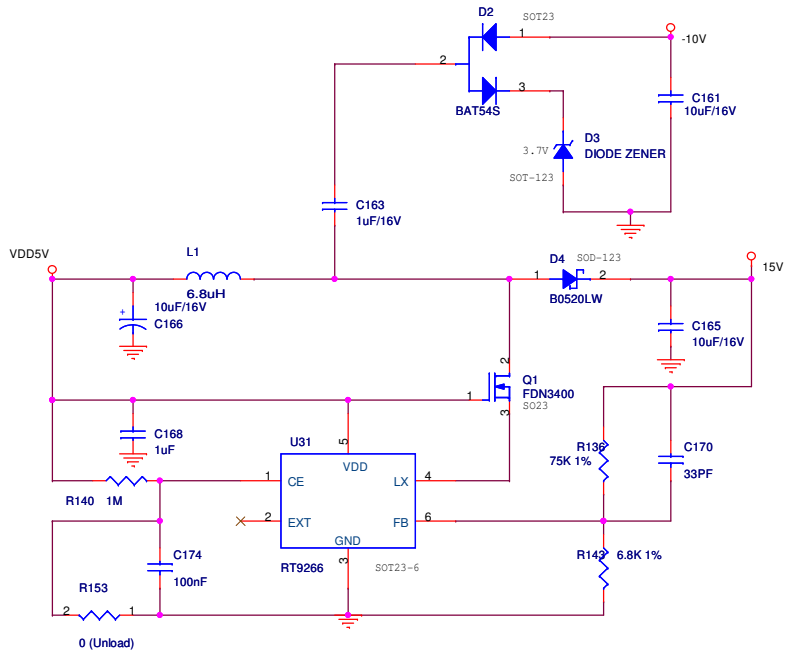
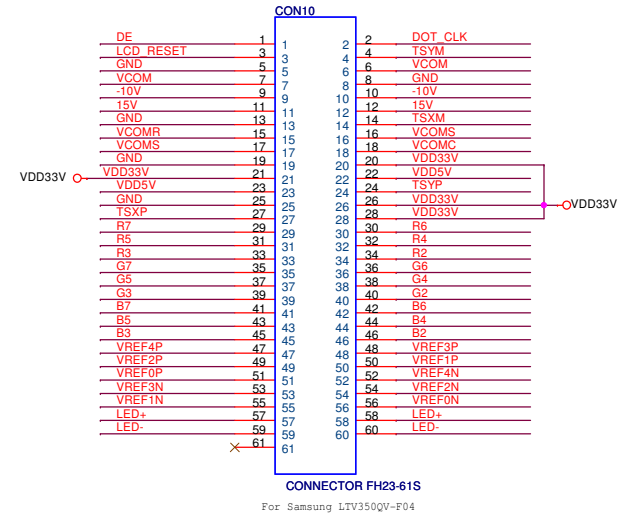
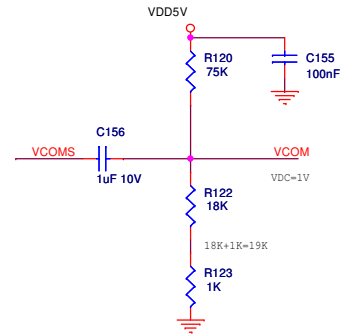
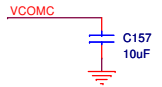
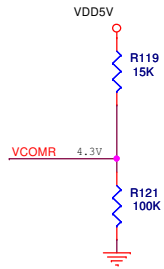
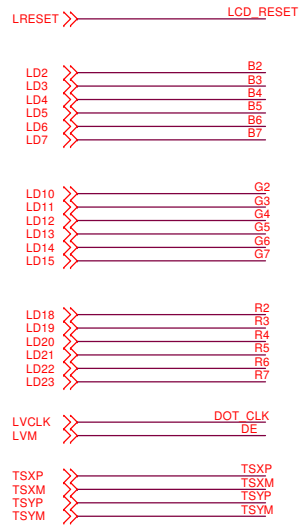
1.8V output







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A3	LCD Power & Gray Scale Voltage		
			1.2
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