Video Processing (VPP) Sample

Overview

VPP Sample works with Intel® Media SDK 2016 and Intel® Media Server Studio 2016 for Windows* Server.

It demonstrates how to use the Intel® Media SDK 2016 and Intel® Media Server Studio – SDK (hereinafter referred to as "SDK") API to create a simple console application that performs video processing of raw video sequences.

Features

VPP Sample supports the following video formats:

Format type	
input (uncompressed)	YV12, NV12, YUY2, RGB4 (RGB 32-bit)
output (uncompressed)	NV12

Hardware Requirements

See <install-folder>\Media Samples Guide.pdf.

Software Requirements

See <install-folder>\Media Samples Guide.pdf.

How to Build the Application

See <install-folder>\Media Samples Guide.pdf.

Running the Software

See <install-folder>\Media Samples Guide.pdf.

The executable file requires the following command-line switches to function properly:

-i <inputyuvfile></inputyuvfile>	Input uncompressed video file name and path
-o <outputyuvfile></outputyuvfile>	Output uncompressed video file name and path
-sw <width></width>	Width of source input YUV image
-dw <width></width>	Width of destination output YUV image
-sh <height></height>	Height of input YUV image
-dh <height></height>	Height of output YUV image

-scc <fourcc></fourcc>	Source Input FourCC
-lib <type></type>	Implementation of SDK (hw by default): hw = platform-specific (default) sw = software
-extapi	Use RunFrameVPPAsyncEx instead of RunFrameVPPAsync for PTIR (Premium Telecine Interlace Reverser) feature from SDK. For details please refer to media_server_studio_ptir_release_notes.pdf
-p guid	32-character hexadecimal guid for VPP plugin.
-d3d	Use Microsoft® Direct3D9® surfaces
-dcc <fourcc></fourcc>	Destination Output FourCC
-sf <framerate></framerate>	Frame Rate (frames/second) of input YUV image
-df <framerate></framerate>	Frame Rate (frames/second) of output YUV image
-scrX <x></x>	X coordinate of the source image ROI top left corner. 0 by default
-scrY <y></y>	Y coordinate of the source image ROI top left corner. 0 by default
-scrW <width></width>	Width of the source image ROI rectangle. Source width by default
-scrH <height></height>	Height of the source image ROI rectangle. Source height by default
-dcrX <x></x>	X coordinate of the destination image ROI top left corner. 0 by default
-dcrY <y></y>	Y coordinate of the destination image ROI top left corner. 0 by default
-dcrW <width></width>	Width of the destination image ROI rectangle. Destination width by default
-dcrH <height></height>	Height of the destination image ROI rectangle. Destination height by default
-denoise <level></level>	Enable Denoise algorithm. Level is the optional value from the interval [0; 100]
-spic	Source Picture Structure: • 0 = Interlaced top field first • 1 = Progressive • 2 = Interlaced bottom field first
-dpic	Destination Picture Structure: • 0 = Interlaced top field first • 1 = Progressive • 2 = Interlaced bottom field first
-va	Video Analysis (scene change detection): • 0 = off • 1 = on
-n	The number of frames to process
-detail <level></level>	Enable Detail Enhancement algorithm. Level is the optional value from the interval [0; 100]
-pa_hue <hue></hue>	The hue parameter for the destination video. Hue is the value from the interval [-180.0; 180.0]. 0.0 by default
-pa_sat <saturation></saturation>	The saturation parameter for the destination video. Saturation is the value from the interval [0.0; 10.0]. 1.0 by default
-pa_con <contrast></contrast>	The contrast parameter for the destination video. Contrast is the value from the interval [0.0; 10.0]. 1.0 by default

-pa_bri <brightness></brightness>	Brightness is the value from the interval [-100.0; 100.0]. 0.0 by default
-frc:interp	Enables interpolating frame rate conversion mode (for progressive NV12 streams only)

Below are examples of a command-line to execute **VPP Sample**:

```
sample_vpp -lib sw -sw 352 -sh 144 -scc rgb4 -dw 320 -dh 240 -dcc nv12
-denoise 32 -istab -i input.rgb -o output.nv12
```

```
$ sample vpp -lib hw -scc nv12 -dcc nv12 -composite
parameters.par -o out.yuv
The example of parameters.par:
primarystream=input 720x480.yuv
width=720
height=480
cropx=0
cropy=0
cropw=720
croph=480
dstx=0
dsty=0
dstw=720
dsth=480
stream=input 480x320.yuv
width=480
height=320
cropx=0
cropy=0
cropw=480
croph=320
dstx=100
dsty=100
dstw=320
dsth=240
```

Known Limitations

- Scene change detection is not supported (-vanalysis option not effective) with platform specific SDK libraries for Intel[®] HD Graphics 3000/2000 and later, also unsupported in software SDK libraries starting with API version 1.6.
- RGB3 (RGB 24-bit) input format is unsupported despite the fact that sample code and sample binary expose it as supported.
- YUY2 output format is unsupported despite the fact that sample code and sample binary expose it as supported.

Legal Information

INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS OTHERWISE AGREED IN WRITING BY INTEL, THE INTEL PRODUCTS ARE NOT DESIGNED NOR INTENDED FORANYAPPLICATION IN WHICH THE FAILURE OF THE INTEL PRODUCT COULD CREATE A SITUATION WHERE PERSONAL INJURY OR DEATH MAY OCCUR.

Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined." Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.

The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Contact your local Intel sales office or your distributor to obtain the latest specifications and before placing your product order.

Copies of documents which have an order number and are referenced in this document, or other Intel literature, may be obtained by calling 1-800-548-4725, or by visiting *Intel's Web Site*.

MPEG is an international standard for video compression/decompression promoted by ISO. Implementations of MPEG CODECs, or MPEG enabled platforms may require licenses from various entities, including Intel Corporation.

Intel, the Intel logo, Intel Core are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Optimization Notice

Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel.

Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice.

Notice revision #20110804

OpenCL and the OpenCL logo are trademarks of Apple Inc. used by permission by Khronos.

Copyright © 2016, Intel Corporation

^{*} Other names and brands may be claimed as the property of others.