Rev.	History
0.1	Original Release
0.2	Change GPIO71/GPIO74 IO Reset Value to High

Update By	Date
Peng Chen	2012/05/31
Peng Chen	2012/07/10

IO PWR Domain	Pin Num	Driving	IO Reset Value	Name	GPIO reset default mode	Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7
DVDD18_MIPIIO	D2	2/4/6/8mA	Low	PAD_RDP1	Aux. Mode 1	GPIO0	I0:EINT0_0x						
DVDD18_MIPIIO	C2	2/4/6/8mA	Low	PAD_RDN1	Aux. Mode 1	GPIO10	I0:EINT1_0x						
DVDD18_MIPIIO	E2	2/4/6/8mA	Low	PAD_RCP	Aux. Mode 1	GPIO2	I0:EINT2_0x						
DVDD18_MIPIIO	D1	2/4/6/8mA	Low	PAD_RCN	Aux. Mode 1	GPIO1	I0:EINT3_0x						
DVDD18_MIPIIO	F3	2/4/6/8mA	Low	PAD_RDP0	Aux. Mode 1	GPIO4	I0:EINT4_0x						
DVDD18_MIPIIO	E3	2/4/6/8mA	Low	PAD_RDN0	Aux. Mode 1	GPIO3	I0:EINT5_0x						
DVDD18_MIPIIO	F1	2/4/6/8mA	Low	PAD_TDP0	Aux. Mode 1	GPIO5	I0:EINT6_0x						
DVDD18_MIPIIO	F2	2/4/6/8mA	Low	PAD_TDN0	Aux. Mode 1	GPIO6	I0:EINT7_0x						
DVDD18_MIPIIO	G5	2/4/6/8mA	Low	PAD_TCP	Aux. Mode 1	GPIO7	I0:EINT8_0x						
DVDD18_MIPIIO	H4	2/4/6/8mA	Low	PAD_TCN	Aux. Mode 1	GPIO9	I0:EINT9_0x						
DVDD18_MIPIIO	G2	2/4/6/8mA	Low	PAD_TDP1	Aux. Mode 1	GPIO8	I0:EINT10_0x						
DVDD18_MIPIIO	G1	2/4/6/8mA	Low	PAD_TDN1	Aux. Mode 1	GPIO11	I0:EINT11_0x						
DVDD_LCD	J3	4/8/12/16mA	Low	LPA0	Aux. Mode 1	GPIO12	I0:LPA0_1x	I0:EINT16_1x	O:PWM4_1x				
DVDD_LCD	K4	4/8/12/16mA	High	LPCE1B	Aux. Mode 1	GPIO13	I0:LPCE1B_1x	I0:EINT14_1x	O:PWM2_1x				
DVDD_LCD	J2	4/8/12/16mA	Low	LPTE	Aux. Mode 1	GPIO14	I0:LPTE_1x	I0:EINT12_1x	O:PWM0_1x				
DVDD_LCD	K3	4/8/12/16mA	High	LWRB	Aux. Mode 1	GPIO15	I0:LWRB_1x	I0:EINT18_1x	O:PWM6_1x				
DVDD_LCD	K1	4/8/12/16mA	High	LPCE0B	Aux. Mode 1	GPIO16	I0:LPCE0B_1x	I0:EINT15_1x	O:PWM3_1x				
DVDD_LCD	K5	4/8/12/16mA	High	LRDB	Aux. Mode 1	GPIO17	I0:LRDB_1x	I0:EINT17_1x	O:PWM5_1x				
DVDD_LCD	K2	4/8/12/16mA	High	LRSTB	Aux. Mode 1	GPIO18	I0:LRSTB_1x	I0:EINT13_1x	O:PWM1_1x				
DVDD_LCD	M5	4/8/12/16mA	High	DPIHSYNC	Aux. Mode 1	GPIO19	I00:DPIHSYNC_1x	I0:EINT22_1x	I:CM2PCLK_1x				
DVDD_LCD	L2	4/8/12/16mA	High	DPIVSYNC	Aux. Mode 1	GPIO21	I00:DPIVSYNC_1x	I0:EINT21_1x	I:CM2VREF_1x				O:dbg_out0[17]
DVDD_LCD	M1	4/8/12/16mA	Low	DPIB5	Aux. Mode 1	GPIO22	I00:DPIR_PO_1x[5]	I0:EINT25_1x	I:CM2DAT5_1x				O:dbg_out0[10]
DVDD_LCD	N7	4/8/12/16mA	Low	DPIB2	Aux. Mode 1	GPIO23	I00:DPIB_PO_1x[2]	I0:EINT12_2x	O:I2S1_CK_1x				O:dbg_out0[21]
DVDD_LCD	M2	4/8/12/16mA	Low	DPIB5	Aux. Mode 1	GPIO24	I00:DPIB_PO_1x[5]	I0:EINT9_2x	I00:I2S0_CK_1x				O:dbg_out0[18]
DVDD_LCD	N2	4/8/12/16mA	Low	DPIB0	Aux. Mode 1	GPIO25	I00:DPIB_PO_1x[0]	I0:EINT14_2x	O:I2S1_WS_1x				O:dbg_out0[23]
DVDD_LCD	N8	4/8/12/16mA	Low	DPIR7	Aux. Mode 1	GPIO26	I00:DPIR_PO_1x[7]	I0:EINT23_1x	I:CM2DAT7_1x				O:dbg_out0[8]
DVDD_LCD	N1	4/8/12/16mA	Low	DPIR4	Aux. Mode 1	GPIO27	I00:DPIR_PO_1x[4]	I0:EINT26_1x	I:CM2DAT4_1x				O:dbg_out0[4]
DVDD_LCD	N3	4/8/12/16mA	Low	DPIG3	Aux. Mode 1	GPIO28	I00:DPIB_PO_1x[3]	I0:EINT3_2x					O:dbg_out0[11]
DVDD_LCD	N4	4/8/12/16mA	Low	DPIG4	Aux. Mode 1	GPIO29	I00:DPIB_PO_1x[4]	I0:EINT10_2x	I00:I2S0_DAT_1x				O:dbg_out0[19]
DVDD_LCD	N5	4/8/12/16mA	Low	DPIDE	Aux. Mode 1	GPIO30	I00:DPIDE_1x	I0:EINT20_1x	I:CM2HREF_1x				
DVDD_LCD	P2	4/8/12/16mA	Low	DPIB3	Aux. Mode 1	GPIO31	I00:DPIB_PO_1x[3]	I0:EINT11_2x	I00:I2S0_WS_1x				O:dbg_out0[20]
DVDD_LCD	P6	4/8/12/16mA	Low	DPIR6	Aux. Mode 1	GPIO32	I00:DPIR_PO_1x[6]	I0:EINT24_1x	I:CM2DAT6_1x				O:dbg_out0[9]
DVDD_LCD	P4	4/8/12/16mA	Low	DPIG2	Aux. Mode 1	GPIO33	I00:DPIG_PO_1x[2]	I0:EINT4_2x					O:dbg_out0[5]
DVDD_LCD	P5	4/8/12/16mA	Low	DPIG7	Aux. Mode 1	GPIO34	I00:DPIG_PO_1x[7]	I0:EINT31_1x	O:CM2RST_1x				O:dbg_out0[0]
DVDD_LCD	R1	4/8/12/16mA	Low	DPIR2	Aux. Mode 1	GPIO35	I00:DPIR_PO_1x[2]	I0:EINT28_1x	I:CM2DAT2_1x				O:dbg_out0[13]
DVDD_LCD	P8	4/8/12/16mA	Low	DPIB1	Aux. Mode 1	GPIO36	I00:DPIB_PO_1x[1]	I0:EINT13_2x	O:I2S1_DAT_1x				O:dbg_out0[22]
DVDD_LCD	R5	4/8/12/16mA	Low	DPIG0	Aux. Mode 1	GPIO37	I00:DPIG_PO_1x[0]	I0:EINT6_2x					O:dbg_out0[7]
DVDD_LCD	P3	4/8/12/16mA	Low	DPIG6	Aux. Mode 1	GPIO38	I00:DPIG_PO_1x[6]	I0:EINT0_2x	O:CM2PDN_1x				O:dbg_out0[1]
DVDD_LCD	P7	4/8/12/16mA	Low	DPIG4	Aux. Mode 1	GPIO39	I00:DPIG_PO_1x[4]	I0:EINT2_2x					O:dbg_out0[3]
DVDD_LCD	R2	4/8/12/16mA	Low	DPIR3	Aux. Mode 1	GPIO40	I00:DPIR_PO_1x[3]	I0:EINT27_1x	I:CM2DAT3_1x				O:dbg_out0[12]
DVDD_LCD	T4	4/8/12/16mA	High	DPICK	Aux. Mode 1	GPIO41	I00:DPICK_1x	I0:EINT19_1x	O:CM2MCLK_1x				O:dbg_out0[16]
DVDD_LCD	T1	4/8/12/16mA	Low	DPIB7	Aux. Mode 1	GPIO42	I00:DPIB_PO_1x[7]	I0:EINT7_2x					O:dbg_out0[6]
DVDD_LCD	T5	4/8/12/16mA	Low	DPIG1	Aux. Mode 1	GPIO43	I00:DPIG_PO_1x[1]	I0:EINT5_2x					O:dbg_out0[15]
DVDD_LCD	T2	4/8/12/16mA	Low	DPIR0	Aux. Mode 1	GPIO44	I00:DPIR_PO_1x[0]	I0:EINT30_1x	I:CM2DAT0_1x				O:dbg_out0[15]
DVDD_LCD	U4	4/8/12/16mA	Low	DPIR1	Aux. Mode 1	GPIO45	I00:DPIR_PO_1x[1]	I0:EINT29_1x	I:CM2DAT1_1x				O:dbg_out0[14]
DVDD_LCD	U5	4/8/12/16mA	Low	DPIG5	Aux. Mode 1	GPIO46	I00:DPIG_PO_1x[5]	I0:EINT1_2x					O:dbg_out0[2]
DVDD_LCD	V4	4/8/12/16mA	High	LSCE0B	Aux. Mode 1	GPIO47	O:LSCE0B_1x	I1:URXD3_1x	I00:SCL_2_1x	I0:EINT10_8x			O:TCTIRQ1_1x
DVDD_LCD	U3	4/8/12/16mA	Low	LSDI	Aux. Mode 1	GPIO48	I1:LSDI_1x	I0:EINT11_8x	I00:SDA_2_1x	I0:EINT13_8x			
DVDD_LCD	V3	4/8/12/16mA	Low	LSA0	Aux. Mode 1	GPIO49	O:LSA0_1x	O:MD_TXD1_1x	I1:MD_RXD1_7x	I00:SDA_0_1x	I0:EINT13_8x		O:TDTIRQ1_1x
DVDD_LCD	U2	4/8/12/16mA	High	LSCE1B	Aux. Mode 1	GPIO50	O:LSCE1B_1x	I1:URXD3_1x		I0:EINT19_8x			O:TEVTVAL_1x
DVDD_LCD	V5	4/8/12/16mA	High	LSCK	Aux. Mode 1	GPIO51	O:LSCK_1x	O:MD_TXD1_7x	I00:SCL_0_1x	I0:EINT20_8x			
DVDD_LCD	V2	4/8/12/16mA	Low	LSDA	Aux. Mode 1	GPIO52	I00:LSDA_1x	I1:URXD2_1x	I00:SCL_1_1x	I0:EINT23_8x			O:TCTIRQ2_1x
DVDD_NML1	V1	2/4/6/8mA	Low	I2S0_DAT	Aux. Mode 1	GPIO53	I00:I2S0_DAT_2x		O:CLKM4_1x	O:I2S1_DAT_4x			O:dbg_out0[24]
DVDD_NML1	Y6	2/4/6/8mA	High	I2S0_WS	Aux. Mode 1	GPIO54	I00:I2S0_WS_2x		O:CLKM5_1x	O:I2S1_WS_4x			O:dbg_out0[25]
DVDD_NML1	W2	2/4/6/8mA	Low	I2S0_CK	Aux. Mode 1	GPIO55	I00:I2S0_CK_2x	I0:EINT15_2x	O:CLKM3_1x	O:I2S1_CK_4x			O:dbg_out0[26]
DVDD_NML1	W5	2/4/6/8mA	High	I2S1_CK	Aux. Mode 1	GPIO56	O:I2S1_CK_2x	I0:EINT1_3x	I0:IRDA_RXD_1x	I00:I2S0_CK_6x			O:dbg_out0[27]
DVDD_NML1	W1	2/4/6/8mA	High	I2S1_WS	Aux. Mode 1	GPIO57	O:I2S1_WS_2x	I0:EINT0_3x	O:IRDA_PDN_1x	I00:I2S0_WS_6x			O:dbg_out0[28]
DVDD_NML1	Y5	2/4/6/8mA	Low	I2S1_DAT	Aux. Mode 1	GPIO58	O:I2S1_DAT_2x	I0:IRDA_TXD_1x	I1:MD_RXD2_1x				O:dbg_out0[29]
DVDD_NML1	Y2	2/4/6/8mA	Low	DAIRSTB	Aux. Mode 1	GPIO59	I0:DAIRSTB	I0:EINT8_3x	O:PWM6_2x				
DVDD_NML1	Y4	2/4/6/8mA	Low	EINT8	Aux. Mode 1	GPIO60	I0:EINT8_1x	I0:EINT5_3x	O:PWM3_2x	O:watchdog_rstb	O:DSP_GPO3		
DVDD_NML1	AA1	2/4/6/8mA	Low	EINT9	Aux. Mode 1	GPIO61	I0:EINT9_1x	I0:EINT6_3x	O:PWM4_2x	O:gps_sync_4x	O:watchdog_rstb		
DVDD_NML1	AA2	2/4/6/8mA	Low	DAIPCMOUT	Aux. Mode 1	GPIO62	O:DAIPCMOUT	I0:EINT7_3x	O:PWM0_2x	I00:I2S0_DAT_3x			
DVDD_NML1	AA3	2/4/6/8mA	High	DAICLK	Aux. Mode 1	GPIO63	O:DAICLK	I0:EINT3_3x	O:PWM1_2x	I00:I2S0_CK_3x			
DVDD_NML1	AA4	2/4/6/8mA	Low	DAIPCMIN	Aux. Mode 1	GPIO64	I0:DAIPCMIN	I0:EINT4_3x	O:PWM2_2x				
DVDD_NML1	AB1	2/4/6/8mA	Low	BTSYNC	Aux. Mode 1	GPIO65	O:BTSYNC	I0:EINT7_3x	O:PWM5_2x	I00:I2S0_WS_3x			
DVDD_NML2	AB4	4/8/12/16mA	Low	PWM2	Aux. Mode 1	GPIO66	O:PWM2_1x	I0:EINT10_3x	I0:IRDA_RXD_2x	I1:URXD3_2x			O:dbg_out1[17]	O:pwm1_3d_2x_o
DVDD_NML2	AB2	4/8/12/16mA	Low	PWM1	Aux. Mode 1	GPIO67	O:PWM1_6x	I0:EINT9_3x	I0:IRDA_PDN_2x	O:MD_TXD2_2x	O:usb_drvvbus_2x		O:dbg_out1[16]	O:pwm1_3d_1x_o
DVDD_NML2	AB3	4/8/12/16mA	Low	PWM3	Aux. Mode 1	GPIO68	O:PWM3_6x	I0:EINT11_3x	I0:IRDA_TXD_2x	O:UTXD3_3x			O:dbg_out1[18]	O:pwm1_3d_3x_o
DVDD_NML2	AA5	2/4/6/8mA	Low	EINT3	Aux. Mode 1	GPIO69	I0:EINT3_1x	O:PWM3_4x	I1:MD_RXD1_2x	O:CLKM2_2x	O:SPI_MO_1x		I0:dbg_in[9]
DVDD_NML2	AC2	2/4/6/8mA	Low	EINT0	Aux. Mode 1	GPIO70	I0:EINT0_1x	O:PWM0_4x	I1:URXD2_6x	O:CLKM5_2x	O:SPI_CSN_1x		I0:dbg_in[6]
DVDD_NML2	AD1	4/8/12/16mA	High	UTXD3	Aux. Mode 1	GPIO71	O:UTXD3_8x	I0:EINT17_3x	O:PWM6_3x	I00:URXD3_EMU_B_1x			I0:dbg_in[3]
DVDD_NML2	AD4	2/4/6/8mA	Low	EINT4	Aux. Mode 1	GPIO72	I0:EINT4_1x	O:MD_TXD2_3x	O:CLKM1_2x	O:gps_sync_2x			I0:dbg_in[10]
DVDD_NML2	AB5	2/4/6/8mA	Low	EINT7	Aux. Mode 1	GPIO73	I0:EINT7_1x	O:PWM4_4x	O:MD_TXD_3x	I00:I2S0_WS_4x	O:usb_drvvbus_1x		I0:dbg_in[13]
DVDD_NML2	AE1	4/8/12/16mA	Low	UTXD2	Aux. Mode 1	GPIO74	O:UTXD2_8x	I0:EINT13_3x	I0:URXD2_5x	O:PWM2_3x	O:DSP_JTCK_1x		I0:dbg_in[15]
DVDD_NML2	AA6	2/4/6/8mA	Low	EINT6	Aux. Mode 1	GPIO75	I0:EINT6_1x	O:PWM6_4x	I0:IRDA_RXD_3x	I00:I2S0_DAT_4x	I1:MD_RXD2_3x		I0:dbg_in[12]
DVDD_NML2	AD2	2/4/6/8mA	Low	EINT1	Aux. Mode 1	GPIO76	I0:EINT1_1x	O:PWM1_4x	I0:UTXD2_7x	O:CLKM4_2x	O:SPI_CLK_1x		I0:dbg_in[7]
DVDD_NML2	AC5	4/8/12/16mA	High:Note1	URXD2	Aux. Mode 1	GPIO77	I1:URXD2_4x	I0:EINT12_3x	O:UTXD2_5x	O:PWM1_3x	O:DSP_JTRST_B_1x		I0:dbg_in[14]
DVDD_NML2	AE2	2/4/6/8mA	Low	EINT5	Aux. Mode 1	GPIO78	I0:EINT5_1x	O:PWM5_4x	I0:IRDA_PDN_3x	I00:I2S0_CK_4x	O:usb_drvvbus_3x		I0:dbg_in[11]
DVDD_NML2	AD5	4/8/12/16mA	Low	URXD3	Aux. Mode 1	GPIO79	I1:URXD3_8x	I0:EINT16_3x	O:UTXD3_4x	O:PWM6_3x	O:DSP_JTDO_1x		I0:dbg_in[2]
DVDD_NML2	AE3	4/8/12/16mA	High	UCTS2	Aux. Mode 1	GPIO80	I1:UCTS2_4x	I0:EINT14_3x	O:MD_TXD2_3x	O:PWM3_3x	I1:DSP_JTMS_1x		
DVDD_NML2	AE4	2/4/6/8mA	Low	EINT2	Aux. Mode 1	GPIO81	I0:EINT2_1x	O:PWM2_4x	O:MD_TXD1_2x	O:CLKM3_2x	O:SPI_MI_1x		I0:dbg_in[8]
DVDD_NML2	AF2	2/4/6/8mA	Low	URTS2	Aux. Mode 1	GPIO82	O:URTS2_4x	I0:EINT15_3x	I1:MD_RXD2_2x	O:PWM4_3x	I1:DSP_JTDI_1x		
DVDD_NML3	AF4	4/8/12/16mA	High	SPI_CSN	Aux. Mode 1	GPIO83	O:SPI_CSN_2x	I0:EINT18_3x	I0:IRDA_PDN_4x	O:SPI_CSN_3x			
DVDD_NML3	AG3	4/8/12/16mA	Low	SPI_MI	Aux. Mode 1	GPIO84	O:SPI_MI_2x	I0:EINT20_3x	I0:IRDA_TXD_4x	O:SPI_MO_3x			
DVDD_NML3	AG2	4/8/12/16mA	Low	SPI_MO	Aux. Mode 1	GPIO85	O:SPI_MO_2x	I0:EINT21_3x	O:CLKM5_3x	I0:SPI_MI_3x			O:dbg_out0[30]
DVDD_NML3	AH1	4/8/12/16mA	Low	SPI_CLK	Aux. Mode 1	GPIO86	O:SPI_CLK_2x	I0:EINT19_3x	I0:IRDA_RXD_4x	O:SPI_CLK_3x			
DVDD_NML3	AG1	4/8/12/16mA	Low	SCL_0	Aux. Mode 1	GPIO87	I00:SCL_0_2x	I0:EINT22_3x	O:CLKM4_3x				
DVDD_NML3	AH2	4/8/12/16mA	High	SDA_0	Aux. Mode 1	GPIO88	I00:SDA_0_2x	I0:EINT23_3x	O:CLKM3_3x				I0:dbg_in[1]
DVDD_MC3	AG4	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT2	Aux. Mode 1	GPIO89	I00:MSDC3_DAT2	I0:EINT27_3x	I1:URXD3_6x	O:CLKM1_4x			O:dbg_out0[4]
DVDD_MC3	AH3	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT1	Aux. Mode 1	GPIO90	I00:MSDC3_DAT1	I0:EINT28_3x	I1:MD_RXD2_4x	O:CLKM2_4x	O:MD_TXD2_5x		O:dbg_out0[11]
DVDD_MC3	AJ3	16/20/24/28/40/44/48/52mA	High	MSDC3_CMD	Aux. Mode 1	GPIO91	I00:MSDC3_CMD	I0:EINT25_3x	I1:MD_RXD1_3x	O:CLKM4_4x	O:MD_TXD1_4x		O:dbg_out0[2]
DVDD_MC3	AH4	16/20/24/28/40/44/48/52mA	High	MSDC3_CLK	Aux. Mode 1	GPIO92	I00:MSDC3_CLK	I0:EINT24_3x	O:MD_TXD1_3x	O:CLKM5_4x			O:dbg_out0[1]
DVDD_MC3	AK3	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT3	Aux. Mode 1	GPIO93	I00:MSDC3_DAT3	I0:EINT29_3x	O:UTXD3_5x	O:CLKM0_4x	I1:URXD3_6x		O:dbg_out0[3]
DVDD_MC3	AJ4	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT0	Aux. Mode 1	GPIO94	I00:MSDC3_DAT0	I0:EINT26_3x	O:MD_TXD2_4x	O:CLKM3_4x	I1:MD_RXD2_5x		I0:dbg_in[29]
DVDD_NML4	AF6	2/4/6/8mA	Low	KP_ROW2	Aux. Mode 0	GPIO95	I01:KP_ROW2	I0:EINT8_4x	O:CLKM2_5x	I00:I2S1_DAT_3x			I0:dbg_in[16]
DVDD_NML4	AJ5	2/4/6/8mA	Low	KP_COL7	Aux. Mode 0	GPIO96	I1:KP_COL7	I0:EINT5_4x	O:CLKM5_5x	I00:I2S0_DAT_5x			I0:dbg_in[30]
DVDD_NML4	AC10	2/4/6/8mA	Low	KP_ROW1	Aux. Mode 0	GPIO97	I01:KP_ROW1	I0:EINT5_4x	O:CLKM3_5x	I00:I2S1_CK_3x			I0:dbg_in[30]
DVDD_NML4	AG6	2/4/6/8mA	Low	KP_ROW0	Aux. Mode 0	GPIO98	I01:KP_ROW0	I0:EINT6_4x	O:CLKM4_5x	I00:I2S0_WS_5x			I0:dbg_in[31]
DVDD_NML4	AF7	2/4/6/8mA	Low	KP_ROW3	Aux. Mode 0	GPIO99	I01:KP_ROW3	I0:EINT9_4x	O:CLKM1_5x	I00:I2S1_WS_3x			I0:dbg_in[28]
DVDD_NML4	AG7	2/4/6/8mA	Low	KP_COL5	Aux. Mode 0	GPIO100	I1:KP_COL5	I0:EINT7_4x	O:PWM5_5x	I0:DSP_JTCK_2x			I0:dbg_in[18]
DVDD_NML4	AF8	2/4/6/8mA	Low	KP_COL3	Aux. Mode 0	GPIO101	I1:KP_COL3	I0:EINT1_4x	O:PWM3_5x	I0:DSP_JTRST_B_2x			I0:dbg_in[20]
DVDD_NML4	AH7	2/4/6/8mA	Low	KP_COL4	Aux. Mode 0	GPIO102	I1:KP_COL4	I0:EINT2_4x	O:PWM4_5x	I0:DSP_JEMU_B_2x			I0:dbg_in[19]
DVDD_NML4	AF9	2/4/6/8mA	High:Note1	KP_COL0	Aux. Mode 0	GPIO103	I1:KP_COL0	I0:EINT30_3x	O:PWM0_5x	I1:DSP_JTDI_2x			I0:dbg_in[23]
DVDD_NML4	AJ6	2/4/6/8mA	Low	KP_ROW4	Aux. Mode 0	GPIO104	I01:KP_ROW4	I0:EINT10_4x	O:CLKM0_5x	I00:SCL_0_3x			I0:dbg_in[27]
DVDD_NML4	AB11	2/4/6/8mA	Low	KP_COL2	Aux. Mode 0	GPIO105	I1:KP_COL2	I0:EINT0_4x	O:PWM2_5x	I0:DSP_JTDO_2x			I0:dbg_in[21]
DVDD_NML4	AK6	2/4/6/8mA	Low	KP_ROW5	Aux. Mode 0	GPIO106	I01:KP_ROW5	I0:EINT11_4x	I0:IRDA_PDN_5x	I00:SDA_0_3x			I0:dbg_in[26]
DVDD_NML4	AK7	2/4/6/8mA	Low	KP_ROW7	Aux. Mode 0	GPIO107	I01:KP_ROW7	I0:EINT13_4x	O:IRDA_TXD_6x	I00:SDA_1_3x			I0:dbg_in[24]
DVDD_NML4	AJ7	2/4/6/8mA	High	KP_COL1	Aux. Mode 0	GPIO108	I1:KP_COL1	I0:EINT31_3x	O:PWM1_5x	I1:DSP_JTMS_2x			I0:dbg_in[22]
DVDD_NML4	AG8	2/4/6/8mA	Low	KP_COL6	Aux. Mode 0	GPIO109	I1:KP_COL6	I0:EINT4_4x	O:PWM6_5x	I00:I2S0_CK_5x			I0:dbg_in[17]
DVDD_NML4	AH8	2/4/6/8mA	Low	KP_ROW6	Aux. Mode 0	GPIO110	I01:KP_ROW6	I0:EINT12_4x	I0:IRDA_RXD_6x	I00:SCL_1_3x	I0:ext_frame_sync_2x		I0:dbg_in[25]
DVDD_BPI	AF10	2/4/6/8mA	Low	BPI_BUS3	Aux. Mode 1	GPIO111	O:DUAL_BPI_BUS3	I0:EINT17_5x					O:dbg_out1[22]
DVDD_BPI	AD11	2/4/6/8mA	Low	TDD_AFC_SW	Aux. Mode 1	GPIO112	O:TDD_AFC_SW						O:dbg_out1[20]
DVDD_BPI	AJ9	2/4/6/8mA	Low	BPI_BUS1	Aux. Mode 1	GPIO113	O:DUAL_BPI_BUS1	I0:EINT29_4x	O:IRDA_TXD_7x	I00:SDA_1_4x			O:dbg_out1[23]
DVDD_BPI	AG10	2/4/6/8mA	Low	BPI_BUS4	Aux. Mode 1	GPIO114	O:DUAL_BPI_BUS4	I0:EINT18_5x					O:dbg_out1[19]
DVDD_BPI	AK9	2/4/6/8mA	Low	BPI_BUS0	Aux. Mode 1	GPIO115	O:DUAL_BPI_BUS0	I0:EINT14_5x					
DVDD_BPI	AJ10	2/4/6/8mA	Low	BPI_BUS5	Aux. Mode 1	GPIO116	O:DUAL_BPI_BUS5	I0:EINT19_5x					O:dbg_out1[24]
DVDD_BPI	AJ8	2/4/6/8mA	Low	BPI_BUS2	Aux. Mode 1	GPIO117	O:DUAL_BPI_BUS2	I0:EINT16_5x					O:dbg_out1[21]
DVDD_BPI	AF11	4/8/12/16mA	Low	BPI_BUS8	Aux. Mode 1	GPIO118	O:DUAL_BPI_BUS8	I0:EINT0_5x	O:gps_sync_1x				O:dbg_out1[27]
DVDD_BPI	AK10	2/4/6/8mA	Low	BPI_BUS9	Aux. Mode 1	GPIO119	O:DUAL_BPI_BUS9	I0:EINT22_5x					O:dbg_out1[28]
DVDD_BPI	AJ11	2/4/6/8mA	Low	BPI_BUS7	Aux. Mode 1	GPIO120	O:DUAL_BPI_BUS7	I0:EINT21_4x					O:dbg_out1[26]
DVDD_BPI	AH11	2/4/6/8mA	Low	BPI_BUS6	Aux. Mode 1	GPIO121	O:DUAL_BPI_BUS6	I0:EINT20_4x					O:dbg_out1[25]
DVDD_BPI	AF12	2/4/6/8mA	Low	TDD_TIMINGSYNC	Aux. Mode 1	GPIO122	O:TDD_TIMINGSYNC	I0:EINT28_4x	I0:IRDA_RXD_7x	I00:SCL_1_4x			
DVDD_BPI	AG11	2/4/6/8mA	Low	BPI_BUS10	Aux. Mode 1	GPIO123	O:DUAL_BPI_BUS10	I0:EINT23_4x					O:dbg_out1[29]
DVDD_BPI	AH12	2/4/6/8mA	Low	BPI_BUS14	Aux. Mode 1	GPIO124	O:DUAL_BPI_BUS14	I0:EINT27_4x	O:IRDA_PDN_7x	I00:SDA_0_4x			
DVDD_BPI	AK12	2/4/6/8mA	Low	BPI_BUS13	Aux. Mode 1	GPIO125	O:DUAL_BPI_BUS13	I0:EINT26_4x	I0:ext_frame_sync_1x	I00:SCL_0_4x			
DVDD_BPI	AJ12	2/4/6/8mA	Low	BPI_BUS11	Aux. Mode 1	GPIO126	O:DUAL_BPI_BUS11	I0:EINT24_4x					O:dbg_out1[30]
DVDD_BPI	AG12	2/4/6/8mA	Low	BPI_BUS12	Aux. Mode 1	GPIO127	O:DUAL_BPI_BUS12	I0:EINT25_4x					O:dbg_out1[31]
DVDD_BPI	AJ13	2/4/6/8mA	Low	VM1	Aux. Mode 1	GPIO128	O:VM1	I0:EINT31_4x					
DVDD_BPI	AK13	2/4/6/8mA	Low	VM0	Aux. Mode 1	GPIO129	O:VM0	I0:EINT30_4x					
DVDD_BSI	AC14	4/8/12/16mA	Low	BSI0_CS1	Aux. Mode 1	GPIO130	O:BSI0_CS1	I0:EINT4_5x					
DVDD_BSI	AJ14	4/8/12/16mA	Low	BSI1_DATA	Aux. Mode 1	GPIO131	I00:BSI1_DATA	I0:EINT6_5x					
DVDD_BSI	AG14	4/8/12/16mA	Low	BSI1_CS1	Aux. Mode 1	GPIO132	I00:BSI1_DATA1_1x	I0:EINT8_5x	O:BSI1_CS1				
DVDD_BSI	AH15	4/8/12/16mA	Low	BSI1_CS0	Aux. Mode 1	GPIO133	O:BSI1_CS0	I0:EINT7_5x					
DVDD_BSI	AG15	4/8/12/16mA	Low	BSI1_CLK	Aux. Mode 1	GPIO134	O:BSI1_CLK	I0:EINT5_5x					
DVDD_BSI	AF14	4/8/12/16mA	Low	BSI0_DATA	Aux. Mode 1	GPIO135	I00:BSI0_DATA	I0:EINT3_5x					

IO PWR Domain	Pin Num	Driving	IO Reset Value	Name	GPIO reset default mode	Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7
DVDD_BSI	AB14	4/8/12/16mA	Low	BSI0_CS0	Aux. Mode 1	GPIO136	O:BSI0_CS0	IO:EINT1_5x					
DVDD_BSI	AA13	4/8/12/16mA	Low	BSI0_CLK	Aux. Mode 1	GPIO137	O:BSI0_CLK	IO:EINT2_5x					
AVDD30_VSIM2	AD30		Low	PAD_SIO2	Aux. Mode 1	GPIO138	O:CLKM2_8x	IO:EINT9_5x	O:PWM0_6x	O:IRDA_PDN_8x			
AVDD30_VSIM2	AD29		Low	PAD_SRST2	Aux. Mode 1	GPIO139	O:CLKM1_8x	IO:EINT10_5x	O:PWM5_6x	I0:IRDA_RXD_8x			
AVDD30_VSIM2	AD28		Low	PAD_SCLK2	Aux. Mode 1	GPIO140	O:CLKM0_8x	IO:EINT11_5x	O:PWM6_6x	O:IRDA_TXD_8x			
DVDD_NML5	Y29	2/4/6/8mA	Low	IDDIG	Aux. Mode 1	GPIO141	I1:IDDIG	IO:EINT12_5x					
DVDD_NML5	Y26	2/4/6/8mA	High:Note1	SDA_2	Aux. Mode 1	GPIO142	IO0:SDA_2_2x	IO:EINT14_5x					
DVDD_NML5	W29	2/4/6/8mA	Low	PACTRL2	Aux. Mode 1	GPIO143	O:PACTRL2	IO:EINT19_5x				O:dbg_out1[15]	
DVDD_NML5	W27	2/4/6/8mA	Low	PMUCTRL0	Aux. Mode 1	GPIO144	O:PMUCTRL0	IO:EINT15_5x				O:dbg_out1[11]	
DVDD_NML5	W26	2/4/6/8mA	High:Note1	SCL_2	Aux. Mode 1	GPIO145	IO0:SCL_2_2x	IO:EINT13_5x					
DVDD_NML5	U29	2/4/6/8mA	Low	SRCLKENAI	Aux. Mode 1	GPIO231	I0:SRCLKENAI						
DVDD_NML5	V28	2/4/6/8mA	Low	PACTRL1	Aux. Mode 1	GPIO146	O:PACTRL1	IO:EINT18_5x				O:dbg_out1[14]	
DVDD_NML5	V27	2/4/6/8mA	Low	PMUCTRL1	Aux. Mode 1	GPIO147	O:PMUCTRL1	IO:EINT16_5x				O:dbg_out1[12]	
DVDD_NML5	V30	2/4/6/8mA	Low	PACTRL0	Aux. Mode 1	GPIO148	O:PACTRL0	IO:EINT17_5x				O:dbg_out1[13]	
DVDD_NML6	U26	4/8/12/16mA	Low	SWCLKTCK	Aux. Mode 1	GPIO149	IO0:SWCLKTCK						
DVDD_NML6	U28	4/8/12/16mA	Low	SWDIOTMS	Aux. Mode 1	GPIO150	IO0:SWDIOTMS						
DVDD_NML6	T26	4/8/12/16mA	High	JTCK	Aux. Mode 1	GPIO151	IO:JTCK	IO:DSP_JTCK_3x					
DVDD_NML6	T30	4/8/12/16mA	Low	JTDO	Aux. Mode 1	GPIO152	O:JTDO	O:DSP_JTDO_3x					
DVDD_NML6	R30	4/8/12/16mA	Low	JTRST_B	Aux. Mode 1	GPIO153	IO:JTRST_B	IO:DSP_JTRST_B_3x					
DVDD_NML6	T29	4/8/12/16mA	High	JTDI	Aux. Mode 1	GPIO154	I1:JTDI	I1:DSP_JTDI_3x					
DVDD_NML6	R29	4/8/12/16mA	Low	JRTCK	Aux. Mode 1	GPIO155	O:JRTCK	IO0:DSP_EMU_B_3x					
DVDD_NML6	U27	4/8/12/16mA	High	JTMS	Aux. Mode 1	GPIO156	IO1:JTMS	I1:DSP_JTMS_3x					
DVDD_NML6	R27	4/8/12/16mA	High:Note1	UTXD1	Aux. Mode 1	GPIO157	O:UTXD1_1x	IO:EINT20_5x	I1:URXD1_2x	I1:MD_RXD1_5x	I1:MD_TXD1_8x		
DVDD_NML6	P28	4/8/12/16mA	High:Note1	UTXD4	Aux. Mode 1	GPIO158	O:UTXD4_1x	IO:EINT24_5x	I1:URXD4_2x	I1:MD_RXD2_6x	O:MD_TXD2_7x	IO:dbg_in[5]
DVDD_NML6	R26	4/8/12/16mA	High	URXD4	Aux. Mode 1	GPIO159	I1:URXD4_1x	IO:EINT23_5x	O:UTXD4_2x	O:MD_TXD2_6x	I1:MD_RXD2_7x	IO:dbg_in[4]
DVDD_NML6	P26	4/8/12/16mA	High	URXD1	Aux. Mode 1	GPIO160	I1:URXD1_1x	IO:EINT22_5x	O:UTXD1_2x	O:MD_TXD1_5x	I1:MD_RXD1_8x		
DVDD_NML6	P27	4/8/12/16mA	High:Note1	URTS1	Aux. Mode 1	GPIO161	O:URTS1_1x	IO:EINT22_5x	I1:MD_RXD1_6x				
DVDD_NML6	R25	4/8/12/16mA	Low	UCTS1	Aux. Mode 1	GPIO162	I1:UCTS1_1x	IO:EINT21_5x	O:MD_TXD1_6x				
DVDD_NML6	N29	4/8/12/16mA	Low	MSDC1_INSI	Aux. Mode 1	GPIO163	IO:MSDC1_INSI	IO:EINT25_5x					
DVDD_MC0	N30	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT6	Aux. Mode 1	GPIO164	IO0:MSDC0_DAT6	IO:EINT2_6x	IO:SPI_MI_6x	IO0:NLD11_2x			
DVDD_MC0	M30	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT7	Aux. Mode 1	GPIO165	IO0:MSDC0_DAT7	IO:EINT3_6x	O:SPI_MO_6x	IO0:NLD3_2x			
DVDD_MC0	P29	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT5	Aux. Mode 1	GPIO166	IO0:MSDC0_DAT5	IO:EINT1_6x	O:SPI_CLK_6x	IO0:NLD2_2x			
DVDD_MC0	L29	4/8/12/16mA	High	MSDC0_RSTB	Aux. Mode 1	GPIO167	O:MSDC0_RSTB	O:NCLE					
DVDD_MC0	N28	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT4	Aux. Mode 1	GPIO168	IO0:MSDC0_DAT4	IO:EINT0_6x	O:SPI_CSN_6x	IO0:NLD10_2x			
DVDD_MC0	K27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT2	Aux. Mode 1	GPIO169	IO0:MSDC0_DAT2	IO:EINT30_5x	IO0:NLD9_2x				
DVDD_MC0	N26	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT3	Aux. Mode 1	GPIO170	IO0:MSDC0_DAT3	IO:EINT31_5x	IO0:NLD1_2x				
DVDD_MC0	M29	16/20/24/28/40/44/48/52mA	High	MSDC0_CMD	Aux. Mode 1	GPIO171	IO0:MSDC0_CMD	IO:EINT27_5x	I0:NRNB				
DVDD_MC0	M26	16/20/24/28/40/44/48/52mA	High	MSDC0_CLK	Aux. Mode 1	GPIO172	IO0:MSDC0_CLK	IO:EINT26_5x					
DVDD_MC0	L27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT1	Aux. Mode 1	GPIO173	IO0:MSDC0_DAT1	IO:EINT29_5x	IO0:NLD0_2x				
DVDD_MC0	L26	4/8/12/16mA	Low	MSDC1_SDWPI	Aux. Mode 1	GPIO174	IO:MSDC1_SDWPI	IO:EINT4_6x					
DVDD_MC0	N27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT0	Aux. Mode 1	GPIO175	IO0:MSDC0_DAT0	IO:EINT28_5x	IO0:NLD8_2x				
DVDD_MC1	K30	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT1	Aux. Mode 1	GPIO176	IO0:MSDC1_DAT1	IO:EINT7_6x					
DVDD_MC1	K29	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT2	Aux. Mode 1	GPIO177	IO0:MSDC1_DAT2	IO:EINT8_6x					
DVDD_MC1	K28	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT0	Aux. Mode 1	GPIO178	IO0:MSDC1_DAT0	IO:EINT6_6x					
DVDD_MC1	J27	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT3	Aux. Mode 1	GPIO179	IO0:MSDC1_DAT3	IO:EINT9_6x					
DVDD_MC1	K26	16/20/24/28/40/44/48/52mA	High	MSDC1_CLK	Aux. Mode 1	GPIO180	IO0:MSDC1_CLK	IO:EINT5_6x					
DVDD_MC1	J26	16/20/24/28/40/44/48/52mA	High	MSDC1_CMD	Aux. Mode 1	GPIO181	IO0:MSDC1_CMD	IO:EINT5_6x					
DVDD_MC2	H26	16/20/24/28/40/44/48/52mA	Low	MSDC2_CLK	Aux. Mode 1	GPIO182	IO0:MSDC2_CLK	IO:EINT10_6x	O:UTXD2_7x				O:dbg_out1[5]
DVDD_MC2	J28	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT3	Aux. Mode 1	GPIO183	IO0:MSDC2_DAT3	IO:EINT15_6x	O:SPI_MO_4x	IO:SPI_MI_5x			O:dbg_out1[10]
DVDD_MC2	J29	16/20/24/28/40/44/48/52mA	High	MSDC2_CMD	Aux. Mode 1	GPIO184	IO0:MSDC2_CMD	IO:EINT11_6x	O:IRDA_PDN_5x	I1:URXD2_7x			O:dbg_out1[6]
DVDD_MC2	H29	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT2	Aux. Mode 1	GPIO185	IO0:MSDC2_DAT2	IO:EINT14_6x	I1:URXD2_8x	IO:SPI_MI_4x	O:SPI_CSN_4x		O:dbg_out1[9]
DVDD_MC2	K24	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT0	Aux. Mode 1	GPIO186	IO0:MSDC2_DAT0	IO:EINT12_6x	IO:IRDA_RXD_5x	O:SPI_MO_5x	O:SPI_CSN_5x		O:dbg_out1[7]
DVDD_MC2	J30	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT1	Aux. Mode 1	GPIO187	IO0:MSDC2_DAT1	IO:EINT13_6x	O:IRDA_TXD_5x	O:SPI_CLK_4x	O:SPI_CLK_5x		O:dbg_out1[8]
DVDD_NFI	G30	4/8/12/16mA	Low	NLD12	Aux. Mode 1	GPIO188	IO0:NLD12	IO:EINT2_7x	O:CLKM3_7x	O:PWM3_7x			
DVDD_NFI	G27	4/8/12/16mA	High	NREB	Aux. Mode 1	GPIO189	IO0:NREB	IO:EINT28_6x					
DVDD_NFI	F30	4/8/12/16mA	Low:Note1	NWEB	Aux. Mode 1	GPIO190	O:NREB	IO:EINT17_6x					
DVDD_NFI	F29	4/8/12/16mA	Low:Note1	NLD3	Aux. Mode 1	GPIO191	IO0:NLD3	IO:EINT25_6x					
DVDD_NFI	E29	4/8/12/16mA	Low	NLD11	Aux. Mode 1	GPIO192	IO0:NLD11	IO:EINT1_7x	O:CLKM4_7x	O:PWM2_7x			
DVDD_NFI	G29	4/8/12/16mA	Low:Note1	NLD5	Aux. Mode 1	GPIO193	IO0:NLD5	IO:EINT27_6x					
DVDD_NFI	D30	4/8/12/16mA	Low:Note1	NLD9	Aux. Mode 1	GPIO194	IO0:NLD9	IO:EINT31_6x	I1:URXD3_7x	O:PWM0_7x			
DVDD_NFI	F28	4/8/12/16mA	High	NCEB1	Aux. Mode 1	GPIO195	O:NCEB1	IO:EINT20_6x					
DVDD_NFI	C30	4/8/12/16mA	Low	NLD10	Aux. Mode 1	GPIO196	IO0:NLD10	IO:EINT0_7x	O:CLKM5_7x	O:PWM1_7x			
DVDD_NFI	F27	4/8/12/16mA	High:Note1	NCEB0	Aux. Mode 1	GPIO197	O:NCEB0	IO:EINT19_6x					
DVDD_NFI	D29	4/8/12/16mA	Low:Note1	NLD4	Aux. Mode 1	GPIO198	IO0:NLD4	IO:EINT26_6x					
DVDD_NFI	E28	4/8/12/16mA	High:Note1	NWEB	Aux. Mode 1	GPIO199	O:NWEB	IO:EINT18_6x					
DVDD_NFI	C29	4/8/12/16mA	Low:Note1	NALE	Aux. Mode 1	GPIO200	O:NALE	IO:EINT16_6x	O:CLKM0_7x	O:PWM6_7x			
DVDD_NFI	B30	4/8/12/16mA	Low	NLD15	Aux. Mode 1	GPIO201	IO0:NLD15	IO:EINT5_7x	O:CLKM1_7x	O:PWM5_7x			
DVDD_NFI	B29	4/8/12/16mA	Low:Note1	NLD14	Aux. Mode 1	GPIO202	IO0:NLD14	IO:EINT4_7x	O:CLKM1_7x	O:PWM5_7x			
DVDD_NFI	D28	4/8/12/16mA	Low:Note1	NLD2	Aux. Mode 1	GPIO203	IO0:NLD2	IO:EINT24_6x					
DVDD_NFI	A29	4/8/12/16mA	Low	NRNB	Aux. Mode 1	GPIO204	I0:NRNB	IO:EINT21_6x					
DVDD_NFI	E26	4/8/12/16mA	Low:Note1	NLD1	Aux. Mode 1	GPIO205	IO0:NLD1	IO:EINT23_6x					
DVDD_NFI	A28	4/8/12/16mA	Low:Note1	NLD8	Aux. Mode 1	GPIO206	IO0:NLD8	IO:EINT30_6x	O:UTXD3_7x	O:gps_sync_3x			
DVDD_NFI	C28	4/8/12/16mA	Low:Note1	NLD7	Aux. Mode 1	GPIO207	IO0:NLD7	IO:EINT29_6x					
DVDD_NFI	B27	4/8/12/16mA	Low:Note1	NCLE	Aux. Mode 1	GPIO208	O:NCLE						
DVDD_NFI	E27	4/8/12/16mA	Low:Note1	NLD13	Aux. Mode 1	GPIO209	IO0:NLD13	IO:EINT3_7x	O:CLKM2_7x	O:PWM4_7x			
DVDD_NFI	B28	4/8/12/16mA	Low:Note1	NLD0	Aux. Mode 1	GPIO210	IO0:NLD0	IO:EINT22_6x					
DVDD_CAM	B6	4/8/12/16mA	Low	CMDAT2	Aux. Mode 1	GPIO211	I:CMDAT2	IO:EINT20_7x	I:CAM_CSD2_S1_1x	O:TBRXFS			
DVDD_CAM	E6	4/8/12/16mA	Low	CMMCLK	Aux. Mode 1	GPIO212	O:CMMCLK	IO:EINT6_7x	I:CAM_CMCLK_S1_1x				
DVDD_CAM	B5	4/8/12/16mA	Low	CMDAT7	Aux. Mode 1	GPIO213	I:CMDAT7	IO:EINT15_7x	I:CAM_CSD3_S1_1x				
DVDD_CAM	C4	4/8/12/16mA	Low	CMDAT0	Aux. Mode 1	GPIO214	I:CMDAT0	IO:EINT22_7x	I:CAM_CSD0_S1_1x	O:TBTXFS			
DVDD_CAM	E4	4/8/12/16mA	Low	CMDAT4	Aux. Mode 1	GPIO215	I:CMDAT4	IO:EINT18_7x	I:CAM_CSD0_S2_1x				
DVDD_CAM	A4	4/8/12/16mA	Low	CMDAT6	Aux. Mode 1	GPIO216	I:CMDAT6	IO:EINT16_7x	I:CAM_CSD2_S2_1x				
DVDD_CAM	B4	4/8/12/16mA	Low	CMDAT3	Aux. Mode 1	GPIO217	I:CMDAT3	IO:EINT19_7x	I:CAM_CSD3_S1_1x				
DVDD_CAM	D6	4/8/12/16mA	Low	CMDAT8	Aux. Mode 1	GPIO218	I:CMDAT8	IO:EINT23_7x		O:DSP_GPO0			
DVDD_CAM	A3	4/8/12/16mA	Low	CMDAT9	Aux. Mode 1	GPIO219	O:PWM4_6x	IO:EINT24_7x		O:DSP_GPO1			
DVDD_CAM	K11	4/8/12/16mA	Low	PWM4	Aux. Mode 1	GPIO220	O:CMRST	IO:EINT9_7x					
DVDD_CAM	B2	4/8/12/16mA	Low	CMRST	Aux. Mode 1	GPIO221	O:CMRST	IO:EINT12_7x	O:CMPDN_S2_1x				
DVDD_CAM	K10	4/8/12/16mA	Low	SCL_1	Aux. Mode 1	GPIO222	IO0:SCL_1_2x	IO:EINT7_7x	IO0:SCL_1_5x				I0:dbg_in[32]
DVDD_CAM	A2	4/8/12/16mA	Low	CMDAT5	Aux. Mode 1	GPIO223	I:CMDAT5	IO:EINT17_7x	I:CAM_CSD1_S2_1x				
DVDD_CAM	K9	4/8/12/16mA	Low	SDA_1	Aux. Mode 1	GPIO224	IO0:SDA_1_2x	IO:EINT8_7x	IO0:SDA_1_5x				
DVDD_CAM	B3	4/8/12/16mA	Low	CMVSYNC	Aux. Mode 1	GPIO225	I:CMVREF	IO:EINT11_7x					
DVDD_CAM	E7	4/8/12/16mA	Low	CMPCLK	Aux. Mode 1	GPIO226	I:CMPCLK	IO:EINT13_7x	I:CAM_CSK_S1_1x				
DVDD_CAM	B1	4/8/12/16mA	Low	CMHSYNC	Aux. Mode 1	GPIO227	I:CMHREF	IO:EINT10_7x	I:CAM_CSK_S2_1x				
DVDD_CAM	E5	4/8/12/16mA	Low	CMPDN	Aux. Mode 1	GPIO228	O:CMPDN	IO:EINT14_7x	O:CMPDN_S1_1x				
DVDD_CAM	C3	4/8/12/16mA	Low	CMDAT1	Aux. Mode 1	GPIO229	I:CMDAT1	IO:EINT21_7x	I:CAM_CSD1_S1_1x	O:TBRXEN			
DVDD_CAM	D4	4/8/12/16mA	Low	CMFLASH	Aux. Mode 1	GPIO230	O:CMFLASH_1x	IO:EINT26_7x	I:CAM_CMCLK_S2_1x	O:DSP_GPO2			

Note 1:不建议客户用来作为关键器件的伸能（Do not recommend customers use the device as a LDO enable / Backlight enable / Speaker enable …）