

[illegible]

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IO PWR Domain	Pin Num	Driving	IO Reset Value	Name	GPIO reset default mode	Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7
DVDD18_MIP1IO	D2	2/4/6/8mA	Low	PAD_RDP1	Aux. Mode 1	GPIO0	I0-EINT0_0x						
DVDD18_MIP1IO	C2	2/4/6/8mA	Low	PAD_RDN1	Aux. Mode 1	GPIO10	I0-EINT1_0x						
DVDD18_MIP1IO	E2	2/4/6/8mA	Low	PAD_RCP	Aux. Mode 1	GPIO2	I0-EINT2_0x						
DVDD18_MIP1IO	D1	2/4/6/8mA	Low	PAD_RCN	Aux. Mode 1	GPIO1	I0-EINT3_0x						
DVDD18_MIP1IO	F3	2/4/6/8mA	Low	PAD_RDP0	Aux. Mode 1	GPIO4	I0-EINT4_0x						
DVDD18_MIP1IO	E3	2/4/6/8mA	Low	PAD_RDN0	Aux. Mode 1	GPIO3	I0-EINT5_0x						
DVDD18_MIP1IO	F1	2/4/6/8mA	Low	PAD_TDP0	Aux. Mode 1	GPIO5	I0-EINT6_0x						
DVDD18_MIP1IO	F2	2/4/6/8mA	Low	PAD_TDN0	Aux. Mode 1	GPIO6	I0-EINT7_0x						
DVDD18_MIP1IO	G5	2/4/6/8mA	Low	PAD_TCP	Aux. Mode 1	GPIO7	I0-EINT8_0x						
DVDD18_MIP1IO	H4	2/4/6/8mA	Low	PAD_TCN	Aux. Mode 1	GPIO9	I0-EINT9_0x						
DVDD18_MIP1IO	G2	2/4/6/8mA	Low	PAD_TDP1	Aux. Mode 1	GPIO8	I0-EINT10_0x						
DVDD18_MIP1IO	G1	2/4/6/8mA	Low	PAD_TDN1	Aux. Mode 1	GPIO11	I0-EINT11_0x						
DVDD_LCD	J3	4/8/12/16mA	Low	LPA0	Aux. Mode 1	GPIO12	O-LPA0_1x	I0-EINT16_1x	O-PWM4_1x				
DVDD_LCD	K4	4/8/12/16mA	High	LPCE1B	Aux. Mode 1	GPIO13	O-LPCE1B_1x	I0-EINT14_1x	O-PWM2_1x				
DVDD_LCD	J2	4/8/12/16mA	Low	LPTE	Aux. Mode 1	GPIO14	I0-LPTE_1x	I0-EINT12_1x	O-PWM0_1x				
DVDD_LCD	K3	4/8/12/16mA	High	LWRB	Aux. Mode 1	GPIO15	O-LWRB_1x	I0-EINT18_1x	O-PWM6_1x				
DVDD_LCD	K1	4/8/12/16mA	High	LPCE0B	Aux. Mode 1	GPIO16	O-LPCE0B_1x	I0-EINT15_1x	O-PWM3_1x				
DVDD_LCD	K5	4/8/12/16mA	High	LRDB	Aux. Mode 1	GPIO17	O-LRDB_1x	I0-EINT17_1x	O-PWM5_1x				
DVDD_LCD	K2	4/8/12/16mA	High	LSTB	Aux. Mode 1	GPIO18	O-LSTB_1x	I0-EINT13_1x	O-PWM1_1x				
DVDD_LCD	M5	4/8/12/16mA	High	DPHHSYNC	Aux. Mode 1	GPIO19	I00-DPHHSYNC_1x	I0-EINT22_1x	I0CM2PCLK_1x				
DVDD_LCD	L2	4/8/12/16mA	Low	DP1B6	Aux. Mode 1	GPIO20	I00-DPIB_PO_1x[6]	I0-EINT8_2x				O-dbg_out0[17]	
DVDD_LCD	L5	4/8/12/16mA	High	DP1VSYNC	Aux. Mode 1	GPIO21	I00-DPIVSYNC_1x	I0-EINT21_1x	I0CM2VREF_1x				
DVDD_LCD	M1	4/8/12/16mA	Low	DP1R5	Aux. Mode 1	GPIO22	I00-DPIR_PO_1x[5]	I0-EINT25_1x	I0CM2DAT5_1x			O-dbg_out0[10]	
DVDD_LCD	N7	4/8/12/16mA	Low	DP1B2	Aux. Mode 1	GPIO23	I00-DPIB_PO_1x[2]	I0-EINT12_2x	O-I2S1_CK_1x			O-dbg_out0[21]	
DVDD_LCD	M2	4/8/12/16mA	Low	DP1B5	Aux. Mode 1	GPIO24	I00-DPIB_PO_1x[5]	I0-EINT9_2x	I00-I2S0_CK_1x			O-dbg_out0[18]	
DVDD_LCD	N2	4/8/12/16mA	Low	DP1B0	Aux. Mode 1	GPIO25	I00-DPIB_PO_1x[0]	I0-EINT14_2x	O-I2S1_WS_1x			O-dbg_out0[23]	
DVDD_LCD	N8	4/8/12/16mA	Low	DP1R7	Aux. Mode 1	GPIO26	I00-DPIR_PO_1x[7]	I0-EINT23_1x	I0CM2DAT7_1x			O-dbg_out0[8]	
DVDD_LCD	N1	4/8/12/16mA	Low	DP1R4	Aux. Mode 1	GPIO27	I00-DPIR_PO_1x[4]	I0-EINT26_1x	I0CM2DAT4_1x			O-dbg_out0[11]	
DVDD_LCD	N4	4/8/12/16mA	Low	DP1G3	Aux. Mode 1	GPIO28	I00-DPIG_PO_1x[3]	I0-EINT3_2x				O-dbg_out0[4]	
DVDD_LCD	N3	4/8/12/16mA	Low	DP1B4	Aux. Mode 1	GPIO29	I00-DPIB_PO_1x[4]	I0-EINT10_2x	I00-I2S0_DAT_1x			O-dbg_out0[19]	
DVDD_LCD	N5	4/8/12/16mA	Low	DP1DE	Aux. Mode 1	GPIO30	I00-DPIDE_1x	I0-EINT20_1x	I0CM2HREF_1x				
DVDD_LCD	P2	4/8/12/16mA	Low	DP1B3	Aux. Mode 1	GPIO31	I00-DPIB_PO_1x[3]	I0-EINT11_2x	I00-I2S0_WS_1x			O-dbg_out0[20]	
DVDD_LCD	P6	4/8/12/16mA	Low	DP1R6	Aux. Mode 1	GPIO32	I00-DPIR_PO_1x[6]	I0-EINT24_1x	I0CM2DAT6_1x			O-dbg_out0[9]	
DVDD_LCD	P4	4/8/12/16mA	Low	DP1G2	Aux. Mode 1	GPIO33	I00-DPIG_PO_1x[2]	I0-EINT4_2x				O-dbg_out0[5]	
DVDD_LCD	P5	4/8/12/16mA	Low	DP1G7	Aux. Mode 1	GPIO34	I00-DPIG_PO_1x[7]	I0-EINT31_1x	O-CM2RST_1x			O-dbg_out0[0]	
DVDD_LCD	R1	4/8/12/16mA	Low	DP1R2	Aux. Mode 1	GPIO35	I00-DPIR_PO_1x[2]	O-CM-EINT8_1x	I0CM2DAT2_1x			O-dbg_out0[6]	
DVDD_LCD	P8	4/8/12/16mA	Low	DP1B1	Aux. Mode 1	GPIO36	I00-DPIB_PO_1x[1]	I0-EINT13_2x	O-I2S1_DAT_1x			O-dbg_out0[22]	
DVDD_LCD	R5	4/8/12/16mA	Low	DP1G0	Aux. Mode 1	GPIO37	I00-DPIG_PO_1x[0]	I0-EINT6_2x				O-dbg_out0[7]	
DVDD_LCD	P3	4/8/12/16mA	Low	DP1G6	Aux. Mode 1	GPIO38	I00-DPIG_PO_1x[6]	I0-EINT0_2x	O-CM2PDN_1x			O-dbg_out0[1]	
DVDD_LCD	P7	4/8/12/16mA	Low	DP1G4	Aux. Mode 1	GPIO39	I00-DPIG_PO_1x[4]	I0-EINT2_2x				O-dbg_out0[3]	
DVDD_LCD	R2	4/8/12/16mA	Low	DP1R3	Aux. Mode 1	GPIO40	I00-DPIR_PO_1x[3]	I0-EINT27_1x	I0CM2DAT3_1x			O-dbg_out0[12]	
DVDD_LCD	T4	4/8/12/16mA	High	DP1CK	Aux. Mode 1	GPIO41	I00-DPICK_1x	I0-EINT19_1x	O-CM2MCLK_1x				
DVDD_LCD	T1	4/8/12/16mA	Low	DP1B7	Aux. Mode 1	GPIO42	I00-DPIB_PO_1x[7]	I0-EINT7_2x				O-dbg_out0[16]	
DVDD_LCD	T5	4/8/12/16mA	Low	DP1G1	Aux. Mode 1	GPIO43	I00-DPIG_PO_1x[1]	I0-EINT5_2x				O-dbg_out0[6]	
DVDD_LCD	T2	4/8/12/16mA	Low	DP1R0	Aux. Mode 1	GPIO44	I00-DPIR_PO_1x[0]	I0-EINT30_1x	I0CM2DAT0_1x			O-dbg_out0[15]	
DVDD_LCD	U4	4/8/12/16mA	Low	DP1R1	Aux. Mode 1	GPIO45	I00-DPIR_PO_1x[1]	I0-EINT29_1x	I0CM2DAT1_1x			O-dbg_out0[14]	
DVDD_LCD	U5	4/8/12/16mA	Low	DP1G5	Aux. Mode 1	GPIO46	I00-DPIG_PO_1x[5]	I0-EINT1_2x				O-dbg_out0[2]	
DVDD_LCD	V4	4/8/12/16mA	High	LSCE0B	Aux. Mode 1	GPIO47	O-LSCE0B_1x			I00-SCL_2_1x	I0-EINT10_8x	O-TCTIRQ1_1x	
DVDD_LCD	U3	4/8/12/16mA	Low	LSD1	Aux. Mode 1	GPIO48	I1-LSD1_1x	I0-EINT11_8x	O-UTXD2_1x	I00-SDA_1_1x			
DVDD_LCD	V3	4/8/12/16mA	Low	LSA0	Aux. Mode 1	GPIO49	O-LSA0_1x	O-MD_TXD1_1x	I1-MD_RXD1_7x	I00-SDA_0_1x	I0-EINT13_8x	O-TDTRIQ_1x	
DVDD_LCD	U2	4/8/12/16mA	High	LSCE1B	Aux. Mode 1	GPIO50	O-LSCE1B_1x	O-UTXD3_1x	I00-SDA_2_1x	I0-EINT19_8x	O-TEVTVAL_1x		
DVDD_LCD	V5	4/8/12/16mA	High	LSCK	Aux. Mode 1	GPIO51	O-LSCK_1x	I1-MD_RXD1_1x	O-MD_TXD1_7x	I00-SCL_0_1x	I0-EINT20_8x		
DVDD_LCD	V6	2/4/8/16mA	Low	LSDA	Aux. Mode 1	GPIO52	I00-LSDA_1x	I1-URXD2_1x	I00-SCL_1_4x	I0-EINT23_8x	O-TCTIRQ2_1x		
DVDD_NML1	V1	2/4/6/8mA	Low	I2S0_DAT	Aux. Mode 1	GPIO53	I00-I2S0_DAT_2x	O-CLKM4_1x	O-I2S1_DAT_4x			O-dbg_out0[24]	
DVDD_NML1	Y1	2/4/6/8mA	High	I2S0_WS	Aux. Mode 1	GPIO54	I00-I2S0_WS_2x	O-CLKM5_1x	O-I2S1_WS_4x			O-dbg_out0[25]	
DVDD_NML1	W2	2/4/6/8mA	High	I2S0_CK	Aux. Mode 1	GPIO55	I00-I2S0_CK_2x	I0-EINT15_2x	O-CLKM3_1x	O-I2S1_CK_4x		O-dbg_out0[26]	
DVDD_NML1	W5	2/4/6/8mA	High	I2S1_CK	Aux. Mode 1	GPIO56	O-I2S1_CK_2x		I0-IRDA_RXD_1x	I00-I2S0_CK_6x		O-dbg_out0[27]	
DVDD_NML1	W1	2/4/6/8mA	High	I2S1_WS	Aux. Mode 1	GPIO57	O-I2S1_WS_2x	I0-EINT1_3x	O-IRDA_PDN_1x	I00-I2S0_WS_6x		O-dbg_out0[28]	
DVDD_NML1	Y5	2/4/6/8mA	Low	I2S1_DAT	Aux. Mode 1	GPIO58	O-I2S1_DAT_2x	I0-EINT0_3x	O-IRDA_TXD_1x	I00-I2S0_DAT_6x		O-dbg_out0[29]	
DVDD_NML1	Y2	2/4/6/8mA	Low	DAIRSTB	Aux. Mode 1	GPIO59	I0-DAIRSTB	I0-EINT8_3x	O-PWM6_2x	I1-MD_RXD2_1x			
DVDD_NML1	Y4	2/4/6/8mA	Low	EINT8	Aux. Mode 1	GPIO60	I0-EINT8_1x	O-PWM3_2x	O-PWM2_3x				
DVDD_NML1	AA1	2/4/6/8mA	Low	EINT9	Aux. Mode 1	GPIO61	I0-EINT9_1x	O-PWM4_2x	O-gps_sync_4x	O-watchdog_rstb	O-DSP_GP03		
DVDD_NML1	AA2	2/4/6/8mA	Low	DAIPCMIOUT	Aux. Mode 1	GPIO62	O-DAIPCMIOUT	I0-PWM0_2x	O-PWM0_2x			O-watchdog_rstb	
DVDD_NML1	AA3	2/4/6/8mA	High	DAICLK	Aux. Mode 1	GPIO63	O-DAICLK	I0-EINT3_3x	O-PWM1_2x	I00-I2S0_CK_3x			
DVDD_NML1	AA4	2/4/6/8mA	Low	DAIPCMIN	Aux. Mode 1	GPIO64	I0-DAIPCMIN	I0-EINT4_3x	O-PWM2_2x				
DVDD_NML1	AB1	2/4/6/8mA	Low	BTSYNC	Aux. Mode 1	GPIO65	O-BTSYNC	I0-EINT7_3x	O-PWM5_2x	I00-I2S0_WS_3x			
DVDD_NML2	AB4	4/8/12/16mA	Low	PWM2	Aux. Mode 1	GPIO66	O-PWM2_6x	I0-EINT10_3x	I0-IRDA_RXD_2x	I1-URXD3_2x	O-UTXD3_3x	O-dbg_out1[17]	O-pwm1_3d_2x.o
DVDD_NML2	AB2	4/8/12/16mA	Low	PWM1	Aux. Mode 1	GPIO67	O-PWM1_6x	I0-EINT9_3x	O-IRDA_PDN_2x	O-MD_TXD2_1x	Ousb_drvvbus_2x	O-dbg_out1[16]	O-pwm1_3d_1x.o
DVDD_NML2	AB3	4/8/12/16mA	Low	PWM3	Aux. Mode 1	GPIO68	O-PWM3_6x	I0-EINT11_3x	O-IRDA_TXD_2x	O-UTXD3_2x	I1-URXD3_3x	O-dbg_out1[18]	O-pwm1_3d_3x.o
DVDD_NML2	AA5	2/4/6/8mA	Low	EINT3	Aux. Mode 1	GPIO69	I0-EINT3_1x	O-PWM4_4x	I1-MD_RXD1_2x	O-CLKM2_2x	O-SPI_MO_1x	I0-dbg_in[9]	
DVDD_NML2	AD2	2/4/6/8mA	Low	EINT0	Aux. Mode 1	GPIO70	I0-EINT0_1x	O-PWM0_4x	I1-URXD0_6x	O-CLKM0_1x	O-SPI_CSN_1x	I0-dbg_in[3]	
DVDD_NML2	AD1	4/8/12/16mA	Low	UTXD3	Aux. Mode 1	GPIO71	O-UTXD3_8x	I0-EINT17_3x	I1-URXD3_4x	O-PWM6_3x	I00-DSP_EMU_B_1x	I0-dbg_in[5]	
DVDD_NML2	AD4	2/4/6/8mA	Low	EINT4	Aux. Mode 1	GPIO72	I0-EINT4_1x	O-PWM4_4x	O-MD_TXD2_3x	O-CLKM1_2x	O-gps_sync_2x	I0-dbg_in[10]	
DVDD_NML2	AB5	2/4/6/8mA	Low	EINT7	Aux. Mode 1	GPIO73	I0-EINT7_1x	O-CLKM0_2x	O-IRDA_TXD_3x	I00-I2S0_WS_4x	Ousb_drvvbus_1x	I0-dbg_in[13]	
DVDD_NML2	AE1	4/8/12/16mA	Low	UTXD2	Aux. Mode 1	GPIO74	O-UTXD2_4x	I0-EINT13_3x	I1-URXD2_5x	O-PWM2_3x	I0-DSP_ITCK_1x	I0-dbg_in[15]	
DVDD_NML2	AA6	2/4/6/8mA	Low	EINT6	Aux. Mode 1	GPIO75	I0-EINT6_1x	O-PWM6_4x	O-IRDA_RXD_3x	I00-I2S0_DAT_4x	I1-MD_RXD2_3x	I0-dbg_in[12]	
DVDD_NML2	AD2	2/4/6/8mA	Low	EINT1	Aux. Mode 1	GPIO76	I0-EINT1_1x	O-PWM1_4x	O-UTXD2_6x	O-CLKM4_2x	O-SPI_CLK_1x	I0-dbg_in[7]	
DVDD_NML2	AC5	4/8/12/16mA	High>Note1	URXD2	Aux. Mode 1	GPIO77	I1-URXD2_4x	I0-EINT12_3x	O-UTXD2_5x	O-PWM1_3x	I0-DSP_ITRST_B_1x	I0-dbg_in[14]	
DVDD_NML2	AD3	2/4/6/8mA	Low	EINT5	Aux. Mode 1	GPIO78	I0-EINT5_1x	O-PWM5_4x	O-IRDA_PDN_3x	I00-I2S0_CK_4x	Ousb_drvvbus_3x	I0-dbg_in[11]	
DVDD_NML2	AD5	4/8/12/16mA	High	URXD3	Aux. Mode 1	GPIO79	I1-URXD3_8x	I0-EINT16_3x	O-UTXD3_4x	O-PWM5_3x	O-DSP_ITD0_1x	I0-dbg_in[2]	
DVDD_NML2	AE3	4/8/12/16mA	Low	IUCTS2	Aux. Mode 1	GPIO80	I1-IUCTS2_4x	I0-EINT14_3x	O-MD_TXD2_2x	O-PWM3_3x	I1-DSP_ITMS_1x		
DVDD_NML2	AE4	2/4/6/8mA	Low	EINT2	Aux. Mode 1	GPIO81	I0-EINT2_1x	O-PWM2_4x	O-MD_TXD1_2x	O-CLKM3_2x	I0-SPI_MI_1x	I0-dbg_in[8]	
DVDD_NML2	AF2	4/8/12/16mA	High	URTS2	Aux. Mode 1	GPIO82	O-URTS2_4x	I0-EINT15_3x	I1-MD_RXD2_2x	O-PWM4_3x	I1-DSP_ITD1_1x		
DVDD_NML3	AF4	4/8/12/16mA	High	SPI_CSN	Aux. Mode 1	GPIO83	O-SPI_CSN_2x	I0-EINT18_3x	O-IRDA_PDN_4x	O-SPI_CSN_3x			
DVDD_NML3	AG3	4/8/12/16mA	Low	SPI_MI	Aux. Mode 1	GPIO84	I0-SPI_MI_2x	I0-EINT20_3x	O-IRDA_TXD_4x	O-SPI_MO_3x			
DVDD_NML3	AG2	4/8/12/16mA	Low	SPI_MO	Aux. Mode 1	GPIO85	O-SPI_MO_2x	I0-EINT21_3x	O-CLKM5_3x				
DVDD_NML3	AH1	4/8/12/16mA	Low	SPI_CLK	Aux. Mode 1	GPIO86	O-SPI_CLK_2x	I0-EINT19_3x	O-IRDA_RXD_4x	O-SPI_CLK_3x		O-dbg_out0[30]	
DVDD_NML3	AH2	4/8/12/16mA	High	SCL_0	Aux. Mode 1	GPIO87	I00-SCL_0_2x	I0-EINT22_3x	O-CLKM4_3x	O-CLKM1_3x		I0-dbg_in[0]	
DVDD_NML3	AH3	4/8/12/16mA	High	SDA_0	Aux. Mode 1	GPIO88	I00-SDA_0_2x	I0-EINT23_3x	O-CLKM3_3x			I0-dbg_in[1]	
DVDD_MC3	AG4	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT2	Aux. Mode 1	GPIO89	I00-MSDC3_DAT2	I0-EINT28_3x	I1-URXD3_5x	O-CLKM1_4x	O-1TXD3_6x	O-dbg_out1[4]	
DVDD_MC3	AH3	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT1	Aux. Mode 1	GPIO90	I00-MSDC3_DAT1	I0-EINT27_3x	I1-MD_RXD2_4x	O-CLKM2_4x	O-MD_TXD2_5x	O-dbg_out1[10]	
DVDD_MC3	AJ3	16/20/24/28/40/44/48/52mA	High	MSDC3_CMD	Aux. Mode 1	GPIO91	I00-MSDC3_CMD	I0-EINT25_3x	I1-MD_RXD1_3x	O-CLKM4_4x	O-MD_TXD1_4x	O-dbg_out1[1]	
DVDD_MC3	AH4	16/20/24/28/40/44/48/52mA	Low	MSDC3_CLK	Aux. Mode 1	GPIO92	I00-MSDC3_CLK	I0-EINT24_3x	O-MD_TXD1_3x	O-CLKM5_4x	I1-MD_RXD1_4x	O-dbg_out1[12]	
DVDD_MC3	AK3	16/20/24/28/40/44/48/52mA	High	MSDC3_DAT3	Aux. Mode 1	GPIO93	I00-MSDC3_DAT3	I0-EINT29_3x	O-UTXD3_5x	O-CLKM0_4x	I1-URXD3_6x	O-dbg_out1[31]	
DVDD_MC3	AJ4	16/20/24/28/40/44/48/52mA	High										

IO PWR Domain	Pin Num	Driving	IO Reset Value	Name	GPIO reset default mode	Mode 0	Mode 1	Mode 2	Mode 3	Mode 4	Mode 5	Mode 6	Mode 7
DVDD_BSI	AB14	4/8/12/16mA	Low	BSIO_CS0	Aux. Mode 1	GPIO136	0-BSIO_CS0	IO-ENT1T_5x					
DVDD_BSI	AA13	4/8/12/16mA	Low	BSIO_CLK	Aux. Mode 1	GPIO137	0-BSIO_CLK	IO-ENT2T_5x					
AVDD30_VSIM2	AD30		Low	PAD_SIO2	Aux. Mode 1	GPIO138	0-CLKM2_8x	IO-ENT9_5x	0-PWM0_6x	0-IRDA_PDN_8x			
AVDD30_VSIM2	AD29		Low	PAD_SRST2	Aux. Mode 1	GPIO139	0-CLKM1_8x	IO-ENT10_5x	0-PWM5_6x	0-IRDA_RXD_8x			
AVDD30_VSIM2	AD28		Low	PAD_SCLK2	Aux. Mode 1	GPIO140	0-CLKM0_8x	IO-ENT11_5x	0-PWM6_6x	0-IRDA_TXD_8x			
DVDD_NMI.5	V26	2/4/6/8mA	Low	DDIOG	Aux. Mode 1	GPIO141	0-DDIOG	IO-ENT12_5x					
DVDD_NMI.5	V26	2/4/6/8mA	High>Note1	SDA_2	Aux. Mode 1	GPIO142	IO-SDA_2_2x	IO-ENT14_5x					
DVDD_NMI.5	W29	2/4/6/8mA	Low	PACTRL2	Aux. Mode 1	GPIO143	0-PACTRL2	IO-ENT19_5x				0-dbg_out[11]5	
DVDD_NMI.5	W27	2/4/6/8mA	Low	PMUCTRL0	Aux. Mode 1	GPIO144	0-PMUCTRL0	IO-ENT15_5x				0-dbg_out[11]11	
DVDD_NMI.5	W26	2/4/6/8mA	High>Note1	SCL_2	Aux. Mode 1	GPIO145	IO-SCL_2_2x	IO-ENT13_5x					
DVDD_NMI.5	U29	2/4/6/8mA	Low	SRCLKENAI	Aux. Mode 1	GPIO231	IO-SRCLKENAI						
DVDD_NMI.5	V28	2/4/6/8mA	Low	PACTRL1	Aux. Mode 1	GPIO146	0-PACTRL1	IO-ENT18_5x					0-dbg_out[11]14
DVDD_NMI.5	V27	2/4/6/8mA	Low	PMUCTRL1	Aux. Mode 1	GPIO147	0-PMUCTRL1	IO-ENT16_5x					0-dbg_out[11]12
DVDD_NMI.5	V30	2/4/6/8mA	Low	PACTRL0	Aux. Mode 1	GPIO148	0-PACTRL0	IO-ENT17_5x					0-dbg_out[11]13
DVDD_NMI.6	U26	4/8/12/16mA	Low	SWCLKCTCK	Aux. Mode 1	GPIO149	0-SWCLKCTCK						
DVDD_NMI.6	T26	4/8/12/16mA	Low	SWDIOTMS	Aux. Mode 1	GPIO150	IO-SWDIOTMS						
DVDD_NMI.6	T30	4/8/12/16mA	High	ITCK	Aux. Mode 1	GPIO151	IO-ITCK	0-DSP_ITCK_3x					
DVDD_NMI.6	U28	4/8/12/16mA	Low	ITDO	Aux. Mode 1	GPIO152	IO-ITDO	0-DSP_ITDO_3x					
DVDD_NMI.6	R30	4/8/12/16mA	Low	ITRST_B	Aux. Mode 1	GPIO153	IO-ITRST_B	0-DSP_ITRST_B_3x					
DVDD_NMI.6	T29	4/8/12/16mA	High	ITDI	Aux. Mode 1	GPIO154	IO-ITDI	0-DSP_ITDI_3x					
DVDD_NMI.6	R29	4/8/12/16mA	Low	IRTKC	Aux. Mode 1	GPIO155	IO-IRTKC	IO-DSP_EMU_B_3x					
DVDD_NMI.6	U27	4/8/12/16mA	High	ITMS	Aux. Mode 1	GPIO156	IO-ITMS	0-DSP_ITMS_3x					
DVDD_NMI.6	R27	4/8/12/16mA	High>Note1	URXD1	Aux. Mode 1	GPIO157	IO-URXD1_1x	IO-ENT20_5x	1-IRXD1_2x	1-IMD_RXD1_5x	0-MD_TXD1_8x		
DVDD_NMI.6	P28	4/8/12/16mA	High	URXD4	Aux. Mode 1	GPIO158	IO-URXD4_1x	IO-ENT24_5x	1-IRXD4_2x	0-MD_RXD2_6x	0-MD_TXD2_7x	10-dbg_in[5]	
DVDD_NMI.6	R26	4/8/12/16mA	High	URXD4	Aux. Mode 1	GPIO159	1-URXD4_1x	IO-ENT23_5x	0-UTXD2_4x	0-MD_TXD2_6x	1-IMD_RXD2_7x	10-dbg_in[4]	
DVDD_NMI.6	P26	4/8/12/16mA	High	URXD1	Aux. Mode 1	GPIO160	1-URXD1_1x	0-UTXD1_2x		0-MD_TXD1_5x	1-IMD_RXD1_8x		
DVDD_NMI.6	P27	4/8/12/16mA	High>Note1	URTS1	Aux. Mode 1	GPIO161	0-URTS1_1x	IO-ENT22_5x	1-IMD_RXD1_6x				
DVDD_NMI.6	R25	4/8/12/16mA	Low	UCTS1	Aux. Mode 1	GPIO162	1-UCTS1_1x	IO-ENT21_5x	0-MD_TXD1_6x				
DVDD_NMI.6	N29	4/8/12/16mA	Low	MSDC0_INSI	Aux. Mode 1	GPIO163	IO-MSDC0_INSI	IO-ENT25_5x					
DVDD_MC0	N30	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT6	Aux. Mode 1	GPIO164	IO-MSDC0_DAT6	IO-ENT2T_6x	0-SPI_M1_6x	IO-0-NLD11_2x			
DVDD_MC0	M30	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT7	Aux. Mode 1	GPIO165	IO-MSDC0_DAT7	IO-ENT23_6x	IO-SPI_M0_6x	IO-0-NLD3_2x			
DVDD_MC0	P30	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT5	Aux. Mode 1	GPIO166	IO-MSDC0_DAT5	IO-ENT21_6x	0-SPI_CLK_6x	IO-0-NLD2_2x			
DVDD_MC0	L29	4/8/12/16mA	High	MSDC0_RSTB	Aux. Mode 1	GPIO167	0-MSDC0_RSTB	0-NCLE					
DVDD_MC0	N28	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT4	Aux. Mode 1	GPIO168	IO-MSDC0_DAT4	IO-ENT0T_6x	0-SPI_CSN_6x	IO-0-NLD10_2x			
DVDD_MC0	K27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT2	Aux. Mode 1	GPIO169	IO-MSDC0_DAT2	IO-ENT30_5x	IO-0-NLD9_2x				
DVDD_MC0	N26	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT3	Aux. Mode 1	GPIO170	IO-MSDC0_DAT3	IO-ENT31_5x	IO-0-NLD1_2x				
DVDD_MC0	M29	16/20/24/28/40/44/48/52mA	High	MSDC0_CMD	Aux. Mode 1	GPIO171	IO-MSDC0_CMD	IO-ENT27_5x	IO-0-NRNB				
DVDD_MC0	M26	16/20/24/28/40/44/48/52mA	Low	MSDC0_CLK	Aux. Mode 1	GPIO172	IO-MSDC0_CLK	IO-ENT26_5x					
DVDD_MC0	L27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT1	Aux. Mode 1	GPIO173	IO-MSDC0_DAT1	IO-ENT29_5x	IO-0-NLD0_2x				
DVDD_MC0	L26	4/8/12/16mA	Low	MSDC0_SDWPH	Aux. Mode 1	GPIO174	IO-MSDC0_SDWPH	IO-ENT4_6x					
DVDD_MC0	N27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT0	Aux. Mode 1	GPIO175	IO-MSDC0_DAT0	IO-ENT28_5x	IO-0-NLD8_2x				
DVDD_MC1	K30	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT1	Aux. Mode 1	GPIO176	IO-MSDC1_DAT1	IO-ENT7T_6x					
DVDD_MC1	K29	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT2	Aux. Mode 1	GPIO177	IO-MSDC1_DAT2	IO-ENT8T_6x					
DVDD_MC1	J28	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT0	Aux. Mode 1	GPIO178	IO-MSDC1_DAT0	IO-ENT6T_6x					
DVDD_MC1	K27	16/20/24/28/40/44/48/52mA	High	MSDC1_DAT3	Aux. Mode 1	GPIO179	IO-MSDC1_DAT3	IO-ENT9T_6x					
DVDD_MC1	K26	16/20/24/28/40/44/48/52mA	Low	MSDC1_CLK	Aux. Mode 1	GPIO180	IO-MSDC1_CLK						
DVDD_MC1	J26	16/20/24/28/40/44/48/52mA	High	MSDC1_CMD	Aux. Mode 1	GPIO181	IO-MSDC1_CMD	IO-ENT5T_6x					
DVDD_MC2	H26	16/20/24/28/40/44/48/52mA	Low	MSDC2_CLK	Aux. Mode 1	GPIO182	IO-MSDC2_CLK	IO-ENT10_6x		0-UTXD2_7x		0-dbg_out[11]5	
DVDD_MC2	J28	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT3	Aux. Mode 1	GPIO183	IO-MSDC2_DAT3	IO-ENT15_6x	0-UTXD2_8x	0-SPI_M0_4x	10-SPI_M1_5x	0-dbg_out[11]10	
DVDD_MC2	J29	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT4	Aux. Mode 1	GPIO184	IO-MSDC2_DAT4	IO-ENT16_6x	0-IRDA_PDN_5x	1-IRXD2_7x			0-dbg_out[11]6
DVDD_MC2	H29	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT2	Aux. Mode 1	GPIO185	IO-MSDC2_DAT2	IO-ENT14_6x	1-IRXD2_8x	0-SPI_M1_4x	0-SPI_M0_5x	0-dbg_out[11]9	
DVDD_MC2	K24	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT0	Aux. Mode 1	GPIO186	IO-MSDC2_DAT0	IO-ENT12_6x	0-IRDA_RXD_5x	0-SPI_CSN_4x	0-SPI_CSN_5x	0-dbg_out[11]7	
DVDD_MC2	J30	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT1	Aux. Mode 1	GPIO187	IO-MSDC2_DAT1	IO-ENT13_6x	0-IRDA_TXD_5x	0-SPI_CLK_4x	0-SPI_CLK_5x	0-dbg_out[11]8	
DVDD_NFI	G30	4/8/12/16mA	Low	NLD12	Aux. Mode 1	GPIO188	IO-NLD12	IO-ENT2T_7x	0-CLKM3_7x	0-PWM3_7x			
DVDD_NFI	G27	4/8/12/16mA	Low>Note1	NLD6	Aux. Mode 1	GPIO189	IO-NLD6	IO-ENT28_6x					
DVDD_NFI	F30	4/8/12/16mA	High	NREB	Aux. Mode 1	GPIO190	IO-NREB	IO-ENT17_6x					
DVDD_NFI	F29	4/8/12/16mA	Low>Note1	NLD3	Aux. Mode 1	GPIO191	IO-NLD3	IO-ENT25_6x					
DVDD_NFI	E29	4/8/12/16mA	Low	NLD11	Aux. Mode 1	GPIO192	IO-NLD11	IO-ENT1T_7x	0-CLKM4_7x	0-PWM2_7x			
DVDD_NFI	G28	4/8/12/16mA	Low>Note1	NLD5	Aux. Mode 1	GPIO193	IO-NLD5	IO-ENT27_6x					
DVDD_NFI	D30	4/8/12/16mA	Low>Note1	NLD9	Aux. Mode 1	GPIO194	IO-NLD9	IO-ENT31_6x	1-IRXD3_7x	0-PWM0_7x			
DVDD_NFI	F28	4/8/12/16mA	High	NCEB1	Aux. Mode 1	GPIO195	IO-NCEB1	IO-ENT20_6x					
DVDD_NFI	C30	4/8/12/16mA	Low	NLD10	Aux. Mode 1	GPIO196	IO-NLD10	IO-ENT0T_7x	0-CLKM5_7x	0-PWM1_7x			
DVDD_NFI	F27	4/8/12/16mA	High>Note1	NCEB0	Aux. Mode 1	GPIO197	IO-NCEB0	IO-ENT19_6x					
DVDD_NFI	D29	4/8/12/16mA	Low>Note1	NLD4	Aux. Mode 1	GPIO198	IO-NLD4	IO-ENT26_6x					
DVDD_NFI	E28	4/8/12/16mA	High>Note1	NWEB	Aux. Mode 1	GPIO199	IO-NWEB	IO-ENT18_6x					
DVDD_NFI	C29	4/8/12/16mA	Low	NALF	Aux. Mode 1	GPIO200	0-NALF	IO-ENT16_6x					
DVDD_NFI	B30	4/8/12/16mA	Low	NLD15	Aux. Mode 1	GPIO201	IO-NLD15	IO-ENT5T_7x	0-CLKM0_7x	0-PWM6_7x			
DVDD_NFI	B29	4/8/12/16mA	Low>Note1	NLD14	Aux. Mode 1	GPIO202	IO-NLD14	IO-ENT4T_7x	0-CLKM1_7x	0-PWM5_7x			
DVDD_NFI	D28	4/8/12/16mA	Low>Note1	NLD2	Aux. Mode 1	GPIO203	IO-NLD2	IO-ENT24_6x					
DVDD_NFI	A29	4/8/12/16mA	Low>Note1	NRNB	Aux. Mode 1	GPIO204	IO-NRNB	IO-ENT21_6x					
DVDD_NFI	E26	4/8/12/16mA	Low>Note1	NLD1	Aux. Mode 1	GPIO205	IO-NLD1	IO-ENT23_6x					
DVDD_NFI	A28	4/8/12/16mA	Low>Note1	NLD8	Aux. Mode 1	GPIO206	IO-NLD8	IO-ENT30_6x	0-UTXD3_7x	0-gps_sync_3x			
DVDD_NFI	C28	4/8/12/16mA	Low>Note1	NLD7	Aux. Mode 1	GPIO207	IO-NLD7	IO-ENT29_6x					
DVDD_NFI	B27	4/8/12/16mA	Low>Note1	NCLE	Aux. Mode 1	GPIO208	0-NCLE						
DVDD_NFI	E27	4/8/12/16mA	Low	NLD13	Aux. Mode 1	GPIO209	IO-NLD13	IO-ENT3T_7x	0-CLKM2_7x	0-PWM4_7x			
DVDD_NFI	B28	4/8/12/16mA	Low	NLD0	Aux. Mode 1	GPIO210	IO-NLD0	IO-ENT2T_6x					
DVDD_CAM	B6	4/8/12/16mA	Low	CMDAT2	Aux. Mode 1	GPIO211	0-CMDAT2	IO-ENT20_7x	0-CAM_CS2_S1_1x	0-TBRXFS			
DVDD_CAM	B6	4/8/12/16mA	Low	CMCMCLK	Aux. Mode 1	GPIO212	0-CMCMCLK	IO-ENT6T_7x	0-CMCMCLK_S1_1x				
DVDD_CAM	B5	4/8/12/16mA	Low	CMDAT7	Aux. Mode 1	GPIO213	0-CMDAT7	IO-ENT15T_7x	0-CAM_CS3_S2_1x				
DVDD_CAM	C4	4/8/12/16mA	Low	CMDAT0	Aux. Mode 1	GPIO214	0-CMDAT0	IO-ENT22T_7x	0-CAM_CS0_S1_1x	0-TBTXFS			
DVDD_CAM	A4	4/8/12/16mA	Low	CMDAT4	Aux. Mode 1	GPIO215	0-CMDAT4	IO-ENT18T_7x	0-CAM_CS2_S2_1x				
DVDD_CAM	B4	4/8/12/16mA	Low	CMDAT6	Aux. Mode 1	GPIO216	0-CMDAT6	IO-ENT16T_7x	0-CAM_CS2_S1_1x				
DVDD_CAM	B4	4/8/12/16mA	Low	CMDAT3	Aux. Mode 1	GPIO217	0-CMDAT3	IO-ENT19T_7x	0-CAM_CS3_S1_1x				
DVDD_CAM	D6	4/8/12/16mA	Low	CMDAT8	Aux. Mode 1	GPIO218	0-CMDAT8	IO-ENT23T_7x			0-DSP_GP00		
DVDD_CAM	A3	4/8/12/16mA	Low	CMDAT9	Aux. Mode 1	GPIO219	0-CMDAT9	IO-ENT24T_7x			0-DSP_GP01		
DVDD_CAM	K11	4/8/12/16mA	Low	PWM4	Aux. Mode 1	GPIO220	0-PWM4_6x	IO-ENT9T_7x					
DVDD_CAM	B2	4/8/12/16mA	Low	CMRST	Aux. Mode 1	GPIO221	0-CMRST	IO-ENT12T_7x	0-CMPDN_S2_1x				
DVDD_CAM	K10	4/8/12/16mA	Low	SCL_1	Aux. Mode 1	GPIO222	IO-SCL_1_2x	IO-ENT7T_7x	IO-SCL_1_5x			10-dbg_in[32]	
DVDD_CAM	A2	4/8/12/16mA	Low	CMDAT5	Aux. Mode 1	GPIO223	0-CMDAT5	IO-ENT17T_7x	0-CAM_CS1_S2_1x				
DVDD_CAM	K9	4/8/12/16mA	Low	SDA_1	Aux. Mode 1	GPIO224	IO-SDA_1_2x	IO-ENT8T_7x	IO-SDA_1_5x				
DVDD_CAM	B3	4/8/12/16mA	Low	CMVSYNC	Aux. Mode 1	GPIO225	0-CMVREF	IO-ENT11T_7x					
DVDD_CAM	E7	4/8/12/16mA	Low	CMPLCK	Aux. Mode 1	GPIO226	0-CMPLCK	IO-ENT13T_7x	0-CAM_CSK_S1_1x				
DVDD_CAM	B1	4/8/12/16mA	Low	CMHSYNC	Aux. Mode 1	GPIO227	0-CMHSYNC	IO-ENT10T_7x	0-CAM_CSK_S2_1x				
DVDD_CAM	E5	4/8/12/16mA	Low	CMCPDN	Aux. Mode 1	GPIO228	0-CMPDN	IO-ENT14T_7x					
DVDD_CAM	C3	4/8/12/16mA	Low	CMDAT1	Aux. Mode 1	GPIO229	0-CMDAT1	IO-ENT21T_7x	0-CAM_CS1_S1_1x	0-TBRXEN			
DVDD_CAM	D4	4/8/12/16mA	Low	CMFLASH	Aux. Mode 1	GPIO230	0-CMFLASH_1x	IO-ENT26T_7x	0-CMCMCLK_S2_1x	0-DSP_GP02			
Note 1: 不建议客户用来作为关键器件的使能 (Do not recommend customers use the device as a LDO enable / Backlight enable / Speaker enable...)													