Rev.	History
0.2	BPI_BUS8 IO PWR corrected to be DVDD_BPI / Add Driving information
0.3	Add GPIO reset default mode
	Remove PU/PD Status
	Add IO Reset Value

Update By	Date
Peng Chen	2012/01/04
Peng Chen	2012/01/30

			r				1	ī					
DVDD18_MIPIIO	D2	2/4/6/8mA	Low	PAD_RDP1		GPIO0	I0:EINTO_0x						
DVDD18_MIPIIO DVDD18_MIPIIO DVDD18_MIPIIO	C2 E2 D1	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	PAD_RDN1 PAD_RCP PAD_RCN	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO10 GPIO2 GPIO1	I0:EINT1_0x I0:EINT2_0x I0:EINT3_0x						
DVDD18_MIPIIO DVDD18_MIPIIO DVDD18_MIPIIO	F3 E3 F1	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	PAD_RDP0 PAD_RDN0 PAD_TDP0		GPIO4 GPIO3 GPIO5	I0:EINT4_0x I0:EINT5_0x I0:EINT6_0x						
OVDD18_MIPIIO OVDD18_MIPIIO	F2 G5	2/4/6/8mA 2/4/6/8mA	Low Low	PAD_TDN0 PAD_TCP	Aux. Mode 1 Aux. Mode 1	GPIO6 GPIO7	I0:EINT7_0x I0:EINT8_0x						
OVDD18_MIPIIO OVDD18_MIPIIO OVDD18_MIPIIO	H4 G2 G1	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	PAD_TCN PAD_TDP1 PAD_TDN1	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO9 GPIO8 GPIO11	I0:EINT9_0x I0:EINT10_0x I0:EINT11_0x						
OVDD_LCD OVDD_LCD OVDD_LCD	J3 K4 I2	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low High Low	LPA0 LPCE1B LPTE	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO12 GPIO13 GPIO14	O:LPA0_1x O:LPCE1B_1x IO:LPTE_1x	I0:EINT16_1x I0:EINT14_1x I0:EINT12_1x	0:PWM2_1x				
OVDD_LCD OVDD_LCD	K3 K1	4/8/12/16mA 4/8/12/16mA	High High	LWRB LPCE0B	Aux. Mode 1 Aux. Mode 1	GPIO15 GPIO16	0:LWRB_1x 0:LPCE0B_1x	I0:EINT18_1x I0:EINT15_1x	0:PWM6_1x 0:PWM3_1x				
OVDD_LCD OVDD_LCD OVDD_LCD	K5 K2 M5	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	High High High	LRDB LRSTB DPIHSYNC	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO17 GPIO18 GPIO19	O:LRDB_1x O:LRSTB_1x IOO:DPIHSYNC_1x	I0:EINT17_1x I0:EINT13_1x I0:EINT22_1x					
OVDD_LCD OVDD_LCD OVDD_LCD	L2 L5 M1	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low High Low	DPIB6 DPIVSYNC DPIR5	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO20 GPIO21 GPIO22	IO0:DPIB_PO_1x[6] IO0:DPIVSYNC_1x IO0:DPIR_PO_1x[5]		I:CM2VREF_1x I:CM2DAT5_1x			0:dbg_out0[17] 0:dbg_out0[10]	
VDD_LCD VDD_LCD	N7 M2	4/8/12/16mA 4/8/12/16mA	Low Low	DPIB2 DPIB5	Aux. Mode 1 Aux. Mode 1	GPIO23 GPIO24	IO0:DPIB_PO_1x[2] IO0:DPIB_PO_1x[5]	I0:EINT12_2x I0:EINT9_2x	0:I2S1_CK_1x I00:I2S0_CK_1x			0:dbg_out0[21] 0:dbg_out0[18]	
VDD_LCD VDD_LCD VDD_LCD	N2 N8 N1	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	DPIB0 DPIR7 DPIR4	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO25 GPIO26 GPIO27	IO0:DPIB_PO_1x[0] IO0:DPIR_PO_1x[7] IO0:DPIR_PO_1x[4]	I0:EINT23_1x	0:I2S1_WS_1x I:CM2DAT7_1x I:CM2DAT4_1x			0:dbg_out0[23] 0:dbg_out0[8] 0:dbg_out0[11]	
VDD_LCD VDD_LCD VDD_LCD	N4 N3 N5	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	DPIG3 DPIB4 DPIDE	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO28 GPIO29 GPIO30	IO0:DPIG_PO_1x[3] IO0:DPIB_PO_1x[4] IO0:DPIDE_1x	I0:EINT3_2x I0:EINT10_2x	IO0:I2S0_DAT_1x I:CM2HREF_1x			0:dbg_out0[4] 0:dbg_out0[19]	
VDD_LCD VDD_LCD	P2 P6	4/8/12/16mA 4/8/12/16mA	Low Low	DPIB3 DPIR6	Aux. Mode 1 Aux. Mode 1	GPIO31 GPIO32	IO0:DPIB_PO_1x[3] IO0:DPIR_PO_1x[6]	I0:EINT11_2x I0:EINT24_1x	I00:I2S0_WS_1x			0:dbg_out0[20] 0:dbg_out0[9]	
VDD_LCD VDD_LCD VDD_LCD	P4 P5 R1	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	DPIG2 DPIG7 DPIR2	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO33 GPIO34 GPIO35	IO0:DPIG_PO_1x[2] IO0:DPIG_PO_1x[7] IO0:DPIR_PO_1x[2]	I0:EINT4_2x I0:EINT31_1x I0:EINT28_1x	O:CM2RST_1x I:CM2DAT2_1x			0:dbg_out0[5] 0:dbg_out0[0] 0:dbg_out0[13]	
VDD_LCD VDD_LCD	P8 R5	4/8/12/16mA 4/8/12/16mA	Low Low	DPIB1 DPIG0	Aux. Mode 1 Aux. Mode 1	GPIO36 GPIO37	IO0:DPIB_PO_1x[1] IO0:DPIG_PO_1x[0]	I0:EINT13_2x I0:EINT6_2x	0:I2S1_DAT_1x			0:dbg_out0[22] 0:dbg_out0[7]	
VDD_LCD VDD_LCD VDD_LCD	P3 P7 R2	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	DPIG6 DPIG4 DPIR3	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO38 GPIO39 GPIO40	IO0:DPIG_PO_1x[6] IO0:DPIG_PO_1x[4] IO0:DPIR_PO_1x[3]	I0:EINT2_2x	O:CM2PDN_1x I:CM2DAT3_1x			0:dbg_out0[1] 0:dbg_out0[3] 0:dbg_out0[12]	
VDD_LCD VDD_LCD	T4 T1	4/8/12/16mA 4/8/12/16mA	High Low	DPICK DPIB7	Aux. Mode 1 Aux. Mode 1	GPIO41 GPIO42	IO0:DPICK_1x IO0:DPIB_PO_1x[7]	I0:EINT19_1x I0:EINT7_2x	O:CM2MCLK_1x			0:dbg_out0[16]	
VDD_LCD VDD_LCD VDD_LCD	T5 T2 U4	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	DPIG1 DPIR0 DPIR1	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO43 GPIO44 GPIO45	IO0:DPIG_PO_1x[1] IO0:DPIR_PO_1x[0] IO0:DPIR_PO_1x[1]	I0:EINT29_1x	I:CM2DAT0_1x I:CM2DAT1_1x			0:dbg_out0[6] 0:dbg_out0[15] 0:dbg_out0[14]	
VDD_LCD VDD_LCD VDD_LCD	U5 V4 U3	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low High Low	DPIG5 LSCE0B LSDI	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO46 GPIO47 GPIO48	IO0:DPIG_PO_1x[5] O:LSCE0B_1x I1:LSDI_1x	I0:EINT1_2x I0:EINT11_8x	I1:URXD3_1x O:UTXD2_1x	IO0:SCL_2_1x IO0:SDA_1_1x	I0:EINT10_8x	O:dbg_out0[2] O:TCTIRQ1_1x	
VDD_LCD VDD_LCD	V3 U2	4/8/12/16mA 4/8/12/16mA	Low High	LSA0 LSCE1B	Aux. Mode 1 Aux. Mode 1	GPIO49 GPIO50	0:LSA0_1x 0:LSCE1B_1x	O:MD_TXD1_1x	I1:MD_RXD1_7x O:UTXD3_1x	IO0:SDA_0_1x IO0:SDA_2_1x	I0:EINT13_8x I0:EINT19_8x	0:TDTIRQ_1x 0:TEVTVAL_1x	
VDD_LCD VDD_LCD VDD_NML1	V5 V2 V1	4/8/12/16mA 4/8/12/16mA 2/4/6/8mA	High Low Low	LSCK LSDA I2S0_DAT	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO51 GPIO52 GPIO53	0:LSCK_1x I00:LSDA_1x I00:I2S0_DAT_2x	I1:MD_RXD1_1	O:MD_TXD1_7x I1:URXD2_1x O:CLKM4_1x	IO0:SCL_0_1x IO0:SCL_1_1x O:I2S1_DAT_4x	I0:EINT20_8x I0:EINT23_8x	0:TCTIRQ2_1x 0:dbg_out0[24]	
VDD_NML1 VDD_NML1 VDD_NML1	Y6 W2 W5	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	High High High	I2S0_WS I2S0_CK I2S1_CK	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPI054 GPI055 GPI056	I00:I2S0_WS_2x I00:I2S0_CK_2x O:I2S1_CK_2x	I0:EINT15_2x	0:CLKM5_1x 0:CLKM3_1x I0:IRDA_RXD_1x	0:I2S1_WS_4x 0:I2S1_CK_4x I00:I2S0_CK_6x		0:dbg_out0[25] 0:dbg_out0[26] 0:dbg_out0[27]	
/DD_NML1 /DD_NML1	W1 Y5	2/4/6/8mA 2/4/6/8mA	High Low	I2S1_WS I2S1_DAT	Aux. Mode 1 Aux. Mode 1	GPIO57 GPIO58	0:I2S1_WS_2x 0:I2S1_DAT_2x	I0:EINT1_3x I0:EINT0_3x	0:IRDA_PDN_1x 0:IRDA_TXD_1x	IO0:I2S0_WS_6x IO0:I2S0_DAT_6x		0:dbg_out0[27] 0:dbg_out0[28] 0:dbg_out0[29]	
/DD_NML1 /DD_NML1 /DD_NML1	Y2 Y4 AA1	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	DAIRSTB EINT8 EINT9	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO59 GPIO60 GPIO61	I0:DAIRSTB I0:EINT8_1x I0:EINT9_1x	I0:EINT5_3x	0:PWM6_2x 0:PWM3_2x 0:PWM4_2x	I1:MD_RXD2_1x 0:watchdog_rstb 0:gps_sync_4x	0:DSP_GP03 0:watchdog_rstb		
VDD_NML1 VDD_NML1	AA2 AA3	2/4/6/8mA 2/4/6/8mA	Low High	DAIPCMOUT DAICLK	Aux. Mode 1 Aux. Mode 1	GPIO62 GPIO63	O:DAIPCMOUT O:DAICLK	I0:EINT2_3x I0:EINT3_3x	0:PWM0_2x 0:PWM1_2x	I00:I2S0_DAT_3x I00:I2S0_CK_3x	<u></u>		
VDD_NML1 VDD_NML1 VDD_NML2	AA4 AB1 AB4	2/4/6/8mA 2/4/6/8mA 4/8/12/16mA	Low Low Low	DAIPCMIN BTSYNC PWM2	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO64 GPIO65 GPIO66	I0:DAIPCMIN 0:BTSYNC 0:PWM2_6x	I0:EINT7_3x I0:EINT10_3x	0:PWM2_2x 0:PWM5_2x I0:IRDA_RXD_2x	I00:I2S0_WS_3x I1:URXD3_2x	O:UTXD3_3x	0:dbg_out1[17]	0:pwm1_3d_2x_
VDD_NML2 VDD_NML2 VDD_NML2	AB2 AB3 AA5	4/8/12/16mA 4/8/12/16mA 2/4/6/8mA	Low Low Low	PWM1 PWM3 EINT3	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPI067 GPI068 GPI069	0:PWM1_6x 0:PWM3_6x I0:EINT3_1x			O:MD_TXD2_1x O:UTXD3_2x O:CLKM2_2x	I1:URXD3_3x	0:dbg_out1[16] 0:dbg_out1[18] I0:dbg_in[9]	
VDD_NML2 VDD_NML2	AC2 AD1	2/4/6/8mA 4/8/12/16mA	Low Low	EINTO UTXD3	Aux. Mode 1 Aux. Mode 1	GPIO70 GPIO71	I0:EINT0_1x O:UTXD3_8x	O:PWM0_4x I0:EINT17_3x	I1:URXD2_6x I1:URXD3_4x	0:CLKM5_2x 0:PWM6_3x	O:SPI_CSN_1x IOO:DSP_EMU_B_1x	I0:dbg_in[6] I0:dbg_in[3]	
VDD_NML2 VDD_NML2 VDD_NML2	AD4 AB5 AE1	2/4/6/8mA 2/4/6/8mA 4/8/12/16mA	Low Low Low	EINT4 EINT7 UTXD2	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO72 GPIO73 GPIO74	I0:EINT4_1x I0:EINT7_1x O:UTXD2_4x	O:CLKM0_2x	O:MD_TXD2_3x O:IRDA_TXD_3x I1:URXD2_5x	0:CLKM1_2x I00:I2S0_WS_4x 0:PWM2_3x	-	I0:dbg_in[10] I0:dbg_in[13] I0:dbg_in[15]	
VDD_NML2 VDD_NML2	AA6 AD2	2/4/6/8mA 2/4/6/8mA	Low Low	EINT6 EINT1	Aux. Mode 1 Aux. Mode 1	GPIO75 GPIO76	I0:EINT6_1x I0:EINT1_1x	0:PWM6_4x 0:PWM1_4x	I0:IRDA_RXD_3x O:UTXD2_6x	IO0:I2S0_DAT_4x O:CLKM4_2x	I1:MD_RXD2_3x O:SPI_CLK_1x	I0:dbg_in[12] I0:dbg_in[7]	
VDD_NML2 VDD_NML2 VDD_NML2	AC5 AE2 AD5	4/8/12/16mA 2/4/6/8mA 4/8/12/16mA	Low High	URXD2 EINT5 URXD3	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPI077 GPI078 GPI079	I1:URXD2_4x I0:EINT5_1x I1:URXD3_8x	0:PWM5_4x	O:UTXD2_5x O:IRDA_PDN_3x O:UTXD3_4x	0:PWM1_3x I00:I2S0_CK_4x 0:PWM5_3x	0:usb_drvvbus_3x	I0:dbg_in[14] I0:dbg_in[11] I0:dbg_in[2]	
VDD_NML2 VDD_NML2 VDD_NML2	AE3 AE4 AF2	4/8/12/16mA 2/4/6/8mA 4/8/12/16mA	Low Low High	UCTS2 EINT2 URTS2	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO80 GPIO81 GPIO82	I1:UCTS2_4x I0:EINT2_1x O:URTS2_4x	0:PWM2_4x	O:MD_TXD2_2x O:MD_TXD1_2x I1:MD_RXD2_2x	0:PWM3_3x 0:CLKM3_2x 0:PWM4_3x	I1:DSP_JTMS_1x I0:SPI_MI_1x I1:DSP_JTDI_1x	I0:dbg_in[8]	
VDD_NML3 VDD_NML3	AF4 AG3	4/8/12/16mA 4/8/12/16mA	High Low	SPI_CSN SPI_MI	Aux. Mode 1 Aux. Mode 1	GPIO83 GPIO84	O:SPI_CSN_2x I0:SPI_MI_2x	I0:EINT18_3x I0:EINT20_3x	O:IRDA_PDN_4x O:IRDA_TXD_4x	O:SPI_CSN_3x O:SPI_MO_3x	11.001_]101_1x		
VDD_NML3 VDD_NML3 VDD_NML3	AG2 AH1 AG1	4/8/12/16mA 4/8/12/16mA 4/8/12/16mA	Low Low High	SPI_MO SPI_CLK SCL_0	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO85 GPIO86 GPIO87	O:SPI_MO_2x O:SPI_CLK_2x IO0:SCL_0_2x	I0:EINT21_3x I0:EINT19_3x I0:EINT22_3x	O:CLKM5_3x I0:IRDA_RXD_4x O:CLKM4_3x	I0:SPI_MI_3x O:SPI_CLK_3x		0:dbg_out0[30] I0:dbg_in[0]	
VDD_NML3 VDD_MC3	AH2 AG4	4/8/12/16mA 16/20/24/28/40/44/48/52mA	High High	SDA_0 MSDC3_DAT2	Aux. Mode 1 Aux. Mode 1	GPIO88 GPIO89	IO0:SDA_0_2x IO0:MSDC3_DAT2	I0:EINT23_3x I0:EINT28_3x	O:CLKM3_3x I1:URXD3_5x	O:CLKM1_4x	O:UTXD3_6x	I0:dbg_in[1] O:dbg_out1[4]	
VDD_MC3 VDD_MC3 VDD_MC3	AH3 AJ3 AH4	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA	High High Low	MSDC3_DAT1 MSDC3_CMD MSDC3_CLK	Aux. Mode 1 Aux. Mode 1 Aux. Mode 1	GPIO90 GPIO91 GPIO92	IO0:MSDC3_DAT1 IO0:MSDC3_CMD IO0:MSDC3_CLK	I0:EINT25_3x	I1:MD_RXD2_4x I1:MD_RXD1_3x O:MD_TXD1_3x	O:CLKM2_4x O:CLKM4_4x O:CLKM5_4x	0:MD_TXD2_5x 0:MD_TXD1_4x I1:MD_RXD1_4x	0:dbg_out1[0] 0:dbg_out1[1] 0:dbg_out1[2]	
VDD_MC3 VDD_MC3 VDD_NML4	AK3 AJ4 AF6	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA 2/4/6/8mA	High High Low	MSDC3_DAT3 MSDC3_DAT0 KP_ROW2	Aux. Mode 1 Aux. Mode 1 Aux. Mode 0	GPIO93 GPIO94 GPIO95	IO0:MSDC3_DAT3 IO0:MSDC3_DAT0 IO1:KP_ROW2	I0:EINT29_3x I0:EINT26_3x		0:CLKM0_4x 0:CLKM3_4x 0:I2S1_DAT_3x	I1:URXD3_6x I1:MD_RXD2_5x	0:dbg_out1[3] 0:dbg_out0[31] I0:dbg_in[29]	
VDD_NML4 VDD_NML4	AJ5 AC10	2/4/6/8mA 2/4/6/8mA	Low Low	KP_COL7 KP_ROW1	Aux. Mode 0 Aux. Mode 0	GPIO96 GPIO97	I1:KP_COL7 IO1:KP_ROW1	I0:EINT5_4x I0:EINT7_4x	O:CLKM5_5x O:CLKM3_5x	IO0:I2S0_DAT_5x O:I2S1_CK_3x		I0:dbg_in[16] I0:dbg_in[30]	
VDD_NML4 VDD_NML4 VDD_NML4	AG6 AF7 AG7	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	KP_ROW0 KP_ROW3 KP_COL5	Aux. Mode 1 Aux. Mode 0 Aux. Mode 0	GPIO98 GPIO99 GPIO100	IO1:KP_ROW0 IO1:KP_ROW3 I1:KP_COL5	I0:EINT6_4x I0:EINT9_4x I0:EINT3_4x	0:CLKM4_5x 0:CLKM1_5x 0:PWM5_5x	I00:I2S0_WS_5x 0:I2S1_WS_3x I0:DSP_JTCK_2x		I0:dbg_in[31] I0:dbg_in[28] I0:dbg_in[18]	
VDD_NML4 VDD_NML4	AF8 AH7	2/4/6/8mA 2/4/6/8mA	Low Low	KP_COL3 KP_COL4	Aux. Mode 0 Aux. Mode 0	GPIO101 GPIO102	I1:KP_COL3 I1:KP_COL4	I0:EINT1_4x I0:EINT2_4x	0:PWM3_5x 0:PWM4_5x	I0:DSP_JTRST_B_2x IO0:DSP_EMU_B_2x		I0:dbg_in[20] I0:dbg_in[19]	
VDD_NML4 VDD_NML4 VDD_NML4	AF9 AJ6 AB11	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low	KP_COL0 KP_ROW4 KP_COL2	Aux. Mode 1 Aux. Mode 0 Aux. Mode 0	GPIO104 GPIO105	I1:KP_COL0 IO1:KP_ROW4 I1:KP_COL2	I0:EINT10_4x I0:EINT0_4x	0:PWM0_5x 0:CLKM0_5x 0:PWM2_5x	I1:DSP_JTDI_2x I00:SCL_0_3x O:DSP_JTDO_2x		I0:dbg_in[23] I0:dbg_in[27] I0:dbg_in[21]	
VDD_NML4 VDD_NML4 VDD_NML4	AK6 AK7 AJ7	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA		KP_ROW5 KP_ROW7 KP_COL1	Aux. Mode 0	GPIO107	IO1:KP_ROW5 IO1:KP_ROW7 I1:KP_COL1		O:IRDA_TXD_6x	IO0:SDA_0_3x IO0:SDA_1_3x I1:DSP_JTMS_2x		I0:dbg_in[26] I0:dbg_in[24] I0:dbg_in[22]	
/DD_NML4 /DD_NML4	AG8 AH8	2/4/6/8mA 2/4/6/8mA	Low Low	KP_COL6 KP_ROW6	Aux. Mode 0 Aux. Mode 0	GPIO109 GPIO110	I1:KP_COL6 IO1:KP_ROW6	I0:EINT4_4x I0:EINT12_4x	0:PWM6_5x 10:IRDA_RXD_6x	IO:I2SO_CK_5x IO0:SCL_1_3x	I0:ext_frame_sync_2x	I0:dbg_in[17] I0:dbg_in[25]	
/DD_BPI /DD_BPI /DD_BPI	AF10 AD11 AJ9	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	BPI_BUS3 TDD_AFC_SW BPI_BUS1	Aux. Mode 1	GPIO112	O:DUAL_BPI_BUS3 O:TDD_AFC_SW O:DUAL_BPI_BUS1	I0:EINT17_4x I0:EINT29_4x I0:EINT15_4x	O:IRDA_TXD_7x	IO0:SDA_1_4x		0:dbg_out1[22] 0:dbg_out1[20]	
VDD_BPI VDD_BPI VDD_BPI	AG10 AK9 AJ10	2/4/6/8mA 2/4/6/8mA	Low Low	BPI_BUS4 BPI_BUS0 BPI_BUS5	Aux. Mode 1 Aux. Mode 1	GPIO114 GPIO115	O:DUAL_BPI_BUS4 O:DUAL_BPI_BUS0 O:DUAL_BPI_BUS5	I0:EINT18_4x I0:EINT14_4x I0:EINT19_4x				0:dbg_out1[23] 0:dbg_out1[19]	
DD_BPI DD_BPI	AJ8 AF11	2/4/6/8mA 2/4/6/8mA 4/8/12/16mA	Low Low Low	BPI_BUS2 BPI_BUS8	Aux. Mode 1 Aux. Mode 1	GPIO117 GPIO118	O:DUAL_BPI_BUS2 O:DUAL_BPI_BUS8	I0:EINT16_4x I0:EINT0_5x	0:gps_sync_1x			0:dbg_out1[24] 0:dbg_out1[21] 0:dbg_out1[27]	
OD_BPI OD_BPI OD_BPI	AK10 AJ11 AH11	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	BPI_BUS9 BPI_BUS7 BPI_BUS6	Aux. Mode 1	GPIO120	O:DUAL_BPI_BUS9 O:DUAL_BPI_BUS7 O:DUAL_BPI_BUS6	I0:EINT22_4x I0:EINT21_4x I0:EINT20_4x				0:dbg_out1[28] 0:dbg_out1[26] 0:dbg_out1[25]	
/DD_BPI /DD_BPI	AF12 AG11	2/4/6/8mA 2/4/6/8mA	Low Low	TDD_TIMINGSYNO BPI_BUS10	Aux. Mode 1 Aux. Mode 1	GPIO122 GPIO123	O:TDD_TIMINGSYNC O:DUAL_BPI_BUS10	I0:EINT28_4x I0:EINT23_4x	I0:IRDA_RXD_7x	IO0:SCL_1_4x		0:dbg_out1[25]	
/DD_BPI /DD_BPI /DD_BPI	AH12 AK12 AJ12	2/4/6/8mA 2/4/6/8mA 2/4/6/8mA	Low Low Low	BPI_BUS14 BPI_BUS13 BPI_BUS11	Aux. Mode 1	GPIO125	O:DUAL_BPI_BUS14 O:DUAL_BPI_BUS13 O:DUAL_BPI_BUS11	I0:EINT26_4x	O:IRDA_PDN_7x I0:ext_frame_sync_1x I0:ext_frame_sync_3x			0:dbg_out1[30]	
VDD_BPI VDD_BPI	AG12 AJ13	2/4/6/8mA 2/4/6/8mA	Low Low	BPI_BUS12 VM1	Aux. Mode 1 Aux. Mode 1	GPIO127 GPIO128	O:DUAL_BPI_BUS12 O:VM1	I0:EINT25_4x I0:EINT31_4x				0:dbg_out1[31]	
VDD_BPI VDD_BSI VDD_BSI	AK13 AC14 AJ14	2/4/6/8mA 4/8/12/16mA 4/8/12/16mA	Low Low Low	VM0 BSI0_CS1 BSI1_DATA	Aux. Mode 1 Aux. Mode 1	GPIO130 GPIO131	0:VM0 0:BSI0_CS1 IO0:BSI1_DATA	I0:EINT30_4x I0:EINT4_5x I0:EINT6_5x					
VDD_BSI VDD_BSI	AG14 AH15	4/8/12/16mA 4/8/12/16mA	Low Low	BSI1_CS1 BSI1_CS0	Aux. Mode 1 Aux. Mode 1	GPI0132 GPI0133	IO0:BSI1_DATA1_1x O:BSI1_CS0	I0:EINT8_5x I0:EINT7_5x	0:BSI1_CS1				
VDD_BSI VDD_BSI	AG15 AF14	4/8/12/16mA 4/8/12/16mA	Low Low	BSI1_CLK BSI0_DATA			0:BSI1_CLK IO0:BSI0_DATA	I0:EINT5_5x I0:EINT3_5x					

VDD_BSI	AB14	4/8/12/16mA		BSIO_CSO	Aux. Mode 1	GPI0136 0:BSI0_CS0	I0:EINT1_5x					
VDD_BSI VDD30_VSIM2	AA13 AD30	4/8/12/16mA	Low Low	BSIO_CLK PAD_SIO2	Aux. Mode 1 Aux. Mode 1	GPIO137 O:BSIO_CLK GPIO138 O:CLKM2_8x	I0:EINT2_5x I0:EINT9_5x	0:PWM0_6x	O:IRDA_PDN_8x		<del>                                     </del>	
VDD30_VSIM2	AD29			PAD_SRST2 PAD_SCLK2	Aux. Mode 1	GPIO139 O:CLKM1_8x	I0:EINT10_5x	0:PWM5_6x	I0:IRDA_RXD_8x			
VDD30_VSIM2 VDD_NML5	AD28 Y29	2/4/6/8mA	Low Low	IDDIG	Aux. Mode 1 Aux. Mode 1	GPI0140 0:CLKM0_8x GPI0141 I1:IDDIG	I0:EINT11_5x I0:EINT12_5x	U:PWM6_6X	O:IRDA_TXD_8x			
VDD_NML5 VDD_NML5	Y26 W29	2/4/6/8mA 2/4/6/8mA	Low	SDA_2 PACTRL2	Aux. Mode 1 Aux. Mode 1	GPIO142 IO0:SDA_2_2x GPIO143 O:PACTRL2	I0:EINT14_5x I0:EINT19_5x				Ordba out1[15]	
VDD_NML5	W27	2/4/6/8mA	_	PMUCTRL0	Aux. Mode 1	GPIO144 O:PMUCTRL0	I0:EINT15_5x				0:dbg_out1[15] 0:dbg_out1[11]	
VDD_NML5 VDD_NML5	W26 U29	2/4/6/8mA 2/4/6/8mA	Low	SCL_2 SRCLKENAI	Aux. Mode 1 Aux. Mode 1	GPIO145 IO0:SCL_2_2x GPIO231 IO:SRCLKENAI	I0:EINT13_5x					
VDD_NML5	V28	2/4/6/8mA	Low	PACTRL1	Aux. Mode 1	GPIO146 O:PACTRL1	I0:EINT18_5x				0:dbg_out1[14]	
VDD_NML5 VDD_NML5	V27 V30	2/4/6/8mA 2/4/6/8mA		PMUCTRL1 PACTRL0	Aux. Mode 1 Aux. Mode 1	GPIO147 O:PMUCTRL1 GPIO148 O:PACTRL0	I0:EINT16_5x I0:EINT17_5x				0:dbg_out1[12] 0:dbg_out1[13]	
VDD_NML6	U26	4/8/12/16mA	Low	SWCLKTCK	Aux. Mode 1	GPIO149 I0:SWCLKTCK	10.BHV117_5X				0.005_0001[10]	
VDD_NML6 VDD_NML6	T26 T30	4/8/12/16mA 4/8/12/16mA	Low High	SWDIOTMS JTCK	Aux. Mode 1 Aux. Mode 1	GPIO150 IO0:SWDIOTMS GPIO151 IO:JTCK	I0:DSP_JTCK_3x	<u> </u> x			<del>                                     </del>	
VDD_NML6	U28	4/8/12/16mA	Low	JTD0	Aux. Mode 1	GPIO152 O:JTDO	O:DSP_JTDO_3x	K.				
VDD_NML6 VDD_NML6	R30 T29	4/8/12/16mA 4/8/12/16mA	Low High	JTRST_B JTDI	Aux. Mode 1 Aux. Mode 1	GPIO153 I0:JTRST_B GPIO154 I1:JTDI	I0:DSP_JTRST_I				+	
VDD_NML6	R29	4/8/12/16mA	Low	JRTCK	Aux. Mode 1	GPIO155 O:JRTCK	IOO:DSP_EMU_					
VDD_NML6 VDD_NML6	U27 R27	4/8/12/16mA 4/8/12/16mA	High	JTMS UTXD1	Aux. Mode 1 Aux. Mode 1	GPI0156 I01:JTMS GPI0157 0:UTXD1_1x	I1:DSP_JTMS_3: I0:EINT20_5x		I1:MD_RXD1_5x	0:MD_TXD1_8x	<del>                                     </del>	
VDD_NML6 VDD_NML6	P28	4/8/12/16mA 4/8/12/16mA	II: ab	UTXD4 URXD4	Aux. Mode 1	GPIO158 O:UTXD4_1x GPIO159 I1:URXD4_1x	I0:EINT24_5x I0:EINT23_5x		I1:MD_RXD2_6x O:MD_TXD2_6x	0:MD_TXD2_7x	I0:dbg_in[5]	
/DD_NML6 /DD_NML6	R26 P26	4/8/12/16mA	High High	URXD4 URXD1	Aux. Mode 1 Aux. Mode 1	GPI0160 I1:URXD1_1x	10:EIN123_5X	0:UTXD4_2x 0:UTXD1_2x	0:MD_TXD2_6x 0:MD_TXD1_5x	I1:MD_RXD2_7x I1:MD_RXD1_8x	I0:dbg_in[4]	
/DD_NML6 /DD_NML6	P27 R25	4/8/12/16mA 4/8/12/16mA	Low	URTS1 UCTS1	Aux. Mode 1 Aux. Mode 1	GPIO161 O:URTS1_1x GPIO162 I1:UCTS1_1x		I1:MD_RXD1_6x O:MD_TXD1_6x				
/DD_NML6	N29	4/8/12/16mA	Low	MSDC0_INSI	Aux. Mode 1	GPIO163 I0:MSDC0_INSI	I0:EINT25_5x					
/DD_MC0 /DD_MC0	N30 M30	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA	High High	MSDC0_DAT6 MSDC0_DAT7	Aux. Mode 1 Aux. Mode 1	GPI0164 I00:MSDC0_DAT6 GPI0165 I00:MSDC0_DAT7	I0:EINT2_6x I0:EINT3_6x	I0:SPI_MI_6x O:SPI_MO_6x	IO0:NLD11_2x IO0:NLD3_2x		<del>                                     </del>	
VDD_MC0	P29	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT5	Aux. Mode 1	GPIO166 IO0:MSDC0_DAT5	I0:EINT1_6x	O:SPI_MO_6x O:SPI_CLK_6x	IO0:NLD3_2x IO0:NLD2_2x			
VDD_MC0 VDD MC0	L29 N28	4/8/12/16mA 16/20/24/28/40/44/48/52mA	High High	MSDCO_RSTB MSDCO_DAT4	Aux. Mode 1 Aux. Mode 1	GPIO167 O:MSDCO_RSTB GPIO168 IOO:MSDCO_DAT4	O:NCLE I0:EINTO_6x	O:SPI_CSN_6x	IO0:NLD10_2x		<del>                                     </del>	
VDD_MC0	K27	16/20/24/28/40/44/48/52mA	High	MSDC0_DAT2	Aux. Mode 1	GPIO169 IO0:MSDC0_DAT2	I0:EINT30_5x	IO0:NLD9_2x				
VDD_MC0 VDD_MC0	N26 M29	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA		MSDC0_DAT3 MSDC0_CMD	Aux. Mode 1 Aux. Mode 1	GPI0170 I00:MSDC0_DAT3 GPI0171 I00:MSDC0_CMD	I0:EINT31_5x I0:EINT27_5x			+	+ +	
VDD_MC0	M26	16/20/24/28/40/44/48/52mA	Low	MSDC0_CLK	Aux. Mode 1	GPIO172 IO0:MSDC0_CLK	I0:EINT26_5x					
VDD_MC0 VDD_MC0	L27 L26	16/20/24/28/40/44/48/52mA 4/8/12/16mA	High Low	MSDC0_DAT1 MSDC0_SDWPI	Aux. Mode 1 Aux. Mode 1	GPIO173 IO0:MSDC0_DAT1 GPIO174 I0:MSDC0_SDWPI	I0:EINT29_5x I0:EINT4_6x	IOU:NLD0_2x				
/DD_MC0	N27	16/20/24/28/40/44/48/52mA		MSDC0_DAT0	Aux. Mode 1	GPIO175 IO0:MSDC0_DAT0	I0:EINT28_5x	IO0:NLD8_2x				
/DD_MC1 /DD_MC1	K30 K29	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA	Low Low	MSDC1_DAT1 MSDC1_DAT2	Aux. Mode 1 Aux. Mode 1	GPIO176 IO0:MSDC1_DAT1 GPIO177 IO0:MSDC1_DAT2	I0:EINT7_6x I0:EINT8_6x				+	
VDD_MC1	K28	16/20/24/28/40/44/48/52mA	Low	MSDC1_DAT0	Aux. Mode 1	GPI0178 I00:MSDC1_DAT0	I0:EINT6_6x					
VDD_MC1 VDD_MC1	J27 K26	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA	Low Low	MSDC1_DAT3 MSDC1_CLK	Aux. Mode 1 Aux. Mode 1	GPI0179 I00:MSDC1_DAT3 GPI0180 I00:MSDC1_CLK	I0:EINT9_6x					
VDD_MC1 VDD_MC2	J26 H26	16/20/24/28/40/44/48/52mA	Low	MSDC1_CMD MSDC2_CLK	Aux. Mode 1 Aux. Mode 1	GPI0181 I00:MSDC1_CMD GPI0182 I00:MSDC2_CLK	I0:EINT5_6x I0:EINT10_6x		O:UTXD2_7x		Ordba out1[5]	
VDD_MC2 VDD_MC2	л26 J28	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA	Low High	MSDC2_CLK MSDC2_DAT3	Aux. Mode 1 Aux. Mode 1	GPIO183 IOO:MSDC2_CLK GPIO183 IOO:MSDC2_DAT3	I0:EINT15_6x	O:UTXD2_8x	0:SPI_MO_4x	I0:SPI_MI_5x	0:dbg_out1[5] 0:dbg_out1[10]	
VDD_MC2 VDD_MC2	J29 H29	16/20/24/28/40/44/48/52mA 16/20/24/28/40/44/48/52mA		MSDC2_CMD MSDC2_DAT2	Aux. Mode 1 Aux. Mode 1	GPIO184 IO0:MSDC2_CMD GPIO185 IO0:MSDC2_DAT2	I0:EINT11_6x I0:EINT14_6x	O:IRDA_PDN_5x	I1:URXD2_7x I0:SPI_MI_4x	O:SPI_MO_5x	0:dbg_out1[6] 0:dbg_out1[9]	
VDD_MC2	K24	16/20/24/28/40/44/48/52mA	High	MSDC2_DAT0	Aux. Mode 1	GPIO186 IO0:MSDC2_DAT0	I0:EINT12_6x	I0:IRDA_RXD_5x	O:SPI_CSN_4x	O:SPI_CSN_5x	0:dbg_out1[7]	
VDD_MC2 VDD_NFI	J30 G30	16/20/24/28/40/44/48/52mA 4/8/12/16mA	High Low	MSDC2_DAT1 NLD12	Aux. Mode 1 Aux. Mode 1	GPIO187 IO0:MSDC2_DAT1 GPIO188 IO0:NLD12		O:IRDA_TXD_5x O:CLKM3_7x	O:SPI_CLK_4x O:PWM3_7x	O:SPI_CLK_5x	0:dbg_out1[8]	
/DD_NFI	G27	4/8/12/16mA		NLD6	Aux. Mode 1	GPI0189 I00:NLD6	I0:EINT28_6x	O.CERWIS_7 X	0.1 WM3_7X			
VDD_NFI VDD_NFI	F30 F29	4/8/12/16mA 4/8/12/16mA	High	NREB NLD3	Aux. Mode 1 Aux. Mode 1	GPIO190 O:NREB GPIO191 IO0:NLD3	I0:EINT17_6x I0:EINT25_6x				<del>                                     </del>	
VDD_NFI	E29	4/8/12/16mA	Low	NLD11	Aux. Mode 1	GPI0192 I00:NLD11	I0:EINT1_7x	O:CLKM4_7x	O:PWM2_7x			
/DD_NFI /DD_NFI	G29 D30	4/8/12/16mA 4/8/12/16mA		NLD5 NLD9	Aux. Mode 1 Aux. Mode 1	GPIO193 IO0:NLD5 GPIO194 IO0:NLD9	I0:EINT27_6x I0:EINT31_6x	I1·IIRXD3 7x	O:PWM0_7x		<del>                                     </del>	
VDD_NFI	F28	4/8/12/16mA	High	NCEB1	Aux. Mode 1	GPIO195 O:NCEB1	I0:EINT20_6x		_			
VDD_NFI VDD_NFI	C30 F27	4/8/12/16mA 4/8/12/16mA	Low	NLD10 NCEB0	Aux. Mode 1 Aux. Mode 1	GPIO196 IO0:NLD10 GPIO197 O:NCEB0	I0:EINT0_7x I0:EINT19_6x	O:CLKM5_7x	O:PWM1_7x		1	
/DD_NFI	D29	4/8/12/16mA		NLD4	Aux. Mode 1	GPIO198 IO0:NLD4	I0:EINT26_6x	ļ		1		
VDD_NFI VDD_NFI	E28 C29	4/8/12/16mA 4/8/12/16mA		NWEB NALE	Aux. Mode 1 Aux. Mode 1	GPIO199 O:NWEB GPIO200 O:NALE	I0:EINT18_6x I0:EINT16_6x					
VDD_NFI	B30	4/8/12/16mA	Low	NLD15	Aux. Mode 1	GPIO201 IO0:NLD15	I0:EINT5_7x		0:PWM6_7x			
VDD_NFI VDD_NFI	B29 D28	4/8/12/16mA 4/8/12/16mA		NLD14 NLD2	Aux. Mode 1 Aux. Mode 1	GPI0203 I00:NLD2	I0:EINT24_6x	O:CLKM1_7x	O:PWM5_7x			
/DD_NFI /DD_NFI	A29 E26	4/8/12/16mA 4/8/12/16mA		NRNB NLD1	Aux. Mode 1 Aux. Mode 1	GPIO204 I0:NRNB GPIO205 IO0:NLD1	I0:EINT21_6x I0:EINT23_6x					
/DD_NFI	A28	4/8/12/16mA		NLD8	Aux. Mode 1	GPI0206 IO0:NLD8	I0:EINT30_6x	O:UTXD3_7x	0:gps_sync_3x			
/DD_NFI /DD_NFI	C28 B27	4/8/12/16mA 4/8/12/16mA		NLD7 NCLE	Aux. Mode 1 Aux. Mode 1	GPIO207 IO0:NLD7 GPIO208 O:NCLE	I0:EINT29_6x					
/DD_NFI	E27	4/8/12/16mA	Low	NLD13	Aux. Mode 1	GPI0209 I00:NLD13		O:CLKM2_7x	O:PWM4_7x			
/DD_NFI /DD_CAM	B28 B6	4/8/12/16mA 4/8/12/16mA	Low	NLD0 CMDAT2	Aux. Mode 1 Aux. Mode 1	GPIO210 IO0:NLD0 GPIO211 I:CMDAT2	I0:EINT22_6x	I:CAM_CSD2_S1_1x	O:TBRXFS		<del>                                     </del>	
/DD_CAM	E6	4/8/12/16mA	Low	CMMCLK	Aux. Mode 1	GPIO212 O:CMMCLK	I0:EINT6_7x	O:CMMCLK_S1_1x	J. I DIGH U			
/DD_CAM /DD_CAM	B5 C4	4/8/12/16mA 4/8/12/16mA		CMDAT7 CMDAT0	Aux. Mode 1 Aux. Mode 1	GPIO213 I:CMDAT7 GPIO214 I:CMDAT0		I:CAM_CSD3_S2_1x I:CAM_CSD0_S1_1x	O:TBTXFS		<del>                                     </del>	
/DD_CAM	A4	4/8/12/16mA	Low	CMDAT4	Aux. Mode 1	GPIO215 I:CMDAT4	I0:EINT18_7x	I:CAM_CSD0_S2_1x				
DD_CAM DD_CAM	E4 B4	4/8/12/16mA 4/8/12/16mA	Low Low	CMDAT6 CMDAT3	Aux. Mode 1 Aux. Mode 1	GPIO216 I:CMDAT6 GPIO217 I:CMDAT3		I:CAM_CSD2_S2_1x I:CAM_CSD3_S1_1x			+	
DD_CAM	D6	4/8/12/16mA	Low	CMDAT8	Aux. Mode 1	GPIO218 I:CMDAT8	I0:EINT23_7x		O:DSP_GPO0			
DD_CAM DD_CAM	A3 K11	4/8/12/16mA 4/8/12/16mA	Low Low	CMDAT9 PWM4	Aux. Mode 1 Aux. Mode 1	GPIO219 I:CMDAT9 GPIO220 O:PWM4_6x	I0:EINT24_7x I0:EINT9_7x	+	O:DSP_GPO1	+	<del>                                     </del>	
DD_CAM	B2	4/8/12/16mA	Low	CMRST	Aux. Mode 1	GPIO221 O:CMRST	I0:EINT12_7x	O:CMPDN_S2_1x			10.11 + 5003	
DD_CAM DD_CAM	K10 A2	4/8/12/16mA 4/8/12/16mA		SCL_1 CMDAT5	Aux. Mode 1 Aux. Mode 1	GPI0222 I00:SCL_1_2x GPI0223 I:CMDAT5		IO0:SCL_1_5x I:CAM_CSD1_S2_1x			I0:dbg_in[32]	
DD_CAM	К9	4/8/12/16mA	Low	SDA_1	Aux. Mode 1	GPIO224 IO0:SDA_1_2x	I0:EINT8_7x	IO0:SDA_1_5x				
/DD_CAM /DD_CAM	B3 E7	4/8/12/16mA 4/8/12/16mA		CMVSYNC CMPCLK	Aux. Mode 1 Aux. Mode 1	GPIO225 I:CMVREF GPIO226 I:CMPCLK	I0:EINT11_7x I0:EINT13_7x	I:CAM_CSK_S1_1x			+ +	
	B1	4/8/12/16mA	Low	CMHSYNC	Aux. Mode 1	GPIO227 I:CMHREF	I0:EINT10_7x	I:CAM_CSK_S2_1x		1		
	177	4/8/12/16mA	Low	CMPDN	Aux. Mode 1	GPIO228 O:CMPDN GPIO229 I:CMDAT1		O:CMPDN_S1_1x I:CAM_CSD1_S1_1x	O.TRRYEN	+	+	
VDD_CAM VDD_CAM VDD_CAM	E5 C3	4/8/12/16mA	Low	CMDAT1	Aux. Mode 1	ui iozzy i.dviDhi i	10.EIN121_/X	1.0/11/1_03D1_31_1A	O. I DIVALIN	<u> </u>	<u></u>	