SAN JING ELECTRONICS CO.,LTD

6 Channel Audio Selector SJ2323

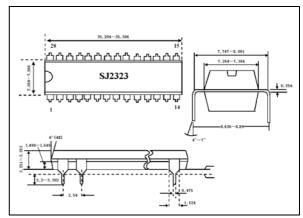
DESCRIPTION

SJ2323 is a 6-Channel Audio Selector utilizing CMOS Technology specially designed for Home Theater System. It provides a built-in 2-Channel to 6-Channel Translator, which can directly mix traditional stereo channel to simulate a 6-Channel audio output. Single power supply (9V), I2C Bus Interface as well as very low Total Harmonic Distortion (THD < 0.005%) and other features are incorporated into a single chip thereby providing very high performance. Pin assignments and application circuit are optimized for easy PCB layout and cost saving advantages.

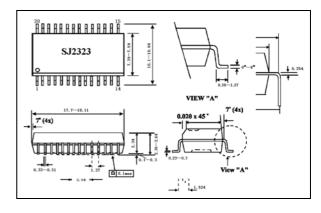
FEATURES

- Supply Voltage: 9V
- 4 Stereo Inputs
- One 6-Channel Input
- Maximum Input Voltage: 3.75Vrms (1kHz, THD<1%)
 Low Total Harmonic Distortion, THD<0.005% (1kHz, 0.2Vrms)
- Low Noise: No<4μVrms
- I²C Bus Interface

Outline Drawing



DIP-28



SOP28

APPLICATIONS

AV System Mini Compo Car Stereo Multi-Media Audio Systerms

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PIN CONFIGURATION

				1
L1=	1		28	□R1
L2⊏	2		27	□R2
L3 <u></u>	3		26	□ R3
L4⊏	4		25	□R4
FLI⊏	5		24	□LPF_I
FRI⊏	6		23	LPF_O
CTI⊏	7	C 12222	22	□FLO
SUBI⊏	8	SJ2323	21	□ FRO
SLI⊏	9		20	<u></u> сто
SRI⊏	10		19	SUBO
MIXO	11		18	SLO
VCC □	12		17	⇒sro
REF□	13		16	⇒scl
GND <u></u>	14		15	⇒ SDA

ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Characteristic	Symbol	Value	Unit
Operating Supply Voltage	Vs	12	V
Operating Temperature	Topr	-40~+85	°C
Storage Temperature	Tstg	-55~+150	°C

ELECTRICAL CHARACTERISTICS

(Unless otherwise specified: Ta=25°C, Vcc=9.0V, RL=100K Ω , f=1KHz)

Characteristics	Symbol	Test conditions	Min	Тур	Max	Unit
Supply Voltage	Vcc		4.5	9		V
Supply Current	Is			4		mA
Total Harmonic Distortion	THD	INPUT=0.2Vrms	0.001	0.005		%
		A-weighting				
Noise Output	No	RIS=600Ω A-Weighting		6		μΑ
Signal to noise Ratio	S/N	0dB=1Vrms		100	104	dB
		A-Weighting				
Max. Input Voltage	VIMAX	RL=50KΩ,1KHz		3.75		Vrms
		THD=1%				
Voltage Gain	GN	f=1KHz	-1	0	+1	dB
Cross Talk	CT	Vin=1Vrms		90		dB
Channel Separation	CS	Vin=1Vrms		80		dB
I ² C Input High Level	Vih		0.7Vdd			V
I ² C Input Low Level	VIL				0.3Vdd	V
I ² C Bus Initial Time	Tinit	Cref=10µf		300		ms

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APPLICATION CIRCUIT **10** *µ* **f** 100K Rch1 **100**Κ **10**μ f R1 L1 Rch2 R2 L2 Lch2 Rch3 R3 Lch3 Rch4 **R4** L4 Lch4 5 From MIXO **FLI** LPF_I C2 6 C1 LPF O **FRI** 270<u>()</u> (NOTE) 10 µ f 100K **FLO** CTI 6CH IN 8 **SUBI SJ2323 FRO** 270Ω(note) ₊ 9 270\(\)(NOTE) + SLI **CTO** 6CH OUT 10 **SUBO SRI** 11 270Ω(NOTE). **MIXO** SLO **9**V 12 **VCC 270**Ω(note). **SRO** 13 16 SCL **REF** 14 **SDA GND** I²C BUS

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