STM32L496G-DISCO

MB1261

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Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

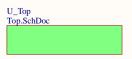
Notes to generate the board layout.

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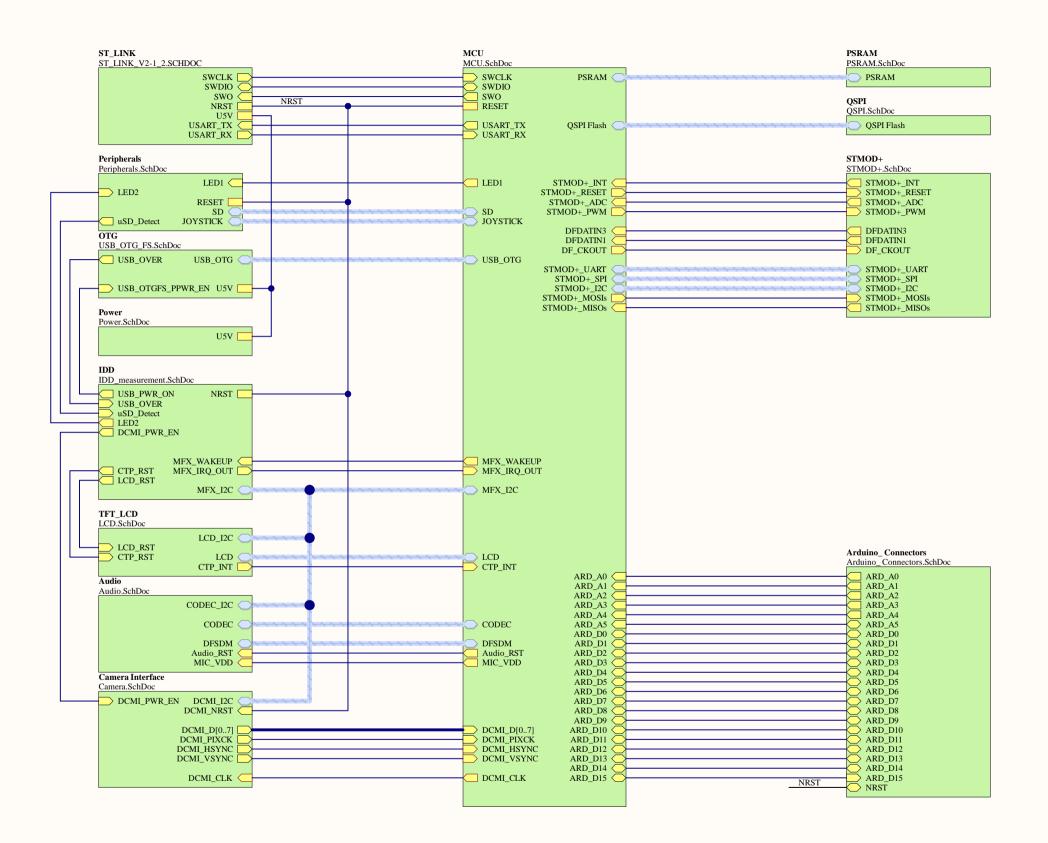
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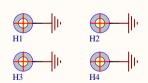
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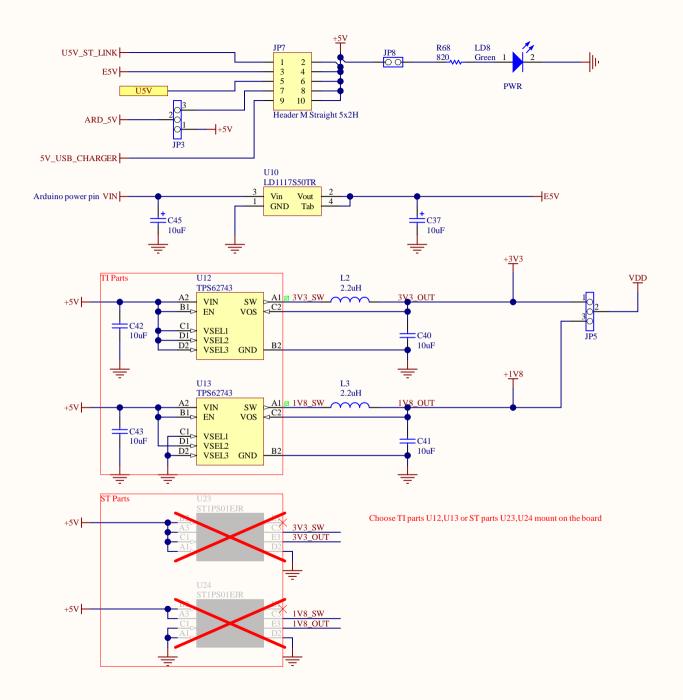


	ct overview	
	432L496G-DISCO	
/ariant: CEI		
Revision: B	-06	Reference: MB1261
lize: A4	Date: 09/06/2021	Sheet: 1 of 15

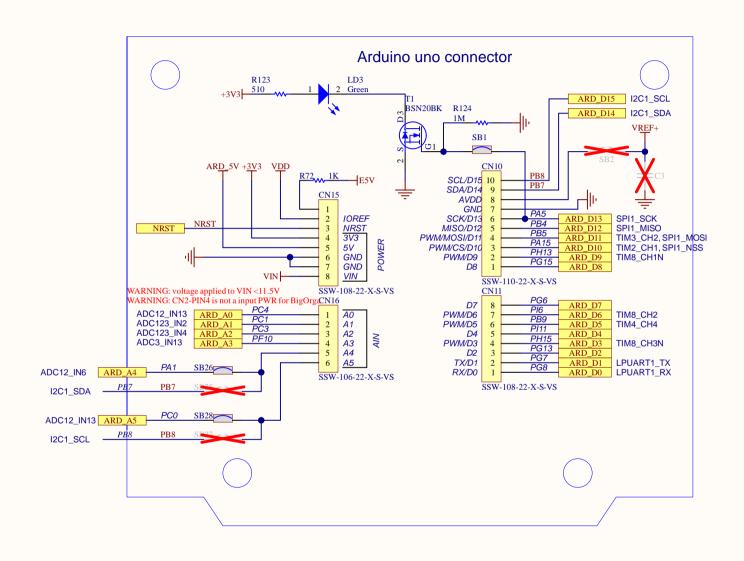




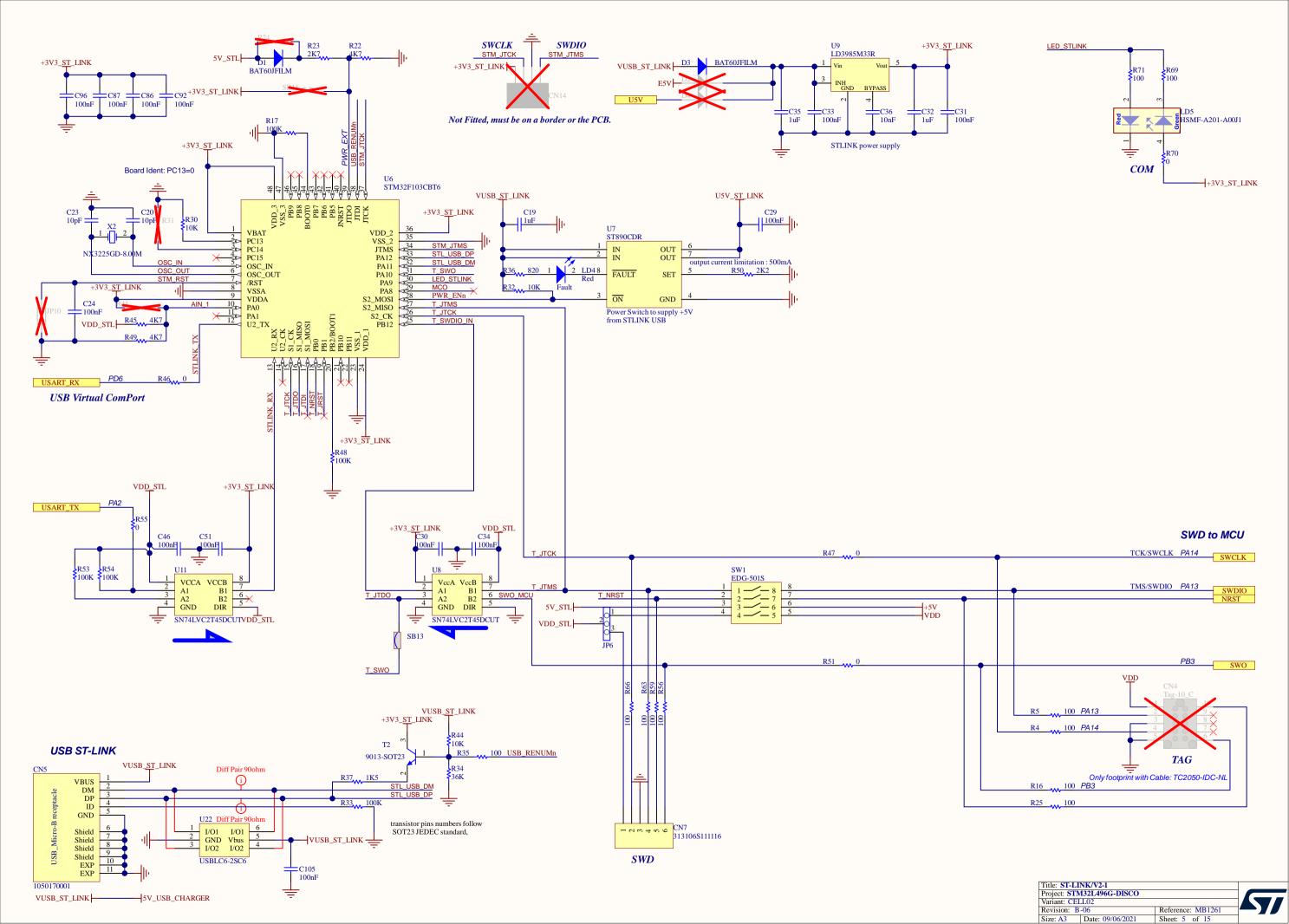
Title: TOP			
Project: STM32L496G-DISCO			
Variant: CELL02			
Revision: B-06		Reference: MB1261	
Size: A3	Date: 09/06/2021	Sheet: 2 of 15	

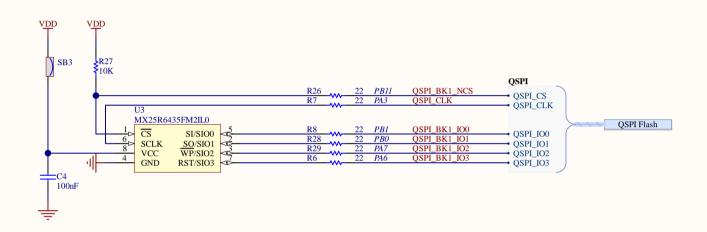


Title: Power			_
Project: STN	432L496G-DISCO		
Variant: CEI	LL02		
Revision: B-06		Reference: MB1261	
Size: A4	Date: 09/06/2021	Sheet: 3 of 15	

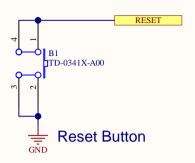


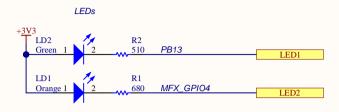
	Title: Ardui	no Uno connector		
Project: STM32L496G-DISCO				
	Variant: CEI	L02		
Revision: B-06		-06	Reference: MB1261	
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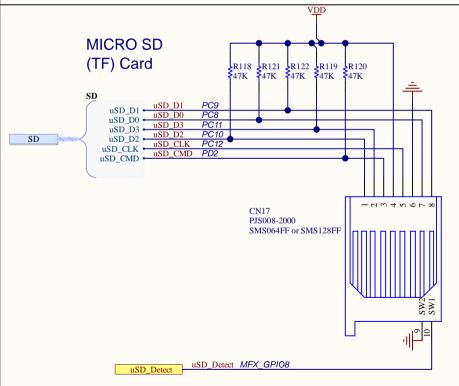


Title: QSPI	Flash		
Project: STM32L496G-DISCO			
Variant: CEI	LL02		
Revision: B	-06	Reference: MB1261	—,
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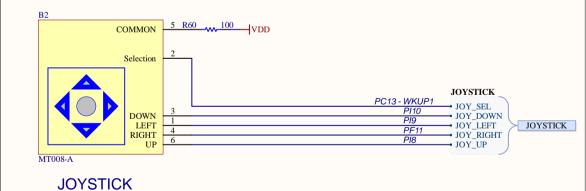


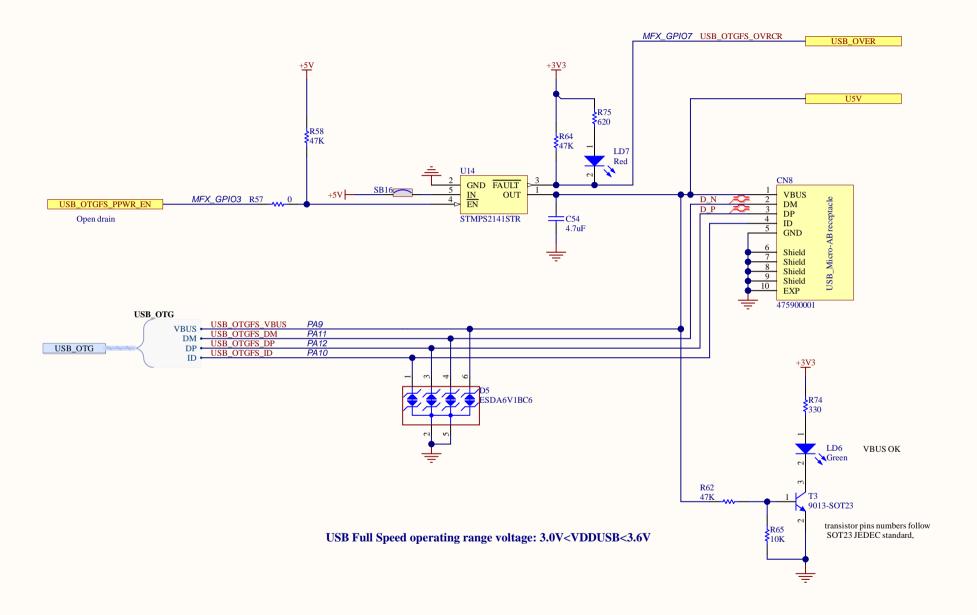


The 2 LEDs are top side

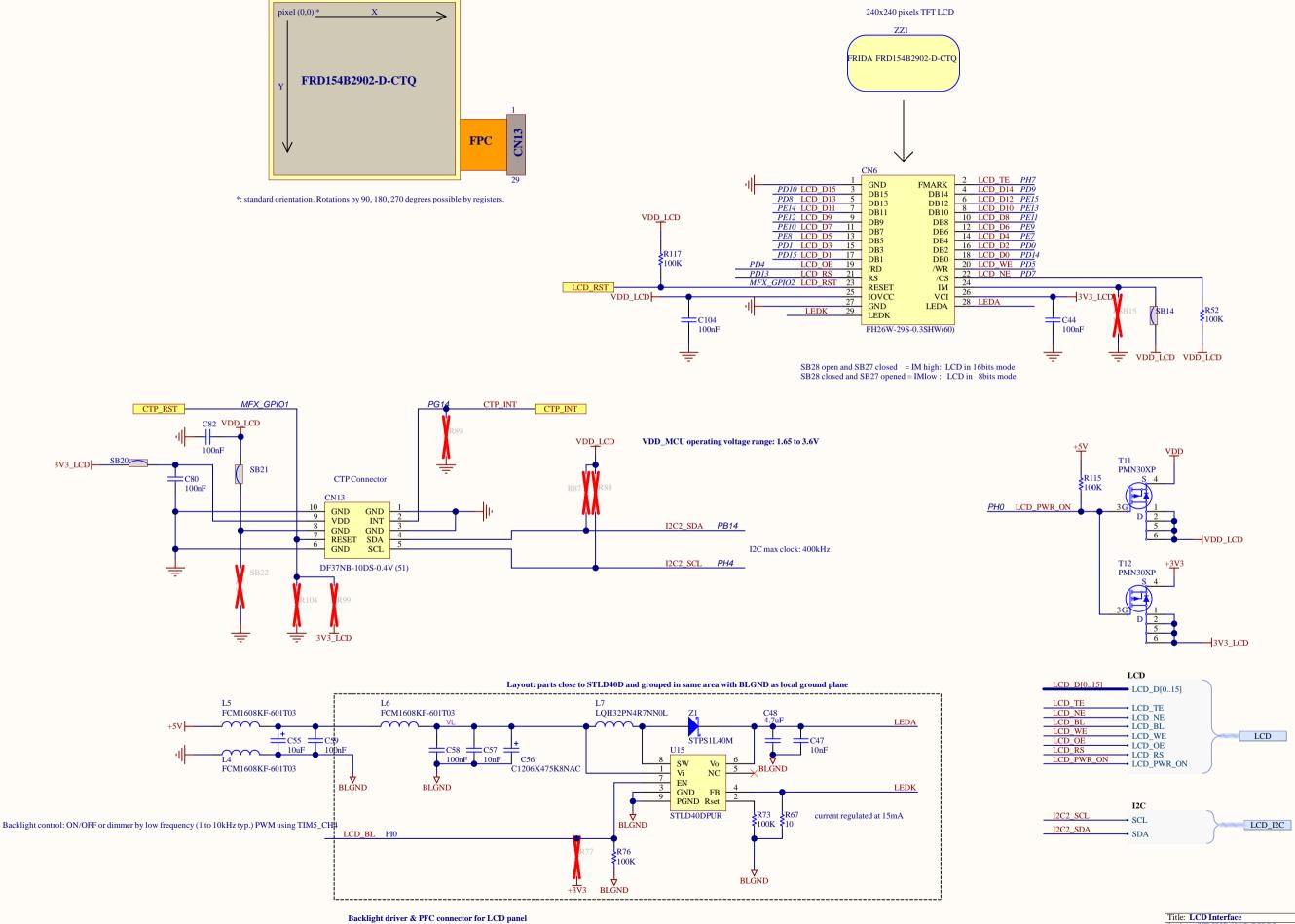


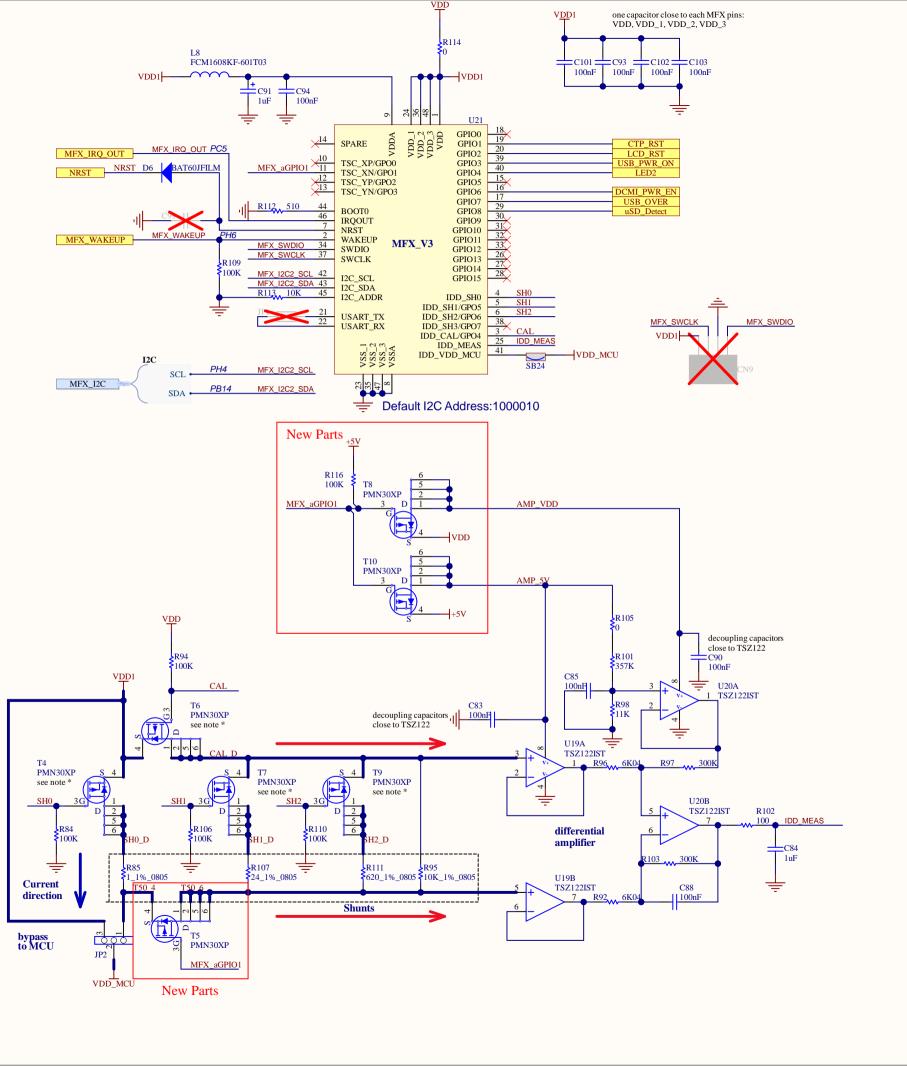
 $SD\ can\ only\ works\ under\ 3V3$

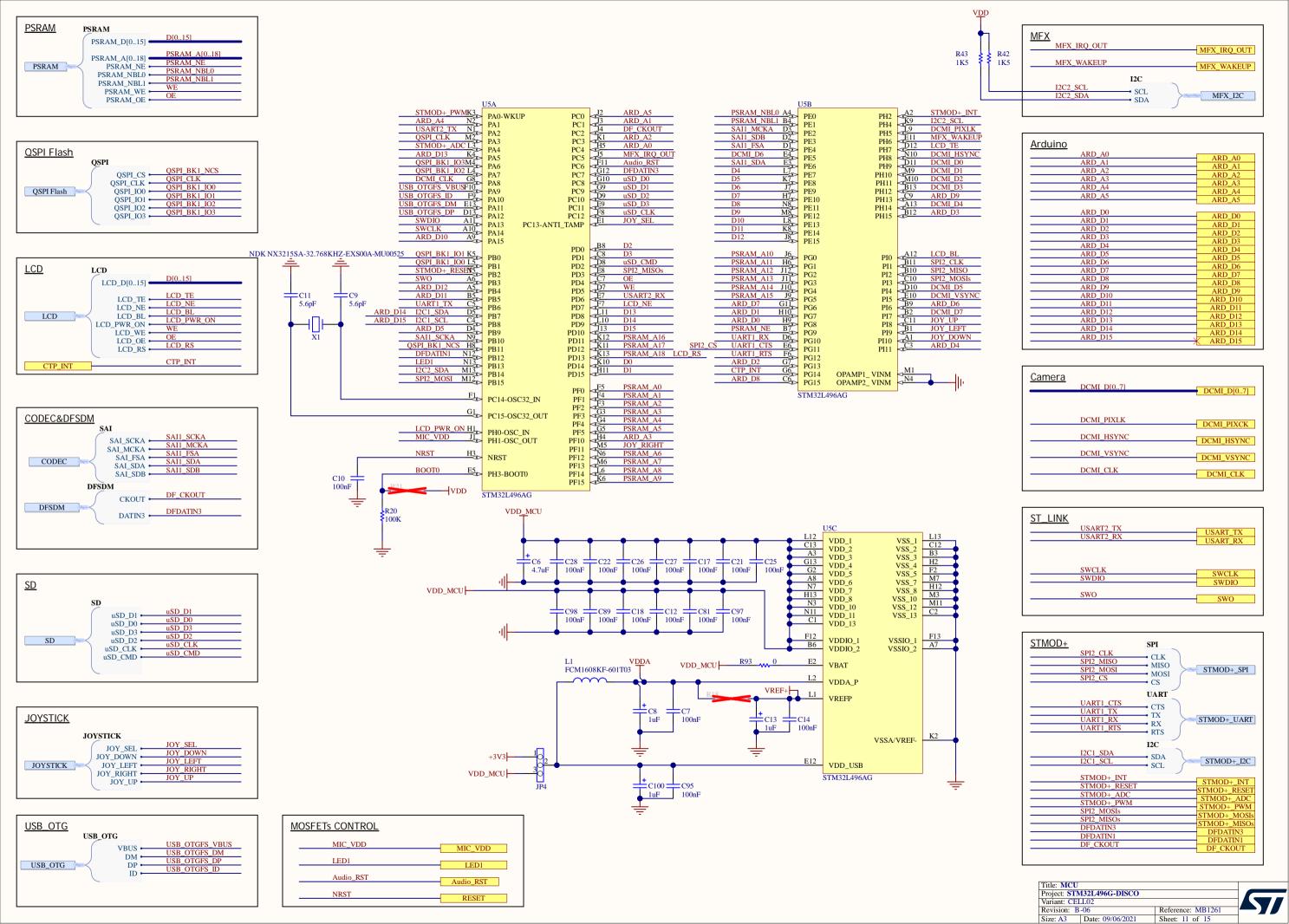


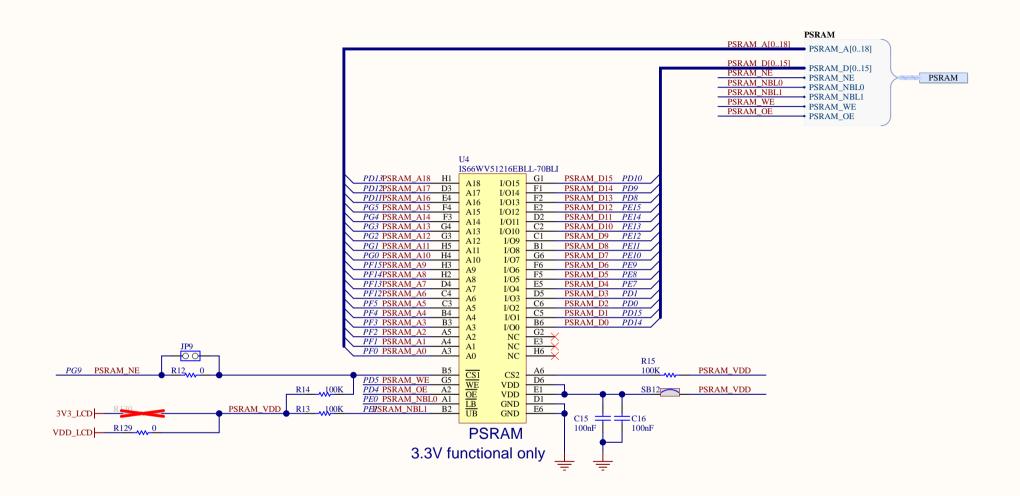


Title: USB_			
Project: STN	132L496G-DISCO		
Variant: CEI	L02		
Revision: B	-06	Reference: MB1261	
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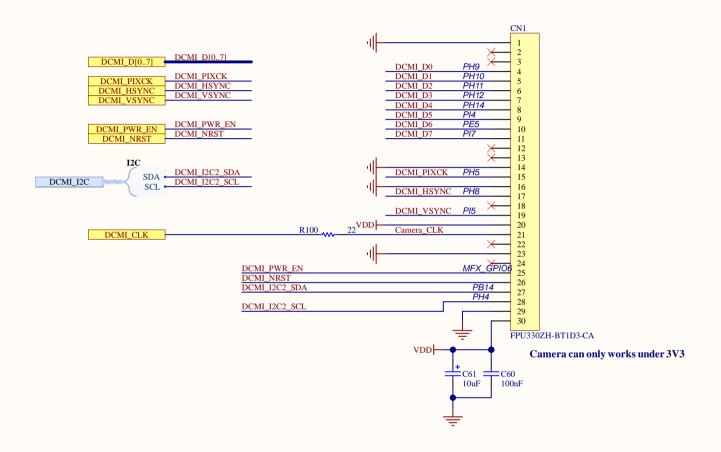




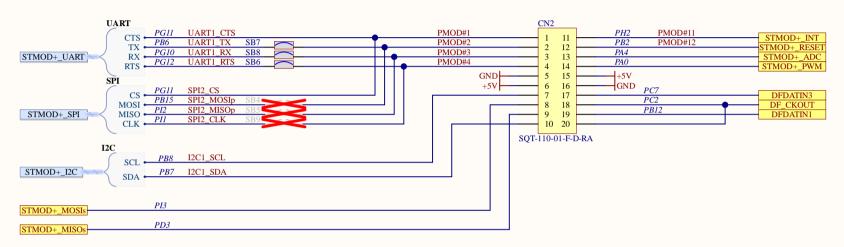


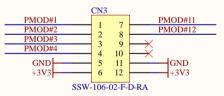


Title: PSRA			
Project: STN	432L496G-DISCO		
Variant: CEI	LL02		
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Title: Came			
Project: STM32L496G-DISCO			
Variant: CELL02			
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Title: STMC	DD+ Interface		
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