

v8::internal::compiler
::turbohaft::anonymous
_namespace{loop-unrolling
_reducer::cc}::SubWillOverflow

```
graph LR; A["v8::internal::compiler  
::turbohaft::anonymous  
_namespace{loop-unrolling  
_reducer::cc}::SubWillOverflow"] --> B["v8::base::bits::SignedSub  
Overflow32"]; A --> C["v8::base::bits::SignedSub  
Overflow64"];
```

v8::base::bits::SignedSub
Overflow32

v8::base::bits::SignedSub
Overflow64