

v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
Callable

v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
Undetectable

v8::internal::maglev  
::MaglevAssembler::TestTypeOf

v8::internal::maglev  
::MaglevAssembler::TestUint8  
AndJumpIfAllClear

```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
Callable"] --> D["v8::internal::maglev  
::MaglevAssembler::TestUint8  
AndJumpIfAllClear"]; B["v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
Undetectable"] --> D; C["v8::internal::maglev  
::MaglevAssembler::TestTypeOf"] --> D;
```

The diagram illustrates a mapping or inheritance relationship between three source functions on the left and a single target function on the right. The source functions are: 1. `v8::internal::maglev::MaglevAssembler::JumpIfNotCallable`, 2. `v8::internal::maglev::MaglevAssembler::JumpIfNotUndetectable`, and 3. `v8::internal::maglev::MaglevAssembler::TestTypeOf`. All three source functions have arrows pointing to the target function, `v8::internal::maglev::MaglevAssembler::TestUint8AndJumpIfAllClear`. The target function box is shaded gray, while the source function boxes are white.