

v8::internal::compiler
::PPCOperandConverter
::CompareLogical

v8::internal::compiler
::S390OperandConverter
::CompareLogical

v8::internal::compiler
::Instruction::flags_condition

```
graph LR; A["v8::internal::compiler::PPCOperandConverter::CompareLogical"] --> C["v8::internal::compiler::Instruction::flags_condition"]; B["v8::internal::compiler::S390OperandConverter::CompareLogical"] --> C;
```

The diagram illustrates a mapping or inheritance relationship. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::compiler::PPCOperandConverter::CompareLogical'. The bottom box contains 'v8::internal::compiler::S390OperandConverter::CompareLogical'. Blue arrows point from the right side of each of these boxes to a single gray rectangular box on the right. This gray box contains the text 'v8::internal::compiler::Instruction::flags_condition'.