

v8::internal::wasm
::LiftoffAssembler::
emit_i32_popcnt

v8::internal::wasm
::LiftoffAssembler::
emit_i64_popcnt

v8::internal::wasm
::liftoff::GeneratePopCnt

```
graph LR; A["v8::internal::wasm::LiftoffAssembler::emit_i32_popcnt"] --> C["v8::internal::wasm::liftoff::GeneratePopCnt"]; B["v8::internal::wasm::LiftoffAssembler::emit_i64_popcnt"] --> C;
```

The diagram illustrates a code transformation or inlining process. On the left, two white rectangular boxes represent source functions: 'v8::internal::wasm::LiftoffAssembler::emit_i32_popcnt' (top) and 'v8::internal::wasm::LiftoffAssembler::emit_i64_popcnt' (bottom). On the right, a gray rectangular box represents the target function: 'v8::internal::wasm::liftoff::GeneratePopCnt'. Two blue arrows point from the source boxes to the target box, indicating that the logic of the two source functions is being consolidated into the target function.