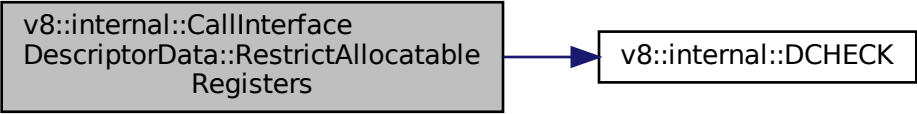


v8::internal::CallInterface
DescriptorData::RestrictAllocatable
Registers



```
graph LR; A["v8::internal::CallInterface  
DescriptorData::RestrictAllocatable  
Registers"] --> B["v8::internal::DCHECK"]
```

v8::internal::DCHECK