


v8::internal::MacroAssembler  
::Claim

v8::internal::MacroAssembler  
::Drop

v8::internal::MacroAssembler  
::AssertPositiveOrZero



```
graph LR; A[v8::internal::MacroAssembler::Claim] --> C[v8::internal::MacroAssembler::AssertPositiveOrZero]; B[v8::internal::MacroAssembler::Drop] --> C;
```

The diagram illustrates a control flow or dependency. Two source nodes, 'v8::internal::MacroAssembler::Claim' and 'v8::internal::MacroAssembler::Drop', are positioned on the left. Arrows from both of these nodes point towards a single target node on the right, 'v8::internal::MacroAssembler::AssertPositiveOrZero'. The target node is shaded gray, while the source nodes are white with black borders.