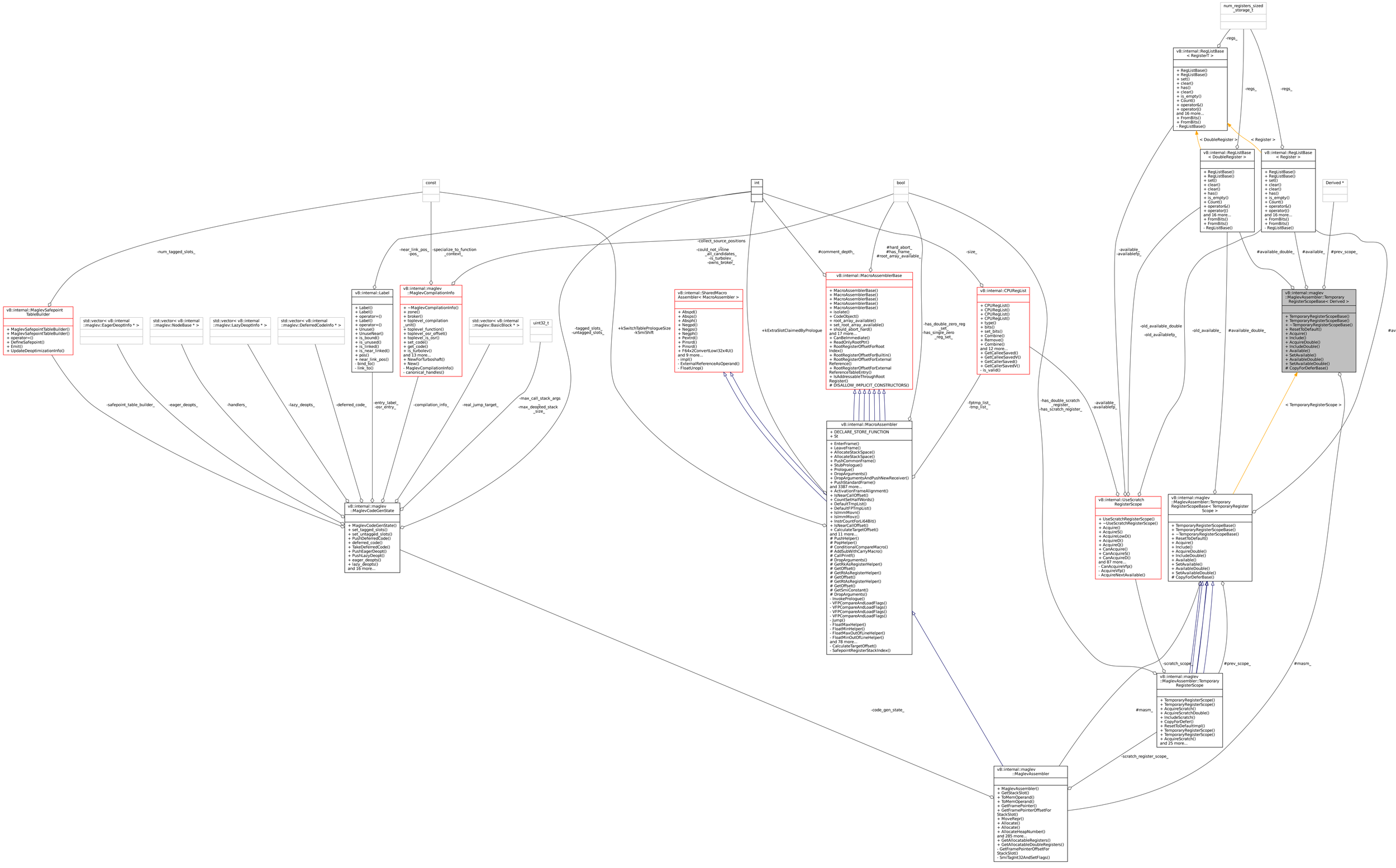
num_registers_sized
_storage_t
-regs_
v8::internal::RegListBase
< RegisterT >
+ RegListBase()
+ RegListBase()
+ set()
+ clear()
+ has()
+ clear()
+ is_empty()
+ Count()
+ operator&()
+ operator|()
and 16 more...
+ FromBits()
+ FromBits()
- RegListBase()
< DoubleRegister >
< Register >
v8::internal::RegListBase
< DoubleRegister >
v8::internal::RegListBase
< Register >
Derived *
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int
bool
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-near_link_pos_
-pos_
-specialize_to_function
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-collect_source_positions
-could_not_inline
_all_candidates_
-is_turbolev_
-owns_broker_
#comment_depth_
#hard_abort_
#has_frame_
#root_array_available_
-size_
-available_
-availablefp_
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-old_availablefp_
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#available_
#prev_scope_
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+ MaglevSafepointTableBuilder()
+ MaglevSafepointTableBuilder()
+ operator=()
+ DefineSafepoint()
+ Emit()
+ UpdateDeoptimizationInfo()
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::maglev::EagerDeoptInfo * >
std::vector< v8::internal
::maglev::NodeBase * >
std::vector< v8::internal
::maglev::LazyDeoptInfo * >
std::vector< v8::internal
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+ Label()
+ Label()
+ operator=()
+ Label()
+ operator=()
+ Unuse()
+ UnuseNear()
+ is_bound()
+ is_unused()
+ is_linked()
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+ pos()
+ near_link_pos()
- bind_to()
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v8::internal::maglev
::MaglevCompilationInfo
+ ~MaglevCompilationInfo()
+ zone()
+ broker()
+ toplevel_compilation
_unit()
+ toplevel_function()
+ toplevel_osr_offset()
+ toplevel_is_osr()
+ set_code()
+ get_code()
+ is_turbolev()
and 13 more...
+ NewForTurboshaft()
+ New()
- MaglevCompilationInfo()
- canonical_handles()
std::vector< v8::internal
::maglev::BasicBlock * >
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-tagged_slots_
-untagged_slots_
+kSwitchTablePrologueSize
-kSmiShift
v8::internal::SharedMacro
Assembler< MacroAssembler >
+ Abspd()
+ Absps()
+ Absph()
+ Negpd()
+ Negps()
+ Negph()
+ Pextrd()
+ Pinsrd()
+ Pinsrd()
+ F64x2ConvertLowI32x4U()
and 9 more...
- impl()
- ExternalReferenceAsOperand()
- FloatUnop()
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v8::internal::MacroAssemblerBase
+ MacroAssemblerBase()
+ MacroAssemblerBase()
+ MacroAssemblerBase()
+ MacroAssemblerBase()
+ MacroAssemblerBase()
+ isolate()
+ CodeObject()
+ root_array_available()
+ set_root_array_available()
+ should_abort_hard()
and 17 more...
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+ ReadOnlyRootPtr()
+ RootRegisterOffsetForRoot
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+ RootRegisterOffsetForBuiltin()
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+ RootRegisterOffsetForExternal
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-has_single_zero
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+ CPURegList()
+ CPURegList()
+ CPURegList()
+ CPURegList()
+ type()
+ bits()
+ set_bits()
+ Combine()
+ Remove()
+ Combine()
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+ GetCalleeSaved()
+ GetCalleeSavedV()
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-tmp_list_
-has_double_scratch
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-has_scratch_register_
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-availablefp_
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::MaglevAssembler::Temporary
RegisterScopeBase< Derived >
+ TemporaryRegisterScopeBase()
+ TemporaryRegisterScopeBase()
+ ~TemporaryRegisterScopeBase()
+ ResetToDefault()
+ Acquire()
+ Include()
+ AcquireDouble()
+ IncludeDouble()
+ Available()
+ SetAvailable()
+ AvailableDouble()
+ SetAvailableDouble()
# CopyForDeferBase()
#available_
-safepoint_table_builder_
-eager_deopts_
-handlers_
-lazy_deopts_
-deferred_code_
-entry_label_
-osr_entry_
-compilation_info_
-real_jump_target_
-max_call_stack_args
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v8::internal::MacroAssembler
+ DECLARE_STORE_FUNCTION
+ St
+ EnterFrame()
+ LeaveFrame()
+ AllocateStackSpace()
+ AllocateStackSpace()
+ PushCommonFrame()
+ StubPrologue()
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+ DropArguments()
+ DropArgumentsAndPushNewReceiver()
+ PushStandardFrame()
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+ ActivationFrameAlignment()
+ IsNearCallOffset()
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+ DefaultTmpList()
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+ IsImmMovn()
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+ InstrCountForLi64Bit()
+ IsNearCallOffset()
+ CalculateTargetOffset()
and 11 more...
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PopHelper()
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CallPrintf()
DropArguments()
GetRkAsRegisterHelper()
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GetOffset()
GetRtAsRegisterHelper()
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GetSmiConstant()
DropArguments()
- InvokePrologue()
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- VFPCompareAndLoadFlags()
- VFPCompareAndLoadFlags()
- VFPCompareAndLoadFlags()
- Jump()
- FloatMaxHelper()
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and 78 more...
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- SafepointRegisterStackIndex()
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::MaglevCodeGenState
+ MaglevCodeGenState()
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+ PushDeferredCode()
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+ UseScratchRegisterScope()
+ ~UseScratchRegisterScope()
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+ AcquireLowD()
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+ CanAcquire()
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::MaglevAssembler::Temporary
RegisterScopeBase< TemporaryRegister
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+ TemporaryRegisterScopeBase()
+ TemporaryRegisterScopeBase()
+ ~TemporaryRegisterScopeBase()
+ ResetToDefault()
+ Acquire()
+ Include()
+ AcquireDouble()
+ IncludeDouble()
+ Available()
+ SetAvailable()
+ AvailableDouble()
+ SetAvailableDouble()
# CopyForDeferBase()
< TemporaryRegisterScope >
-scratch_scope_
#prev_scope_
#masm_
v8::internal::maglev
::MaglevAssembler::Temporary
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+ TemporaryRegisterScope()
+ AcquireScratch()
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+ IncludeScratch()
+ CopyForDefer()
+ ResetToDefaultImpl()
+ TemporaryRegisterScope()
+ TemporaryRegisterScope()
+ AcquireScratch()
and 25 more...
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-scratch_register_scope_
v8::internal::maglev
::MaglevAssembler
+ MaglevAssembler()
+ GetStackSlot()
+ ToMemOperand()
+ ToMemOperand()
+ GetFramePointer()
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and 285 more...
+ GetAllocatableRegisters()
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- GetFramePointerOffsetFor
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- SmiTagInt32AndSetFlags()