

v8::internal::maglev  
::anonymous\_namespace  
{maglev-code-generator  
::cc}::MaglevCodeGeneratingNode  
Processor::Process

v8::internal::maglev  
::LoopOptimizationProcessor  
::Process

v8::internal::maglev  
::LiveRangeAndNextUseProcessor  
::Process

v8::internal::maglev  
::NodeTMixin::properties

```
graph LR; A["v8::internal::maglev  
::anonymous_namespace  
{maglev-code-generator  
::cc}::MaglevCodeGeneratingNode  
Processor::Process"] --> D["v8::internal::maglev  
::NodeTMixin::properties"]; B["v8::internal::maglev  
::LoopOptimizationProcessor  
::Process"] --> D; C["v8::internal::maglev  
::LiveRangeAndNextUseProcessor  
::Process"] --> D;
```

The diagram illustrates a dependency or inheritance relationship. Three source boxes on the left point to a single target box on the right. The target box is shaded gray, while the source boxes are white with black borders. The arrows are blue and point from the source boxes to the target box.