

v8::internal::CommonMachineType

v8::internal::compiler
::WasmGraphBuilder::GetSafeLoad
Operator

v8::internal::compiler
::CallDescriptor::GetTaggedParameterSlots

v8::internal::MachineType
::IsTagged

```
graph LR; A[v8::internal::CommonMachineType] --> D[v8::internal::MachineType::IsTagged]; B[v8::internal::compiler::WasmGraphBuilder::GetSafeLoadOperator] --> D; C[v8::internal::compiler::CallDescriptor::GetTaggedParameterSlots] --> D;
```

The diagram illustrates three source nodes on the left, each with a black border, pointing via blue arrows to a single target node on the right. The target node, labeled 'v8::internal::MachineType::IsTagged', has a gray fill and a black border. The first source node is 'v8::internal::CommonMachineType'. The second source node is 'v8::internal::compiler::WasmGraphBuilder::GetSafeLoadOperator', with the text split across three lines. The third source node is 'v8::internal::compiler::CallDescriptor::GetTaggedParameterSlots', also with text split across three lines.