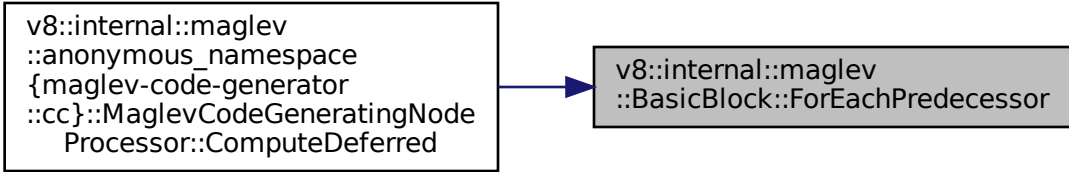


```
v8::internal::maglev  
::anonymous_namespace  
{maglev-code-generator  
::cc}::MaglevCodeGeneratingNode  
Processor::ComputeDeferred
```



A diagram illustrating a call from a code generator to a basic block's predecessor method. On the left, a white rectangular box contains the text: `v8::internal::maglev`, `::anonymous_namespace`, `{maglev-code-generator`, `::cc}::MaglevCodeGeneratingNode`, and `Processor::ComputeDeferred`. A blue arrow points from the right side of this box to the left side of a gray rectangular box on the right. The gray box contains the text: `v8::internal::maglev` and `::BasicBlock::ForEachPredecessor`.

```
v8::internal::maglev  
::BasicBlock::ForEachPredecessor
```