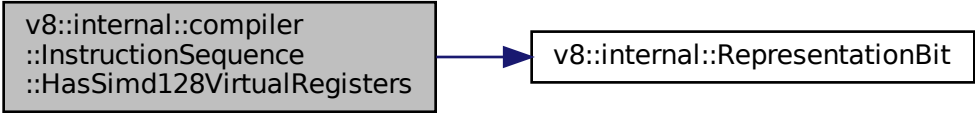


v8::internal::compiler
::InstructionSequence
::HasSimd128VirtualRegisters



```
graph LR; A["v8::internal::compiler  
::InstructionSequence  
::HasSimd128VirtualRegisters"] --> B["v8::internal::RepresentationBit"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'v8::internal::compiler', '::InstructionSequence', and '::HasSimd128VirtualRegisters' stacked vertically. The right box is white with a black border and contains the text 'v8::internal::RepresentationBit'. A blue arrow points from the right side of the gray box to the left side of the white box.

v8::internal::RepresentationBit