

v8::internal::SharedMacro  
AssemblerBase::F64x2Max

v8::internal::SharedMacro  
AssemblerBase::F64x2Min

v8::internal::SharedMacro  
AssemblerBase::I64x2Mul

v8::internal::Assembler  
::vpsrlq

```
graph LR; A["v8::internal::SharedMacro  
AssemblerBase::F64x2Max"] --> D["v8::internal::Assembler  
::vpsrlq"]; B["v8::internal::SharedMacro  
AssemblerBase::F64x2Min"] --> D; C["v8::internal::SharedMacro  
AssemblerBase::I64x2Mul"] --> D;
```

The diagram illustrates a mapping from three specific macro assembler base classes to a common target. Three white rectangular boxes on the left, each containing a C++-style identifier, have blue arrows pointing to a single gray rectangular box on the right. The identifiers are v8::internal::SharedMacroAssemblerBase::F64x2Max, v8::internal::SharedMacroAssemblerBase::F64x2Min, and v8::internal::SharedMacroAssemblerBase::I64x2Mul. The target box contains the identifier v8::internal::Assembler::vpsrlq.