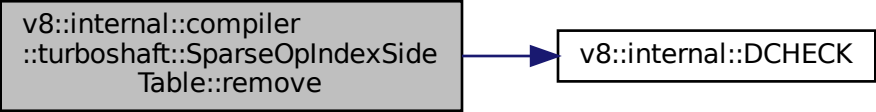


v8::internal::compiler
::turbohaft::SparseOpIndexSide
Table::remove



```
graph LR; A["v8::internal::compiler  
::turbohaft::SparseOpIndexSide  
Table::remove"] --> B["v8::internal::DCHECK"]
```

v8::internal::DCHECK