

v8::internal::compiler
::turbohaft::GraphBuildingNode
Processor::Process



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process"] --> B["v8::internal::maglev  
::SmiCountLeadingZeros  
::input"]
```

v8::internal::maglev
::SmiCountLeadingZeros
::input