

v8::internal::Assembler::dd

v8::internal::MacroAssembler
::LoadAddress

v8::internal::Assembler
::jump_address

```
graph LR; A[v8::internal::Assembler::dd] --> C[v8::internal::Assembler::jump_address]; B[v8::internal::MacroAssembler::LoadAddress] --> C;
```

The diagram illustrates a mapping or inheritance relationship. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::Assembler::dd'. The bottom box contains the text 'v8::internal::MacroAssembler' followed by '::LoadAddress' on a new line. On the right, there is a single gray rectangular box containing the text 'v8::internal::Assembler' followed by '::jump_address' on a new line. Two blue arrows originate from the right side of the two boxes on the left and point towards the left side of the gray box on the right, indicating that both 'v8::internal::Assembler::dd' and 'v8::internal::MacroAssembler::LoadAddress' are associated with or point to 'v8::internal::Assembler::jump_address'.