

v8::internal::maglev
::StraightForwardRegisterAllocator
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```
graph LR; A["v8::internal::maglev  
::StraightForwardRegisterAllocator  
::StraightForwardRegisterAllocator"] --> B["v8::internal::maglev  
::Graph::min_maglev_stackslots  
_for_unoptimized_frame_size"]; B --> C["v8::internal::maglev  
::InitialValue::stack_slot"]
```

v8::internal::maglev
::Graph::min_maglev_stackslots
_for_unoptimized_frame_size

v8::internal::maglev
::InitialValue::stack_slot