

v8::internal::compiler
::Usdc1Latency

v8::internal::compiler
::UStoreDoubleLatency

v8::internal::compiler
::UsdLatency

```
graph LR; A[v8::internal::compiler::Usdc1Latency] --> C[v8::internal::compiler::UsdLatency]; B[v8::internal::compiler::UStoreDoubleLatency] --> C;
```

The diagram illustrates a relationship between three V8 compiler latency classes. On the left, two white boxes represent 'v8::internal::compiler::Usdc1Latency' (top) and 'v8::internal::compiler::UStoreDoubleLatency' (bottom). On the right, a gray box represents 'v8::internal::compiler::UsdLatency'. Two blue arrows point from the white boxes to the gray box, indicating that both 'Usdc1Latency' and 'UStoreDoubleLatency' inherit from or are related to 'UsdLatency'.