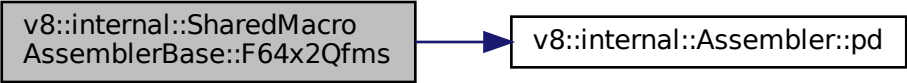


v8::internal::SharedMacro
AssemblerBase::F64x2Qfms



```
graph LR; A["v8::internal::SharedMacroAssemblerBase::F64x2Qfms"] --> B["v8::internal::Assembler::pd"]
```

v8::internal::Assembler::pd