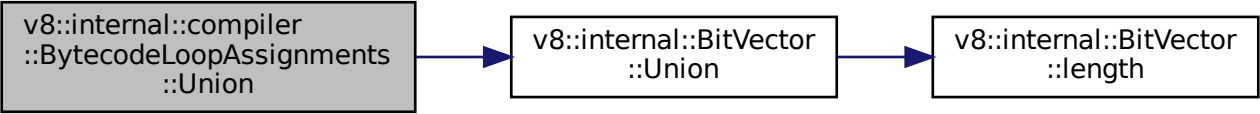


v8::internal::compiler  
::BytecodeLoopAssignments  
::Union



```
graph LR; A["v8::internal::compiler  
::BytecodeLoopAssignments  
::Union"] --> B["v8::internal::BitVector  
::Union"]; B --> C["v8::internal::BitVector  
::length"]
```

The diagram consists of three rectangular boxes arranged horizontally. The first box on the left is shaded gray and contains the text 'v8::internal::compiler', 'v8::BytecodeLoopAssignments', and 'v8::Union' stacked vertically. A blue arrow points from the right side of this box to the left side of the second box. The second box is white with a black border and contains the text 'v8::internal::BitVector' and 'v8::Union' stacked vertically. Another blue arrow points from the right side of the second box to the left side of the third box. The third box is white with a black border and contains the text 'v8::internal::BitVector' and 'v8::length' stacked vertically.

v8::internal::BitVector  
::Union

v8::internal::BitVector  
::length