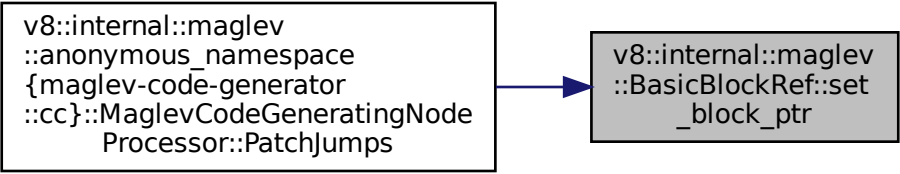


v8::internal::maglev
::anonymous_namespace
{maglev-code-generator
::cc}::MaglevCodeGeneratingNode
Processor::PatchJumps



```
graph LR; A["v8::internal::maglev  
::anonymous_namespace  
{maglev-code-generator  
::cc}::MaglevCodeGeneratingNode  
Processor::PatchJumps"] --> B["v8::internal::maglev  
::BasicBlockRef::set  
_block_ptr"]
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is white with a black border and contains the text 'v8::internal::maglev', '::anonymous_namespace', '{maglev-code-generator', '::cc}::MaglevCodeGeneratingNode', and 'Processor::PatchJumps'. The right box is gray with a black border and contains the text 'v8::internal::maglev', '::BasicBlockRef::set', and '_block_ptr'.

v8::internal::maglev
::BasicBlockRef::set
_block_ptr