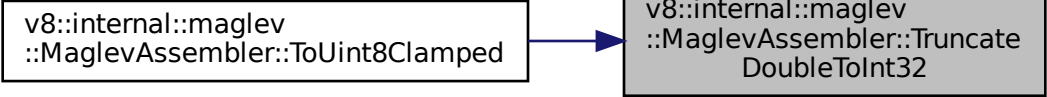


v8::internal::maglev
::MaglevAssembler::ToUint8Clamped



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::ToUint8Clamped"] --> B["v8::internal::maglev  
::MaglevAssembler::TruncateDoubleToInt32"]
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is white with a black border and contains the text 'v8::internal::maglev' on the first line and '::MaglevAssembler::ToUint8Clamped' on the second line. The right box is gray with a black border and contains the text 'v8::internal::maglev' on the first line, '::MaglevAssembler::Truncate' on the second line, and 'DoubleToInt32' on the third line.

v8::internal::maglev
::MaglevAssembler::Truncate
DoubleToInt32