

v8::internal::maglev
::RegisterFrameState
::AllocateRegister

v8::internal::maglev
::RegisterFrameState
::TryChooseInputRegister

v8::internal::maglev
::RegisterFrameState
::TryChooseUnblockedInputRegister

v8::internal::maglev
::anonymous_namespace
{maglev-regalloc::cc}
::OperandForNodeRegister

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graph LR; A["v8::internal::maglev::RegisterFrameState::AllocateRegister"] --> D["v8::internal::maglev::anonymous_namespace{maglev-regalloc::cc}::OperandForNodeRegister"]; B["v8::internal::maglev::RegisterFrameState::TryChooseInputRegister"] --> D; C["v8::internal::maglev::RegisterFrameState::TryChooseUnblockedInputRegister"] --> D;
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The diagram illustrates a mapping from three source functions to a single target function. Three boxes on the left, each containing a function signature, have blue arrows pointing to a single box on the right. The target box is shaded gray and contains the signature for the target function. The source boxes are white with black borders. The arrows are blue and point from the right side of each source box to the left side of the target box.