

v8::internal::SharedMacro
AssemblerBase::Blendvpd

v8::internal::SharedMacro
AssemblerBase::l64x2Abs

v8::internal::Assembler
::vblendvpd

```
graph LR; A[v8::internal::SharedMacroAssemblerBase::Blendvpd] --> C[v8::internal::Assembler::vblendvpd]; B[v8::internal::SharedMacroAssemblerBase::l64x2Abs] --> C;
```

The diagram illustrates a mapping from two source functions to a target function. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::SharedMacroAssemblerBase::Blendvpd' and the bottom box contains 'v8::internal::SharedMacroAssemblerBase::l64x2Abs'. On the right, there is a gray rectangular box containing the text 'v8::internal::Assembler::vblendvpd'. Two blue arrows point from the right side of each white box to the left side of the gray box, indicating that both source functions are mapped to the target function.