

v8::internal::RegExpMacro  
AssemblerRISCV::PushBacktrack

v8::internal::RegExpMacro  
AssemblerRISCV::PushCurrentPosition

v8::internal::RegExpMacro  
AssemblerRISCV::PushRegister

v8::internal::RegExpMacro  
AssemblerRISCV::CheckStackLimit

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graph LR; A["v8::internal::RegExpMacro<br>AssemblerRISCV::PushBacktrack"] --> D["v8::internal::RegExpMacro<br>AssemblerRISCV::CheckStackLimit"]; B["v8::internal::RegExpMacro<br>AssemblerRISCV::PushCurrentPosition"] --> D; C["v8::internal::RegExpMacro<br>AssemblerRISCV::PushRegister"] --> D;
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The diagram illustrates a control flow or dependency where three different assembly routines (PushBacktrack, PushCurrentPosition, and PushRegister) all lead to a common routine (CheckStackLimit). The source boxes are white with black borders, while the target box is gray with a black border. Blue arrows indicate the direction of flow from left to right.