

v8::internal::Assembler
::ImmNEON4

v8::internal::Assembler
::ImmNEON5

v8::internal::LaneSizeIn
BytesLog2FromFormat

```
graph LR; A["v8::internal::Assembler<br>::ImmNEON4"] --> C["v8::internal::LaneSizeIn<br>BytesLog2FromFormat"]; B["v8::internal::Assembler<br>::ImmNEON5"] --> C;
```

The diagram consists of three rectangular boxes. On the left, there are two white boxes with black borders, one above the other. On the right, there is a single gray box with a black border. Two blue arrows originate from the right side of the two white boxes and point towards the left side of the gray box. The top white box contains the text 'v8::internal::Assembler' followed by '::ImmNEON4' on a new line. The bottom white box contains the text 'v8::internal::Assembler' followed by '::ImmNEON5' on a new line. The gray box contains the text 'v8::internal::LaneSizeIn' followed by 'BytesLog2FromFormat' on a new line.