

v8::internal::Assembler
::CheckForEmitInForbiddenSlot

v8::internal::Assembler
::EmitHelper

v8::internal::Assembler
::IsPrevInstrCompactBranch

```
graph LR; A["v8::internal::Assembler::CheckForEmitInForbiddenSlot"] --> C["v8::internal::Assembler::IsPrevInstrCompactBranch"]; B["v8::internal::Assembler::EmitHelper"] --> C;
```

The diagram illustrates a control flow or dependency relationship. Two source functions, 'v8::internal::Assembler::CheckForEmitInForbiddenSlot' and 'v8::internal::Assembler::EmitHelper', are shown in white boxes on the left. Arrows from both of these boxes point to a single target function, 'v8::internal::Assembler::IsPrevInstrCompactBranch', which is shown in a grey box on the right.