

v8::internal::wasm
::liftoff::EmitFloatSetCond

v8::internal::wasm
::liftoff::setcc_32

v8::internal::wasm
::liftoff::GetTmpByteRegister

```
graph LR; A["v8::internal::wasm  
::liftoff::EmitFloatSetCond"] --> C["v8::internal::wasm  
::liftoff::GetTmpByteRegister"]; B["v8::internal::wasm  
::liftoff::setcc_32"] --> C;
```

The diagram illustrates a control flow or dependency where two source functions, `v8::internal::wasm::liftoff::EmitFloatSetCond` and `v8::internal::wasm::liftoff::setcc_32`, both point to a common target function, `v8::internal::wasm::liftoff::GetTmpByteRegister`. The target function is highlighted in a gray box, while the source functions are in white boxes. Blue arrows indicate the direction of the flow from the sources to the target.