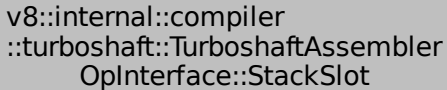


v8::internal::compiler  
::turbohaft::TurbohaftAssembler  
OpInterface::StackSlot



```
graph LR; A["v8::internal::compiler<br>::turbohaft::TurbohaftAssembler<br>OpInterface::StackSlot"] --> B["size"]
```

size