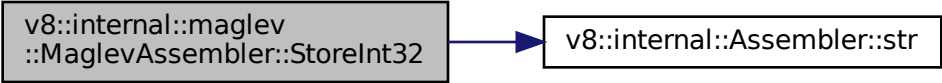


v8::internal::maglev
::MaglevAssembler::StoreInt32



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::StoreInt32"] --> B["v8::internal::Assembler::str"]
```

v8::internal::Assembler::str