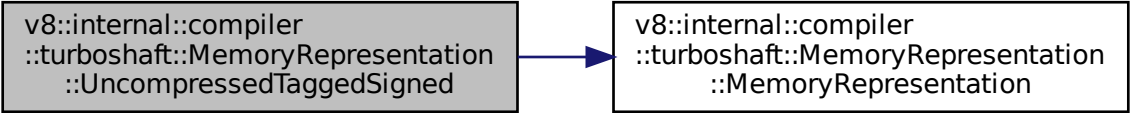


v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::UncompressedTaggedSigned



```
graph LR; A["v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::UncompressedTaggedSigned"] --> B["v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::MemoryRepresentation"]
```

v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::MemoryRepresentation