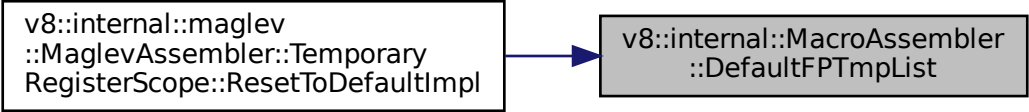


v8::internal::maglev
::MaglevAssembler::Temporary
RegisterScope::ResetToDefaultImpl



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::Temporary  
RegisterScope::ResetToDefaultImpl"] --> B["v8::internal::MacroAssembler  
::DefaultFPTmpList"]
```

v8::internal::MacroAssembler
::DefaultFPTmpList