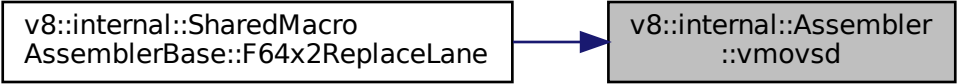


v8::internal::SharedMacro
AssemblerBase::F64x2ReplaceLane



```
graph LR; A["v8::internal::SharedMacroAssemblerBase::F64x2ReplaceLane"] --> B["v8::internal::Assembler::vmovsd"]
```

v8::internal::Assembler
::vmovsd