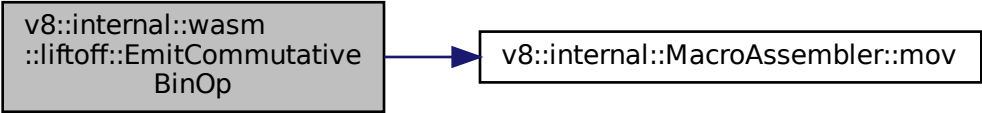


v8::internal::wasm
::liftoff::EmitCommutative
BinOp



```
graph LR; A["v8::internal::wasm  
::liftoff::EmitCommutative  
BinOp"] --> B["v8::internal::MacroAssembler::mov"]
```

v8::internal::MacroAssembler::mov