

v8::internal::compiler
::turbohaft::TurbolevEarlyLowering
Reducer::ExtendPropertiesBackingStore



```
graph LR; A["v8::internal::compiler  
::turbohaft::TurbolevEarlyLowering  
Reducer::ExtendPropertiesBackingStore"] --> B["v8::internal::compiler  
::AccessBuilder::ForProperty  
ArraySlot"]
```

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::AccessBuilder::ForProperty
ArraySlot