

v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process"] --> B["v8::internal::maglev  
::CallBuiltin::slot_type"]
```

v8::internal::maglev  
::CallBuiltin::slot\_type