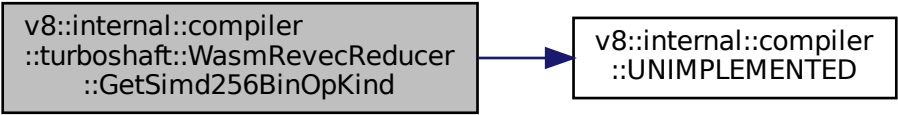


v8::internal::compiler
::turbohaft::WasmRevecReducer
::GetSimd256BinOpKind



```
graph LR; A["v8::internal::compiler  
::turbohaft::WasmRevecReducer  
::GetSimd256BinOpKind"] --> B["v8::internal::compiler  
::UNIMPLEMENTED"]
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box has a light gray background and a black border, containing three lines of text: 'v8::internal::compiler', '::turbohaft::WasmRevecReducer', and '::GetSimd256BinOpKind'. The right box has a white background and a black border, containing two lines of text: 'v8::internal::compiler' and '::UNIMPLEMENTED'.

v8::internal::compiler
::UNIMPLEMENTED