

v8::internal::wasm
::F16x8BinOpViaF32

v8::internal::wasm
::F16x8CmpOpViaF32

v8::internal::Assembler
::vcvtph2ps

```
graph LR; A["v8::internal::wasm  
::F16x8BinOpViaF32"] --> C["v8::internal::Assembler  
::vcvtph2ps"]; B["v8::internal::wasm  
::F16x8CmpOpViaF32"] --> C;
```

The diagram illustrates a mapping from WebAssembly operations to a specific assembler instruction. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::wasm' followed by '::F16x8BinOpViaF32' on the next line. The bottom box contains 'v8::internal::wasm' followed by '::F16x8CmpOpViaF32' on the next line. Blue arrows point from the right side of each of these two boxes to a single gray rectangular box on the right. This gray box contains the text 'v8::internal::Assembler' followed by '::vcvtph2ps' on the next line.