

v8::internal::RegExpMacro  
AssemblerRISCV::SkipUntilBitInTable



```
graph LR; A["v8::internal::RegExpMacro  
AssemblerRISCV::SkipUntilBitInTable"] --> B["v8::internal::RegExpMacro  
AssemblerRISCV::AdvanceCurrent  
Position"]
```

v8::internal::RegExpMacro  
AssemblerRISCV::AdvanceCurrent  
Position