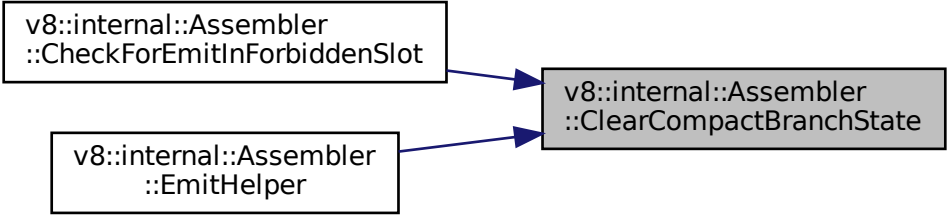


v8::internal::Assembler  
::CheckForEmitInForbiddenSlot

v8::internal::Assembler  
::EmitHelper

v8::internal::Assembler  
::ClearCompactBranchState



```
graph LR; A["v8::internal::Assembler::CheckForEmitInForbiddenSlot"] --> C["v8::internal::Assembler::ClearCompactBranchState"]; B["v8::internal::Assembler::EmitHelper"] --> C; style C fill:#ccc
```