

v8::internal::StringBuiltins
Assembler::GenerateStringEqual

v8::internal::StringBuiltins
Assembler::GenerateStringRelational
Comparison

v8::internal::StringBuiltins
Assembler::StringAdd

v8::internal::StringBuiltins
Assembler::MaybeDerefIndirectStrings

```
graph LR; A["v8::internal::StringBuiltins<br>Assembler::GenerateStringEqual"] --> D["v8::internal::StringBuiltins<br>Assembler::MaybeDerefIndirectStrings"]; B["v8::internal::StringBuiltins<br>Assembler::GenerateStringRelational<br>Comparison"] --> D; C["v8::internal::StringBuiltins<br>Assembler::StringAdd"] --> D;
```

The diagram illustrates a mapping from three source functions to a single target function. Three boxes on the left, each containing a function name, have blue arrows pointing to a single box on the right. The boxes on the left are white with black borders, while the box on the right is gray with a black border. The arrows are blue and point from the bottom-right of the first box, the right side of the second box, and the top-right of the third box to the left side of the target box.