

v8::internal::ActionNode
::Emit

v8::internal::NegativeSubmatch
Success::Emit

v8::internal::RegExpMacro
AssemblerTracer::ReadCurrentPosition
FromRegister

v8::internal::RegExpMacro
Assembler::ReadCurrentPosition
FromRegister

```
graph LR; A["v8::internal::ActionNode::Emit"] --> D["v8::internal::RegExpMacroAssembler::ReadCurrentPositionFromRegister"]; B["v8::internal::NegativeSubmatchSuccess::Emit"] --> D; C["v8::internal::RegExpMacroAssemblerTracer::ReadCurrentPositionFromRegister"] --> D;
```

The diagram illustrates a mapping or inheritance relationship. Three source nodes on the left point via blue arrows to a single target node on the right. The target node is shaded gray, while the source nodes are white with black borders. The source nodes are arranged vertically, and the arrows converge on the target node.