

v8::internal::maglev  
::MaglevAssembler::Prologue



```
graph LR; A[v8::internal::maglev::MaglevAssembler::Prologue] --> B[v8::internal::MacroAssembler::LoadFeedbackVectorFlagsAndCheckIfNeedsProcessing];
```

v8::internal::MacroAssembler  
::LoadFeedbackVectorFlagsAndCheck  
IfNeedsProcessing