

v8::internal::maglev
::BuiltinStringFromCharCode
::GenerateCode

v8::internal::maglev
::anonymous_namespace
{maglev-ir-arm64::cc}
::GenerateReduceInterruptBudget

v8::internal::maglev
::anonymous_namespace
{maglev-ir-riscv::cc}
::GenerateReduceInterruptBudget

v8::internal::maglev
::MapCompare::MapCompare

v8::internal::maglev
::MaglevAssembler::Temporary
RegisterScopeBase::Acquire

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graph LR; A["v8::internal::maglev::BuiltinStringFromCharCode::GenerateCode"] --> D["v8::internal::maglev::MaglevAssembler::TemporaryRegisterScopeBase::Acquire"]; B["v8::internal::maglev::anonymous_namespace{maglev-ir-arm64::cc}::GenerateReduceInterruptBudget"] --> D; C["v8::internal::maglev::anonymous_namespace{maglev-ir-riscv::cc}::GenerateReduceInterruptBudget"] --> D; E["v8::internal::maglev::MapCompare::MapCompare"] --> D;
```

The diagram illustrates a call graph where four different source functions converge on a single target function. The target function, `v8::internal::maglev::MaglevAssembler::TemporaryRegisterScopeBase::Acquire`, is highlighted with a gray background. It receives calls from `v8::internal::maglev::BuiltinStringFromCharCode::GenerateCode`, `v8::internal::maglev::anonymous_namespace{maglev-ir-arm64::cc}::GenerateReduceInterruptBudget`, `v8::internal::maglev::anonymous_namespace{maglev-ir-riscv::cc}::GenerateReduceInterruptBudget`, and `v8::internal::maglev::MapCompare::MapCompare`.