

v8::internal::compiler
::Word32AtomicCompareExchange
Latency

v8::internal::compiler
::Word32AtomicExchangeLatency

v8::internal::compiler
::ExtractBitsLatency

```
graph LR; A["v8::internal::compiler::Word32AtomicCompareExchangeLatency"] --> C["v8::internal::compiler::ExtractBitsLatency"]; B["v8::internal::compiler::Word32AtomicExchangeLatency"] --> C;
```

The diagram illustrates a dependency or mapping relationship. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::compiler::Word32AtomicCompareExchangeLatency'. The bottom box contains the text 'v8::internal::compiler::Word32AtomicExchangeLatency'. On the right, there is a gray rectangular box containing the text 'v8::internal::compiler::ExtractBitsLatency'. Two blue arrows originate from the right side of the two white boxes and point towards the left side of the gray box, indicating that both source nodes contribute to or are mapped to the target node.