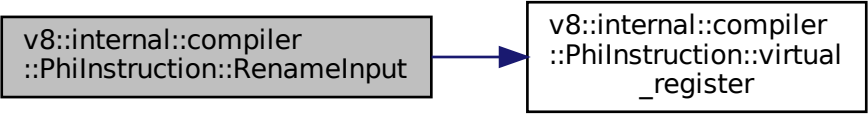


v8::internal::compiler
::PhiInstruction::RenameInput



```
graph LR; A[v8::internal::compiler::PhiInstruction::RenameInput] --> B[v8::internal::compiler::PhiInstruction::virtual_register]
```

v8::internal::compiler
::PhiInstruction::virtual
_register