

v8::internal::compiler
::Float64MaxLatency

v8::internal::compiler
::TruncUIDLatency

v8::internal::compiler
::CompareF64Latency



```
graph LR; A["v8::internal::compiler::Float64MaxLatency"] --> C["v8::internal::compiler::CompareF64Latency"]; B["v8::internal::compiler::TruncUIDLatency"] --> C;
```

The diagram illustrates a dependency or mapping relationship. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::compiler::Float64MaxLatency' and the bottom box contains 'v8::internal::compiler::TruncUIDLatency'. On the right, there is a single gray rectangular box containing the text 'v8::internal::compiler::CompareF64Latency'. Two blue arrows originate from the right side of the two white boxes and point towards the left side of the gray box, indicating that both source nodes contribute to or are mapped to the target node.