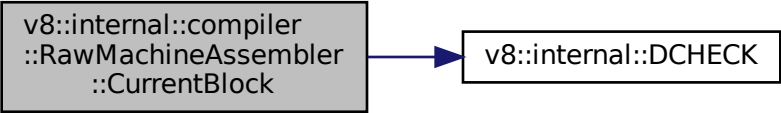


v8::internal::compiler
::RawMachineAssembler
::CurrentBlock



```
graph LR; A["v8::internal::compiler  
::RawMachineAssembler  
::CurrentBlock"] --> B["v8::internal::DCHECK"]
```

v8::internal::DCHECK