

v8::internal::compiler
::turbohaft::AtomicWord32Pair
Op::PrintInputs

v8::internal::compiler
::anonymous_namespace
{instruction-selector-arm
::cc}::VisitPairAtomicBinOp

v8::internal::compiler
::anonymous_namespace
{instruction-selector-ia32
::cc}::VisitPairAtomicBinOp

v8::internal::compiler
::turbohaft::AtomicWord32Pair
Op::value_high

```
graph LR; A["v8::internal::compiler  
::turbohaft::AtomicWord32Pair  
Op::PrintInputs"] --> D["v8::internal::compiler  
::turbohaft::AtomicWord32Pair  
Op::value_high"]; B["v8::internal::compiler  
::anonymous_namespace  
{instruction-selector-arm  
::cc}::VisitPairAtomicBinOp"] --> D; C["v8::internal::compiler  
::anonymous_namespace  
{instruction-selector-ia32  
::cc}::VisitPairAtomicBinOp"] --> D;
```

The diagram illustrates a call graph or dependency structure. Three source functions on the left are shown in white boxes, each with a blue arrow pointing to a single target function on the right, which is shown in a grey box. The source functions are: 1) v8::internal::compiler::turbohaft::AtomicWord32Pair Op::PrintInputs, 2) v8::internal::compiler::anonymous_namespace{instruction-selector-arm::cc}::VisitPairAtomicBinOp, and 3) v8::internal::compiler::anonymous_namespace{instruction-selector-ia32::cc}::VisitPairAtomicBinOp. The target function is v8::internal::compiler::turbohaft::AtomicWord32Pair Op::value_high.