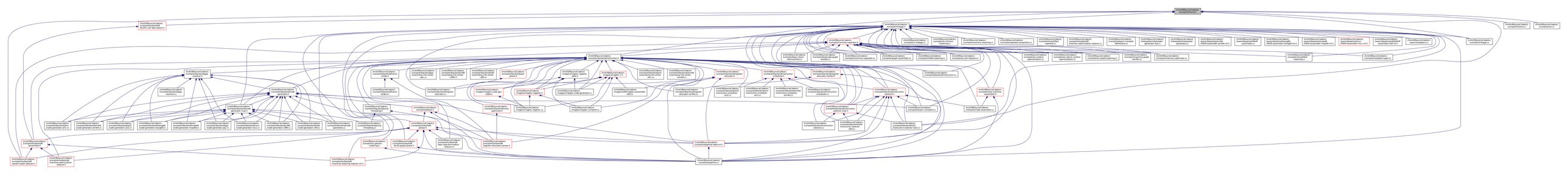

/mnt/V8SourceCode/src
/compiler/frame.h

/mnt/V8SourceCode/src
/compiler/backend/ia32
/instruction-selector-ia32.cc

/mnt/V8SourceCode/src
/compiler/backend/instruction.h

/mnt/V8SourceCode/src
/compiler/turboshaft
/assert-types-reducer.h

/mnt/V8SourceCode/src
/compiler/frame.cc

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/compiler/linkage.cc

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/compiler/linkage.h

/mnt/V8SourceCode/src
/compiler/osr.cc

/mnt/V8SourceCode/src
/compiler/turboshaft
/builtin-call-descriptors.h

/mnt/V8SourceCode/src
/compiler/backend/arm
/unwinding-info-writer
- arm.cc

/mnt/V8SourceCode/src
/compiler/backend/arm64
/unwinding-info-writer
- arm64.cc

/mnt/V8SourceCode/src
/compiler/backend/code
- generator-impl.h

/mnt/V8SourceCode/src
/compiler/backend/code
- generator.h

/mnt/V8SourceCode/src
/compiler/pipeline-data-inl.h

/mnt/V8SourceCode/src
/compiler/pipeline.cc

/mnt/V8SourceCode/src
/compiler/turboshaft
/phase.h

/mnt/V8SourceCode/src
/compiler/turboshaft
/machine-optimization
- reducer.h

/mnt/V8SourceCode/src
/compiler/turboshaft
/late-load-elimination
- reducer.cc

/mnt/V8SourceCode/src
/compiler/backend/frame
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/mnt/V8SourceCode/src
/compiler/backend/gap
- resolver.h

/mnt/V8SourceCode/src
/compiler/backend/ia32
/instruction-scheduler
- ia32.cc

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/mnt/V8SourceCode/src
/compiler/backend/instruction
- selector.h

/mnt/V8SourceCode/src
/compiler/backend/instruction
- selector-impl.h

/mnt/V8SourceCode/src
/compiler/backend/instruction
- selector.cc

/mnt/V8SourceCode/src
/compiler/backend/x64
/instruction-selector
-x64.cc

/mnt/V8SourceCode/src
/compiler/backend/instruction.cc

/mnt/V8SourceCode/src
/compiler/backend/jump
-threading.h

/mnt/V8SourceCode/src
/compiler/backend/move
- optimizer.h

/mnt/V8SourceCode/src
/compiler/backend/ppc
/unwinding-info-writer
- ppc.cc

/mnt/V8SourceCode/src
/compiler/backend/register
- allocator-verifier.cc

/mnt/V8SourceCode/src
/compiler/backend/register
- allocator-verifier.h

/mnt/V8SourceCode/src
/compiler/backend/register
- allocator.h

/mnt/V8SourceCode/src
/compiler/backend/s390
/unwinding-info-writer
- s390.cc

/mnt/V8SourceCode/src
/compiler/backend/spill
- placer.h

/mnt/V8SourceCode/src
/compiler/backend/x64
/unwinding-info-writer
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/mnt/V8SourceCode/src
/maglev/maglev-code-gen
- state.h

/mnt/V8SourceCode/src
/maglev/maglev-regalloc.cc

/mnt/V8SourceCode/src
/maglev/maglev-code-generator.cc

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/maglev/maglev-compiler.cc

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/maglev/maglev-ir.h

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/maglev/maglev-regalloc.h

/mnt/V8SourceCode/src
/maglev/x64/maglev-assembler
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/mnt/V8SourceCode/src
/maglev/maglev-regalloc
- data.h

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/code-generator-arm.cc

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/code-generator-arm64.cc

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/compiler/backend/code
- generator.cc

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/compiler/backend/ia32
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/code-generator-riscv.cc

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/code-generator-s390.cc

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/compiler/backend/x64
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/compiler/phase.h

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/compiler/turboshaft
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/compiler/turboshaft
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/compiler/turboshaft
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/compiler/turboshaft
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/compiler/backend/frame
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/mnt/V8SourceCode/src
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/mnt/V8SourceCode/src
/compiler/backend/arm64
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/compiler/basic-block
- instrumentor.cc

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/compiler/bytecode-graph
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/compiler/c-linkage.cc

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/compiler/common-operator.cc

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/compiler/common-operator.h

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/compiler/int64-lowering.cc

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/compiler/js-call-reducer.cc

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/compiler/js-context
- specialization.cc

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/compiler/js-native-context
- specialization.cc

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/compiler/machine-graph
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/compiler/memory-optimizer.cc

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/mnt/V8SourceCode/src
/compiler/simplified
- lowering.cc

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/compiler/js-generic
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/mnt/V8SourceCode/src
/compiler/js-intrinsic
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/compiler/operator-properties.cc

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- operator.cc

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/memory-optimization-reducer.cc

/mnt/V8SourceCode/src
/compiler/wasm-compiler
- definitions.cc

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/interpreter/interpreter
- generator-tsa.cc

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/interpreter/interpreter
- generator.cc

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/liftoff-assembler-arm64-inl.h

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/wasm/baseline/liftoff
- assembler.cc

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/liftoff-assembler-loong64-inl.h

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/wasm/baseline/mips64
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/liftoff-assembler-riscv-inl.h

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/wasm/baseline/x64/liftoff
- assembler-x64-inl.h

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/wasm/wrappers.cc