

v8::internal::maglev  
::MaglevAssembler::TestInt32  
AndJumpIfAllClear

```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::TestInt32  
AndJumpIfAllClear"] --> B["v8::internal::Assembler::b"]; A --> C["v8::internal::Assembler::tst"];
```

v8::internal::Assembler::b

v8::internal::Assembler::tst