

v8::internal::compiler
::Arm64OperandConverter
::OutputRegister32

v8::internal::compiler
::Arm64OperandConverter
::OutputRegister64

v8::internal::compiler
::InstructionOperandConverter
::OutputRegister

```
graph LR; A["v8::internal::compiler::Arm64OperandConverter::OutputRegister32"] --> C["v8::internal::compiler::InstructionOperandConverter::OutputRegister"]; B["v8::internal::compiler::Arm64OperandConverter::OutputRegister64"] --> C;
```