

v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::PreProcessBasicBlock



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::PreProcessBasicBlock"] --> B["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::IsMaglevMainGeneratorSwitchBlock"]
```

A diagram showing a transition from a white box on the left to a grey box on the right. A blue arrow points from the right side of the white box to the left side of the grey box. Both boxes contain text representing code paths in the V8 compiler.

v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::IsMaglevMainGeneratorSwitchBlock