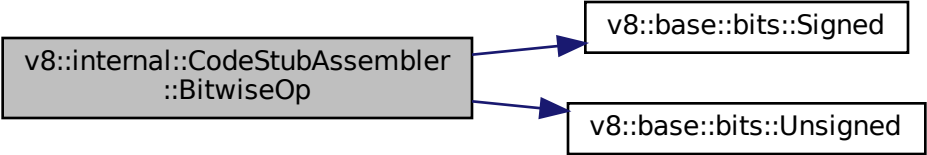


v8::internal::CodeStubAssembler
::BitwiseOp



```
graph LR; A[v8::internal::CodeStubAssembler::BitwiseOp] --> B[v8::base::bits::Signed]; A --> C[v8::base::bits::Unsigned];
```

v8::base::bits::Signed

v8::base::bits::Unsigned