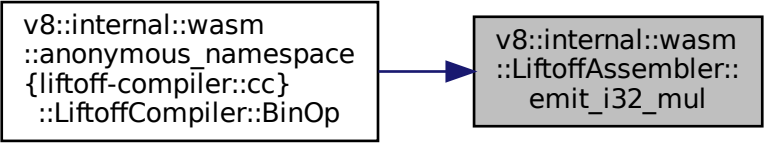


```
v8::internal::wasm  
::anonymous_namespace  
{liftoff-compiler::cc}  
::LiftoffCompiler::BinOp
```



```
graph LR; A["v8::internal::wasm  
::anonymous_namespace  
{liftoff-compiler::cc}  
::LiftoffCompiler::BinOp"] --> B["v8::internal::wasm  
::LiftoffAssembler::  
emit_i32_shr"]
```

```
v8::internal::wasm  
::LiftoffAssembler::  
emit_i32_shr
```