

v8::internal::maglev  
::Int32Compare::options

v8::internal::maglev  
::Int32Compare::PrintParams

v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process

v8::internal::maglev  
::Int32Compare::operation

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graph LR; A["v8::internal::maglev::Int32Compare::options"] --> D["v8::internal::maglev::Int32Compare::operation"]; B["v8::internal::maglev::Int32Compare::PrintParams"] --> D; C["v8::internal::compiler::turbohaft::GraphBuildingNodeProcessor::Process"] --> D;
```

The diagram illustrates a dependency or call graph. Three source nodes on the left point to a single target node on the right. The target node is shaded gray, while the source nodes are white with black borders. The arrows are blue and point from the source nodes to the target node.