

v8::internal::maglev  
::StraightForwardRegisterAllocator  
::SpillAndClearRegisters



```
graph LR; A["v8::internal::maglev  
::StraightForwardRegisterAllocator  
::SpillAndClearRegisters"] --> B["v8::internal::maglev  
::RegisterFrameState  
::FreeRegistersUsedBy"]
```

v8::internal::maglev  
::RegisterFrameState  
::FreeRegistersUsedBy