

v8::internal::maglev
::MaglevAssembler::JumpIfNot
HoleNan

v8::internal::MacroAssembler
::SmiUntag

v8::internal::MemOperand
::IsImmediateOffset

```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
HoleNan"] --> C["v8::internal::MemOperand  
::IsImmediateOffset"]; B["v8::internal::MacroAssembler  
::SmiUntag"] --> C;
```

The diagram illustrates a control flow or data dependency. Two source nodes on the left point to a single target node on the right. The top source node is labeled 'v8::internal::maglev::MaglevAssembler::JumpIfNotHoleNan'. The bottom source node is labeled 'v8::internal::MacroAssembler::SmiUntag'. Both source nodes are white with black borders. The target node is labeled 'v8::internal::MemOperand::IsImmediateOffset' and has a gray background with a black border. Blue arrows indicate the direction of the flow from the source nodes to the target node.