


v8::internal::compiler
::CallLatency



```
graph LR; A[v8::internal::compiler::CallLatency] --> B[v8::internal::compiler::DadduLatency]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'v8::internal::compiler' on the top line and '::CallLatency' on the bottom line. The right box is white with a black border and contains the text 'v8::internal::compiler' on the top line and '::DadduLatency' on the bottom line. A dark blue arrow points from the right side of the left box to the left side of the right box.

v8::internal::compiler
::DadduLatency