

v8::internal::maglev  
::MaglevAssembler::CheckAnd  
EmitDeferredWriteBarrier

v8::internal::maglev  
::detail::PushAllHelper  
< Arg1, Arg2, Args... >::Push

v8::internal::maglev  
::SaveRegisterStateForCall  
::SaveRegisterStateForCall

v8::internal::MacroAssembler  
::PushAll

```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::CheckAnd  
EmitDeferredWriteBarrier"] --> D["v8::internal::MacroAssembler  
::PushAll"]; B["v8::internal::maglev  
::detail::PushAllHelper  
< Arg1, Arg2, Args... >::Push"] --> D; C["v8::internal::maglev  
::SaveRegisterStateForCall  
::SaveRegisterStateForCall"] --> D;
```

The diagram illustrates a call graph where three source functions are calling a common target function. The source functions are arranged vertically on the left, and the target function is in a box on the right. Blue arrows point from each source box to the target box. The target box is shaded gray, while the source boxes are white with black borders.