

v8::internal::compiler
::turbohaft::GraphBuildingNode
Processor::Process



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process"] --> B["v8::internal::maglev  
::TestInstanceOf::callable"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is white with a black border and contains three lines of text: 'v8::internal::compiler', '::turbohaft::GraphBuildingNode', and 'Processor::Process'. The right box is gray with a black border and contains two lines of text: 'v8::internal::maglev' and '::TestInstanceOf::callable'. A blue arrow points from the right side of the left box to the left side of the right box.

v8::internal::maglev
::TestInstanceOf::callable