

v8::internal::maglev
::MaglevAssembler::JumpIfNot
HoleNan

v8::internal::maglev
::MaglevAssembler::LoadAddress

v8::internal::MemOperand::am

```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::JumpIfNot  
HoleNan"] --> C["v8::internal::MemOperand::am"]; B["v8::internal::maglev  
::MaglevAssembler::LoadAddress"] --> C;
```

The diagram illustrates a control flow or data flow. Two source boxes on the left point to a single target box on the right. The top source box contains the text 'v8::internal::maglev' followed by '::MaglevAssembler::JumpIfNot' and 'HoleNan' on separate lines. The bottom source box contains 'v8::internal::maglev' followed by '::MaglevAssembler::LoadAddress' on separate lines. The target box on the right contains 'v8::internal::MemOperand::am'. Blue arrows indicate the flow from each source box to the target box.