

v8::internal::compiler
::turbohaft::MachineLowering
Reducer::ConvertUntaggedToJSPrimitive

v8::internal::compiler
::turbohaft::MachineLowering
Reducer::LoadFieldByIndex

v8::internal::compiler
::turbohaft::MachineLowering
Reducer::TransitionAndStoreArrayElement

v8::internal::compiler
::turbohaft::MachineLowering
Reducer::AllocateHeapNumber

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graph LR; A["v8::internal::compiler<br/>::turbohaft::MachineLowering<br/>Reducer::ConvertUntaggedToJSPrimitive"] --> D["v8::internal::compiler<br/>::turbohaft::MachineLowering<br/>Reducer::AllocateHeapNumber"]; B["v8::internal::compiler<br/>::turbohaft::MachineLowering<br/>Reducer::LoadFieldByIndex"] --> D; C["v8::internal::compiler<br/>::turbohaft::MachineLowering<br/>Reducer::TransitionAndStoreArrayElement"] --> D;
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The diagram illustrates a control flow or dependency graph. Three source nodes on the left point to a single target node on the right. The target node is shaded gray, while the source nodes are white. All nodes represent the same V8 internal compiler context (v8::internal::compiler::turbohaft::MachineLowering) but different reducer methods.