

v8::internal::anonymous
_namespace{experimental
-compiler::cc}::CompileVisitor
::ClearRegisters

v8::internal::anonymous
_namespace{experimental
-compiler::cc}::FilterGroupsCompile
Visitor::VisitLookaround

v8::internal::anonymous
_namespace{experimental
-compiler::cc}::FilterGroupsCompile
Visitor::VisitQuantifier

v8::internal::anonymous
_namespace{experimental
-compiler::cc}::CompileVisitor
::VisitQuantifier

v8::internal::Interval
::is_empty

```
graph LR; A["v8::internal::anonymous  
_namespace{experimental  
-compiler::cc}::CompileVisitor  
::ClearRegisters"] --> D["v8::internal::Interval  
::is_empty"]; B["v8::internal::anonymous  
_namespace{experimental  
-compiler::cc}::FilterGroupsCompile  
Visitor::VisitLookaround"] --> D; C["v8::internal::anonymous  
_namespace{experimental  
-compiler::cc}::FilterGroupsCompile  
Visitor::VisitQuantifier"] --> D; E["v8::internal::anonymous  
_namespace{experimental  
-compiler::cc}::CompileVisitor  
::VisitQuantifier"] --> D;
```

The diagram illustrates a control flow or data flow where four distinct operations or visitor methods converge on a single state-checking operation. The source nodes are arranged vertically on the left, while the target node is positioned on the right. Arrows from each source node point towards the target node, indicating a unified action or condition being reached.