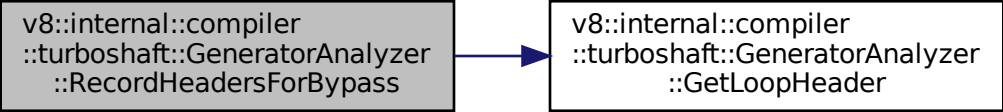


v8::internal::compiler  
::turbohaft::GeneratorAnalyzer  
::RecordHeadersForBypass



```
graph LR; A["v8::internal::compiler  
::turbohaft::GeneratorAnalyzer  
::RecordHeadersForBypass"] --> B["v8::internal::compiler  
::turbohaft::GeneratorAnalyzer  
::GetLoopHeader"]
```

v8::internal::compiler  
::turbohaft::GeneratorAnalyzer  
::GetLoopHeader