

v8::internal::compiler
::turbohaft::IfElseCascadeTo
SwitchReducer::ReduceInputGraphBranch



```
graph LR; A["v8::internal::compiler  
::turbohaft::IfElseCascadeTo  
SwitchReducer::ReduceInputGraphBranch"] --> B["v8::internal::compiler  
::turbohaft::IfElseCascadeTo  
SwitchReducer::EmitSwitch"]
```

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::turbohaft::IfElseCascadeTo
SwitchReducer::EmitSwitch