

v8::internal::SharedMacro
AssemblerBase::Pinsrw

v8::internal::SharedMacro
AssemblerBase::S128Load16Splat

v8::internal::Assembler
::vpinsrw

```
graph LR; A["v8::internal::SharedMacro<br>AssemblerBase::Pinsrw"] --> C["v8::internal::Assembler<br>::vpinsrw"]; B["v8::internal::SharedMacro<br>AssemblerBase::S128Load16Splat"] --> C;
```

The diagram illustrates a mapping from two shared macro definitions to a specific assembly instruction. On the left, two white rectangular boxes represent the source macros: 'v8::internal::SharedMacro AssemblerBase::Pinsrw' (top) and 'v8::internal::SharedMacro AssemblerBase::S128Load16Splat' (bottom). On the right, a gray rectangular box represents the target assembly instruction: 'v8::internal::Assembler ::vpinsrw'. Two blue arrows point from the right side of each source box to the left side of the target box, indicating that both macros are implemented by or mapped to the 'vpinsrw' instruction.