

v8::internal::compiler
::turbohaft::Pipeline
::AllocateRegisters



```
graph LR; A["v8::internal::compiler  
::turbohaft::Pipeline  
::AllocateRegisters"] --> B["v8::internal::compiler  
::RegisterAllocationData  
::RangesDefinedInDeferredStayIn  
Deferred"]
```

v8::internal::compiler
::RegisterAllocationData
::RangesDefinedInDeferredStayIn
Deferred