

v8::internal::SharedMacro
AssemblerBase::F64x2ReplaceLane

v8::internal::SharedMacro
AssemblerBase::S128Load16Splat

v8::internal::Assembler
::movlhps

```
graph LR; A["v8::internal::SharedMacro<br>AssemblerBase::F64x2ReplaceLane"] --> C["v8::internal::Assembler<br>::movlhps"]; B["v8::internal::SharedMacro<br>AssemblerBase::S128Load16Splat"] --> C;
```

The diagram illustrates a mapping from two shared macro definitions to a single assembly instruction. On the left, two white rectangular boxes represent the macros: 'v8::internal::SharedMacro AssemblerBase::F64x2ReplaceLane' (top) and 'v8::internal::SharedMacro AssemblerBase::S128Load16Splat' (bottom). On the right, a gray rectangular box represents the assembly instruction 'v8::internal::Assembler ::movlhps'. Two blue arrows point from each macro box to the assembly box, indicating that both macros are implemented using this specific instruction.