

v8::internal::SharedMacro  
AssemblerBase::l8x16Shl

v8::internal::SharedMacro  
AssemblerBase::l8x16ShrS

v8::internal::SharedMacro  
AssemblerBase::l8x16ShrU

v8::internal::SharedMacro  
AssemblerBase::Add

```
graph LR; A["v8::internal::SharedMacro<br>AssemblerBase::l8x16Shl"] --> D["v8::internal::SharedMacro<br>AssemblerBase::Add"]; B["v8::internal::SharedMacro<br>AssemblerBase::l8x16ShrS"] --> D; C["v8::internal::SharedMacro<br>AssemblerBase::l8x16ShrU"] --> D;
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The diagram illustrates a macro substitution or inheritance relationship. Three source macros on the left, each in a white box with a black border, point via blue arrows to a single target macro on the right, which is in a gray box with a black border. The source macros are: 1) v8::internal::SharedMacro AssemblerBase::l8x16Shl, 2) v8::internal::SharedMacro AssemblerBase::l8x16ShrS, and 3) v8::internal::SharedMacro AssemblerBase::l8x16ShrU. The target macro is v8::internal::SharedMacro AssemblerBase::Add.