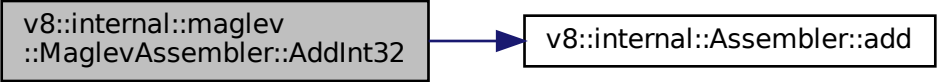


v8::internal::maglev  
::MaglevAssembler::AddInt32



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::AddInt32"] --> B["v8::internal::Assembler::add"]
```

v8::internal::Assembler::add