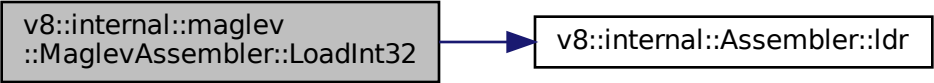


v8::internal::maglev
::MaglevAssembler::LoadInt32



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::LoadInt32"] --> B["v8::internal::Assembler::ldr"]
```

v8::internal::Assembler::ldr