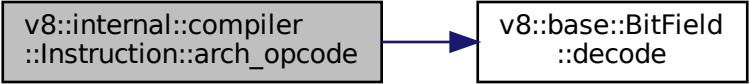


v8::internal::compiler
::Instruction::arch_opcode



```
graph LR; A["v8::internal::compiler  
::Instruction::arch_opcode"] --> B["v8::base::BitField  
::decode"]
```

v8::base::BitField
::decode