

v8::internal::wasm
::LiftoffAssembler::
emit_i32_rems

v8::internal::wasm
::LiftoffAssembler::
emit_i32_remu

v8::internal::Assembler::mls

```
graph LR; A["v8::internal::wasm::LiftoffAssembler::emit_i32_rems"] --> C["v8::internal::Assembler::mls"]; B["v8::internal::wasm::LiftoffAssembler::emit_i32_remu"] --> C;
```

The diagram illustrates a mapping or aliasing relationship. On the left, there are two white rectangular boxes. The top box contains the text 'v8::internal::wasm::LiftoffAssembler::emit_i32_rems'. The bottom box contains the text 'v8::internal::wasm::LiftoffAssembler::emit_i32_remu'. On the right, there is a single gray rectangular box containing the text 'v8::internal::Assembler::mls'. Two blue arrows originate from the right side of the two white boxes and point towards the left side of the gray box, indicating that both source functions are mapped to the target function.