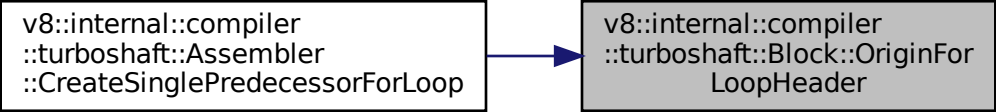


v8::internal::compiler  
::turbohaft::Assembler  
::CreateSinglePredecessorForLoop



```
graph LR; A["v8::internal::compiler  
::turbohaft::Assembler  
::CreateSinglePredecessorForLoop"] --> B["v8::internal::compiler  
::turbohaft::Block::OriginFor  
LoopHeader"]
```

v8::internal::compiler  
::turbohaft::Block::OriginFor  
LoopHeader