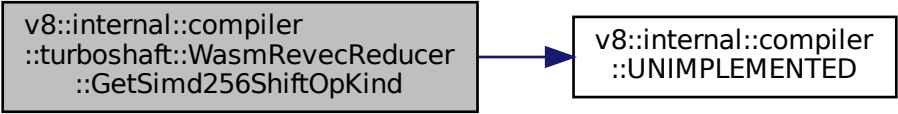


v8::internal::compiler  
::turbohaft::WasmRevecReducer  
::GetSimd256ShiftOpKind



```
graph LR; A["v8::internal::compiler  
::turbohaft::WasmRevecReducer  
::GetSimd256ShiftOpKind"] --> B["v8::internal::compiler  
::UNIMPLEMENTED"]
```

A diagram consisting of two rectangular boxes connected by a blue arrow pointing from left to right. The left box has a light gray background and a black border, containing three lines of text. The right box has a white background and a black border, containing two lines of text.

v8::internal::compiler  
::UNIMPLEMENTED