

v8::internal::SharedArray
BufferBuiltinsAssembler
::AtomicBinopBuiltinCommon



```
graph LR; A["v8::internal::SharedArray  
BufferBuiltinsAssembler  
::AtomicBinopBuiltinCommon"] --> B["v8::internal::SharedArray  
BufferBuiltinsAssembler  
::ValidateAtomicAccess"]
```

The diagram consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box is white with a black border and contains three lines of text. The right box is light gray with a black border and also contains three lines of text. The text in both boxes is identical except for the last line, which is '::AtomicBinopBuiltinCommon' in the left box and '::ValidateAtomicAccess' in the right box.

v8::internal::SharedArray
BufferBuiltinsAssembler
::ValidateAtomicAccess