

v8::internal::maglev
::MaglevCompiler::Compile

v8::internal::maglev
::LoopOptimizationProcessor
::LoopOptimizationProcessor

v8::internal::maglev
::MaglevCompilationUnit
::feedback

```
graph LR; A["v8::internal::maglev  
::MaglevCompiler::Compile"] --> C["v8::internal::maglev  
::MaglevCompilationUnit  
::feedback"]; B["v8::internal::maglev  
::LoopOptimizationProcessor  
::LoopOptimizationProcessor"] --> C;
```

The diagram illustrates a flow where two separate components on the left (a compiler and a loop optimization processor) both provide input or feedback to a single compilation unit on the right. The boxes on the left are white with black borders, while the box on the right is gray with a black border. Blue arrows indicate the direction of the flow from left to right.