

v8::internal::maglev
::StraightForwardRegisterAllocator
::ForceAllocate



```
graph LR; A["v8::internal::maglev  
::StraightForwardRegisterAllocator  
::ForceAllocate"] --> B["v8::internal::maglev  
::ValueLocation::AssignedDouble  
Register"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow pointing from left to right. The left box is white with a black border and contains the text 'v8::internal::maglev', '::StraightForwardRegisterAllocator', and '::ForceAllocate' on three lines. The right box is gray with a black border and contains the text 'v8::internal::maglev', '::ValueLocation::AssignedDouble', and 'Register' on three lines. A blue arrow points from the right side of the left box to the left side of the right box.

v8::internal::maglev
::ValueLocation::AssignedDouble
Register