

v8::internal::MacroAssembler  
::BranchLong

v8::internal::MacroAssembler  
::TailCallBuiltin

v8::internal::MacroAssembler  
::GenPCRelativeJump

```
graph LR; A["v8::internal::MacroAssembler::BranchLong"] --> C["v8::internal::MacroAssembler::GenPCRelativeJump"]; B["v8::internal::MacroAssembler::TailCallBuiltin"] --> C;
```

The diagram illustrates a mapping from two source functions to a single target function. On the left, two white rectangular boxes are stacked vertically. The top box contains the text 'v8::internal::MacroAssembler::BranchLong' and the bottom box contains 'v8::internal::MacroAssembler::TailCallBuiltin'. On the right, a gray rectangular box contains the text 'v8::internal::MacroAssembler::GenPCRelativeJump'. Two blue arrows originate from the right side of the left boxes and point towards the left side of the gray box, indicating that both source functions are mapped to or implemented by the target function.