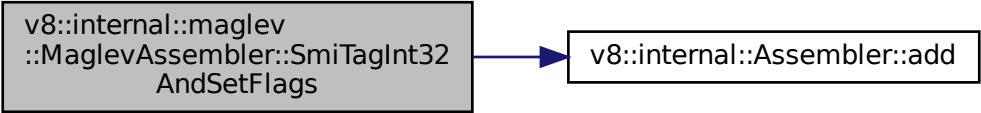


v8::internal::maglev
::MaglevAssembler::SmiTagInt32
AndSetFlags



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::SmiTagInt32  
AndSetFlags"] --> B["v8::internal::Assembler::add"]
```

v8::internal::Assembler::add