

v8::internal::VRegister
::ls1B

v8::internal::CPURegister::lsB

v8::internal::CPURegister
::ls8Bits



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graph LR; A[v8::internal::VRegister::ls1B] --> C[v8::internal::CPURegister::ls8Bits]; B[v8::internal::CPURegister::lsB] --> C;
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The diagram consists of three rectangular boxes. On the left, there are two boxes stacked vertically. The top box contains the text 'v8::internal::VRegister::ls1B'. The bottom box contains the text 'v8::internal::CPURegister::lsB'. On the right, there is a single box containing the text 'v8::internal::CPURegister::ls8Bits'. Two blue arrows point from the right side of the two boxes on the left to the left side of the box on the right. The box on the right has a light gray background, while the other two have white backgrounds.