


v8::internal::compiler
::TruncLSLatency



```
graph LR; A[v8::internal::compiler::TruncLSLatency] --> B[v8::internal::compiler::SituLatency]
```

A diagram showing a transition from a gray box to a white box. The gray box on the left contains the text 'v8::internal::compiler' and '::TruncLSLatency'. A blue arrow points from the right side of the gray box to the left side of the white box on the right. The white box contains the text 'v8::internal::compiler' and '::SituLatency'.

v8::internal::compiler
::SituLatency