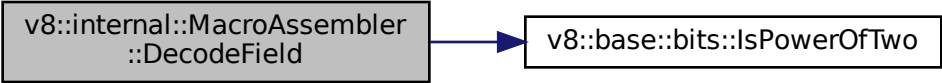


v8::internal::MacroAssembler
::DecodeField



```
graph LR; A[v8::internal::MacroAssembler::DecodeField] --> B[v8::base::bits::IsPowerOfTwo]
```

v8::base::bits::IsPowerOfTwo