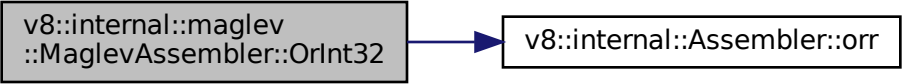


v8::internal::maglev
::MaglevAssembler::OrInt32



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::OrInt32"] --> B["v8::internal::Assembler::orr"]
```

v8::internal::Assembler::orr