

v8::internal::MacroAssembler
::Cvt_s_w

v8::internal::MacroAssembler
::LoadFPRImmediate

v8::internal::AssemblerRISCVF
::fcvt_s_w

```
graph LR; A["v8::internal::MacroAssembler::Cvt_s_w"] --> C["v8::internal::AssemblerRISCVF::fcvt_s_w"]; B["v8::internal::MacroAssembler::LoadFPRImmediate"] --> C;
```

The diagram illustrates a mapping from two macro assembler methods to a single assembler method. On the left, two white rectangular boxes represent the macro assembler methods: `v8::internal::MacroAssembler::Cvt_s_w` (top) and `v8::internal::MacroAssembler::LoadFPRImmediate` (bottom). On the right, a gray rectangular box represents the target assembler method: `v8::internal::AssemblerRISCVF::fcvt_s_w`. Two blue arrows point from the right side of each macro assembler box to the left side of the assembler box, indicating that both macro methods are implemented by or delegate to the `fcvt_s_w` assembler method.