

v8::internal::compiler  
::turbohaft::Assembler  
::AddPredecessor



```
graph LR; A["v8::internal::compiler  
::turbohaft::Assembler  
::AddPredecessor"] --> B["v8::internal::compiler  
::turbohaft::Block::IsLoopOrMerge"]
```

v8::internal::compiler  
::turbohaft::Block::IsLoopOrMerge