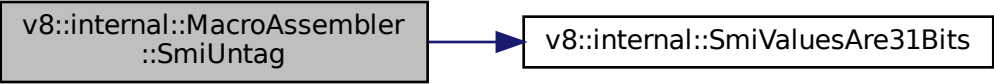


v8::internal::MacroAssembler  
::SmiUntag



```
graph LR; A["v8::internal::MacroAssembler  
::SmiUntag"] --> B["v8::internal::SmiValuesAre31Bits"]
```

v8::internal::SmiValuesAre31Bits