

v8::internal::compiler  
::turbohaft::MemoryRepresentation::Int8



```
graph LR; A["v8::internal::compiler  
::turbohaft::MemoryRepresentation::Int8"] --> B["v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::MemoryRepresentation"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow pointing from left to right. The left box has a light gray background and contains the text 'v8::internal::compiler' followed by '::turbohaft::MemoryRepresentation::Int8' on the next line. The right box has a white background and contains the text 'v8::internal::compiler' followed by '::turbohaft::MemoryRepresentation' on the next line, and '::MemoryRepresentation' on the third line.

v8::internal::compiler  
::turbohaft::MemoryRepresentation  
::MemoryRepresentation