

v8::internal::compiler  
::IA32OperandConverter  
::OutputOperand

v8::internal::compiler  
::X64OperandConverter  
::OutputOperand

v8::internal::compiler  
::InstructionOperandConverter  
::OutputSimd128Register

v8::internal::compiler  
::Instruction::Output

```
graph LR; A["v8::internal::compiler<br>::IA32OperandConverter<br>::OutputOperand"] --> D["v8::internal::compiler<br>::Instruction::Output"]; B["v8::internal::compiler<br>::X64OperandConverter<br>::OutputOperand"] --> D; C["v8::internal::compiler<br>::InstructionOperandConverter<br>::OutputSimd128Register"] --> D;
```

The diagram illustrates a dependency or inheritance structure. Three source boxes on the left point via blue arrows to a single target box on the right. The target box is shaded gray, while the source boxes are white with black borders. The source boxes represent different operand converter output types: IA32OperandConverter::OutputOperand, X64OperandConverter::OutputOperand, and InstructionOperandConverter::OutputSimd128Register. The target box represents the Instruction::Output type.