

v8::internal::compiler
::turbohaft::GraphBuildingNode
Processor::Process



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::Process"] --> B["v8::internal::maglev  
::Int32ShiftRightLogical  
::right_input"]
```

v8::internal::maglev
::Int32ShiftRightLogical
::right_input