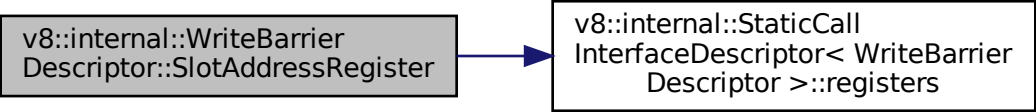


v8::internal::WriteBarrier
Descriptor::SlotAddressRegister



```
graph LR; A[v8::internal::WriteBarrierDescriptor::SlotAddressRegister] --> B[v8::internal::StaticCallInterfaceDescriptor<WriteBarrierDescriptor>::registers];
```

v8::internal::StaticCall
InterfaceDescriptor<WriteBarrier
Descriptor>::registers