

v8::internal::WriteBarrier
::MarkingSlow

v8::internal::ScavengeVisitor
::VisitExternalPointer

v8::internal::IterateAnd
ScavengePromotedObjectsVisitor
::VisitExternalPointer

v8::internal::ExternalPointer
Slot::tag_range

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graph LR; A[v8::internal::WriteBarrier::MarkingSlow] --> D[v8::internal::ExternalPointerSlot::tag_range]; B[v8::internal::ScavengeVisitor::VisitExternalPointer] --> D; C[v8::internal::IterateAndScavengePromotedObjectsVisitor::VisitExternalPointer] --> D;
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The diagram illustrates three source boxes on the left, each with a blue arrow pointing to a single target box on the right. The target box is shaded gray. The source boxes are arranged vertically, with the top box pointing to the top of the target box, the middle box pointing to the middle, and the bottom box pointing to the bottom.