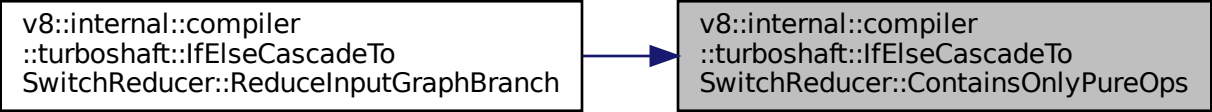


v8::internal::compiler
::turbohaft::IfElseCascadeTo
SwitchReducer::ReduceInputGraphBranch



```
graph LR; A["v8::internal::compiler  
::turbohaft::IfElseCascadeTo  
SwitchReducer::ReduceInputGraphBranch"] --> B["v8::internal::compiler  
::turbohaft::IfElseCascadeTo  
SwitchReducer::ContainsOnlyPureOps"]
```

The diagram consists of two rectangular boxes connected by a horizontal blue arrow pointing from left to right. The left box has a white background and a black border, containing three lines of text. The right box has a light gray background and a black border, also containing three lines of text. The arrow is a solid blue line with a triangular arrowhead.

v8::internal::compiler
::turbohaft::IfElseCascadeTo
SwitchReducer::ContainsOnlyPureOps