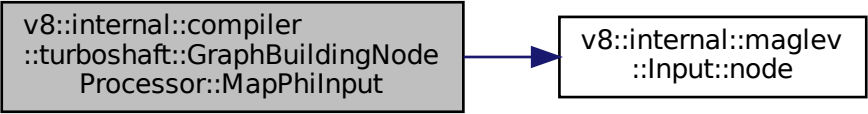


v8::internal::compiler
::turbohaft::GraphBuildingNode
Processor::MapPhiInput



```
graph LR; A["v8::internal::compiler  
::turbohaft::GraphBuildingNode  
Processor::MapPhiInput"] --> B["v8::internal::maglev  
::Input::node"]
```

A diagram showing a mapping from a compiler node to an input node. On the left, a gray rectangular box contains the text 'v8::internal::compiler', '::turbohaft::GraphBuildingNode', and 'Processor::MapPhiInput'. A blue arrow points from the right side of this box to the left side of a white rectangular box on the right. The white box contains the text 'v8::internal::maglev' and '::Input::node'.

v8::internal::maglev
::Input::node