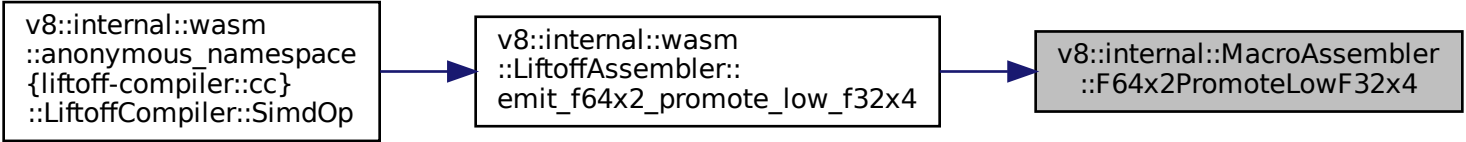


v8::internal::wasm
::anonymous_namespace
{liftoff-compiler::cc}
::LiftoffCompiler::SimdOp



```
graph LR; A["v8::internal::wasm  
::anonymous_namespace  
{liftoff-compiler::cc}  
::LiftoffCompiler::SimdOp"] --> B["v8::internal::wasm  
::LiftoffAssembler::  
emit_f64x2_promote_low_f32x4"]; B --> C["v8::internal::MacroAssembler  
::F64x2PromoteLowF32x4"]
```

v8::internal::wasm
::LiftoffAssembler::
emit_f64x2_promote_low_f32x4

v8::internal::MacroAssembler
::F64x2PromoteLowF32x4