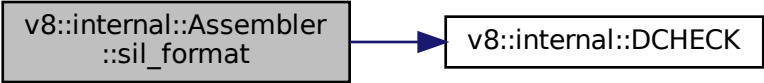


v8::internal::Assembler  
::sil\_format



```
graph LR; A["v8::internal::Assembler  
::sil_format"] --> B["v8::internal::DCHECK"]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The left box is shaded gray and contains the text 'v8::internal::Assembler' on the top line and '::sil\_format' on the bottom line. The right box is white with a black border and contains the text 'v8::internal::DCHECK'. A dark blue arrow points from the right side of the gray box to the left side of the white box.

v8::internal::DCHECK