

v8::internal::SharedMacro  
AssemblerBase::l64x2Abs

v8::internal::SharedMacro  
AssemblerBase::l64x2GeS

v8::internal::SharedMacro  
AssemblerBase::l64x2GtS

v8::internal::Assembler  
::movshdup

```
graph LR; A["v8::internal::SharedMacro<br>AssemblerBase::l64x2Abs"] --> D["v8::internal::Assembler<br>::movshdup"]; B["v8::internal::SharedMacro<br>AssemblerBase::l64x2GeS"] --> D; C["v8::internal::SharedMacro<br>AssemblerBase::l64x2GtS"] --> D;
```

The diagram illustrates a mapping from three source assembly macros to a single target assembly instruction. Three white rectangular boxes on the left, each containing a source macro name, have blue arrows pointing to a single gray rectangular box on the right containing the target instruction name.