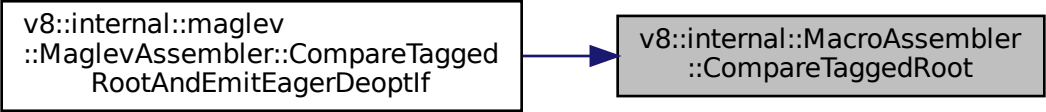


v8::internal::maglev
::MaglevAssembler::CompareTagged
RootAndEmitEagerDeoptIf



```
graph LR; A["v8::internal::maglev  
::MaglevAssembler::CompareTagged  
RootAndEmitEagerDeoptIf"] --> B["v8::internal::MacroAssembler  
::CompareTaggedRoot"]
```

v8::internal::MacroAssembler
::CompareTaggedRoot