

v8::internal::compiler
::turbohaft::RegisterRepresentation
::Compressed



```
graph LR; A["v8::internal::compiler  
::turbohaft::RegisterRepresentation  
::Compressed"] --> B["v8::internal::compiler  
::turbohaft::RegisterRepresentation  
::RegisterRepresentation"]
```

A diagram showing a transformation from a compressed register representation to a standard one. It consists of two rectangular boxes connected by a blue arrow pointing from left to right. The left box has a light gray background and contains the text 'v8::internal::compiler', '::turbohaft::RegisterRepresentation', and '::Compressed' on three lines. The right box has a white background and contains the text 'v8::internal::compiler', '::turbohaft::RegisterRepresentation', and '::RegisterRepresentation' on three lines.

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