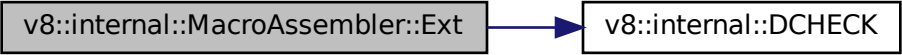


v8::internal::MacroAssembler::Ext



```
graph LR; A[v8::internal::MacroAssembler::Ext] --> B[v8::internal::DCHECK]
```

v8::internal::DCHECK