

v8::internal::maglev  
::anonymous\_namespace  
{maglev-code-generator  
::cc}::ExceptionHandlerTrampoline  
Builder::EmitPopMaterialisedResults

v8::internal::maglev  
::StraightForwardRegisterAllocator  
::ForceAllocate

v8::internal::maglev  
::anonymous\_namespace  
{maglev-code-generator  
::cc}::ExceptionHandlerTrampoline  
Builder::RecordMoves

v8::internal::maglev  
::ValueLocation::AssignedGeneral  
Register

```
graph LR; A["v8::internal::maglev  
::anonymous_namespace  
{maglev-code-generator  
::cc}::ExceptionHandlerTrampoline  
Builder::EmitPopMaterialisedResults"] --> D["v8::internal::maglev  
::ValueLocation::AssignedGeneral  
Register"]; B["v8::internal::maglev  
::StraightForwardRegisterAllocator  
::ForceAllocate"] --> D; C["v8::internal::maglev  
::anonymous_namespace  
{maglev-code-generator  
::cc}::ExceptionHandlerTrampoline  
Builder::RecordMoves"] --> D;
```

The diagram illustrates a control flow or data flow from three source functions to a single target function. The source functions are located on the left, and the target function is on the right. Arrows indicate the direction of flow from each source to the target.