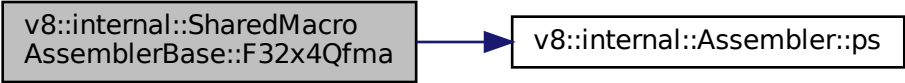


v8::internal::SharedMacro
AssemblerBase::F32x4Qfma



```
graph LR; A["v8::internal::SharedMacroAssemblerBase::F32x4Qfma"] --> B["v8::internal::Assembler::ps"]
```

v8::internal::Assembler::ps