

v8::internal::compiler
::ExtendingLoadMatcher
::Initialize

v8::internal::compiler
::anonymous_namespace
{instruction-selector-mips64
::cc}::VisitAtomicLoad

v8::internal::compiler
::anonymous_namespace
{instruction-selector-mips64
::cc}::VisitAtomicStore

v8::internal::compiler
::anonymous_namespace
{instruction-selector-mips64
::cc}::VisitWordCompare

v8::internal::compiler
::Mips64OperandGenerator
::CanBelImmediate

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graph LR; A["v8::internal::compiler::ExtendingLoadMatcher::Initialize"] --> D["v8::internal::compiler::Mips64OperandGenerator::CanBelImmediate"]; B["v8::internal::compiler::anonymous_namespace{instruction-selector-mips64::cc}::VisitAtomicLoad"] --> D; C["v8::internal::compiler::anonymous_namespace{instruction-selector-mips64::cc}::VisitAtomicStore"] --> D; E["v8::internal::compiler::anonymous_namespace{instruction-selector-mips64::cc}::VisitWordCompare"] --> D;
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The diagram illustrates a call graph where four source functions point to a single target function. The source functions are arranged vertically on the left, and the target function is on the right. Arrows point from each source function to the target function. The target function box is shaded gray, while the source function boxes are white with black borders.