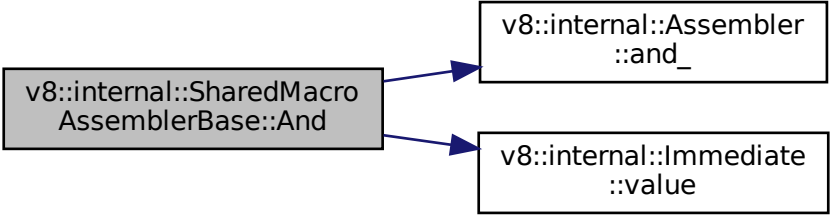


v8::internal::SharedMacro  
AssemblerBase::And



```
graph LR; A["v8::internal::SharedMacro  
AssemblerBase::And"] --> B["v8::internal::Assembler  
::and_"]; A --> C["v8::internal::Immediate  
::value"]
```

v8::internal::Assembler  
::and\_

v8::internal::Immediate  
::value