



-allocatable_double_codes_mask_
-allocatable_float_codes_mask_
-allocatable_general_codes_mask_
-allocatable_simd128_codes_mask_
-allocatable_simd256_codes_mask_

+kMaxFPRegisters
+kMaxGeneralRegisters
+kMaxRegisters
-allocatable_float_codes_
-allocatable_simd128_codes_
-allocatable_simd256_codes_
-num_allocatable_double_registers

-num_allocatable_float_registers

-num_allocatable_general_registers

-num_allocatable_simd128_registers_
...

-fp_aliasing_kind_

-allocatable_double_codes_
-allocatable_general_codes_
-num_double_registers
-num_general_registers_

| |
|--|
| v8::internal::RegisterConfiguration |
| - num_float_registers_ - num_simd128_registers_ - num_simd256_registers_ - num_allocatable_simd256_registers_ |
| + RegisterConfiguration() + num_general_registers() + num_float_registers() + num_double_registers() + num_simd128_registers() + num_simd256_registers() + num_allocatable_general_registers() + num_allocatable_float_registers() + num_allocatable_double_registers() + num_allocatable_simd128_registers() and 24 more... + Default() + Poisoning() + RestrictGeneralRegisters() |

