

v8::internal::SharedMacro
AssemblerBase::Pinsrb

v8::internal::SharedMacro
AssemblerBase::S128Load8Splat

v8::internal::Assembler
::vpinsrb

```
graph LR; A["v8::internal::SharedMacro<br>AssemblerBase::Pinsrb"] --> C["v8::internal::Assembler<br>::vpinsrb"]; B["v8::internal::SharedMacro<br>AssemblerBase::S128Load8Splat"] --> C;
```

The diagram illustrates a mapping from two source macros to a single target instruction. Two white boxes on the left represent the source macros: 'v8::internal::SharedMacro AssemblerBase::Pinsrb' and 'v8::internal::SharedMacro AssemblerBase::S128Load8Splat'. Blue arrows point from each of these boxes to a gray box on the right, which represents the target instruction: 'v8::internal::Assembler ::vpinsrb'.