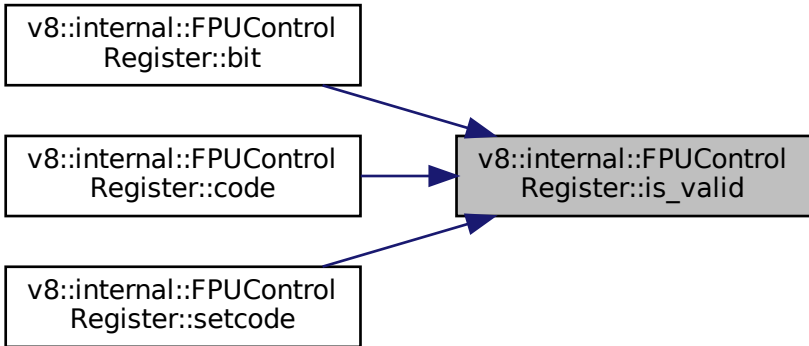


v8::internal::FPUControl
Register::bit

v8::internal::FPUControl
Register::code

v8::internal::FPUControl
Register::setcode

v8::internal::FPUControl
Register::is_valid



```
graph LR; A[v8::internal::FPUControl Register::bit] --> D[v8::internal::FPUControl Register::is_valid]; B[v8::internal::FPUControl Register::code] --> D; C[v8::internal::FPUControl Register::setcode] --> D;
```