

v8::internal::maglev
::StraightForwardRegisterAllocator
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```
graph LR; A["v8::internal::maglev  
::StraightForwardRegisterAllocator  
::StraightForwardRegisterAllocator"] --> B["v8::internal::maglev  
::Graph::set_untagged_stack_slots"]
```

v8::internal::maglev
::Graph::set_untagged
_stack_slots