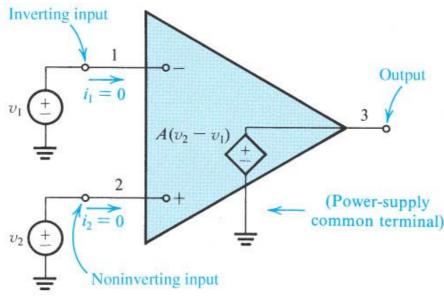
Lecture 11 Operational Amplifiers

Topics

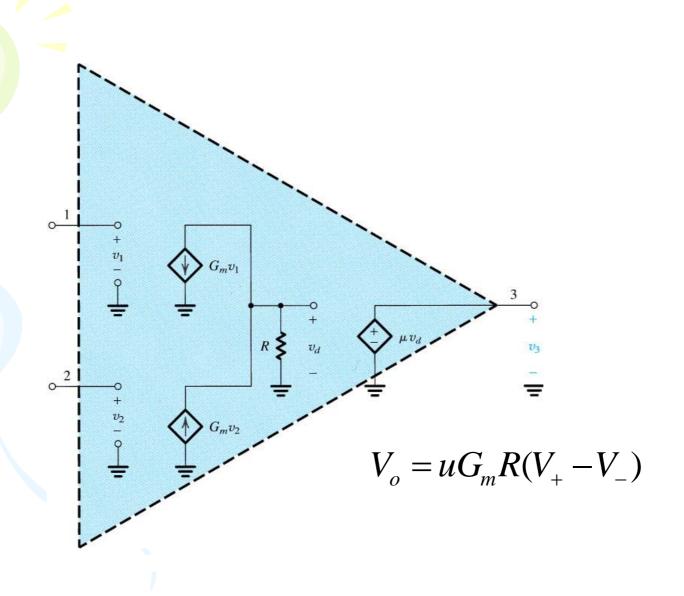
- Ideal op Amplifiers
- Ideal OPA circuits analysis
- Non-ideal op amplifiers
- Non-ideal OPA circuit analysis

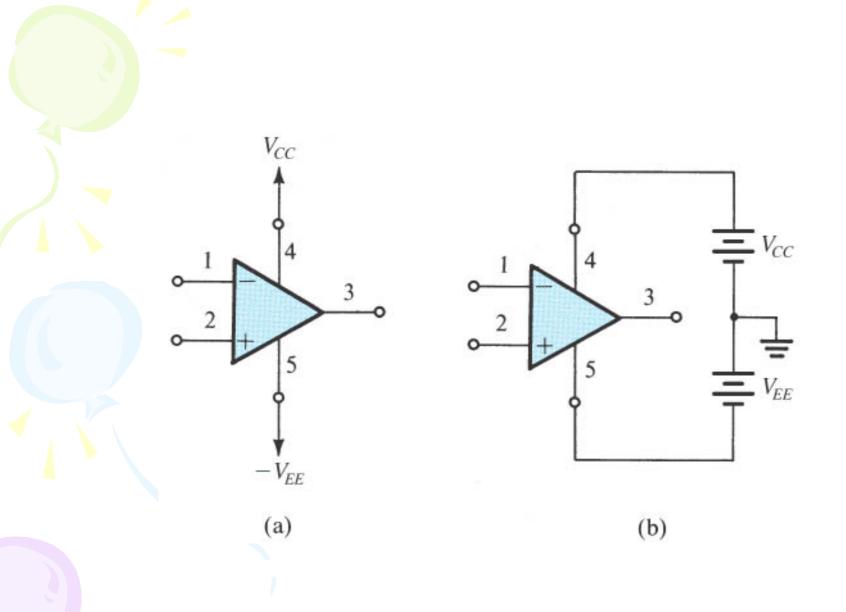
Ideal operational amplifier

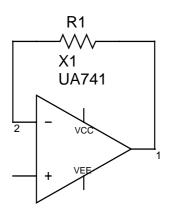


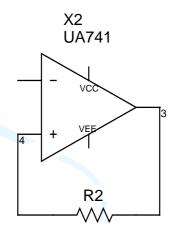
Ideal OPA characters

- 1. Infinite input impedance
- 2. Zero output impedance
- 3. Infinite bandwidth
- 4. Infinite open-loop gain
- 5. Zero common-mode gain (infinite CMRR)









Negative feedback

$$\begin{split} V_{-} &= IR + V_{o} \\ V_{o} &= A(V_{+} - V_{-}) \\ V_{o} &\uparrow \Rightarrow V_{-} \uparrow \Rightarrow V_{o} \downarrow = A(V_{+} - V_{-} \uparrow) \\ V_{o} &\downarrow \Rightarrow V_{-} \downarrow \Rightarrow V_{o} \uparrow \Rightarrow stable \end{split}$$

Positive feedback

$$\begin{split} V_{+} &= IR + V_{o} \\ V_{o} &= A(V_{+} - V_{-}) \\ V_{o} &\uparrow \Rightarrow V_{+} \uparrow \Rightarrow V_{o} \uparrow \Rightarrow + V_{sat} \\ V_{o} &\downarrow \Rightarrow V_{+} \downarrow \Rightarrow V_{o} \downarrow \Rightarrow -V_{sat} \Rightarrow unstable \end{split}$$

Ideal OPA characters

Non-ideal cases

$$R_{in} \rightarrow \infty \Longrightarrow (i_{+} = i_{-} = 0)$$

$$R_o \rightarrow 0$$

$$A \rightarrow \infty \Longrightarrow (V_{+} = V_{-})$$

$$BW \rightarrow \infty$$

$$CMRR \rightarrow \infty$$

$$R_{in} \neq \infty$$

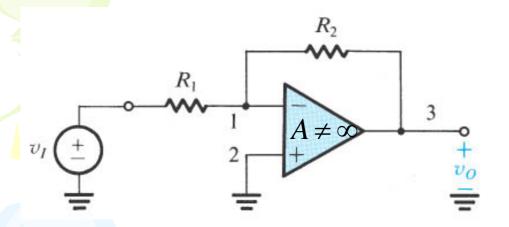
$$R_o \neq 0$$

$$A \neq \infty$$

$$BW \neq \infty$$

$$CMRR \neq \infty$$

Consider finite open-loop gain $A \neq \infty$



$$\therefore A \neq \infty$$

$$\therefore v_1 \neq v_2$$

$$\Rightarrow \frac{v_- - v_i}{R_1} + \frac{v_- - v_o}{R_2} = 0 \cdots (1)$$

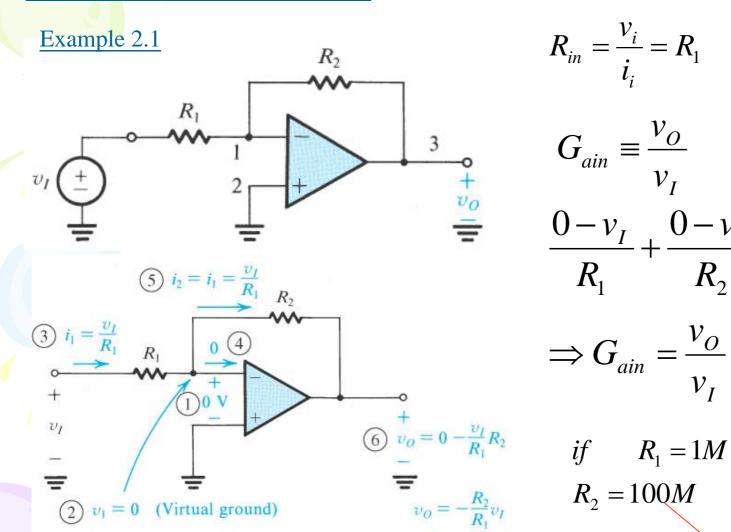
$$v_0 = A(v_+ - v_-) = -Av_- \cdots (2)$$

$$\frac{v_o}{v_i} = \frac{-R_2/R_1}{1 + (1 + R_2/R_1)/A}$$
if $A \to \infty$

$$\frac{v_o}{v_i} = -\frac{R_2}{R_1}$$

Input and output resistance

 $v_1 = 0$ (Virtual ground)



$$R_{in} = \frac{v_i}{i_i} = R_1$$

$$G_{ain} \equiv \frac{v_O}{v_I}$$

$$\frac{0 - v_I}{P} + \frac{0 - v_O}{P} = 0$$

$$\Rightarrow G_{ain} = \frac{v_O}{v_I} = -\frac{R_2}{R_1}$$

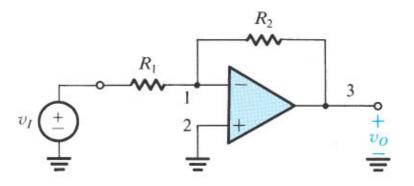
$$if R_1 = 1M$$
$$R_2 = 100M$$

An impractically large value

So we may have the problem of input resistance.

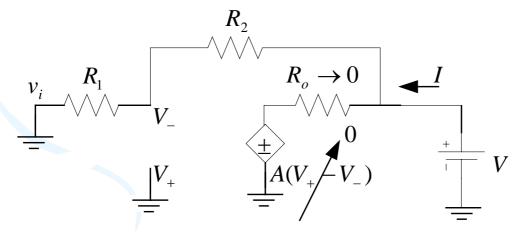
(b)

Output resistance

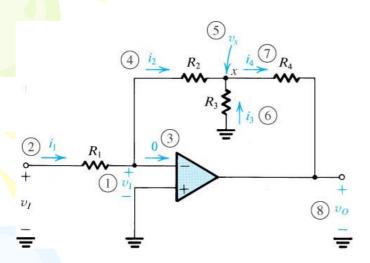




$$R_{out} = \frac{V}{I} = 0$$



Example 2.2



Comparing with Example 2.1 Design a amplifier with a gain –100 and an input resistance of 1M.

Example 2.1:

$$R_1 = 1M$$

$$R_2 = 100M$$

$$\frac{v_X - 0}{R_2} + \frac{v_X}{R_3} + \frac{v_X - v_o}{R_4} = 0 \cdots (1)$$

$$\frac{0 - v_I}{R_1} + \frac{0 - v_X}{R_2} = 0 \cdots (2)$$

$$\frac{v_o}{v_i} = -\frac{R_2}{R_1} (1 + \frac{R_4}{R_2} + \frac{R_4}{R_3})$$

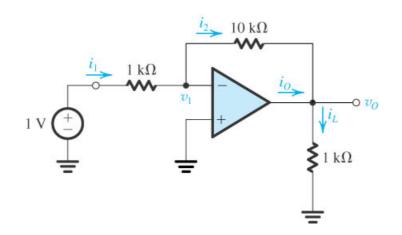
Example 2.2:

$$\frac{v_o}{v_i} = -\frac{R_2}{R_1} (1 + \frac{R_4}{R_2} + \frac{R_4}{R_3})$$

$$R_1 = 1M, R_2 = 1M$$

$$R_3 = 10.2k, R_4 = 1M$$

Exercise 2.6



$$\frac{v_{-}-1}{1k} + \frac{v_{-}-v_{o}}{10k} = 0 \cdots (1)$$

$$v_{-} = 0 \cdots (2)$$

$$\frac{-1}{1k} + \frac{-v_{o}}{10k} = 0 \Rightarrow v_{o} = \frac{-10k}{1k} = -10V$$

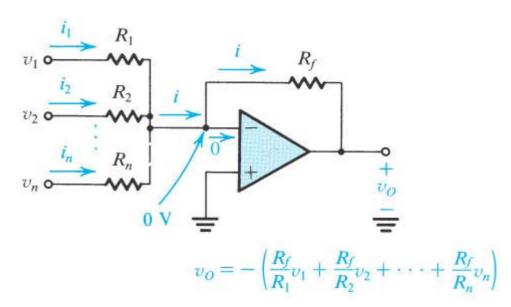
$$i_{L} = \frac{-10}{1k} = -10mA$$

$$i_{2} = \frac{0 - (-10)}{10k} = 1mA$$

$$i_{0} = -11mA$$

$$i_{1} = i_{2} = 1mA$$

Weighted summer

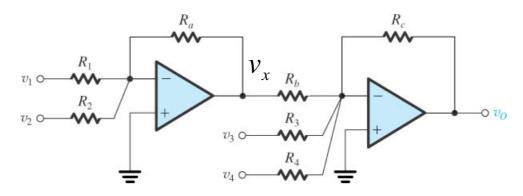


$$\frac{0-v_1}{R_1} + \frac{0-v_2}{R_2} + \dots + \frac{0-v_n}{R_n} + \frac{0-v_o}{R_f} = 0 \dots (1)$$

$$\Rightarrow \frac{-v_1}{R_1} + \frac{-v_2}{R_2} + \dots + \frac{-v_n}{R_n} = \frac{v_o}{R_f}$$

$$\Rightarrow v_o = \frac{-R_f}{R_1}v_1 + \frac{-R_f}{R_2}v_2 + \dots + \frac{-R_f}{R_n}v_n$$

Exercise D2.8

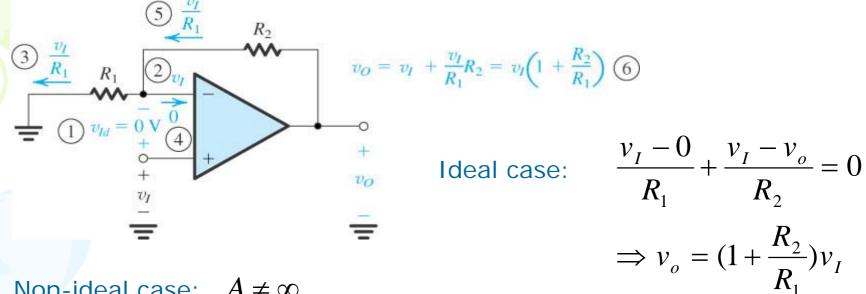


$$v_{x} = -(\frac{R_{a}}{R_{1}}v_{1} + \frac{R_{a}}{R_{2}}v_{2})$$

$$v_o = -(\frac{R_c}{R_3}v_3 + \frac{R_c}{R_4}v_4 + \frac{R_c}{R_b}v_x)$$

$$v_o = -\left(\frac{R_c}{R_3}v_3 + \frac{R_c}{R_4}v_4\right) + \frac{R_c}{R_b}\left(\frac{R_a}{R_1}v_1 + \frac{R_a}{R_2}v_2\right)$$

Non-inverting amplifier



Non-ideal case: $A \neq \infty$

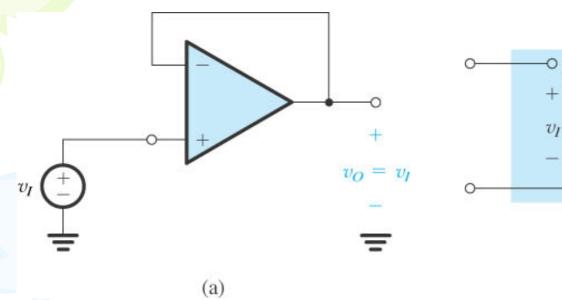
$$\frac{v_i - 0}{R_1} + \frac{v_i - v_o}{R_2} = 0....(1)$$

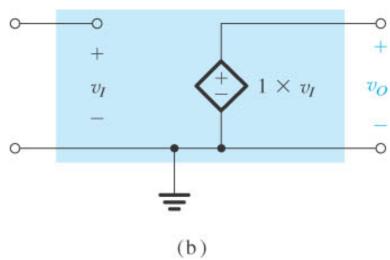
$$v_o = A(v_i - v_-).....(2)$$

$$(1) \to v_- = \frac{v_o}{R_2} (\frac{R_1 R_2}{R_1 + R_2})$$

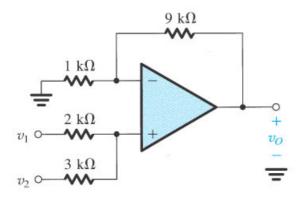
$$\frac{v_o}{v_i} = \frac{1 + \frac{R_2}{R_1}}{1 + \frac{1 + \frac{R_2}{R_1}}{A}}$$

Voltage follower





Exercise D2.9



$$v_{-} = v_{+}$$

$$\frac{v_{-}-0}{1k} + \frac{v_{-}-v_{o}}{9k} = 0 \cdots (1)$$

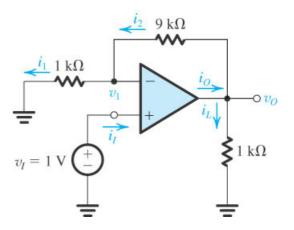
$$\frac{v_{-} - v_{1}}{2k} + \frac{v_{-} - v_{2}}{3k} = 0 \cdots (2)$$

$$(2) \Rightarrow v_{-}(\frac{1}{2k} + \frac{1}{3k}) = (\frac{v_1}{2k} + \frac{v_2}{3k})$$

$$(1) \Rightarrow v_o = 9k(\frac{1}{1k} + \frac{1}{9k})v_-$$

$$=9k(\frac{1}{1k} + \frac{1}{9k})(\frac{2k+3k}{2k\times3k})(\frac{v_1}{2k} + \frac{v_2}{3k})$$

Exercise D2.13



$$v_{-} = v_{+} = 1V$$

$$\frac{1 - 0}{1k} + \frac{1 - v_{o}}{9k} = 0 \cdots (1)$$

$$\Rightarrow v_{o} = (\frac{1}{1k} + \frac{1}{9k})9k = 10V$$

Why use difference amplifier?

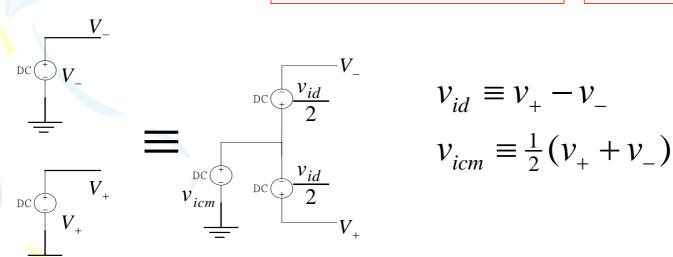
CMRR (Common-Mode Rejection ratio)

$$CMRR \equiv 20\log \frac{\left|A_d\right|}{\left|A_{cm}\right|}$$

$$v_o = A_d v_{id} + A_{cm} v_{icm}$$

Different-mode input

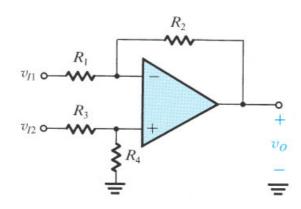
Common-mode input



Microelectric Circuit by Meiling CHEN

Difference Amplifier

Method I:



$$v_{-} = v_{+}$$

$$\frac{v_{-} - v_{i1}}{R_1} + \frac{v_{-} - v_{o}}{R_2} = 0 \cdots (1)$$

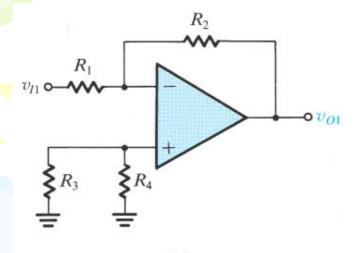
$$\frac{v_{+} - v_{i2}}{R_{3}} + \frac{v_{+}}{R_{4}} = 0 \cdots (2)$$

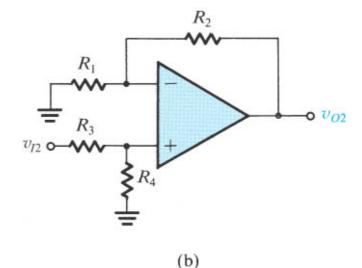
$$(1) \Rightarrow v_o = R_2 \left(\frac{v_+ - v_{i1}}{R_1} + \frac{v_+}{R_2}\right) = R_2 \left(\frac{1}{R_1} + \frac{1}{R_2}\right) v_+ - \frac{R_2}{R_1} v_{i1}$$

(2)
$$\Rightarrow v_+ (\frac{1}{R_3} + \frac{1}{R_4}) = \frac{1}{R_3} v_{i2}$$

Different-mode input

Method I: superposition



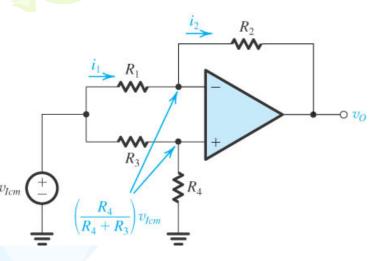


$$v_{o1} = -\frac{R_2}{R_1} v_{i1}$$
 (a)

$$v_{o2} = \frac{R_4}{R_2 + R_4} (1 + \frac{R_2}{R_1}) v_{i2} = \frac{R_2}{R_1} v_{i2} (let R_3 = R_1, R_4 = R_2)$$

$$v_o = v_{o1} + v_{o2} = \frac{R_2}{R_1} (v_{i2} - v_{i1}) = A_d (v_{i2} - v_{i1})$$

Common-mode input



$$i_1 = \frac{1}{R_1} \left[v_{icm} - \frac{R_4}{R_3 + R_4} v_{icm} \right] = \frac{R_3}{R_3 + R_4} \frac{1}{R_1} v_{icm}$$

$$v_o = \frac{R_4}{R_2 + R_4} v_{icm} - i_2 R_2$$

$$: i_1 = i_2 \implies v_o = \left[\frac{R_4}{R_3 + R_4} - \frac{R_3}{R_3 + R_4} \frac{R_2}{R_1}\right] v_{icm}$$

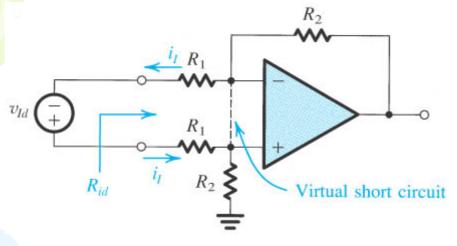
$$A_{cm} = \frac{R_4}{R_3 + R_4} \left[1 - \frac{R_3}{R_4} \frac{R_2}{R_1} \right]$$

$$\therefore R_3 = R_1, R_4 = R_2 \Rightarrow \therefore A_{cm} = 0$$

CMRR=infinite

if
$$R_3 \neq R_1, R_4 \neq R_2 \Rightarrow A_{cm} \neq 0$$

Consider the problem of input resistance



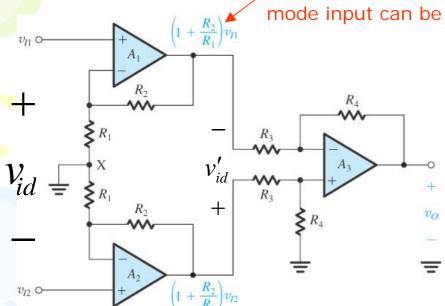
$$v_{id} = i_1 R_1 + i_1 R_1 = 2R_1 i_1$$

$$\Rightarrow R_{in(diff)} = 2R_1$$

Differential mode input resistance

$$v_o = \frac{R_2}{R_1} v_{id}$$
 if $R_{in} \uparrow \rightarrow R_1 \uparrow \rightarrow v_o \downarrow$

Instrumentation Amplifier



(a)

Not is differential mode, common mode input can be pass.

$$v_o = \frac{R_4}{R_3} v'_{id}$$

$$v'_{id} = (1 + \frac{R_2}{R_1})(v_{i2} - v_{i1})$$

$$= v_o = \frac{R_4}{R_3} (1 + \frac{R_2}{R_1}) (v_{i2} - v_{i1})$$

Advantages:

1.
$$R_{in} \rightarrow \infty$$

2.
$$A_d = \frac{R_4}{R_3} (1 + \frac{R_2}{R_1})$$

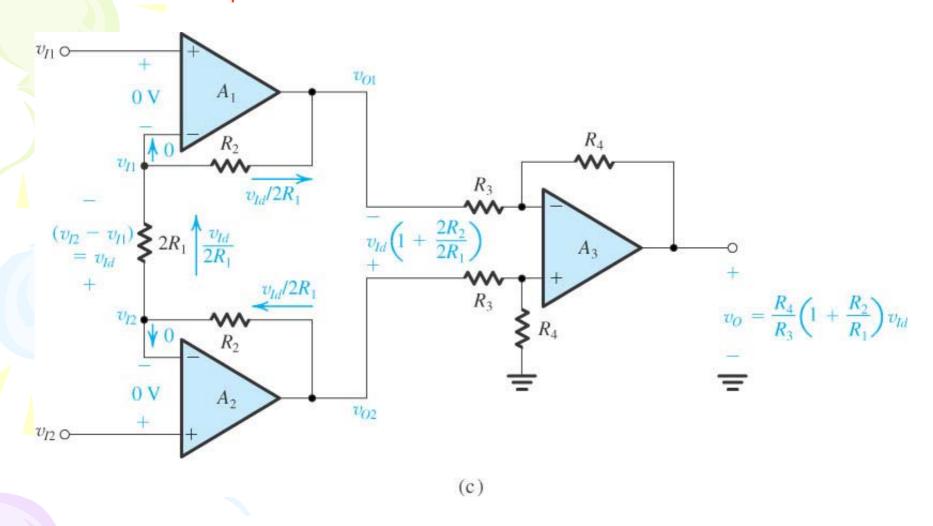
Big differential gain

Defects:

- 1. Common mode gain=differential mode gain. $v_o \rightarrow v_{sat}$
- 2. Resistance R_1 and R_2 have to match.

$$v'_{id} = (1 + \frac{R_2}{R_1})(v_{i2} - v_{i1})$$

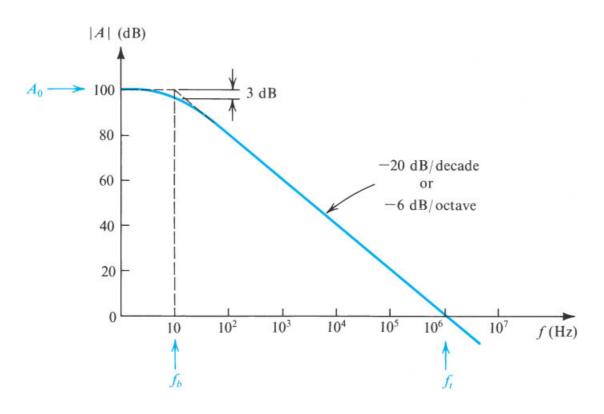
Remove the point *x*



DC non-ideal characters

- 1. Finite open loop gain (finite CMRR)
- 2. Finite BW
- 3. Offset voltage
- 4. Input bias and offset current

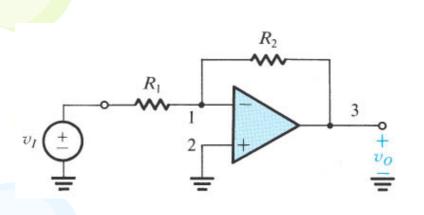
Frequency response (open-loop)

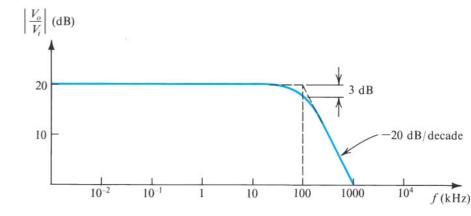


$$A(s) = \frac{A_o}{1 + \frac{s}{\omega_b}} \Rightarrow 0dB : \frac{\omega_t}{\omega_b} = A_o$$

$$\omega_t = A_o \omega_b$$

Frequency response (closed loop)





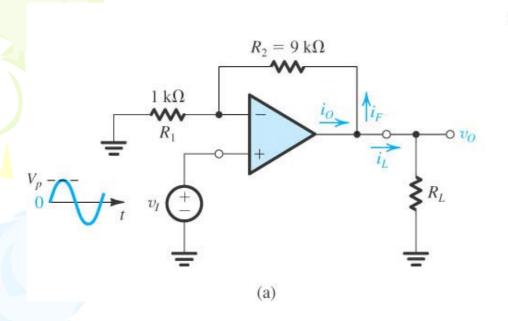
$$\frac{v_o}{v_i} = \frac{-\frac{R_2}{R_1}}{1 + (1 + \frac{R_2}{R_1})/A} \qquad A(s) = \frac{A_o}{1 + \frac{s}{\omega_h}}$$

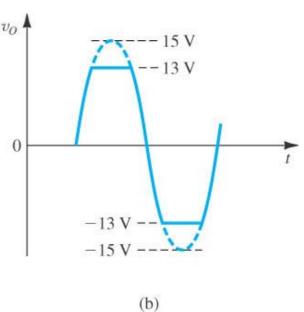
$$A(s) = \frac{A_o}{1 + \frac{s}{\omega_b}}$$

$$\frac{v_o}{v_i} = \frac{\frac{-R_2}{R_1}}{1 + \frac{1}{A_0}(1 + \frac{R_2}{R_1}) + \frac{s}{\omega_t/(1 + \frac{R_2}{R_1})}} \approx \frac{\frac{-R_2}{R_1}}{1 + \frac{s}{\omega_t/(1 + \frac{R_2}{R_1})}}$$

$$\omega_{3dB} = \frac{\omega_t}{1 + \frac{R_2}{R_1}}$$

Output voltage saturation

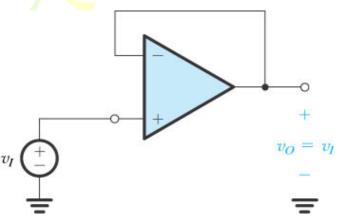


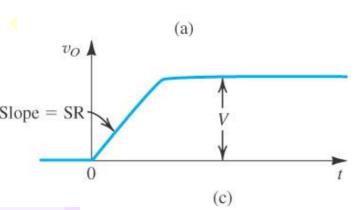


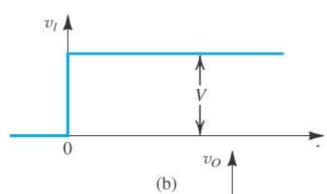
$$v_{o \max} = \pm 13V$$
$$i_{o \max} = \pm 20mA$$

Slew rate

$$SR = \frac{dv_o}{dt} \Big|_{\text{max}} (V / \mu s)$$







$$=\frac{1+\frac{R_{2}}{R_{1}}}{1+\frac{1+\frac{R_{2}}{R_{1}}}{A}}$$

$$1 = \frac{A_o}{1 + s / \omega_b}$$

$$\frac{v_o}{v_i} \approx \frac{1}{1 + \frac{s}{u_o}} (:: R_2 = 0, R_1 = \infty) \qquad \omega_t = A_o \omega_b$$

$$let v_i = V(unit step)$$

$$V_o(s) = \frac{\omega_t}{\omega_t + s} \frac{V}{s} = \frac{-V}{s + \omega_t} + \frac{V}{s} \Longrightarrow V_o(t) = V(1 - e^{-\omega_t t})$$

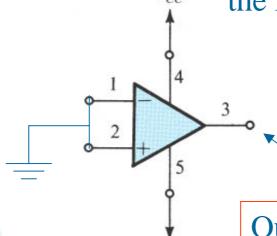
Microelectric Circuit by Meiling CHEN

Slope = $\omega_t V \leq SR$

(d)

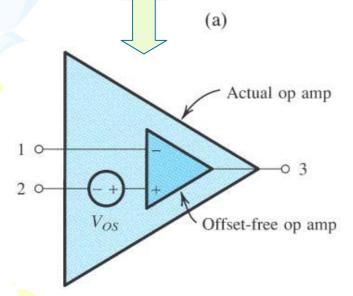
Offset voltage

From the component mismatches in the input differential stage



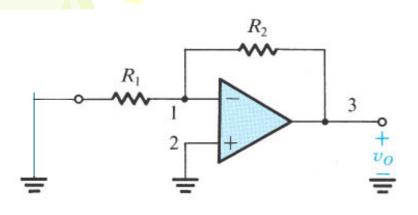
 $-V_{EE}$

Output = + saturation or - saturation



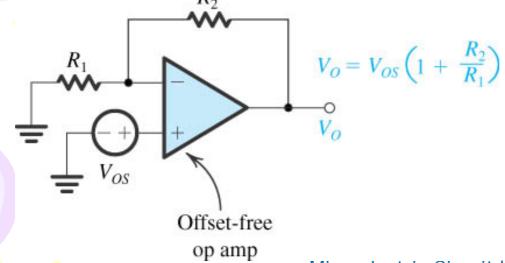
$$v_{os} \approx 1 \sim 5mV$$

May be positive or negative

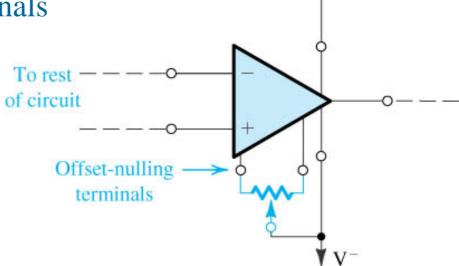




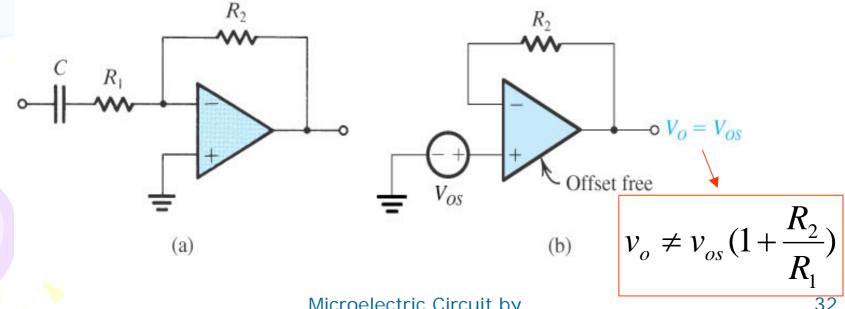
- 1. Reduce the allowable signal swing
- 2. When input is dc we would not know the output is due to v_{os} or signals



Solution 1: Offset-nulling terminals



Solution 2: Capacitive coupling (only ac signal be amplified)



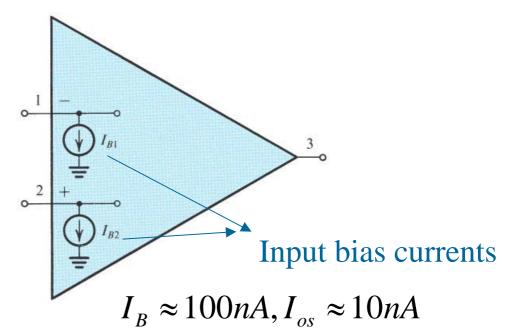
Microelectric Circuit by Meiling CHEN

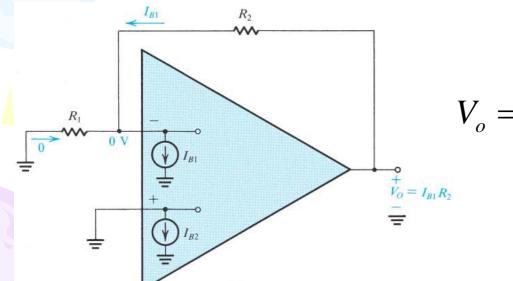
Offset current

$$I_{B} = \frac{I_{B1} + I_{B2}}{2}$$

$$I_{os} \equiv \left| I_{B1} - I_{B2} \right|$$

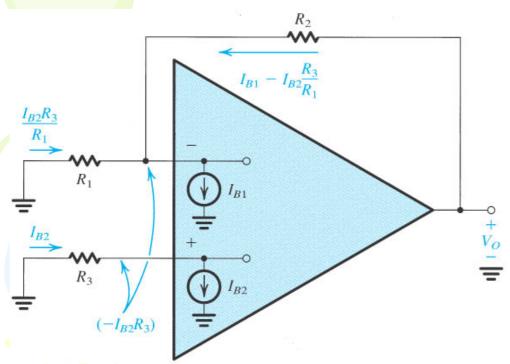
Input offset current





$$V_o = I_{B1}R_2 \Rightarrow R_2 \downarrow$$
Upper limit R_2

Solution: introducing R₃



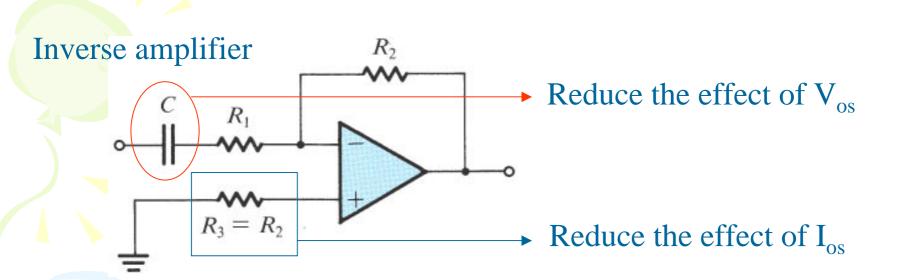
$$V_{o} = -I_{B2}R_{3} + R_{2}(I_{B1} - I_{B2}\frac{R_{3}}{R_{1}})$$

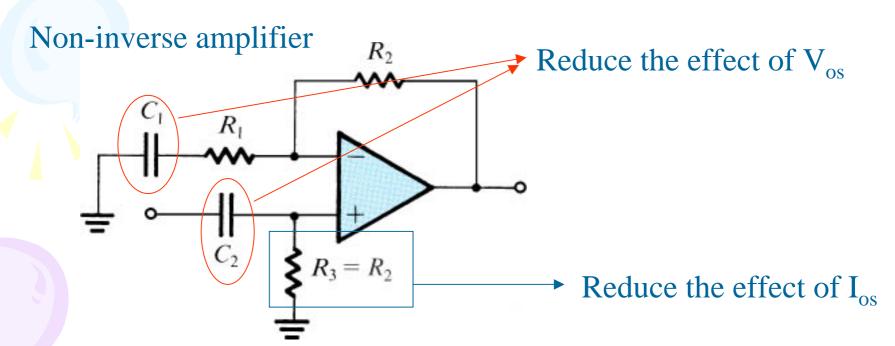
$$if \qquad I_{B1} = I_{B2} = I_{B}$$

$$V_{o} = I_{B}[R_{2} - R_{3}(1 + \frac{R_{2}}{R_{1}})]$$

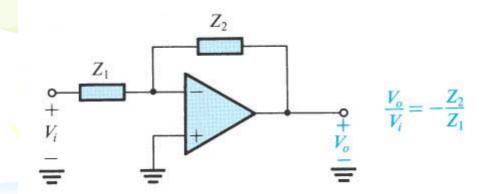
$$\stackrel{+}{=} choose \qquad R_{3} = \frac{R_{2}}{(1 + \frac{R_{2}}{R_{1}})} = R_{1} // R_{2}$$

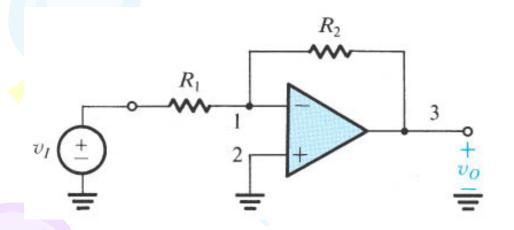
$$\Rightarrow V_{o} = 0$$





Integrator & Differentiator





$$\frac{0 - v_I}{Z_1} + \frac{0 - v_O}{Z_2} = 0$$

$$\Rightarrow G = \frac{v_O}{v_I} = -\frac{Z_2}{Z_1}$$

$$C_2$$
 R_2
 V_i
 V_o
 V_o

$$\frac{0 - v_i(t)}{R_1} + \frac{0 - v_o(t)}{R_2} + C_2 \frac{d[0 - v_o(t)]}{dt} = 0$$

$$\Rightarrow C_2 \frac{dv_o(t)}{dt} + \frac{v_o(t)}{R_2} = \frac{-v_i(t)}{R_1}$$

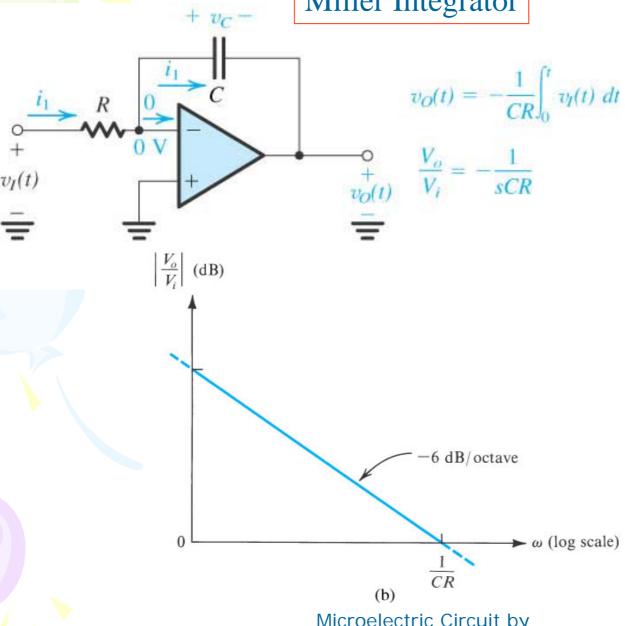
$$\Rightarrow sC_2v_0 + \frac{v_o}{R_2} = \frac{-v_i}{R_1}$$

$$\frac{v_O}{v_I} = -\frac{Z_2}{Z_1} = -\frac{1}{\frac{R_1}{R_2} + sC_2R_1}$$

$$\Rightarrow \frac{v_O}{v_I} = -\frac{\frac{R_2}{R_1}}{1 + sC_2R_2}$$

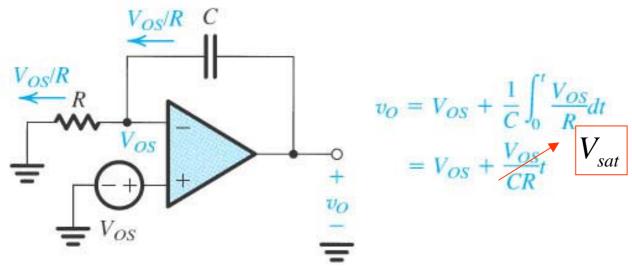
$$\omega_0 = \frac{1}{C_2 R_2}$$

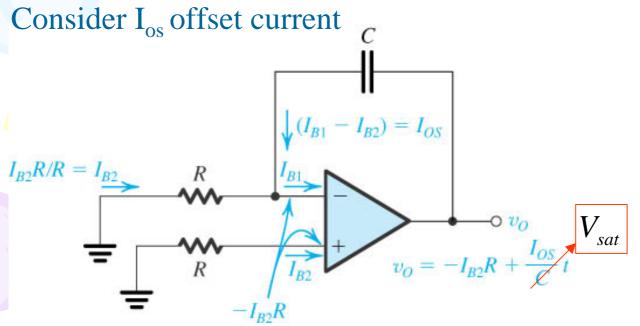
Miller Integrator



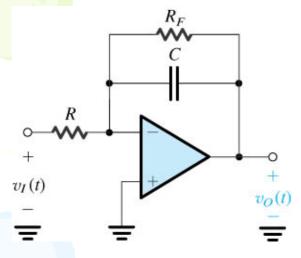
Microelectric Circuit by Meiling CHEN

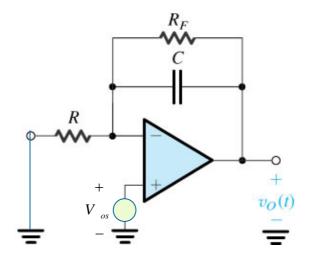
Consider V_{os} offset voltage





Solution:





$$\frac{V_{os}}{R} + \frac{V_{os} - v_o}{\frac{1}{sC}} + \frac{V_{os} - v_o}{R_f} = 0$$

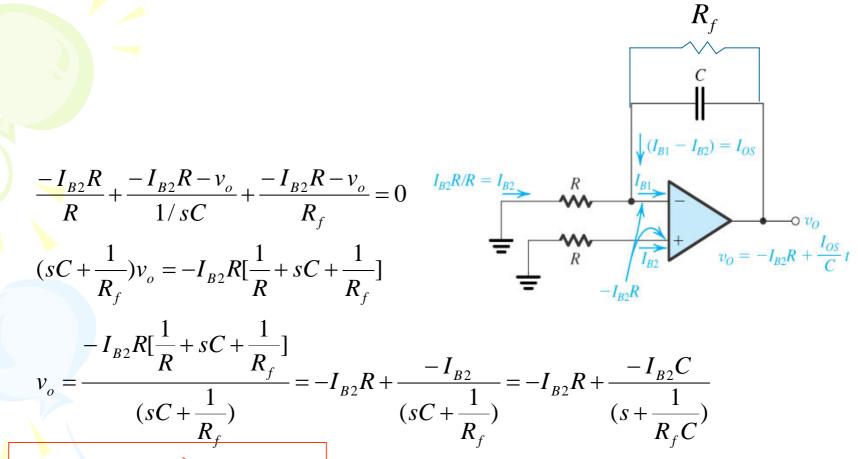
$$\frac{v_o}{V_{os}} = \frac{\frac{1}{R} + \frac{1}{R_f} + sC}{\frac{1}{R_c} + sC} = [1 + \frac{\frac{1}{R}}{\frac{1}{R_c} + sC}]$$

$$v_o = \left[1 + \frac{\frac{1}{R}}{\frac{1}{R_f} + sC}\right] V_{os} = V_{os} + \frac{\frac{1}{RC}}{\frac{1}{R_fC} + s} V_{os}$$

$$v_o(t) = V_{os} + \frac{V_{os}}{RC} e^{-\frac{1}{R_f C}t}$$

Microelectric Circuit by Meiling CHEN

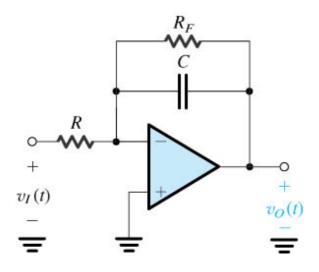
$$v_o(t) = V_{os} + \frac{V_{os}}{RC}t$$



$$v_o(t) = -I_{B2}R + (-I_{B2}C)e^{-\frac{1}{R_fC}t}$$

$$v_o = -I_{B2}R + \frac{I_{os}}{C}t$$

Example 2.7 sketch output response



$$R = 10k$$

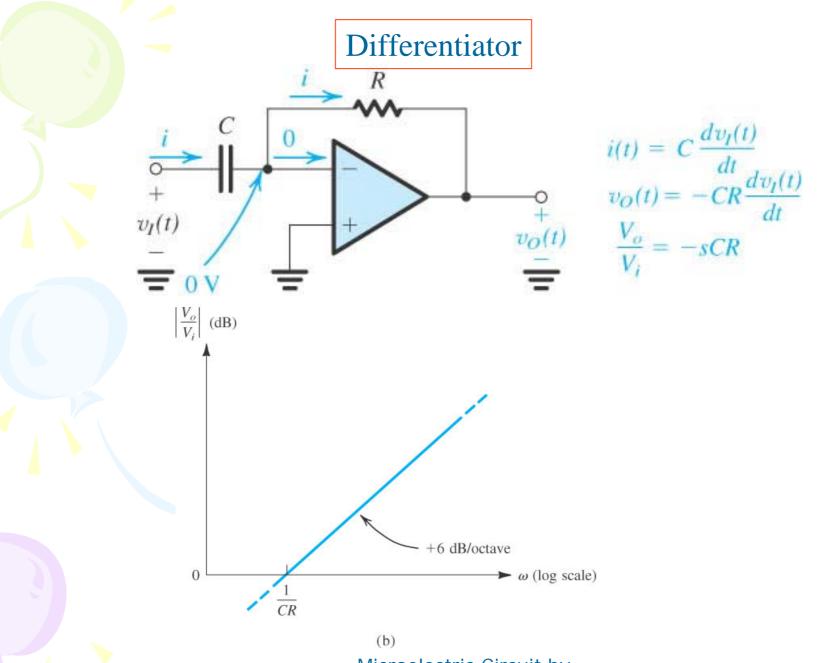
 $C = 10nF$
 $R_F = \infty$, $R_F = 1M$
 $V_{sat} = \pm 13V$

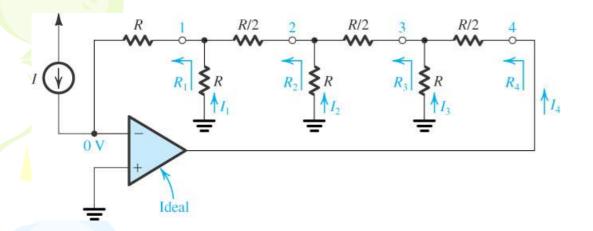
Case I: $R_F = \infty$

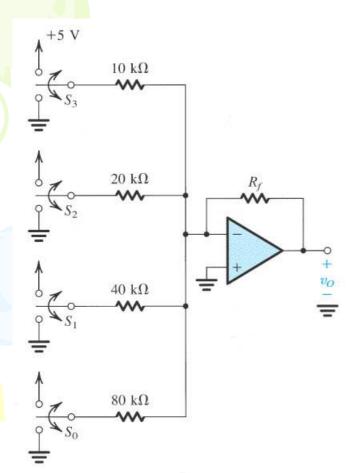
$$v_o(t) = -\frac{1}{RC} \int_0^t v_i(t) dt$$

Case II:
$$R_F = 1M$$

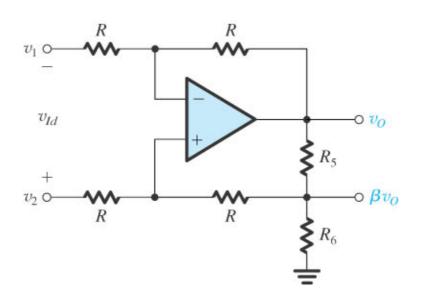
$$\frac{v_{O}}{v_{I}} = -\frac{\frac{R_{F}}{R_{1}}}{1 + sC_{2}R_{F}}$$

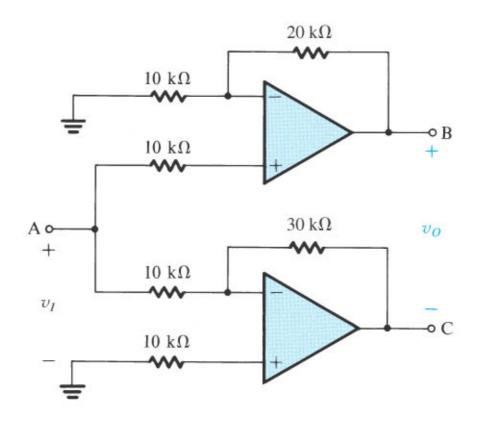


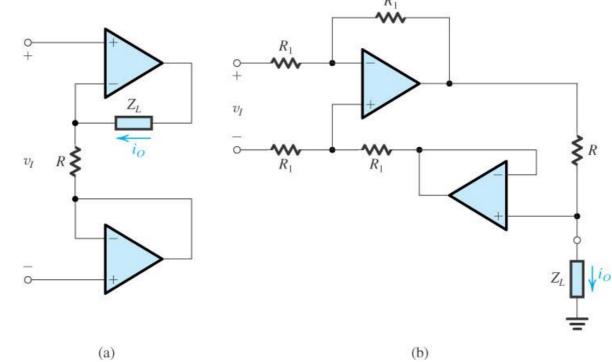


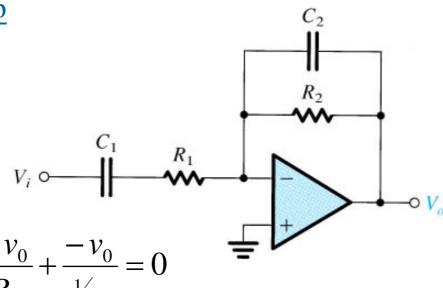


$$\begin{split} &\frac{V_{-} - S_{3}}{10k} + \frac{V_{-} - S_{2}}{20k} + \frac{V_{-} - S_{1}}{40k} + \frac{V_{-} - S_{0}}{80k} + \frac{V_{-} - V_{0}}{R_{f}} = 0 \\ &\frac{0 - 5S_{3}}{10k} + \frac{0 - 5S_{2}}{20k} + \frac{0 - 5S_{1}}{40k} + \frac{0 - 5S_{0}}{80k} + \frac{0 - V_{0}}{R_{f}} = 0 \\ &V_{0} = -5R_{f} \left[\frac{S_{3}}{10k} + \frac{S_{2}}{20k} + \frac{S_{1}}{40k} + \frac{S_{0}}{80k} \right] \\ &V_{0} = -\frac{5R_{f}}{80k} \left[8S_{3} + 4S_{2} + 2S_{1} + 1S_{0} \right] \\ &= -\frac{R_{f}}{16k} \left[8S_{3} + 4S_{2} + 2S_{1} + 1S_{0} \right] \end{split}$$









$$\frac{-v_i}{R_1 + \frac{1}{sC_1}} + \frac{-v_0}{R_2} + \frac{-v_0}{\frac{1}{sC_2}} = 0$$

$$\frac{-v_0}{R_2} + \frac{-v_0}{\frac{1}{sC_2}} = \frac{v_i}{R_1 + \frac{1}{sC_1}}$$

$$\frac{-R_2(R_2 + \frac{1}{sC_2})}{R_2} v_o = \frac{v_i}{R_1 + \frac{1}{sC_1}}$$

$$\frac{v_o}{v_i} = -\frac{R_2}{(R_1 + \frac{1}{sC_1})(R_2 + \frac{1}{sC_2})}$$

Non-ideal OP amplifiers

- 1. Type A: Finite open-loop gain (unknown)
- 2. Type B: Finite open-loop gain = K
- 3. Type C: $A_{vo} \neq \infty, R_o \neq 0, R_i \neq \infty$

- 1. Infinite input impedance
- 2. Zero output impedance
- 3. Zero common-mode gain
- 4. Infinite open-loop gain
- 5. Infinite bandwidth

Ideal OPA characters

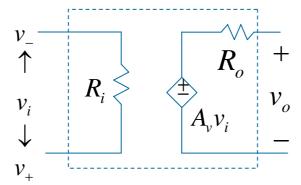
Type A: Finite open-loop gain (unknown)

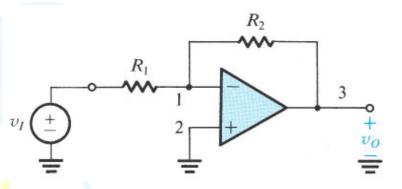
$$v_{-} \neq v_{+}$$

Type B: Finite open-loop gain =K

$$v_o = K(v_- - v_+)$$

$$A_{v} \neq \infty, R_{o} \neq 0, R_{i} \neq \infty$$





$$\frac{v_{-} - v_{I}}{R_{1}} + \frac{v_{-} - v_{+}}{R_{i}} + \frac{v_{-} - v_{o}}{R_{2}} = 0$$

$$\frac{v_o - v_-}{R_2} + \frac{v_o - A_v (v_- - v_+)}{R_o} = 0$$

