

The background features abstract, colorful swirls in shades of green, purple, and blue, interspersed with small yellow triangles. The text is centered in the upper half of the slide.

Lecture 08

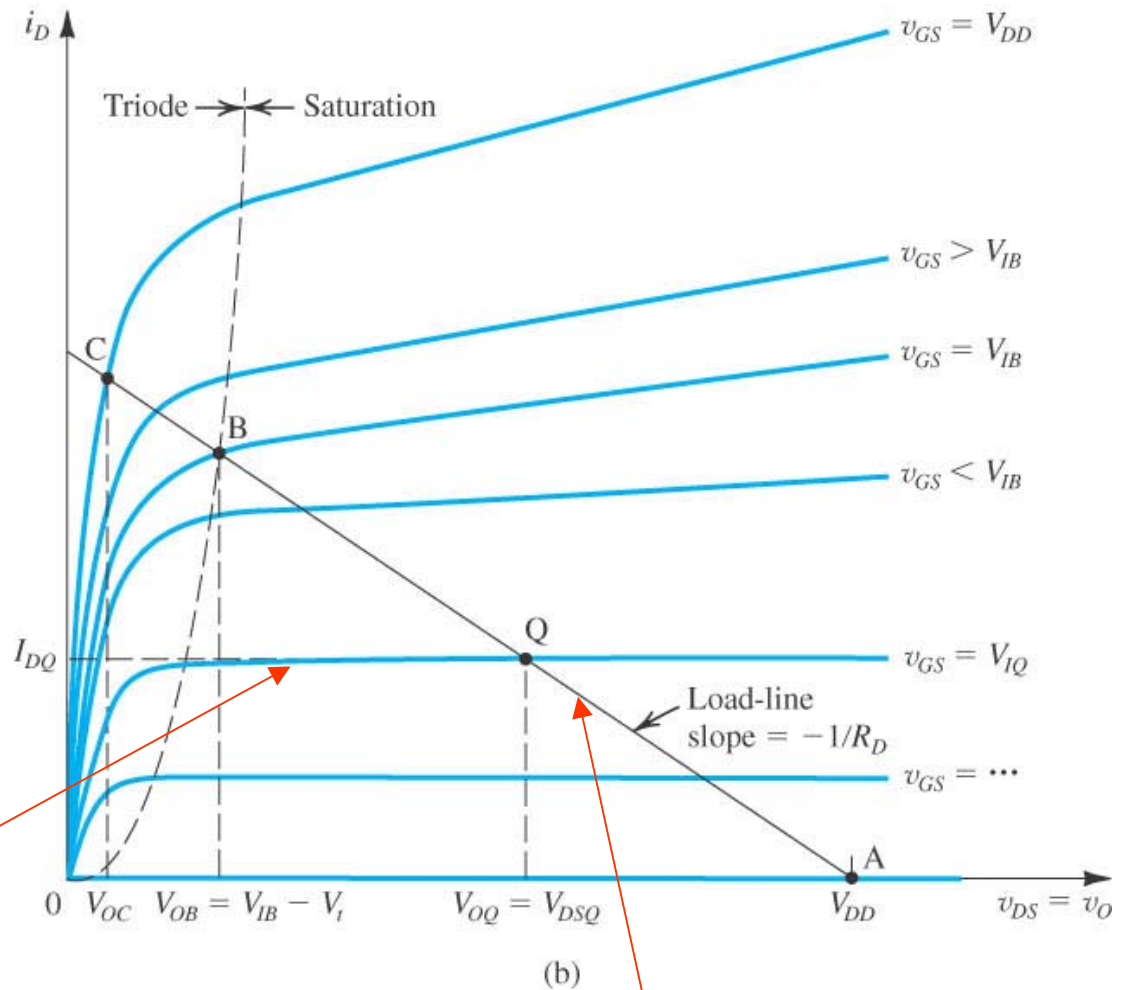
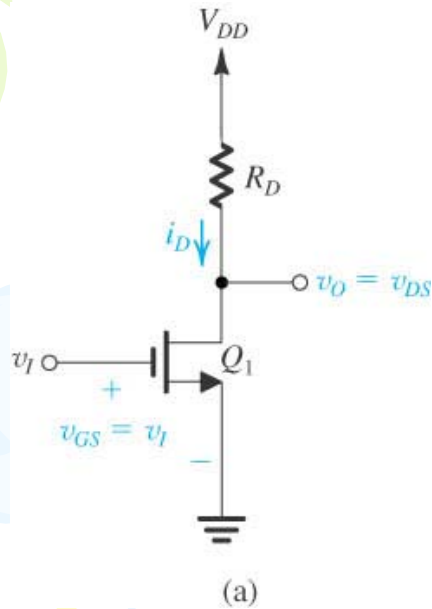
MOSFET's Circuit



topics

1. Large-signal operation
2. FET circuits at DC
3. FET biasing schemes

Large-signal operation

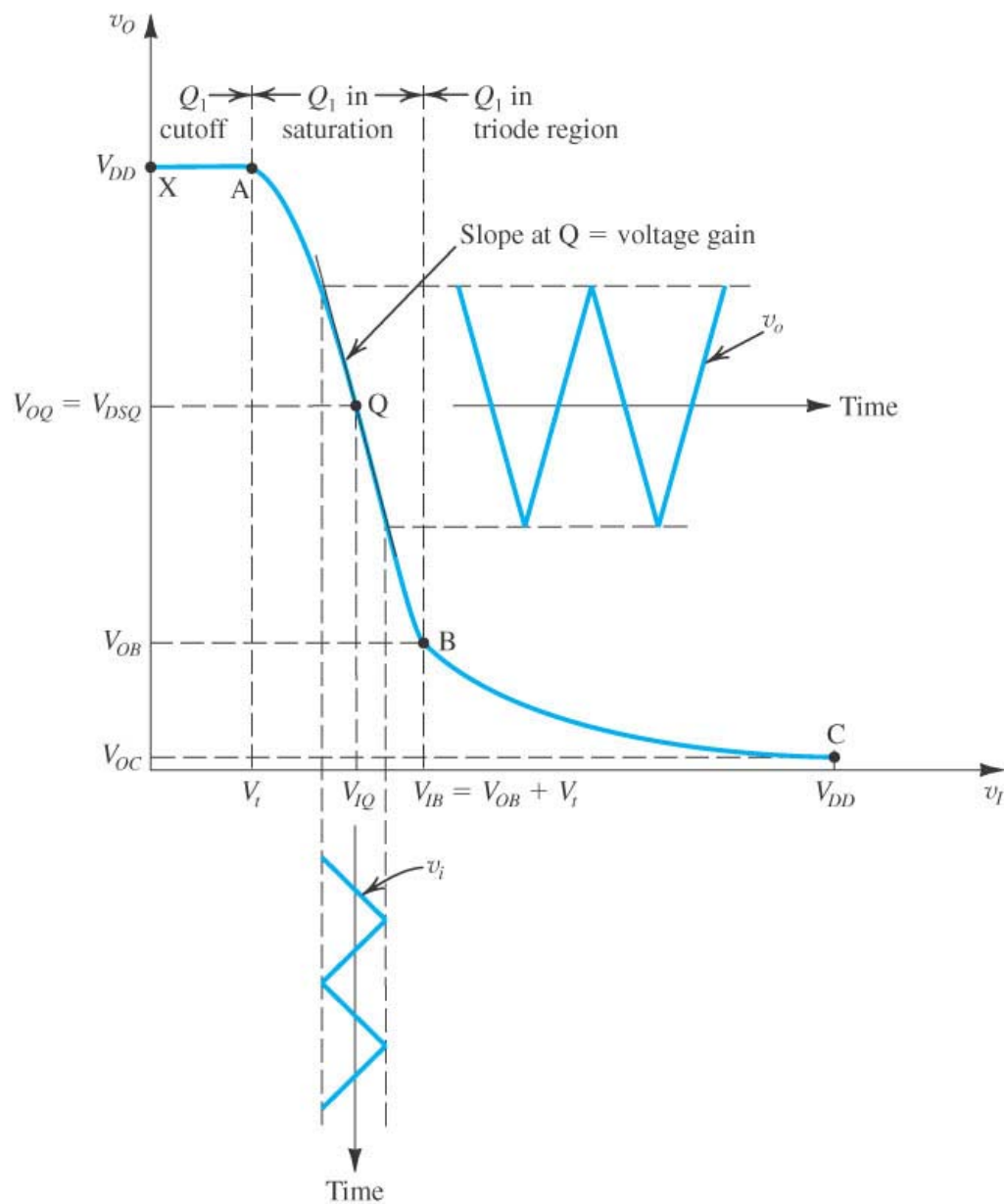


$$I_D = \frac{\mu C_o w}{2L} (v_{GS} - V_t)^2$$

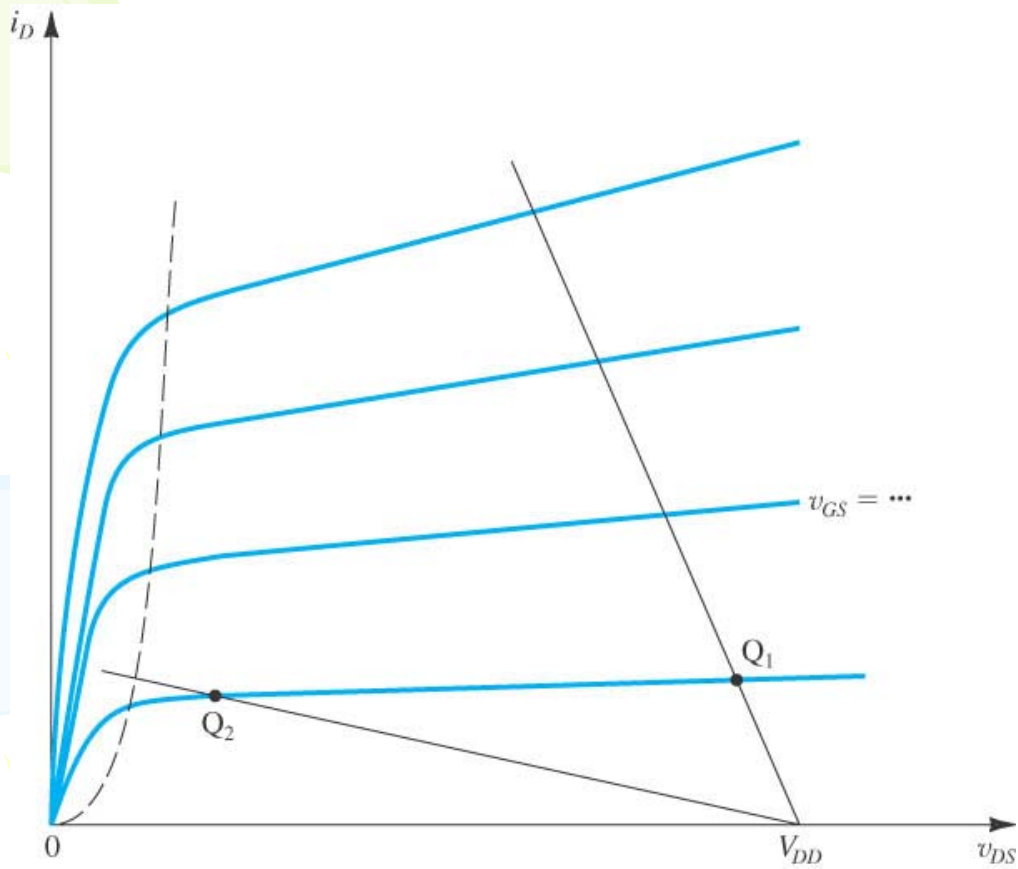
$$I_D = \frac{k'_n w}{2L} (v_{GS} - V_t)^2$$

Load line: $V_{DD} = i_D R_D + v_{DS}$

$$A_v \equiv \left. \frac{dv_o}{dv_I} \right|_{v_I = V_{IQ}}$$



(c)



Example 4.2

Given $I_D = 0.4\text{mA}$

$$V_D = 0.5\text{V}$$

$$V_t = 0.7\text{V}$$

$$\mu_n C_{ox} = 100\mu\text{A}/\text{V}^2$$

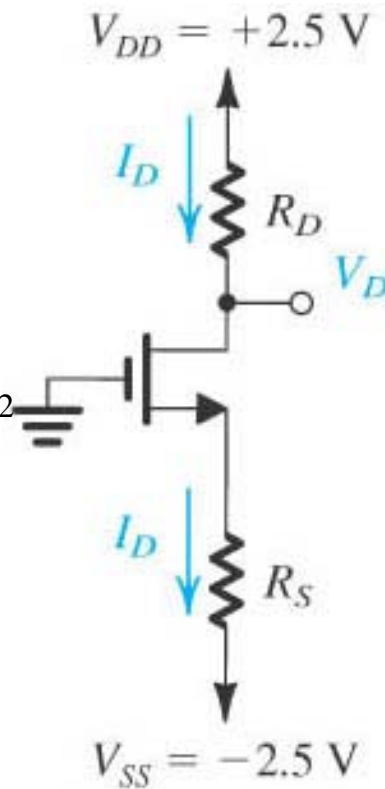
$$L = 1\mu\text{m}$$

$$w = 32\mu\text{m}$$

$$R_D = ?$$

$$R_S = ?$$

DC analysis



$$I_D = \frac{\mu C_o w}{2L} (v_{GS} - V_t)^2$$

$$\Rightarrow v_{GS} = 1.2\text{V}$$

$$V_S = I_D R_S + V_{SS}$$

$$\Rightarrow V_S = I_D R_S - 2.5\text{V}$$

$$R_S = \frac{-1.2 + 2.5}{0.4\text{mA}} = 3.25\text{k}\Omega$$

$$V_{DD} = I_D R_D + V_D$$

$$\Rightarrow 2.5\text{V} = I_D R_D + V_D$$

$$R_D = \frac{2.5 - 0.5}{0.4\text{mA}} = 5\text{k}\Omega$$

Example 4.3

Given

$$I_D = 80 \mu\text{A}$$

$$V_t = 0.6\text{V}$$

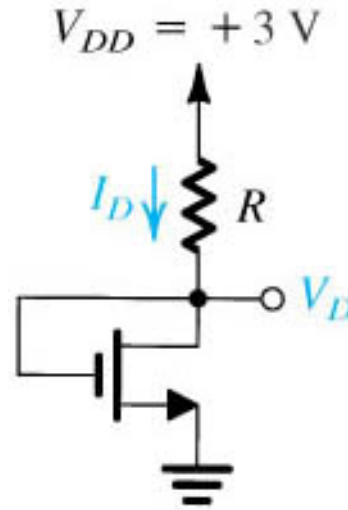
$$\mu_n C_{ox} = 200 \mu\text{A}/\text{V}^2$$

$$L = 0.8 \mu\text{m}$$

$$w = 4 \mu\text{m}$$

$$V_D = ?$$

$$R = ?$$



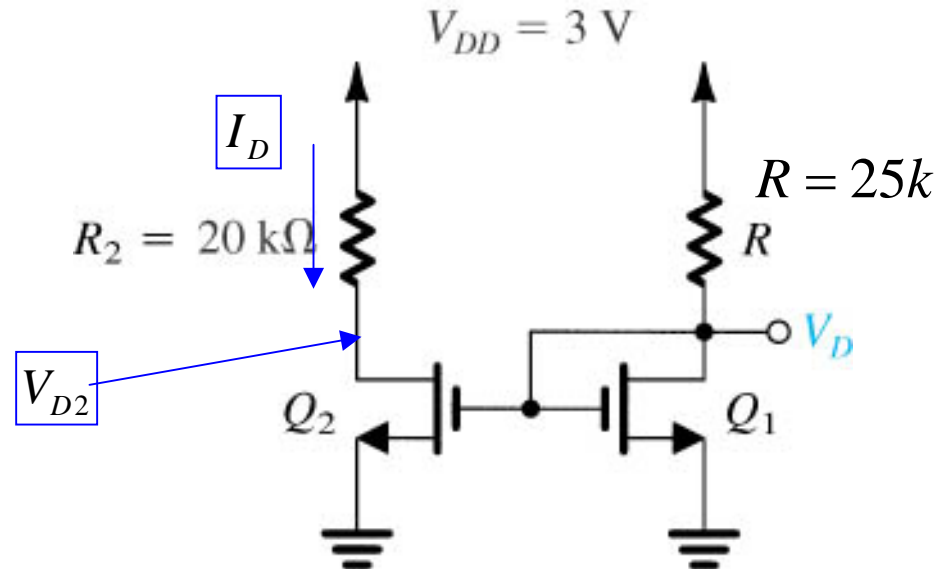
$$I_D = \frac{\mu C_o w}{2L} (V_D - V_t)^2$$

$$\Rightarrow V_D = 1\text{V}$$

$$V_{DD} = 3\text{V} = I_D R + V_D$$

$$\Rightarrow R = \frac{3-1}{0.08\text{mA}} = 25\text{k}\Omega$$

Exercise 4.12



$$I_D = \frac{\mu C_o w}{2L} (V_{GS2} - V_t)^2 = \frac{\mu C_o w}{2L} (V_D - V_t)^2$$

$$\because V_D = 1\text{ V} \therefore I_D = 80\mu\text{A}$$

$$V_{DD} = 3\text{ V} = I_D R_2 + V_{D2}$$

$$\Rightarrow V_{D2} = 3 - 80\mu \times 20\text{ k} = 1.4\text{ V}$$

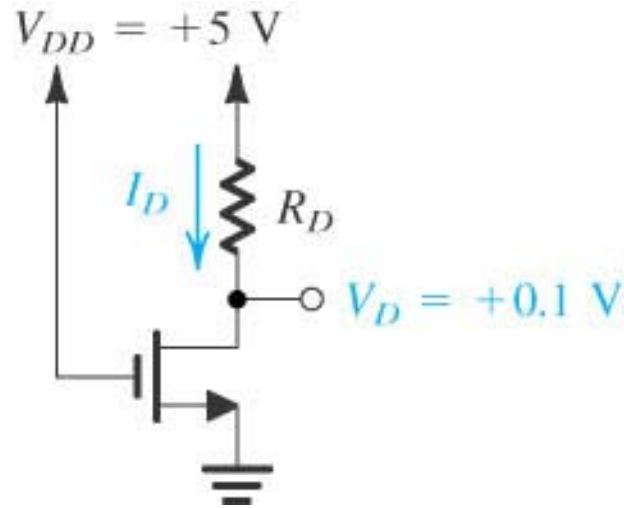
Example 4.4

let

$$V_t = 1V$$

$$K'_n(W/L) = 1mA/V^2$$

find $R_D = ?$



$$v_{DS} = V_D = 0.1V \leq V_{DD} - V_t = 5 - 1 = 4$$

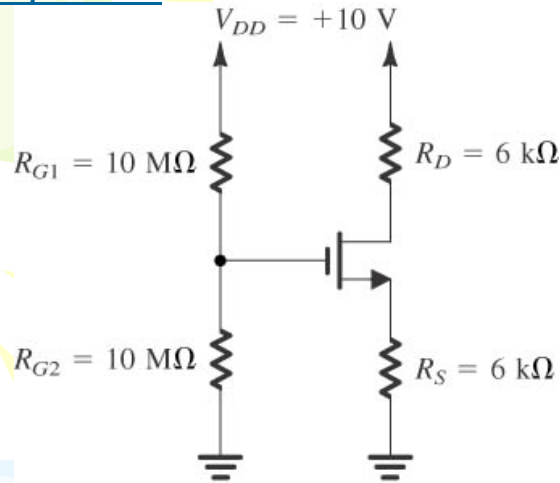
$$v_{DS} \leq v_{GS} - V_t \rightarrow \text{ohmic region}$$

$$I_D = \frac{\mu_n C_o w}{2L} [2(v_{GS} - V_t)v_{DS} - v_{DS}^2]$$

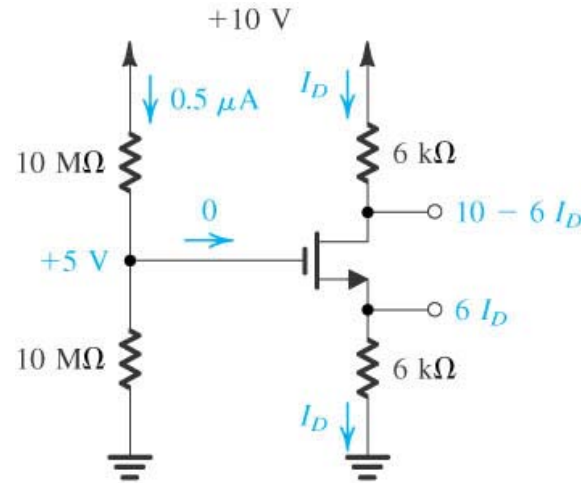
$$\rightarrow I_D = 0.395mA$$

$$R_D = \frac{5 - 0.1}{0.395m} = 12.4k$$

Example 4.5



(a)



(b)

let

$$V_t = 1V$$

$$K'_n (W/L) = 1mA/V^2$$

Assume FET
in saturation
mode since
 $V_G > 0$

$$V_G = \frac{10M}{10M + 10M} 10V = 5V$$

$$I_D = \frac{1}{2} k'_n \frac{W}{L} (V_{GS} - V_t)^2$$

$$\Rightarrow I_D = \frac{1}{2} \times 1 \times [(5 - I_D \times 6k) - 1]^2$$

$$\Rightarrow 18I_D^2 - 25I_D + 8 = 0$$

$$I_D = 0.89mA, I_D = 0.5mA$$

$$\text{if } I_D = 0.89mA$$

$$V_S = 6k \times 0.89mA = 5.34V$$

$$\rightarrow V_{GS} < 0 \text{ contradiction}$$

$$\text{if } I_D = 0.5mA$$

$$V_S = 6k \times 0.5mA = 3V$$

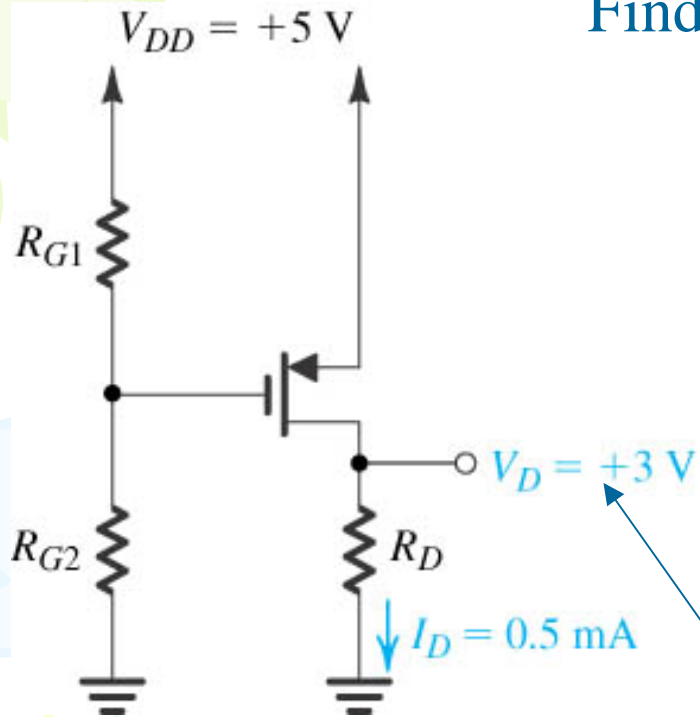
$$\rightarrow V_{GS} = 2V$$

$$\rightarrow V_D = 10 - 6k \times 0.5mA = 7V$$

$$\rightarrow V_D > V_G - V_t$$

Example 4.6

Design the circuit operate in saturation
Find the range of R_D



$$I_D = \frac{1}{2} k'_n \frac{W}{L} (V_{GS} - V_t)^2$$

$$\Rightarrow 0.5 = \frac{1}{2} \times 1 \times [V_{GS} - (-1)]^2$$

$$\Rightarrow V_{GS} = -2\text{ V}$$

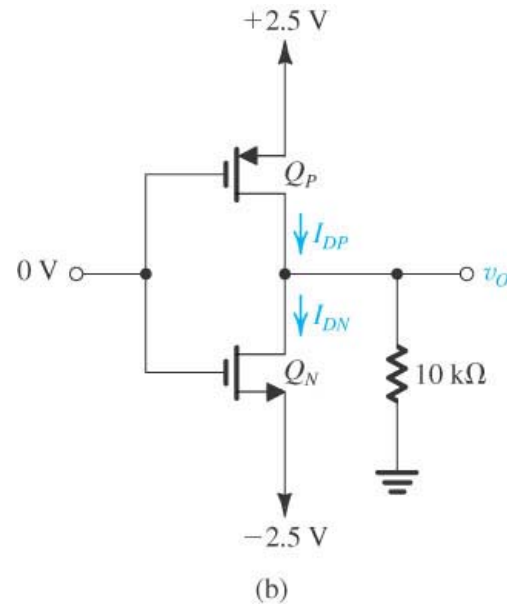
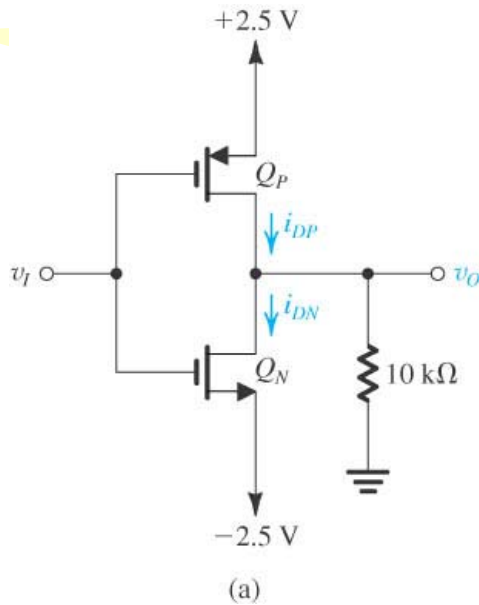
$$V_{DS} \geq V_{GS} - V_t = -2 + 1 = -1$$

$$\Rightarrow V_{D(\max)} = +4\text{ V}$$

$$R_{D(\max)} = \frac{4}{0.5\text{ m}} = 8\text{ k}$$

$$R_{D(\min)} = \frac{3}{0.5\text{ m}} = 6\text{ k}$$

Example 4.7



let

$$V_{nt} = -V_{pt} = 1V$$

$$K'_n (W / L)$$

$$= K'_p (W / L) = 1mA / V^2$$

First case : $v_I = 0V$

Q_p on, Q_n on

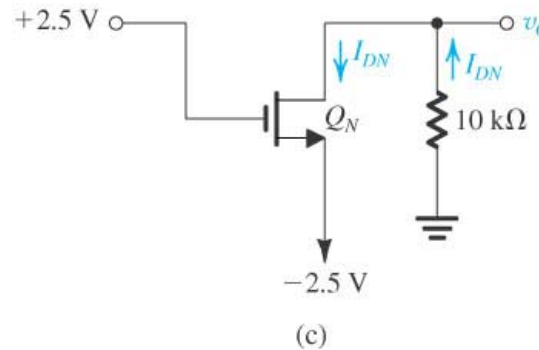
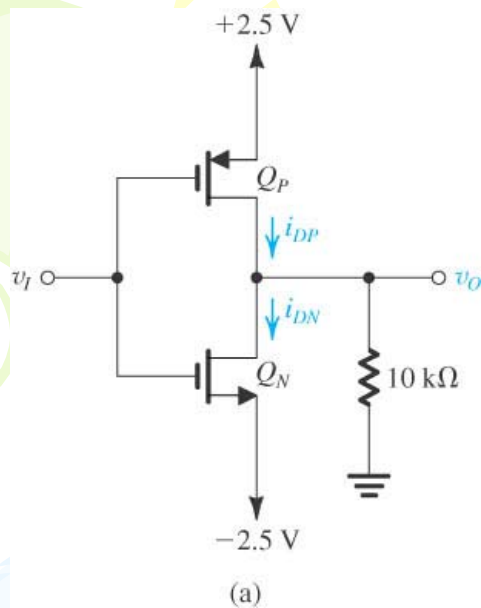
$$V_{GS(p)} = -2.5V$$

$$V_{GS(n)} = 2.5V$$

$$v_o = 0V$$

$$I_{DP} = \frac{1}{2} \times 1[-2.5 - (-1)]^2 = 1.125mA$$

$$I_{Dn} = \frac{1}{2} \times 1[2.5 - 1]^2 = 1.125mA$$



Second case : $v_I = +2.5V$
 Q_p off, Q_n ohmic region

$$I_D = \frac{\mu_n C_o w}{2L} [2(v_{GS} - V_t)v_{DS} - v_{DS}^2] \approx \frac{\mu_n C_o w}{L} (v_{GS} - V_t)v_{DS}$$

$$V_{GS} = 5V$$

$$I_{Dn} = 1 \times 1 [5 - 1] V_{DS}$$

$$= 1 \times 1 [5 - 1] (-I_{Dn} \times 10k)$$

$$\Rightarrow I_{Dn} = 0.244mA$$

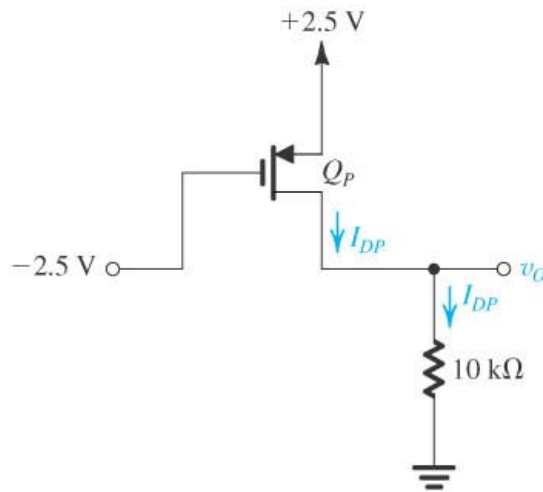
$$V_D = -2.44V = v_o$$

$$V_{DS} = -2.44 + 2.5 = 0.06$$

$$V_{GS} - V_t = 5 - 1 = 4$$

$$V_{DS} \leq V_{GS} - V_t \quad \text{Ohmic region}$$

NOT gate



(d)

Second case : $v_I = -2.5V$

Q_p ohmic region, Q_n off

NOT gate

$$V_{SG} = 5V$$

$$I_{Dp} = 1 \times 1 [5 - 1] V_{SD}$$

$$= 1 \times 1 [5 - 1] (2.5 - I_{Dp} \times 10k)$$

$$\Rightarrow I_{Dp} = 0.244mA$$

$$V_D = v_o = I_{Dp} \times 10k = 2.44V$$

$$V_{SD} = +2.5 - 2.44 = 0.06$$

$$V_{SG} - V_t = 5 - 1 = 4$$

$$V_{SD} \leq V_{SG} - |V_t| \quad \text{Ohmic region}$$

Exercise 4.16 Amplifier

Assume

$$k'_n(W_n/L_n) = k'_p(W_p/L_p) = 1(\text{mA}/\text{V}^2)$$

$$V_{tn} = -V_{tp} = 1\text{V}$$

$$\lambda = 0$$

find

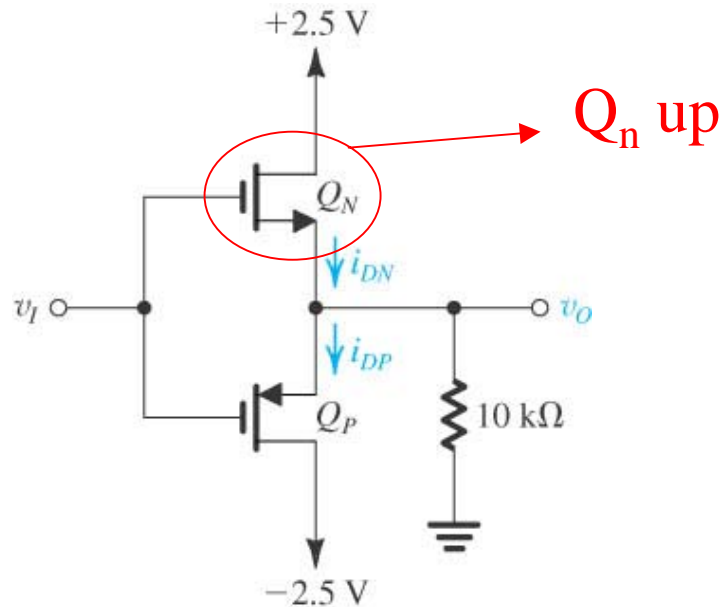
$$i_{DN}, i_{DP}, v_o$$

for

$$v_I = 0\text{V}, 2.5\text{V}, -2.5\text{V}$$

First case : $v_I = 0\text{V}$

Q_n off Q_p off

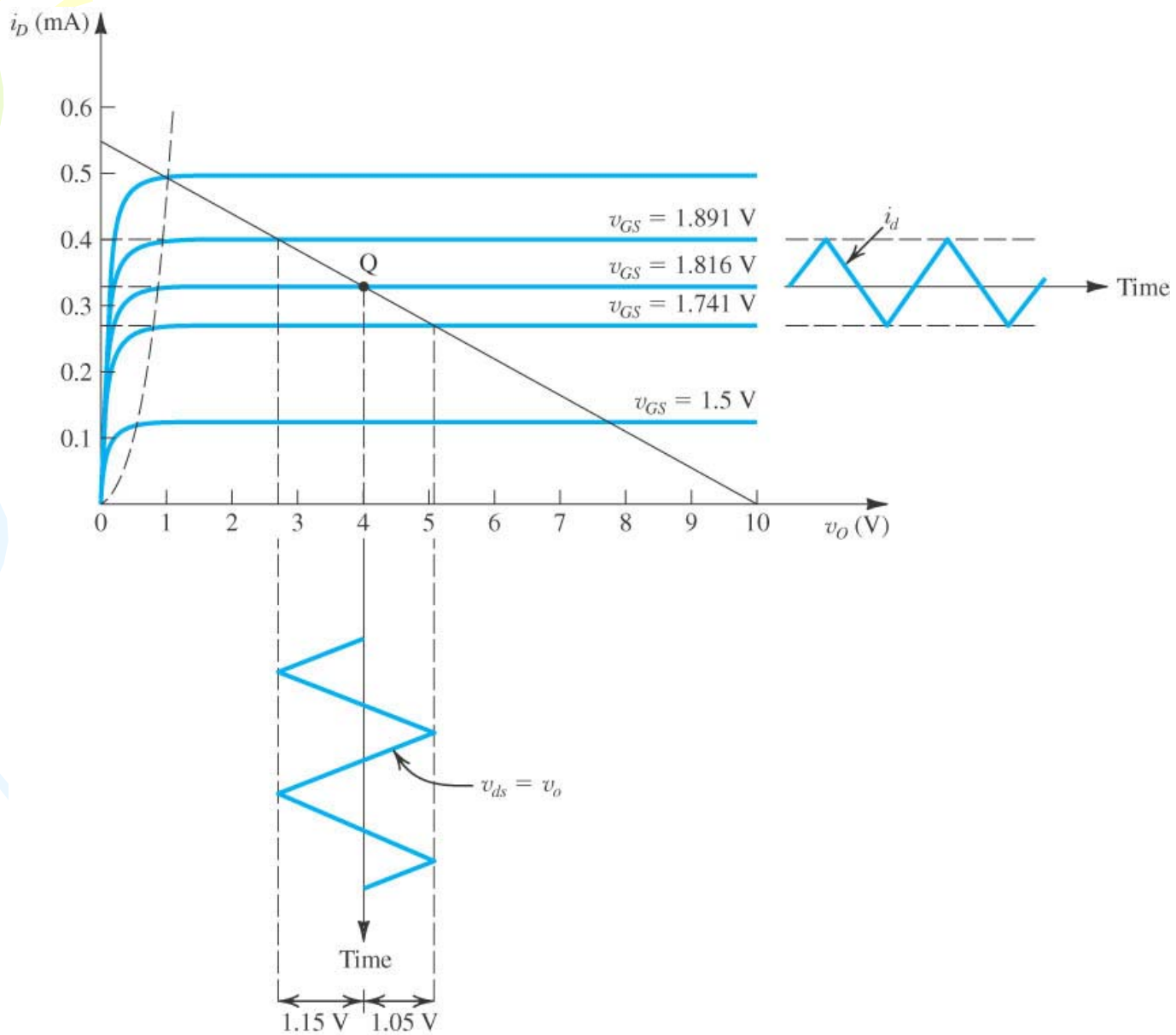


2nd case : $v_I = 2.5\text{V}$

Q_n on Q_p off

3rd case : $v_I = -2.5\text{V}$

Q_n off Q_p on



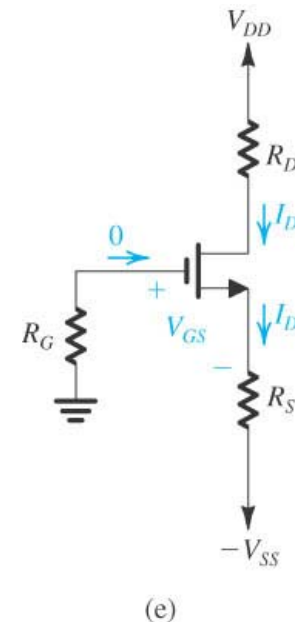
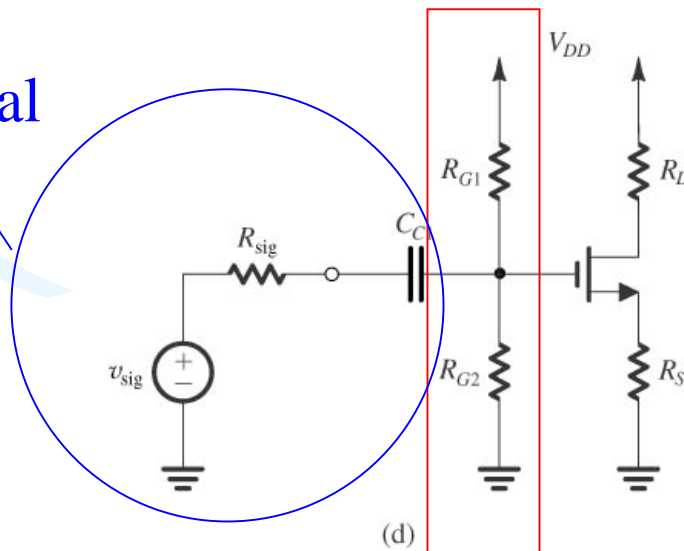
(b)

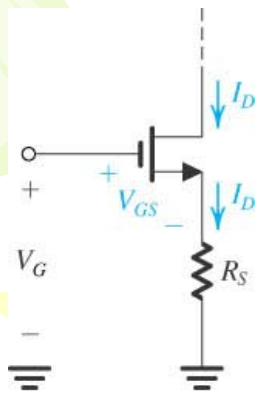
Biasing method

- Fixed Bias by V_{GS}
- Self Bias
- Biasing using Drain –to-Gate feedback resistor
- Biasing using constant current source

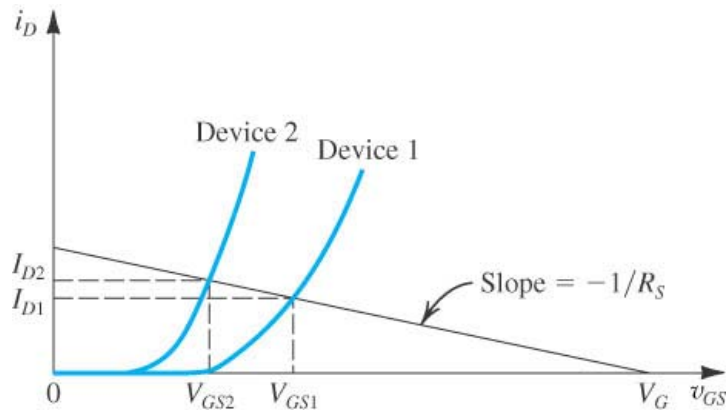
Biasing circuit

Small signal

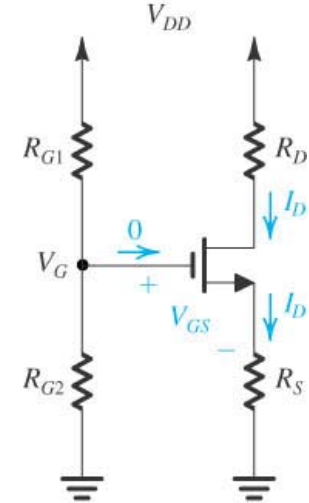




(a)



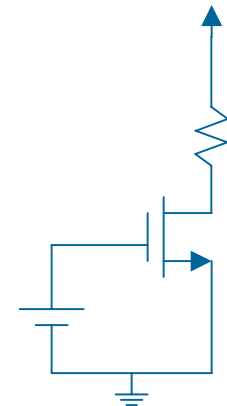
(b)



(c)

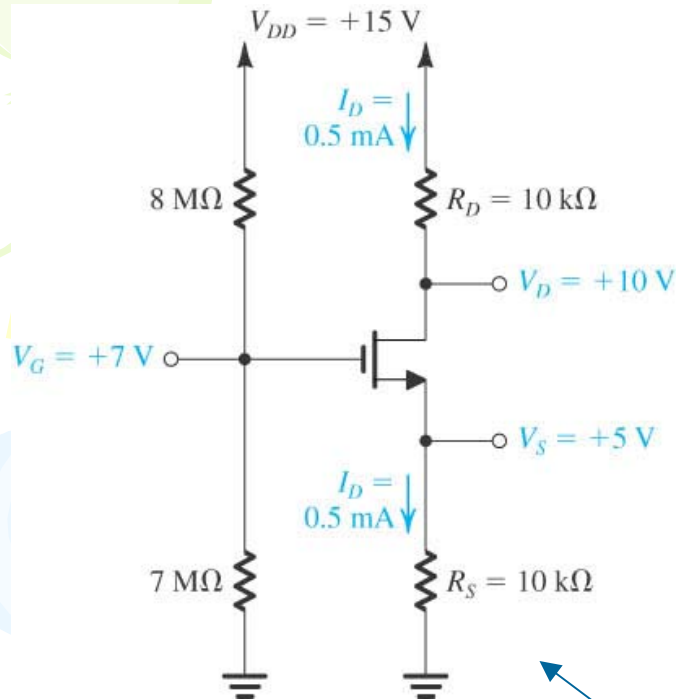
$$I_D = \frac{\mu_n C_o w}{2L} (v_{GS} - V_t)^2$$

1. Device change ($V_t, C_{ox}, W/L$)
 2. Temperature change (V_t, μ_n)
- } Change I_D



Example 4.9

Fixed Bias



when

$$V_t = 1V$$

$$K'_n(W/L) = 1mA/V^2$$

$$I_D = 0.5mA$$

find

$$V_t = 1.5V \quad \text{Change MOSFET}$$

$$K'_n(W/L) = 1mA/V^2$$

$$\Delta I_D = ?mA$$

$$I_D = \frac{k'_n W}{2L} (v_{GS} - V_t)^2 = \frac{1}{2} \times 1 \times (v_{GS} - 1.5)^2$$

$$v_{GS} = 7 - 10k \times I_D$$

$$\Rightarrow I_D = 0.455mA$$

Good biasing scheme

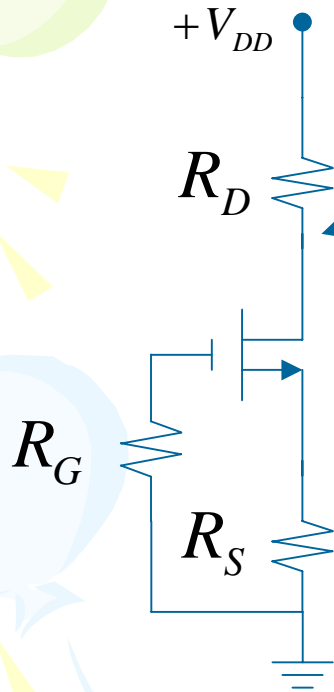
$$I_D \uparrow \Rightarrow I_D R_S \uparrow \Rightarrow \because V_G \text{ fixed} \Rightarrow V_{GS} \downarrow$$

$$\because I_D = \frac{k'_n W}{2L} (v_{GS} \downarrow - V_t)^2 \Rightarrow I_D \downarrow$$

Self Bias

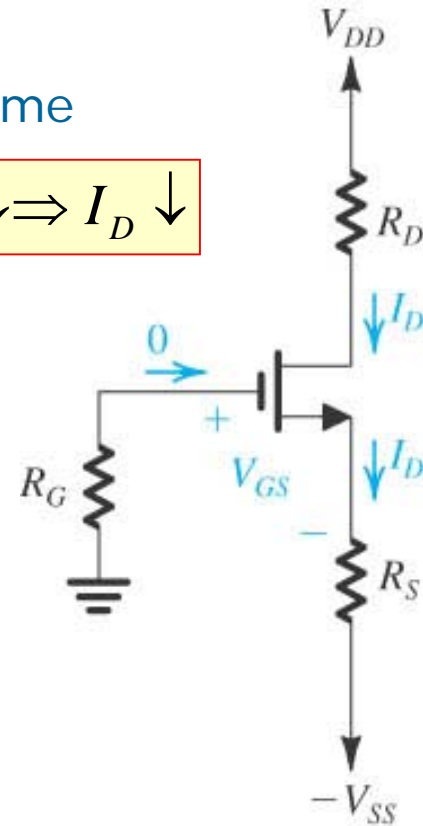
Good biasing scheme

$$I_D \uparrow (I_G = 0) \Rightarrow V_{GS} \downarrow \Rightarrow I_D \downarrow$$



$$I_D = \frac{k'_n w}{2L} (v_{GS} - V_t)^2$$

$$V_{GS} = -I_D R_S$$



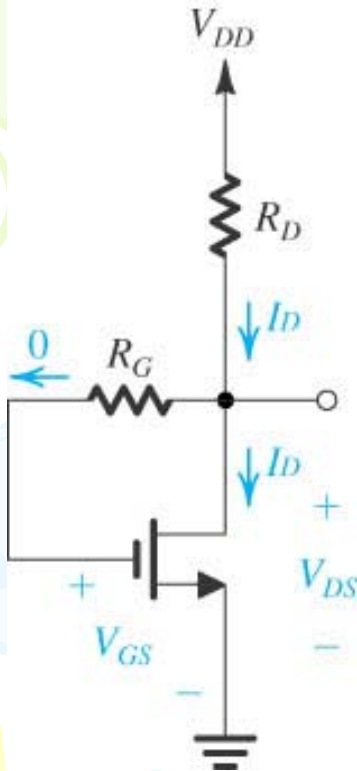
(e)

$$I_D \uparrow \Rightarrow V_S \uparrow \Rightarrow V_{GS} \downarrow \Rightarrow I_D \downarrow$$

Good biasing scheme

Exercise 4.21

Drain –to-Gate feedback bias



parameters

$$V_{DD} = 5V$$

$$V_t = 1V$$

$$K'_n(W/L) = 1mA/V^2$$

requirement

$$I_D = 0.5mA$$

find

$$R_D, V_D$$

$$V_{GS} = V_{DS} \Rightarrow \text{saturation}$$

$$\begin{aligned} I_D \uparrow &\Rightarrow V_D = V_{DD} - I_D R_D \Rightarrow V_D \downarrow \\ &\Rightarrow V_{GS} \Rightarrow I_D \downarrow \end{aligned}$$

Good biasing scheme

$$I_D = \frac{k'_n w}{2L} (V_{GS} - V_t)^2$$

$$0.5mA = \frac{1}{2} \times 1 \times (V_{GS} - 1)^2$$

$$\Rightarrow V_{GS} = 2V$$

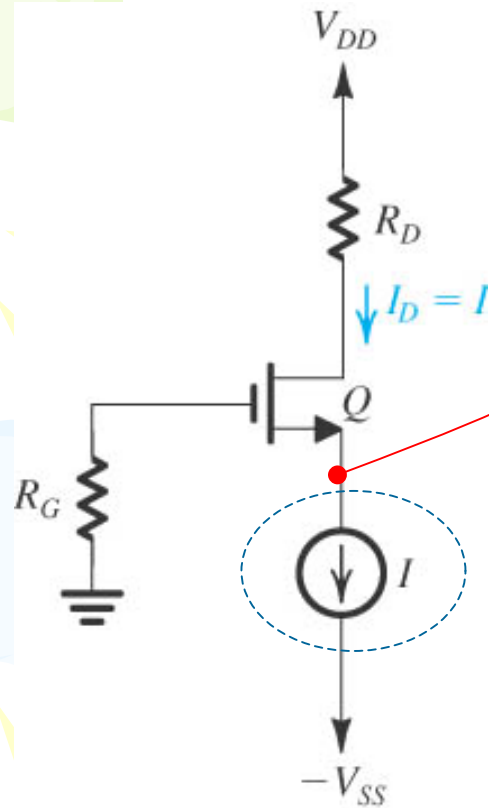
$$V_{GS} = V_{DS} = V_{DD} - I_D R_D$$

$$V_{GS} = V_{DS} = 5 - I_D \times R_D$$

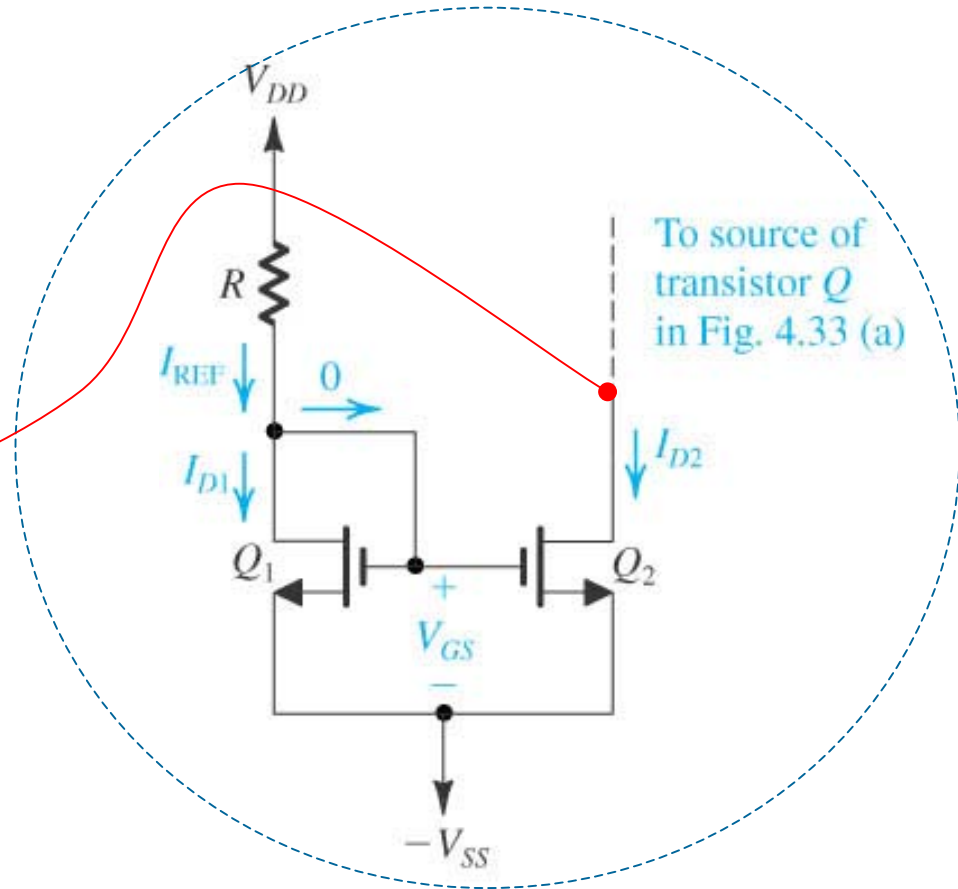
$$2V = 5 - 0.5 \times R_D$$

$$\Rightarrow R_D = 6k$$

Constant current bias



(a)



(b)

$$I_{REF} = I_{D1} = \frac{V_{DD} - V_{GS}}{R} = I_{D2}$$

