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You must also submit a document named `instruction_timeline.pdf` which gives an explanation of the behavior of your processor for the `perf-test-dep-ldst.asm` test. The Instruction Retired field would either be one of the instructions from the test program or a "NOP" if dependencies necessitate any stall cycles. The Reason column should give an explanation of why a stall was needed in that instance.

<i>Cycle</i>	<i>Instruction Retired</i>	<i>Reason</i>
1		
2		
3		
4	<code>lbi r0, 0</code>	
5	<code>lbi r5, 43</code>	
6	<code>lbi r6, 43</code>	
7	<code>lbi r7, 43</code>	
8	<code>ld r1, r0, 0</code>	
9	<code>nop</code>	We have a nop here because we need to wait for the load to finish.
10	<code>st r5, r1, 0</code>	
11	<code>ld r1, r0, 2</code>	
12	<code>nop</code>	We have a nop here because we need to wait for the load to finish.
13	<code>st r6, r1, 1</code>	
14	<code>ld r1, r0, 4</code>	
15	<code>nop</code>	We have a nop here because we need to wait for the load to finish.
16	<code>st r7, r1, 1</code>	
17	<code>halt</code>	