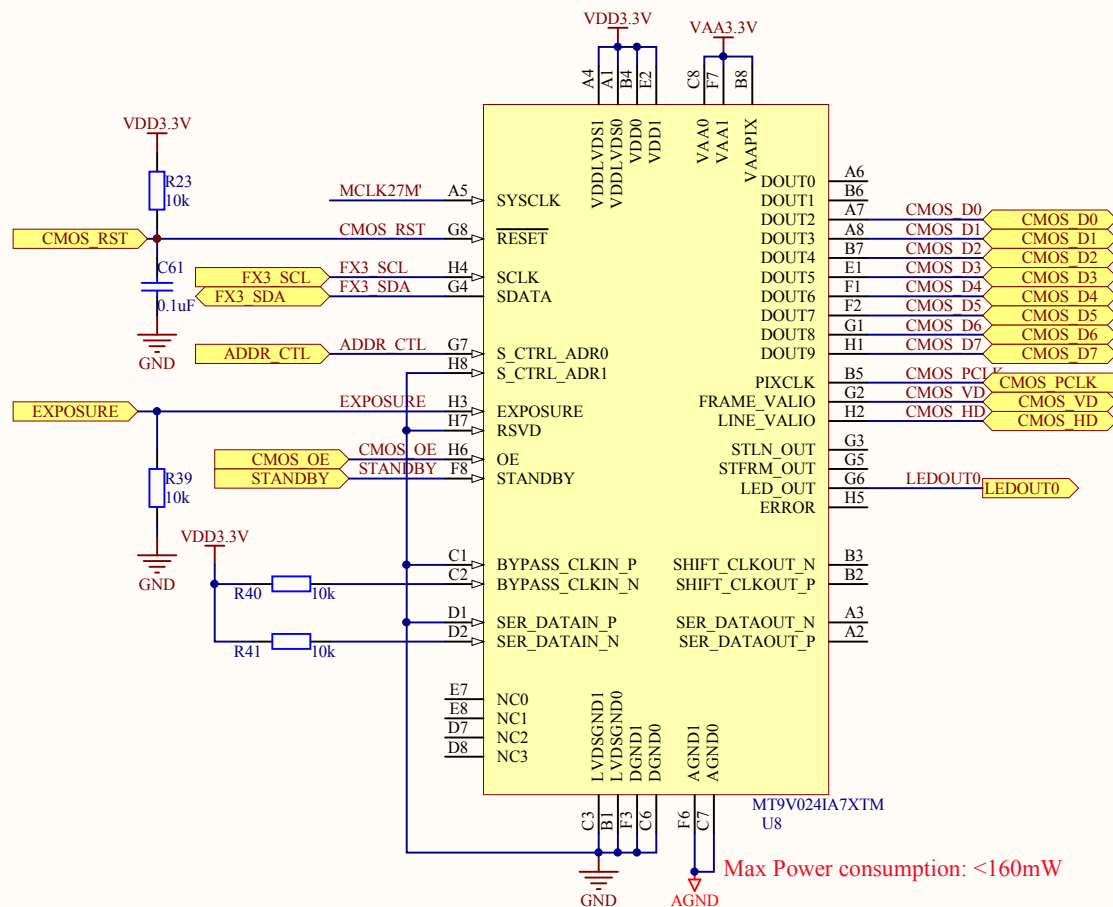
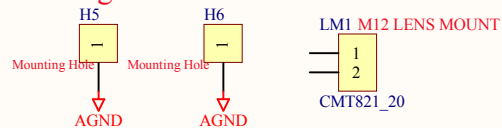


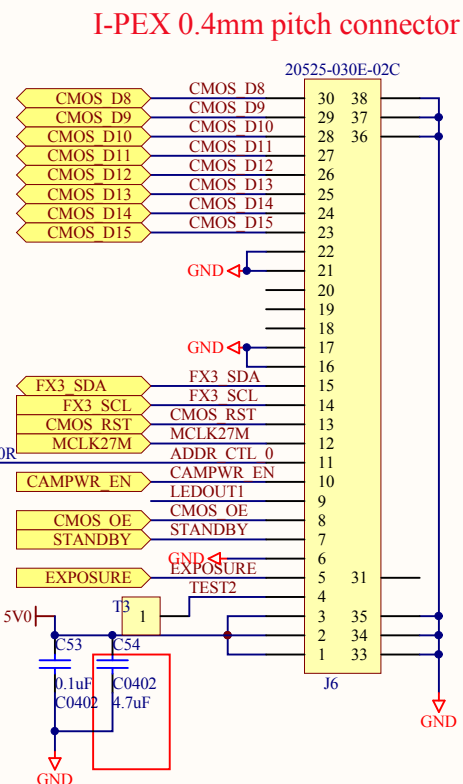
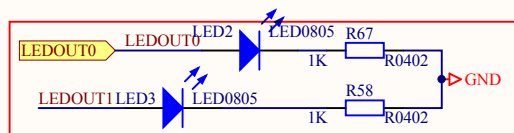
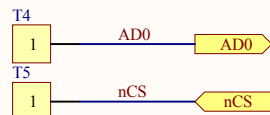
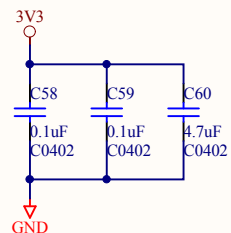
c44,c52,c63,c70 1uF-&gt;4.7uF 0402

2.2uF 0805--&gt;4.7uF 0402

## Mounting Holes for M2 Screws



Title		
Size	Number	Revision
A4		
Date:	2017/12/21	Sheet of
File:	F:\hardware\sch_camera.SchDoc	Drawn By:

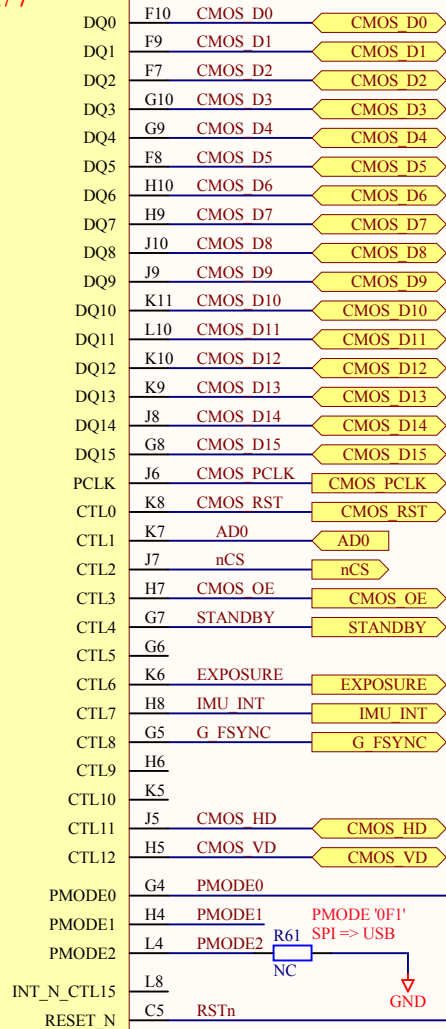


## Sensors Interface & IMU

Title		
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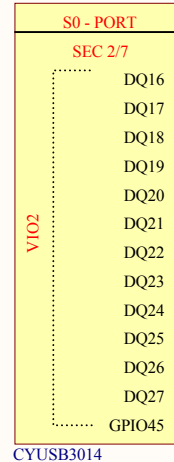
U1A

# P- PORT SEC 1/7



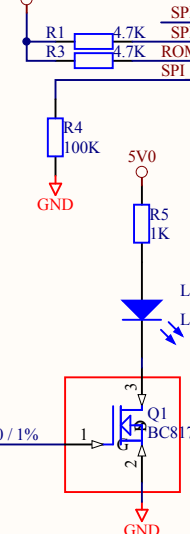
CYUSB3014

U1B

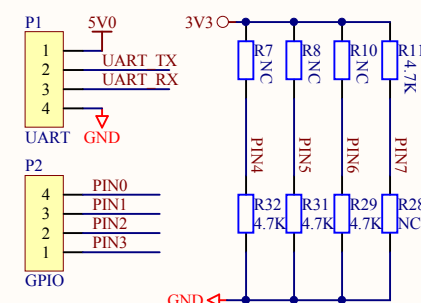


CYUSB3014

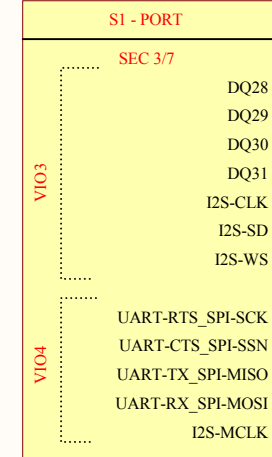
3V3



# UART&&GPIO

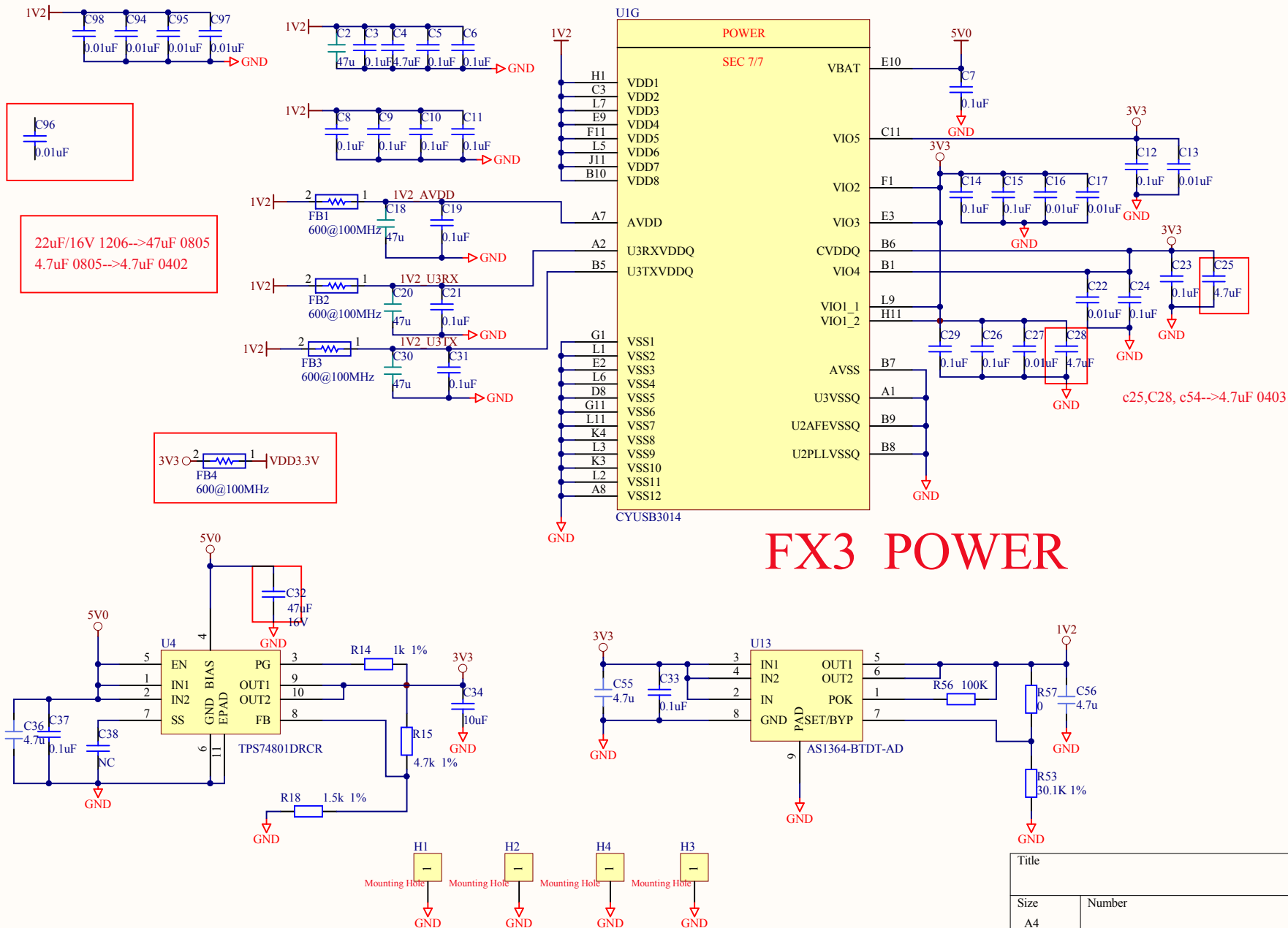


U1C



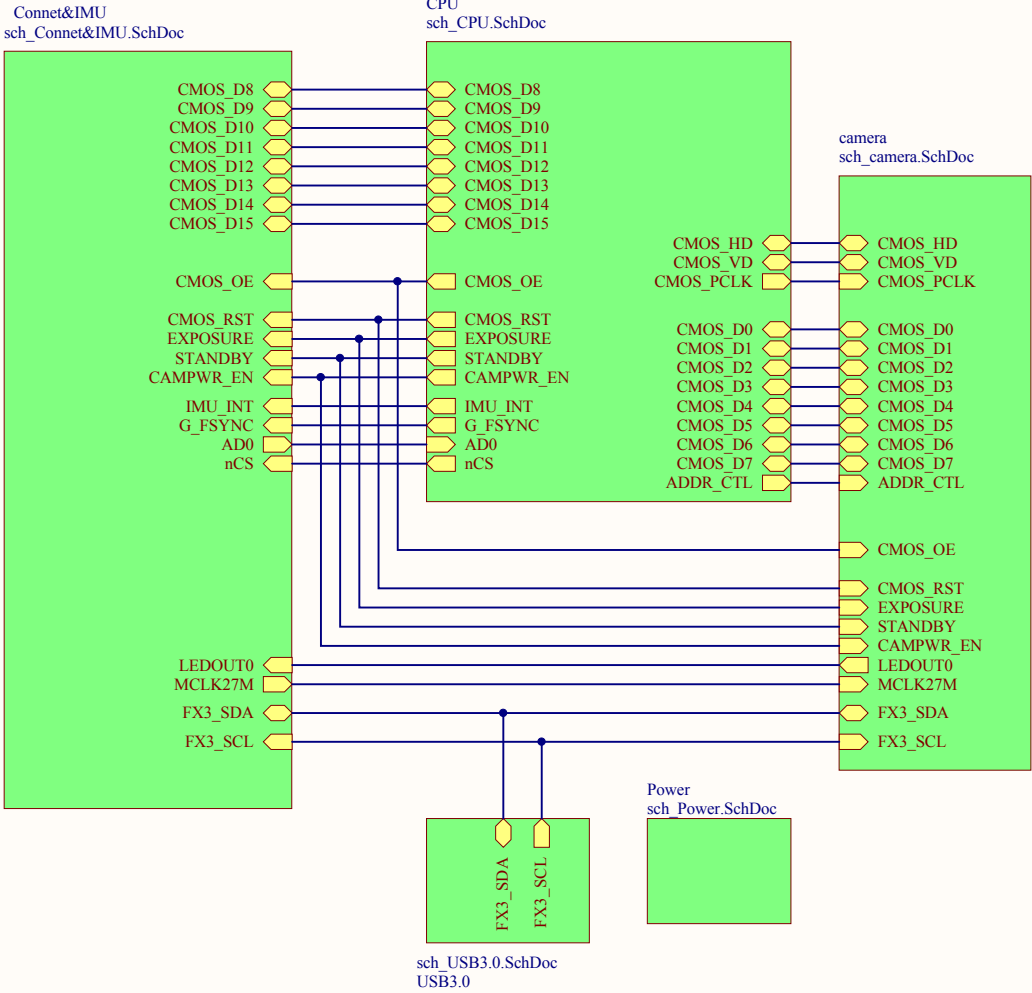
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Title		
Size	Number	Revision
A4		
Date:	2017/12/21	Sheet of
File:	F:\hardware\...\sch CPU.SchDoc	Drawn By:



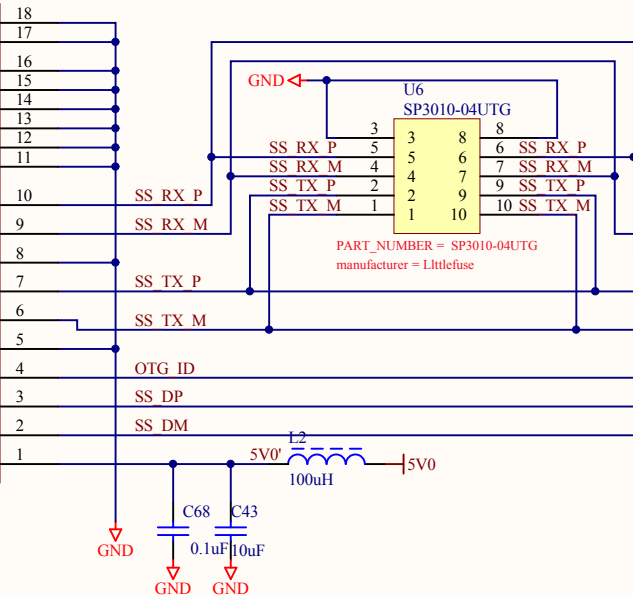
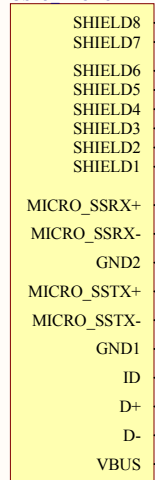
## FX3 POWER

Title		
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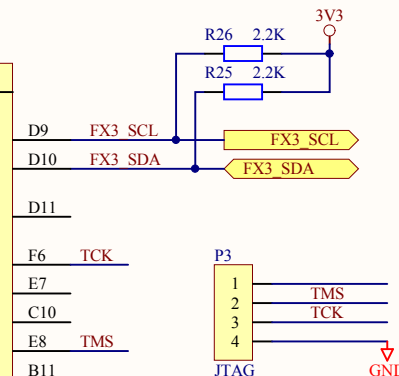
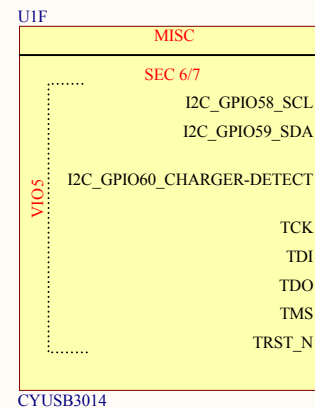
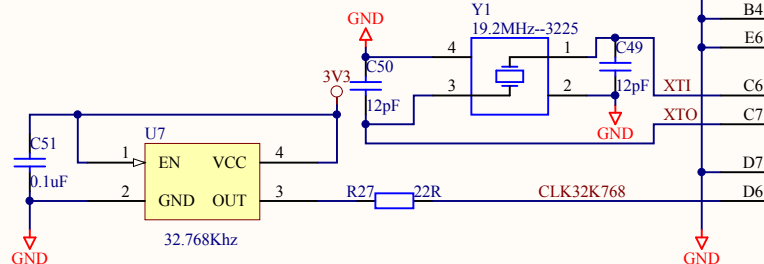
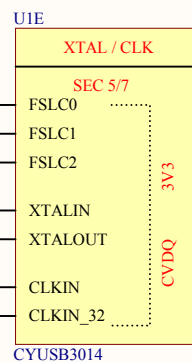
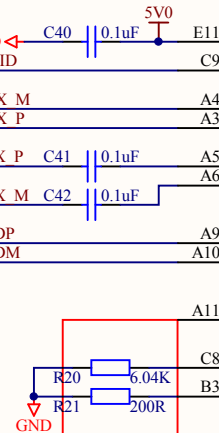
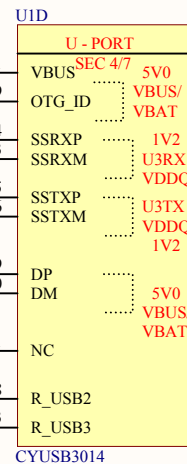


Title		
Size	Number	Revision
A4		
Date:	2017/12/21	Sheet of
File:	F:\hardware\.\sch_Top.SchDoc	Drawn By:

# J1 USB3 MICROB



The following signal pairs:  
SS\_DP : SS\_DM  
SS\_RX\_P : SS\_RX\_M  
SS\_TX\_P : SS\_TX\_M  
UTXP : UTXM  
are designated as differential pairs.  
All differential pairs are to have differential-mode characteristic impedance of 90 Ohm.  
To the fullest reasonable extent they should be length-matched, and layer changes should be minimized.



Title		
Size	Number	Revision
A4		
Date:	2017/12/21	Sheet of
File:	F:\hardware\sch USB3.0.SchDoc	Drawn By: