

# REVISION HISTORY

## 1. Revision 0.1(2003/11/28)

### - Preliminary version for SMDK2440 Ver 0.1

By Y. H. Lee

Item	Page No.	Remark
AC97	1-1	Following words are added - AC97 interface
	1-2	AC97 Block is added into "Figure 1-1. SMDK2440 Function Block Diagram".
	1-3	Following item is added into "FEATURES". - AC97 interface (sound CODEC audio input/output)
	1-5	AC97 Block is added into "Figure 1-3. Detailed SMDK2440 Board Diagram".
	1-19	New contents on IIS & AC97 Interface Configuration are added into SMDK 2440 App Notes.
	4-16 4-17	New contents and Figure 4-14 on AC97 Connection with S3C2440X are added into App Notes.
Camera	1-18	Camera connector pin assignment has been changed in the "CAMERA Interface Connector on SMDK2440".
Programming Intel Strata Flash with OPENICE32	3-51	Changed as follows: - Before J1: 2-3 (short) J2: 1-2 (short) J3: 1-2 (short) J4: 2-3 (short) - After J1-B: 2-3 (short) J2-B: 1-2 (short) J3-B: 1-2 (short) J4-B: 2-3 (short)

## 2. Revision 0.11(2004/01/16)

### - Preliminary version for SMDK2440 Ver 0.18

By Y. H. Lee

Item	Page No.	Remark
Overview	1-1	The CPU Board and Base Board revision number for <b>S3C2440A</b> is added.
		CPU Board Version      Rev 0.18
		Base Board Version      Rev 0.18
Microprocessor Name	1-2	The MCU name is changed from S3C2440X to S3C2440A.
	1-4	In the Figure 1-2, The MCU name is changed from S3C2440X to S3C2440A.
	1-5	In the Figure 1-3, The MCU name is changed from S3C2440X to S3C2440A.
	2-1	In the Figure 2-1, The MCU name is changed from S3C2440X to S3C2440A.
IIS & AC97 Interface	1-19	Changed as follows: register => resistor
SMDK Power Plane	1-4	New Step-Down Voltage Controller Block (U13-C) is added to SMDK Power Plane.
SMDK2440 System Configurations	1-6	Changed as follows: J35-C => J3-C

### 3. Revision 0.191(2004/06/30)

By S. J. Lee

Item	Page No.	Remark
DVS	Chapter 4	chapter 4 is added for DVS(Dynamic Voltage Scaling)