



The screenshot shows the BeagleBoard IDE's project browser. The current project is titled "Untitled\*". Under the "main" directory, there are two sub-folders: "Wiring" and "Gates". The "Gates" folder is currently selected and expanded, revealing its contents: NOT Gate, Buffer, AND Gate, OR Gate, NAND Gate, NOR Gate, XOR Gate, XNOR Gate, Odd Parity, Even Parity, Controlled Buffer, and Controlled Inverter.

	<b>Pin</b>
Facing	East
Output?	No
Data Bits	1
Three-state?	No
Pull Behavior	Unchanged
Label	
Label Location	West
Label Font	SansSerif Plain 12

