

# Software Engineering in Embedded Systems

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Universität Konstanz

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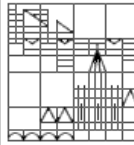
# Embedded Systems - What's that? - I

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## Definition

"An **embedded software system** is part of a hardware/software system that reacts to events in its environment. The software is 'embedded' in the hardware. Embedded systems are nominally real-time systems."

Software Engineering, p.561, Edited by Ian Sommerville, Ninth Edition



Embedded  
Systems Design

Architectural  
patterns

Timing analysis

Real-time  
operating systems

# Embedded Systems - What's that? - I

## Definition

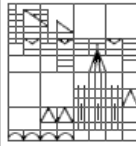
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## └ Embedded Systems - What's that? - II

Embedded Systems - What's that? - II

- Embedded Systems: ...
  - ... respond to physical world
  - ... respond in real time ("have a *deadline*")
  - ... often have little resources
  - ... run on special purpose hardware
  - ... run in real-time operating systems

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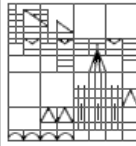
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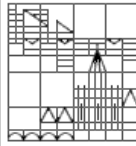
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# Embedded Systems - What's that? - II

resüpm om real time  
("have a deadline") → time in which the result is produced

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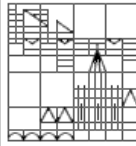
often have little resources  
i.e. not 'computers'

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run on special purpose hardware

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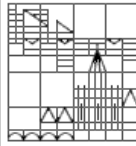
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run in real time operating systems

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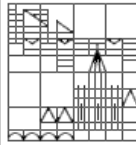


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- airbag
- cell phone / 'modern' phone
- burglar alarm
- (fully automatic) coffee machine
- danger detection
- ...

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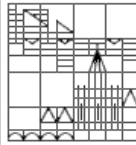
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**airbag:**  
strict deadline  
catastrophic result on failure

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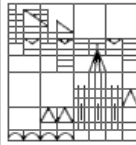
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## Embedded Systems - What's that? - III

**cell phone:**

phone must be answered before call quit vom other side

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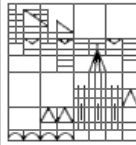
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**burglar alarm:**

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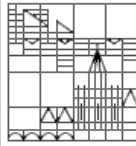
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## Embedded Systems - What's that? - III

**coffee machine:**

dont want to have coffee, when its cold...

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## Embedded Systems - What's that? - III

**danger detection:**

earthquake, toxins, ...

depending on the kind of danger, absolutely no time to spare.

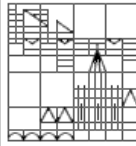
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One can produce more examples on a whim  
especially in cars

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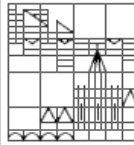
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## └ Motivation

## Motivation

- We see:
  - Embedded Systems are everywhere!
  - There are probably more Embedded Systems than computers out there!
- We realize:
  - Man, they must be important.
  - There sure is some money in this.



# Motivation

We see:

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Systems Design

Architectural  
patterns

Timing analysis

Real-time  
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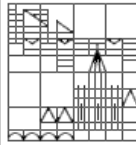


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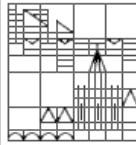
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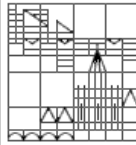
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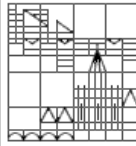
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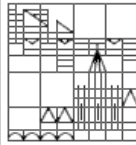
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# Motivation

there sure is some money in this

**some money:**

C-programing

special skillsand i did an internship producing an embedded system  
at PSI

monitoring device for the detector of an particle accelerator

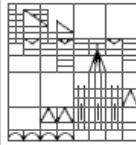
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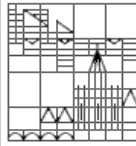
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## └ Outline

this is the structure of my talk:  
embedded systems DESIGN  
architectural patterns  
timing analysis  
real-time operating systems

## Outline

- 1 Embedded Systems Design
- 2 Architectural patterns
- 3 Timing analysis
- 4 Real-time operating systems



## Outline

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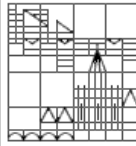
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# Outline

## Embedded Systems Design

Architectural patterns

Timing analysis

Real-time operating systems

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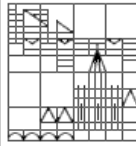
### 2 Architectural patterns

### 3 Timing analysis

### 4 Real-time operating systems



- deadlines
- environment
- continuity
- direct hardware interaction
- safety & reliability



# Problems

several problems in emb-systems that are not in “normal” systems

## Embedded Systems Design

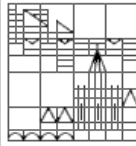
Architectural patterns

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Real-time operating systems

- Problems in embedded Systems:
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  - deadlines
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# Problems

**deadlines:** every process has deadline until result must exist  
hard systems: deadline not met, failure  
soft system: deadline not met, bad results

## Embedded Systems Design

Architectural patterns

Timing analysis

Real-time operating systems

- Problems in embedded Systems:
  - deadlines
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  - continuity
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  - safety & reliability

**environment:**

is unpredictable

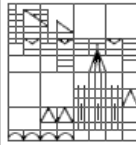
embedded Software  $\Rightarrow$  must be concurrent

## Problems

## Problems in embedded Systems:

- deadlines
- environment

└ direct hardware interaction  
└ safety & reliability



## Embedded Systems Design

Architectural patterns

Timing analysis

Real-time operating systems

# Problems

- Problems in embedded Systems:

- deadlines
- environment
- continuity
- direct hardware interaction
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**continuity:**

embedded Software  $\Rightarrow$  does not normally terminate

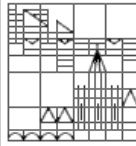
software has to be reliable

may need update while operating

## Problems

## Problems in embedded Systems:

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## Embedded Systems Design

Architectural patterns

Timing analysis

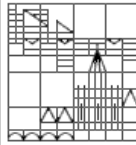
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# Problems

## direct hardware interaction:

uncommon hardware (i.e. detonator in airbag)

speed issues (hardware is faster than software)

### Embedded Systems Design

Architectural patterns

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Real-time operating systems

- Problems in embedded Systems:
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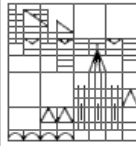
**safety & reliability:**

cost of failure high

either economical or in human life

## Problems

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## Embedded Systems Design

Architectural patterns

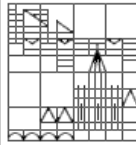
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# Problems

- Problems in embedded Systems:
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- design steps
  - platform selection
  - special purpose hardware
  - stimuli
    - periodic stimuli
    - aperiodic stimuli
  - Timing analysis
  - Process design
  - Algorithm design
  - Data design
  - Process scheduling



# Embedded Systems Design

## design steps

not all are necessary, but most will be.  
no definite order

### Embedded Systems Design

Architectural patterns

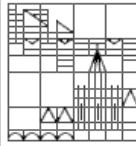
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# Embedded Systems Design

## Platform selection:

what hardware?

choice of Real-time operating system (later)

power consumption (mobile device, backup)

## Embedded Systems Design

Architectural patterns

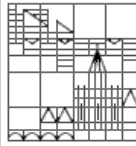
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# Embedded Systems Design

## special purpose hardware:

What is to be implemented in software, what in hardware

do we need uncommon hardware?

design special hardware?

replace software by hardware?

## Embedded Systems Design

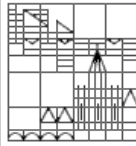
Architectural patterns

Timing analysis

Real-time operating systems

- design steps
  - platform selection
  - special purpose hardware
  - stimuli:
    - periodic stimuli
    - aperiodic stimuli
  - Timing analysis
  - Process design
  - Algorithm design
  - Data design
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# Embedded Systems Design

think about **stimuli**:

describe behavior of system by listing received stimuli and reactions

stimuli = signals

often: stimulus *Rightarrow* defined response

example AFTER THIS SLIDE

## Embedded Systems Design

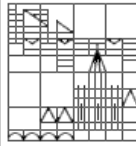
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# Embedded Systems Design

## periodic stimuli:

occur at predictable intervals

predefined reaction per stimulus

i.e. polling

## Embedded Systems Design

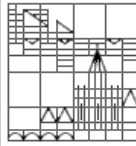
Architectural patterns

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    - 2 aperiodic stimuli
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# Embedded Systems Design

## aperiodic stimuli:

occur irregularly and unpredictably

often interrupts

i.e. alarms, failures, IO operation finished, etc

best practice: stimuli list with **all** stimuli.

example AFTER THIS SLIDE

## Embedded Systems Design

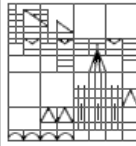
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- Architectural patterns
- Timing analysis
- Real-time operating systems



# Embedded Systems Design

## Timing analysis:

For each stimulus and response  $\Rightarrow$  find timing constraints  
timing constraints  $\Rightarrow$  deadlines

### Embedded Systems Design

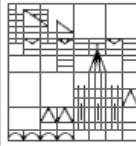
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# Embedded Systems Design

## Process design:

aggregate the stimuli & responses into concurrent processes

SEE Architectural patterns

## Embedded Systems Design

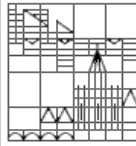
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# Embedded Systems Design

## Algorithm design:

For each stimulus & response  $\Rightarrow$  design algorithm  
especially important for computationally intensive tasks (signal processing)

Do we need to implement these in hardware? (i.e. control systems)

## Embedded Systems Design

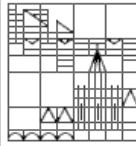
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# Embedded Systems Design

## Data design:

How to store data, that will be exchanged  
semaphore & critical regions & monitors & ...

**circular buffer:** producer & consumer may run at different speeds

## Embedded Systems Design

Architectural patterns

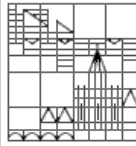
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# Embedded Systems Design

## Process scheduling:

ensure, that processes meet their deadline

## all shown:

not all need to be done, but most probably will  
which & order depends on what we design

## after this design steps:

make sure system can meet deadlines  
static analysis  
simulation

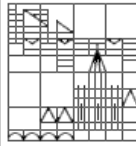
## Embedded Systems Design

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# Example: radiation warning system

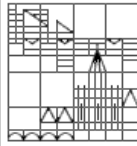
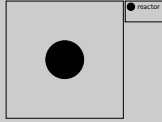
Now for the examples for stimuli:

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Systems Design

Architectural  
patterns

Timing analysis

Real-time  
operating systems



# Example: radiation warning system

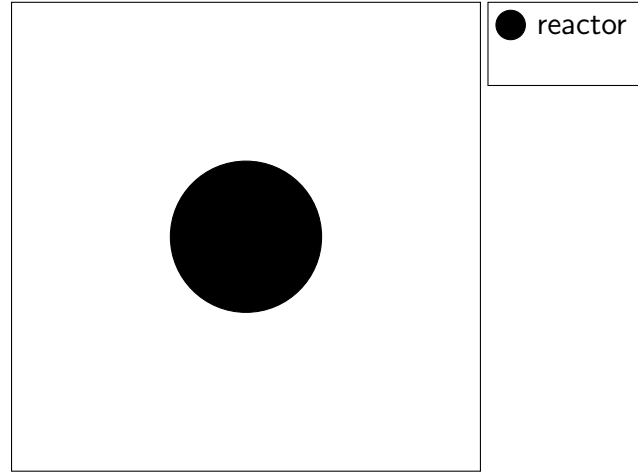
we have this room with an reactor inside

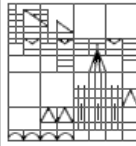
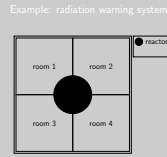
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Systems Design

Architectural  
patterns

Timing analysis

Real-time  
operating systems





## Example: radiation warning system

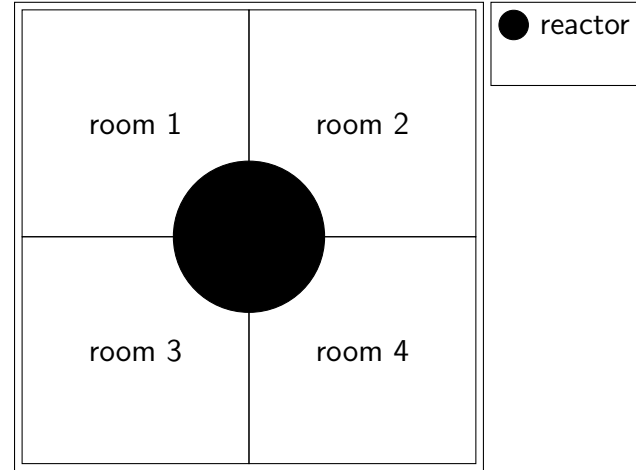
because several people work here, we put several rooms around the reactor  
walls are shielded

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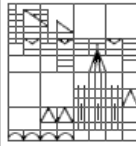
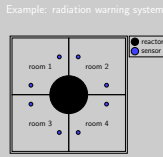
Architectural  
patterns

Timing analysis

Real-time  
operating systems



because people work here, we need some sensors to detect radiation leaks



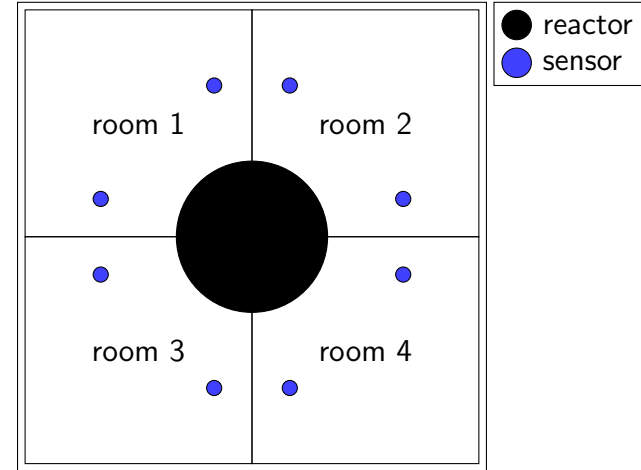
## Example: radiation warning system

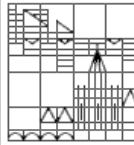
### Embedded Systems Design

Architectural patterns

Timing analysis

Real-time operating systems





# Example: Stimuli-List of a radiation warning system

Now we built a list of stimuli and responses

Stimulus

Response

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Systems Design

Architectural  
patterns

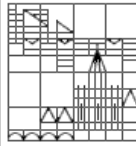
Timing analysis

Real-time  
operating systems

single sensor positive  
want to warn people, that there is something  
flash a yellow light around the sensor

Example: Stimuli-List of a radiation  
warning system

Stimulus	Response
single sensor positive	flash yellow light around sensor



## Example: Stimuli-List of a radiation warning system

Stimulus	Response
single sensor positive	flash yellow light around sensor

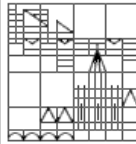
Embedded  
Systems Design

Architectural  
patterns

Timing analysis

Real-time  
operating systems

Stimulus	Response
single sensor positive	flash yellow light around sensor
both sensors in one area positive	flash red light in area, sound acoustic alarm in area



## Embedded Systems Design

Architectural patterns

Timing analysis

Real-time operating systems

## Example: Stimuli-List of a radiation warning system

two sensors in one are positive  
something is really wrong  
flash red light in area  
sound alarm

Stimulus	Response
single sensor positive	flash yellow light around sensor
both sensors in one area positive	flash red light in area, sound acoustic alarm in area

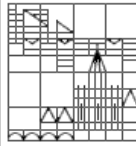


Example: Stimuli-List of a radiation warning system

small voltage drop  
probably nothing bad  
switch to backup power  
run power supply test

Example: Stimuli-List of a radiation warning system

Stimulus	Response
single sensor positive	flash yellow light around sensor
both sensors in one area positive	flash red light in area, sound acoustic alarm in area
Voltage drop of 10-20%	switch to backup power; run power supply test



Embedded Systems Design

Architectural patterns

Timing analysis

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## Example: Stimuli-List of a radiation warning system

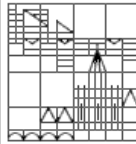
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Voltage drop of 10-20%	switch to backup power; run power supply test

Example: Stimuli-List of a radiation warning system

big voltage drop  
do the same as on small drop  
call technician  
**LAST CELL:**

Example: Stimuli-List of a radiation warning system

Stimulus	Response
single sensor positive	flash yellow light around sensor
both sensors in one area positive	flash red light in area, sound acoustic alarm in area
Voltage drop of 10-20%	switch to backup power; run power supply test
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Embedded Systems Design

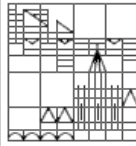
Architectural patterns

Timing analysis

Real-time operating systems

- Embedded Systems are often built as state machines.

⇒ UML state diagrams



# Embedded system modeling

Embedded Systems  $\Rightarrow$  often build as state machines

## Embedded Systems Design

Architectural patterns

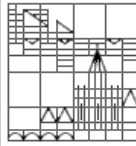
Timing analysis

Real-time operating systems

- Embedded Systems are often built as state machines.

$\Rightarrow$  UML state diagrams

- Embedded Systems are often built as state machines.  
⇒ UML state diagrams



# Embedded system modeling

of course  $\Rightarrow$  UML state diagrams  
very good for understanding the workings of the system  
something like this modelled stimuli+responses into states  
here i modelled two sensor as a result of one sensor  $\Rightarrow$  may be  
done differently

## Embedded Systems Design

Architectural  
patterns

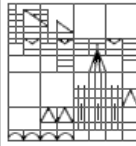
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# Embedded system modeling

Embedded  
Systems Design

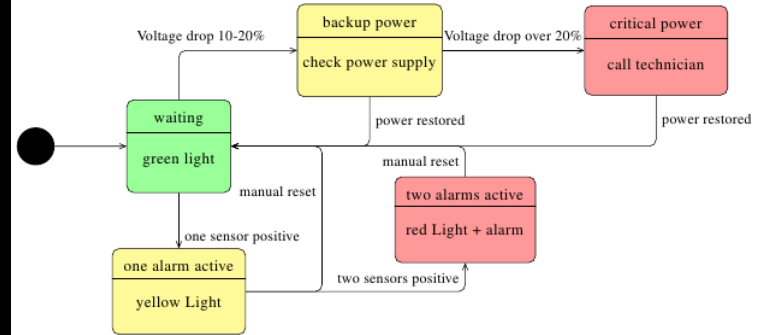
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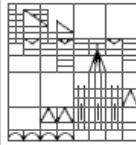
Real-time  
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- Embedded Systems are often built as state machines.

⇒ UML state diagrams



- program has to be... the programming language  
several things need to be taken into account
- program has to be
  - ... fast (i.e. C, Assembler)
  - ... concurrent (i.e. C++, real time Java, ...)
  - speed loses importance
  - it's up to you in the end ...

Embedded  
Systems DesignArchitectural  
patterns

Timing analysis

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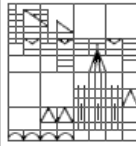
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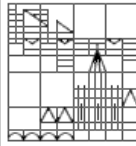
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# Programming language

## concurrent:

and manage shared resources

## concurrent or speed??:

depends on what is more important

simulate concurrency with frequent polling

do something yourself about shared resources

Embedded  
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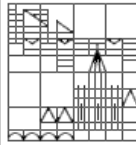
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# Programming language

## speed:

due to faster hardware

ie monitoring device written in C++

ie cell phones in java, objective C, ...

still there are some areas, where you need C & assembler...

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Systems Design

Architectural  
patterns

Timing analysis

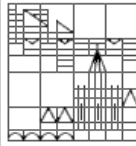
Real-time  
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# Programming language

it's up to you in the end...  
evaluate the needs and decide...

Embedded  
Systems Design

Architectural  
patterns

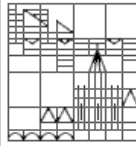
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# Outline

1 Embedded Systems Design

2 Architectural patterns

3 Timing analysis

4 Real-time operating systems

Embedded  
Systems Design

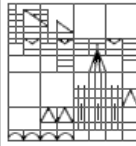
Architectural  
patterns

Timing analysis

Real-time  
operating systems

- Architectural patterns are used to describe a system in an abstract way and help to understand the architecture.

└ Observe and react  
└ Environmental Control  
└ Process Pipeline



# Architectural patterns

## note on the 3 patterns:

The sommerville book describes three rough design pattern  
there are finer patterns that will lead to more exact design

Embedded  
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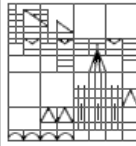
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Embedded  
Systems Design

Architectural  
patterns

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# Architectural patterns

- Architectural patterns are used to describe a system in an abstract way and help to understand the architecture.
  - Observe and react
  - Environmental Control
  - Process Pipeline

## Observe and React:

set of monitored sensors

Something exeptional happens  $\Rightarrow$  we do something

i.e. monitoring, incoming phone call

**Environmental Control:**

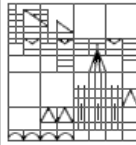
set of sensors and actuators

can change environment

i.e. flash light, when sensor fires

i.e. control water level in a tank

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Architectural  
patterns

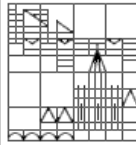
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Embedded  
Systems Design

Architectural  
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# Architectural patterns

- Architectural patterns are used to describe a system in an abstract way and help to understand the architecture.
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## Process Pipeline:

data transformation

series of processing steps

preferably concurrent

**all of those:** can be combined

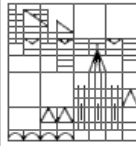
often more than one pattern in the system

ie monitor the actuators

**design patterns:** will lead to **inefficient** system  $\Rightarrow$  only for understanding system

## • Observe and React

- monitor the system with a set of sensors
- display something
- primarily used in: Monitoring systems



# Observe and React

## Observer & React

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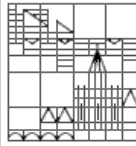
Timing analysis

Real-time  
operating systems

- Observe and React
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# Observe and React

## monitoring:

monitor the system with a set of sensors

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Timing analysis

Real-time  
operating systems

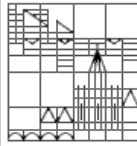
- Observe and React
  - monitor the system with a set of sensors
  - display something
  - primarily used in: Monitoring systems

**display:**

monitoring screen

on exceptional behaviour: alarms, shutdown

- Observe and React
  - monitor the system with a set of sensors
  - display something

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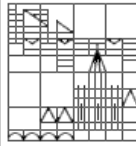
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operating systems

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  - monitor the system with a set of sensors
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# Observe and React

primarily in **monitoring systems**:

often consist of more than one O&R patterns, one for each sensor

optimisation: combine something, ie display on one monitor

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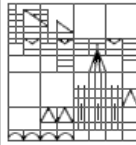
Timing analysis

Real-time  
operating systems

- Observe and React
  - monitor the system with a set of sensors
  - display something
  - primarily used in: Monitoring systems

- Environmental Control

- monitor the system and react to any changes
- Used when there is no requirement for user interaction...
- ...no time for the user to interact...
- ...no way a user can interact...
- ...or there is too much information for users to process.



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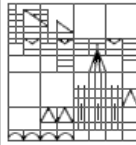
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monitor sytem and react to any changes

no required user interaction

**examples:**

cruise control

water level

pressure control

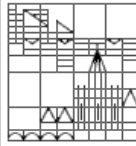
...

#### Environmental Control

##### Environmental Control

- monitor the system and react to any changes
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# Environmental Control

## ● Environmental Control

- monitor the system and react to any changes
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no time for user interaction

**examples:**

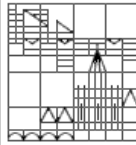
break assist

airbag

## Environmental Control

- Environmental Control

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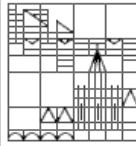
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no way for user interaction

**example:**

CYPRES (parachute, Möllemann did not activate his in 2003)

self destruct of military/sensitive equipment



too much information for users

**example:**

Nuclear Power Plant

Airplane

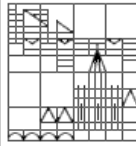
Car

virtually any big system with many subsystems

## Environmental Control

## Environmental Control

- monitor the system and react to any changes
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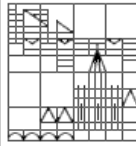
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## • Process Pipeline

- transform data
- often huge amounts of data to be converted in real time
- data acquisition system: storing of data may need to be fast

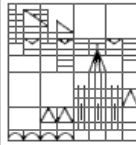
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# Process Pipeline

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transform data

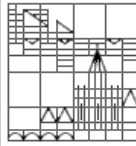
## examples:

signal processing from sensors in other systems

optical sensor

convert digital data to audio

- Process Pipeline
  - transform data
  - often huge amounts of data to be converted in real time
  - data acquisition system: storing of data may need to be fast



# Process Pipeline

**huge amount in real time:**  
concurrency + multicore is the key

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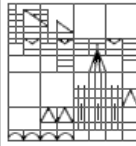
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- Process Pipeline
  - transform data
  - often huge amounts of data to be converted in real time
  - data acquisition system: storing of data may need to be fast



# Process Pipeline

data acquisition system **example:**

particle accelerator

chemical reactions

...

if storing not fast, data will be lost

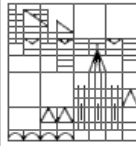
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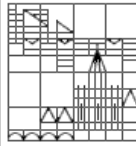
2 Architectural patterns

3 Timing analysis

4 Real-time operating systems

## • timing analysis

- Correctness of systems depends not only on result, but also on the time at which the result is produced.
- How often does each process need to be executed?
- aperiodic stimuli  $\Rightarrow$  make assumptions

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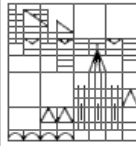
# Timing Analysis - I

- timing analysis
  - Correctness of systems depends not only on result, but also on the time at which the result is produced.
  - How often does each process need to be executed?
  - aperiodic stimuli  $\Rightarrow$  make assumptions

timing analysis

not only result, also time is important  
soft and hard systems

- timing analysis
  - ▶ Correctness of systems depends not only on result, but also on the time at which the result is produced.
  - ▶ How often does each process need to be executed?
  - ▶ aperiodic stimuli ⇒ make assumptions

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Timing analysis

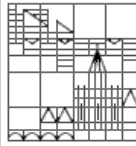
Real-time  
operating systems

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  - ▶ How often does each process need to be executed?

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Timing analysis

Real-time  
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# Timing Analysis - I

## how often?:

then we check, if our system can deliver this  
this can be quite hard, when *mixture of aperiodic and periodic stimuli* or *many aperiodic stimuli* are expected

- timing analysis
  - Correctness of systems depends not only on result, but also on the time at which the result is produced.
  - How often does each process need to be executed?
  - aperiodic stimuli  $\Rightarrow$  make assumptions

**aperiodic stimuli:**

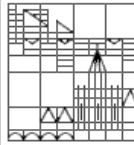
make assumptions

**fast systems:**

use only periodic stimuli

poll frequently for aperiodic stimuli

- Timing analysis
  - Correctness of systems depends not only on result, but also on the time at which the result is produced.
  - How often does each process need to be executed?
  - aperiodic stimuli  $\Rightarrow$  make assumptions

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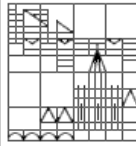
Timing analysis

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- Consider:
  - deadlines
  - frequency
  - execution time



# Timing Analysis - II

Timing analysis must consider:

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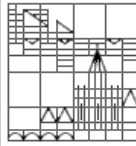
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Timing analysis

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- Consider:
  - deadlines
  - frequency
  - execution time

- Consider:
  - deadlines
  - frequency
  - execution time



# Timing Analysis - II

## deadlines:

By which time must the process have ended.

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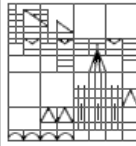
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- Consider:
  - deadlines
  - frequency
  - execution time

- Consider:
  - deadlines
  - frequency



# Timing Analysis - II

## frequency:

The number of times a process must be executed in a given span, so that the *system* meets all deadlines

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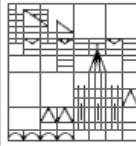
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- Consider:
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# Timing Analysis - II

## execution time:

How long does each single process take (average & worst case)

hard: conditional execution, delays waiting, ...

**hard systems:** always worst case

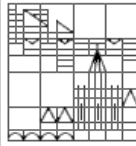
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We list stimuli and response  
then think about how fast this needs to work

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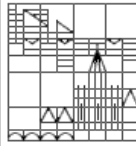
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Stimulus/Response

Timing requirements

voltage drop  $\Rightarrow$  50ms

Stimulus/Response	Timing requirements
voltage drop	switch to backup: 50ms



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Stimulus/Response

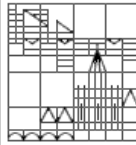
voltage drop

Timing requirements

switch to backup: 50ms



Stimulus/Response	Timing requirements
voltage drop	switch to backup: 50ms
sensor reaction	poll twice a second



sensor reaction  $\Rightarrow$  poll twice a second

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Stimulus/Response

voltage drop

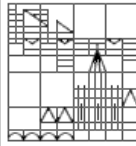
sensor reaction

Timing requirements

switch to backup: 50ms

poll twice a second

Stimulus/Response	Timing requirements
voltage drop	switch to backup: 50ms
sensor reaction	poll twice a second
turn on light	500ms



turn on light  $\Rightarrow$  500ms

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Stimulus/Response

Timing requirements

voltage drop

switch to backup: 50ms

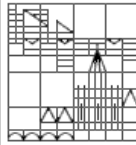
sensor reaction

poll twice a second

turn on light

500ms

Stimulus/Response	Timing requirements
voltage drop	switch to backup: 50ms
sensor reaction	poll twice a second
turn on light	500ms
call technician	5000ms



call technician  $\Rightarrow$  5000ms

may take longer, as technician reaction time is low anyways **LAST**

**CELL:**

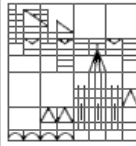
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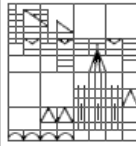
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- normal operating systems not feasible
- special “real-time operating systems” exist
- RTOS must include:
  - real-time clock
  - interrupt handler
  - process manager: scheduler & resource manager
  - dispatcher



# Real-time operating systems

**normal operating systems:**  
too large, too bulky, too slow

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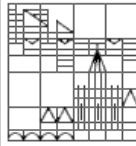
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# Real-time operating systems

## real-time operating systems:

Windows/CE

Vxworks

RTLinux

emdebian

they are small and damn fast

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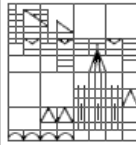
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# Real-time operating systems

RTOS must include

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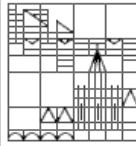
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# Real-time operating systems

## real-time clock:

provides information required to schedule processes

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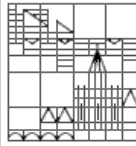
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# Real-time operating systems

## interrupt handler:

manages aperiodic requests for service

may be inside process manager

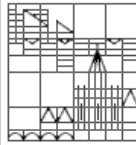
at least **2 levels**:

*interrupt* for processes with fast response time & *clock level* for regular processes

often also background processes with low priority (self checks etc)

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# Real-time operating systems

## scheduler

examines processes and chooses one for execution

processes need enough processor time to *finish before their deadline*

## commonly used:

non-pre-emptive & pre-emptive (execution of processes may be stopped)

*round robin*

rate monolithic scheduling (SJF)

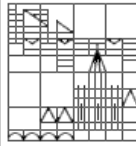
shortest deadline first (HPF)

## resource manager:

allocates memory and processor resources scheduled for execution

- normal operating systems not feasible
- special "real-time operating systems" exist
- RTOS must include:
  - real-time clock
  - interrupt handler
  - process manager: scheduler & resource manager
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# Real-time operating systems

## dispatcher:

starts execution of processes

Embedded  
Systems Design

Architectural  
patterns

Timing analysis

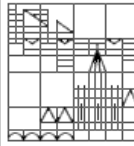
Real-time  
operating systems

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nearly done

important stuff in short

- What you should (at least) remember:



# Embedded Systems Design

## Real-time operating systems

## 30 minutes in short

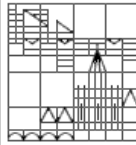
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  - Embedded Systems are a set of processes reacting to stimuli
  - State models help understanding the System.
  - Architectural patterns can be used to help in designing the system.
  - Always do timing analysis in (hard) Embedded Systems.

**Embedded Systems****Real-time operating systems**

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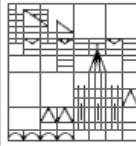
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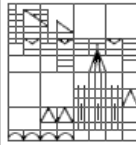
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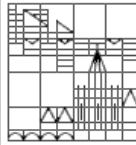
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react to events in real time

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**state models**help understanding the system **Architectural patterns**

help designing the system especially first steps

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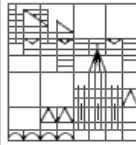
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react to events in real time

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### state models

help understanding the system

help designing the system especially first steps

be done in (hard) systems

### Architectural patterns

timing analysis must always

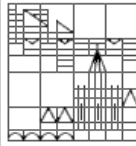
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