Date: Aug.20.2008 Page 1 Rev.: 2

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# Data Sheet

## AWI5822

## 5.8GHz Wireless Receiver IC

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## History

Version	Item	Change	Reason	Date
0			First release	Apr. 2008
1	Add	Add date & Rev. info		Jun. 20,2008
2		Changed PINHEADER and Version (Page20)		Aug. 2008

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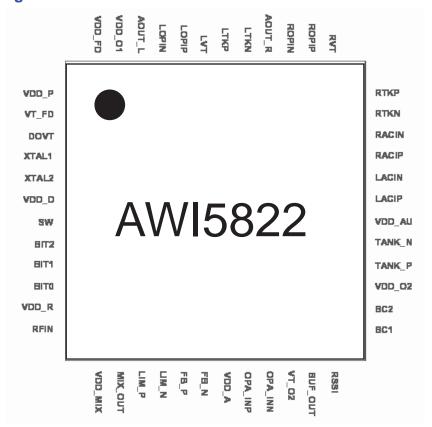
#### **Product Description**

The AWI5822 is an integrated single chip receiver designed for analog A/V sender and wideband digital FSK receiver operating in the 5.8GHz ISM band. The receiver IC consists of a fully integrated frequency synthesizer, LNA, Mixer, IF Amplifier, PLL FM Demodulator, Audio Demodulator and Audio OP Amplifier. The IC is provided in 48-lead QFN7X7 package and is designed to provide a fully functional FM/FM receiver.

#### **Main Features**

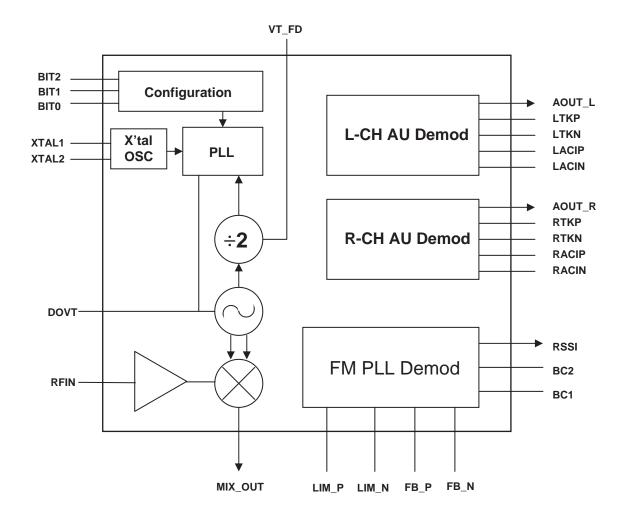
- 3.6V power Supply (Typical)
- Wideband PLL demodulator for adjustment-free production
- 7 channels operation for analog A/V sender and digital FSK transmission applications
- Channel select by use of either a DIP switch or a tact switch for a set of pre-defined frequencies
- Integrated stereo Sound Demodulator and Audio OP Amplifiers

### **Pin Configurations**



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## **Receiver Block Diagram**



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## **Pin Descriptions**

Pin No.	Name	Description	Equivalent Schematic
1	VDD_P	Prescaler Power Supply : Suggestion value of bypass capacitor C2 is 100pF.	
2	VT_FD	5.3GHz Injection Locked Frequency Divider Tuning Voltage Typical range of Vt is 0.4~1.4V	
3	DOVT	5.3GHz LO Tuning Voltage Control Input: Typical range of Vt is 0.35 ~ 1.6V.	<b>—</b> 3
4	XTAL1	One of the two connection pins of external crystal  See Note1	
5	XTAL2	One of the two connection pins of external crystal  See Note1	S D D

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Pin No.	Name	Description	Equivalent Schematic
6	VDD_D	Digital CKT Power Supply :  Suggestion value of bypass capacitor C6 is 0.1uF.	
7	SW	Either PIN7(SW) or the settings of BIT0, BIT1, BIT2 (when PIN7 is grounded) can be used to select the channel out of a set of pre-defined channel frequencies.  PIN7(SW) is normal high and each pull low will advance one channel in the pre-defined channel sequence (CH1, CH2, CH7, CH1) See Note2	VDD VDD
8	BIT2	MSB of the 3 Bits CH Selection PIN.  If PIN7(SW) is grounded, PIN8 is normal high and enabled when pull low. If use PIN7(SW) to select channel, PIN8 outputs the selected channel status according to the operation of PIN7(SW). A series resistor R30 of 47KΩ and a shunt capacitor C62 of 0.068uF are recommended. See Note2	* NDD

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Pin No.	Name	Description	Equivalent Schematic
9	BIT1	One of the 3 Bits CH Selection PIN.  If PIN7(SW) is grounded, PIN9 is normal high and enabled when pull low. If use PIN7(SW) to select channel, PIN9 outputs the selected channel status according to the operation of PIN7(SW). A series resistor R29 of 47KΩ and a shunt capacitor C63 of 39pF are recommended. See Note2	y VDD 
10	віт0	LSB of the 3 Bits CH Selection PIN.  If PIN7(SW) is grounded, PIN10 is normal high and enabled when pull low. If use PIN7(SW) to select channel, PIN10 outputs the selected channel status according to the operation of PIN7(SW). A series resistor R28 of 47KΩ and no shunt capacitor are recommended. See Note2	VDD VDD
11	VDD_R	RF Power Supply :	
		Suggestion value of bypass capacitor C65 is 4.7pF.	

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Pin No.	Name	Description	Equivalent Schematic
12	RF_IN	RF Input to Internal LNA See Note3	Vbias 10K
13	VDD_MIX	Mixer Power Supply:  Suggestion values of bypass capacitors C52 and C36 are 1nF and 10pF.	
14	MIX_OUT	Mixer Output : The impedance level of MIX_OUT (PIN14) and IF Filter is $50\Omega$ .	VDD 14

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Pin No.	Name	Description	Equivalent Schematic
15	LIM_P	Limiting Amplifier Positive Input	15 101uA
16	LIM_N	Limiting Amplifier Negative Input	16 517uA
17	FB_P	Limiting Amplifier Feedback Input (Positive node)	17 102uA
18	FB_N	Limiting Amplifier Feedback Input (Negative node)	18 517uA

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Pin No.	Name	Description	Equivalent Schematic
19	VDD_A	Demodulator Power Supply:  Suggestion values of bypass capacitors C28 and C31 are 100pF and 0.01uF.	
20	OPA_INP	OPA Positive Input	20 - 1.2mA
21	OPA_INN	OPA Negative Input	1.2mA
22	VT_O2	480MHz VCO Tuning Voltage Control Input: Typical range of Vt is 0.9 ~ 1.3V.	
23	BUF_OUT	Demodulated Signal Output	NDD 23

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Pin No.	Name	Description	Equivalent Schematic
24	RSSI	RSSI Output :  The RSSI output voltage is inversely proportional to input signal strength. Typical range of RSSI voltage is 0.3 ~ 1.9V.	
25	BC1	480MHz VCO Frequency Tuning Control Bit : 1C (C: unit capacitance)  Internal pull high, set PIN25 Low to add the extra tuning capacitance.  See Note4	25 VDD
26	BC2	480MHz VCO Frequency Tuning Control Bit : 2C (C: unit capacitance)  Internal pull high, set PIN26 Low to add the extra tuning capacitance.  See Note4	26 EVDD
27	VDD_O2	480MHz VCO Power Supply :	
		Suggestion value of bypass capacitor C39 is 100pF.	

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Pin No.	Name	Description	Equivalent Schematic
28	TANK_P	One of the two connection pins for external Tank components of 480MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L4 and C47 are 22nH and 2pF.	28 600uA
29	TANK_N	One of the two connection pins for external Tank components of 480MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L4 and C48 are 22nH and 2pF.	600uA 29
30	VDD_AU	Audio Power Supply : Suggestion value of bypass capacitor C3 is 0.1uF.	
31	LACIP	6.0MHz Limiting Amplifier Positive Input	31 100K

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Pin No.	Name	Description	Equivalent Schematic
32	LACIN	6.0MHz Limiting Amplifier Negative Input	32 IOOK -
33	RACIP	6.5MHz Limiting Amplifier Positive Input	33 ———————————————————————————————————
34	RACIN	6.5MHz Limiting Amplifier Negative Input	34 - 100K - 100K
35	RTKN	One of the two connection pins for external Tank components of 6.5MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L1 and C34 are 33uH and 6.8pF.	100uA 35

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Pin No.	Name	Description	Equivalent Schematic
36	RTKP	One of the two connection pins for external Tank components of 6.5MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L1 and C34 are 33uH and 6.8pF.	36 100uA 100uA 100uA 100uA
37	RVT	6.5MHz VCO Tuning Voltage Control Input: The range of Vt is 1.1 ~ 1.5V.	<b>─</b> ₩── 37
38	ROPIP	Positive Input of Right CH Audio OPA	38 ————————————————————————————————————
39	ROPIN	Negative Input of Right CH Audio OPA	14.7uA

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Pin No.	Name	Description	Equivalent Schematic
40	AOUT_R	Audio Output of Right CH Audio OPA	VDD 40
41	LTKN	One of the two connection pins for external Tank components of 6.0MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L2 and C44 are 33uH and 10pF.	100uA 41
42	LTKP	One of the two connection pins for external Tank components of 6.0MHz VCO.  Shorter traces for the interconnection of L and C are recommended. Suggestion values of L2 and C44 are 33uH and 6.8pF.	42 100uA

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Pin No.	Name	Description	Equivalent Schematic			
43	LVT	6.0MHz VCO Tuning Voltage Control Input: Typical range of Vt is 1 ~ 1.4V.	<b>─</b> ₩ <b>─</b> 43			
44	LOPIP	Positive Input of Left CH Audio OPA	14.7uA			
45	LOPIN	Negative Input of Left CH Audio OPA	14.7uA			
46	AOUT_L	Audio Output of Left CH Audio OPA	VDD 46			

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Pin No.	Name	Description	Equivalent Schematic
47	VDD_O1 5.3GHz VCO Power Supply: Typical range of VDD_O1 is 4.05 ~ 4.1V.		
		Suggestion value of bypass capacitor C14 is 100pF	
48	VDD_FD	5.3GHz Injection Locked Frequency Divider Power Supply :	
		Suggestion value of bypass capacitor C1 is 100pF.	

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## **Maximum Rating**

Parameter	Maximum Rating	Units	
Supply Voltage (VDD)	5	V	
Storage Temperature	-20 ~ +85	r	

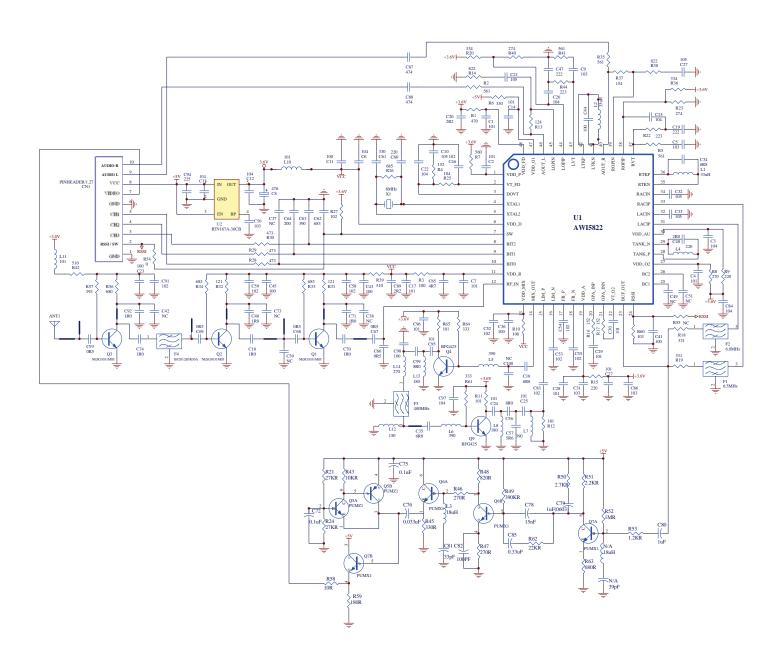
Note: This device is ESD sensitive. Handling and assembly of this device should only be done at proper ESD protection environment.

#### **Electrical Characteristics**

Parameter	Description	Min.	Тур.	Max.	Unit
Operation Temperature	•	-10		75	c
Supply Voltage			3.6	4.5	V
Current Consumption			140		mΑ
RF Frequency		5740, 5760, 57	80, 5800, 5820,	5840, 5860MHz	MHz
LO Frequency		5260, 5280, 53	00, 5320, 5340,	5360, 5380MHz	MHz
Sensitivity			-70		dBm
IF Frequency			480		MHz
RF Gain (LNA & Mixer)			17		dΒ
Reference Frequency			8		MHz
Crystal Accruacy			30		ppm
RSSI Voltage	@ RFIN: -70~-10 dBm	0.5		2.5	V
Demodulation			FM		
Demodulator Input Sensitivity			-50		dBm
Demodulator Output Levet	@ F.D.: ± 2MHz		120		m∨р-р

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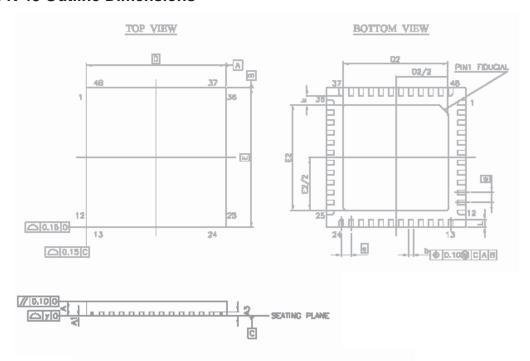
## **Application Circuit:**



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## **Package Information**

#### **QFN 48 Outline Dimensions**



SYMBOL	DIMENSION (MM)		D[MENSION (NIL)			
	MIN	NDM	MAX	MIN	NDM	MAX
Α	0.70	0.75	0.80	27.6	29.5	31.5
AL	0	0.02	0.05	0	0.79	L97
A3	0.203 REF			8 REF		
ь	0.18	0.25	0.30	7.1	9.8	11.8
D	7.00 BSC		276 BSC			
155	5.10	5.20	5.30	201	205	209
E		7.00 BSC		276 BSC		
E22	5.10	520	5.30	<b>201</b>	205	209
•	0.50 BSC		19.7 BSC			
k	0.20			7.9		
L	0.30	0.40	0.50	1L8	15.7	19.7
у	80.0				315	