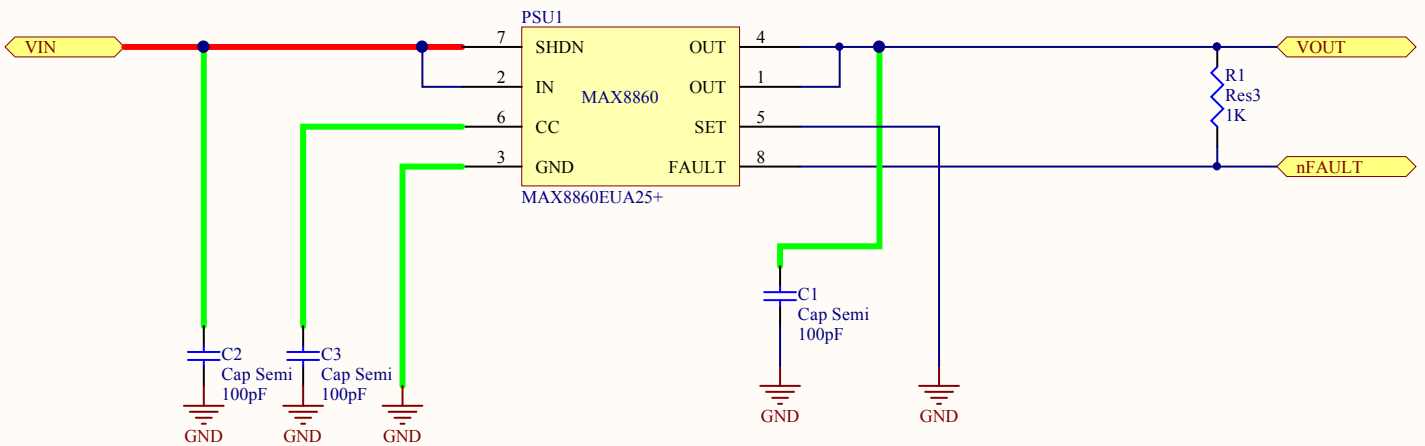
MAX series voltage regulator schematic



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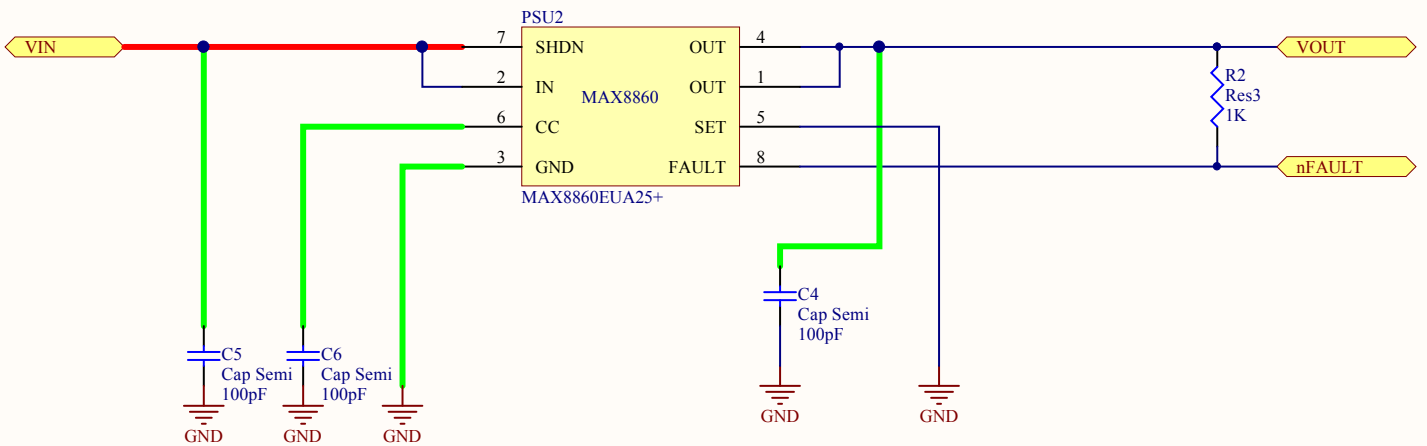
MAX series voltage regulator schematic



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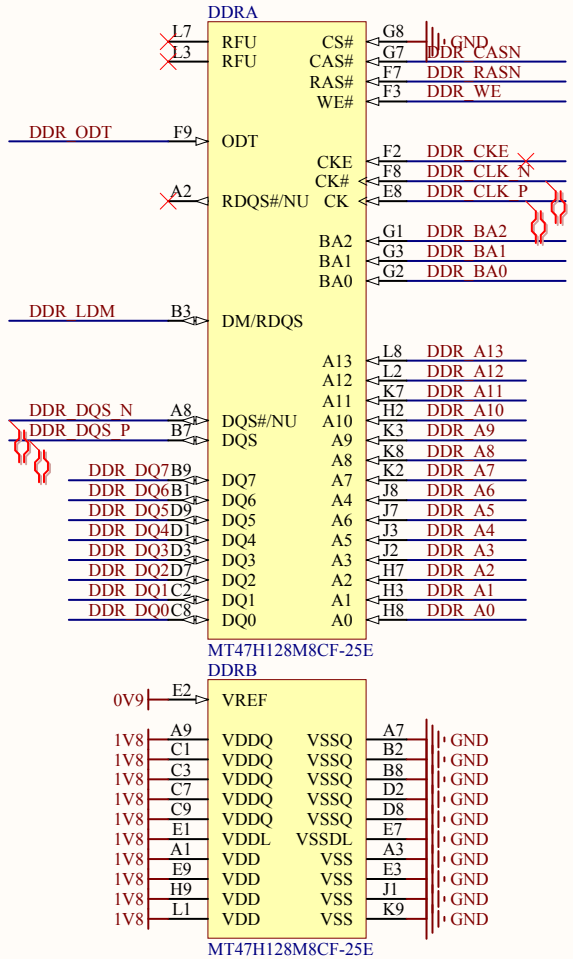
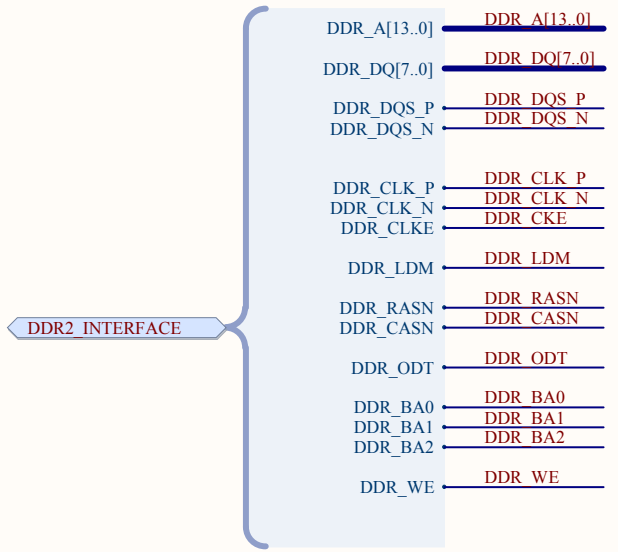
Schematic for DDR2 RAM chip



DDR2 Interface



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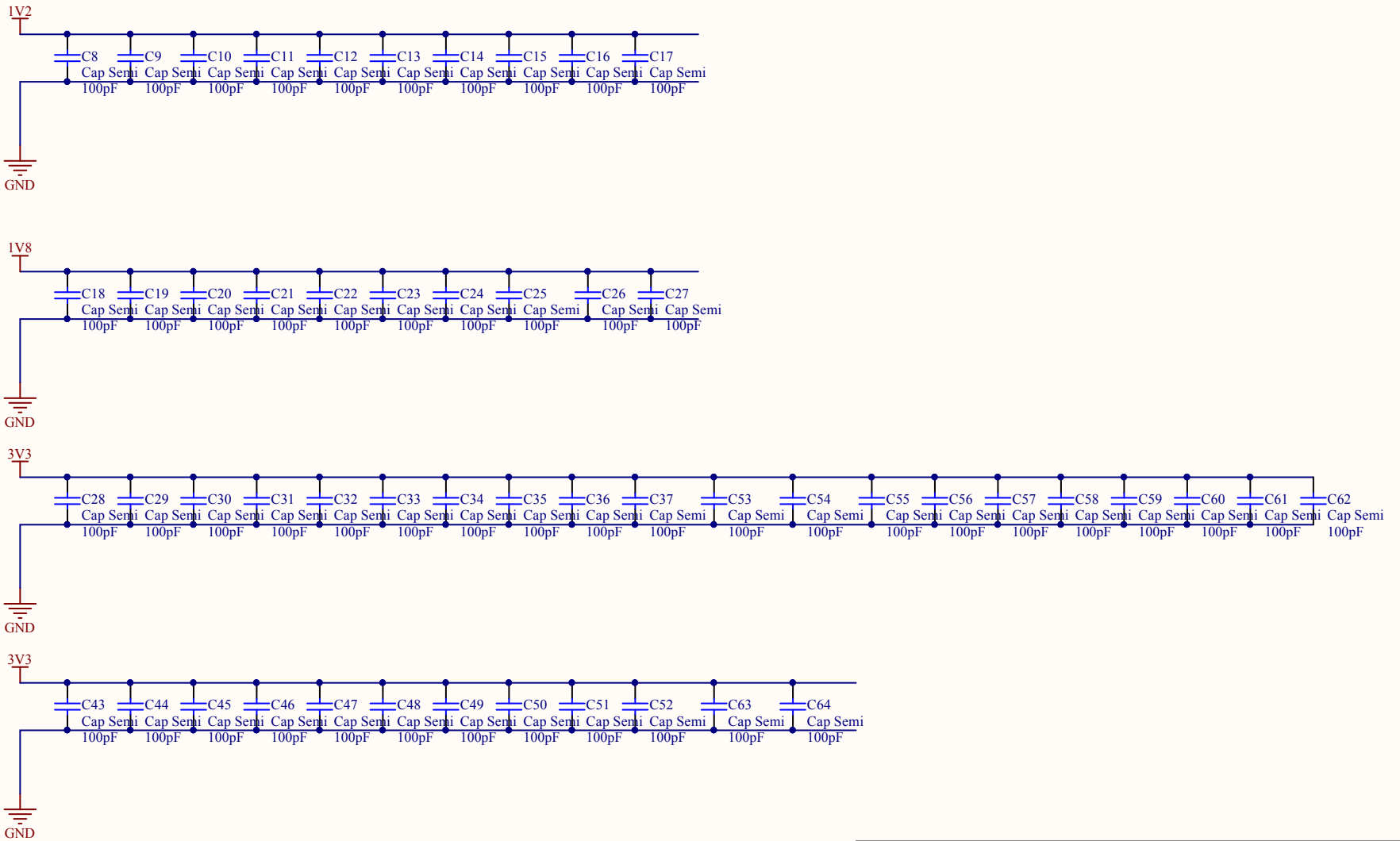
Decoupling capacitors



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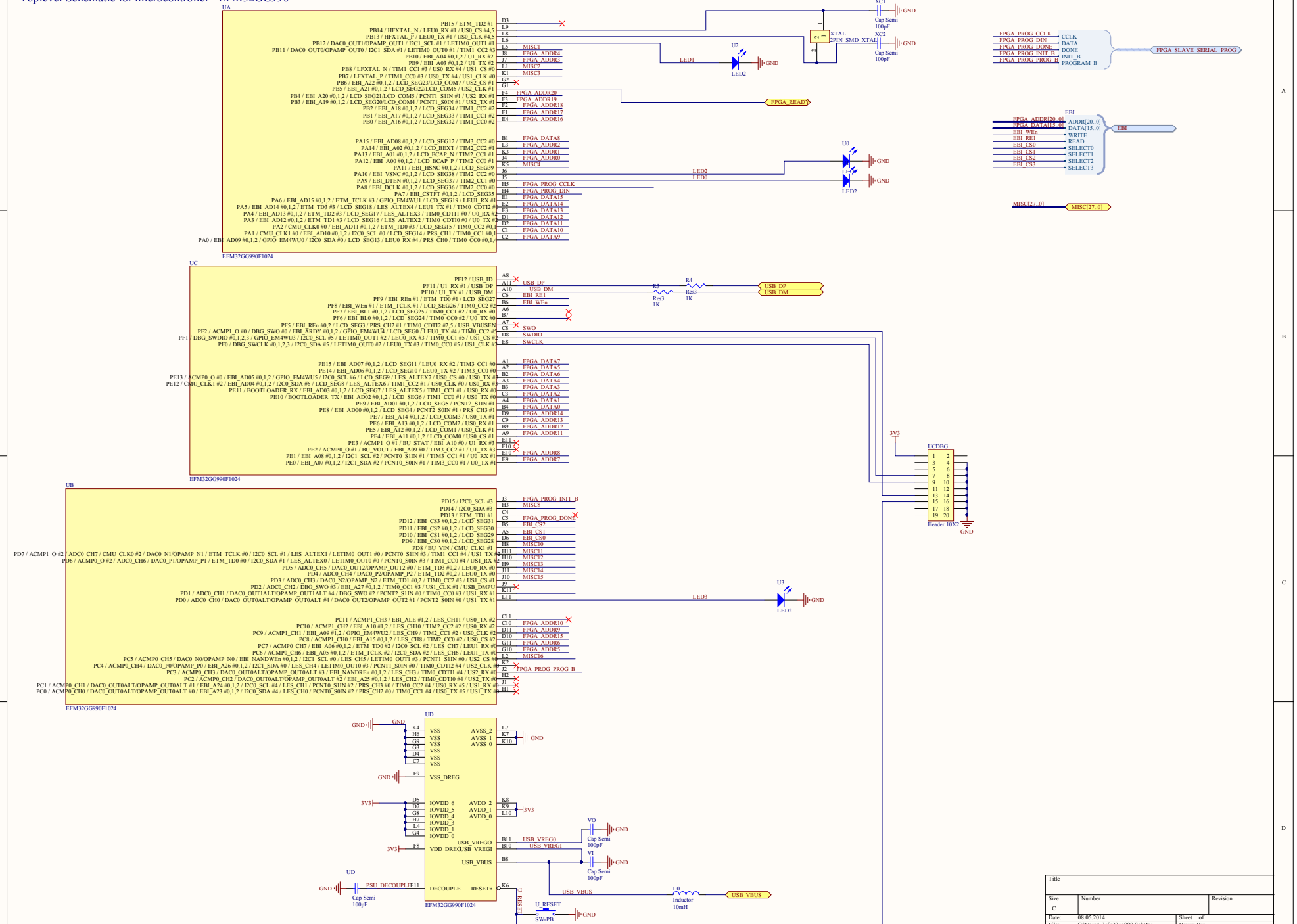
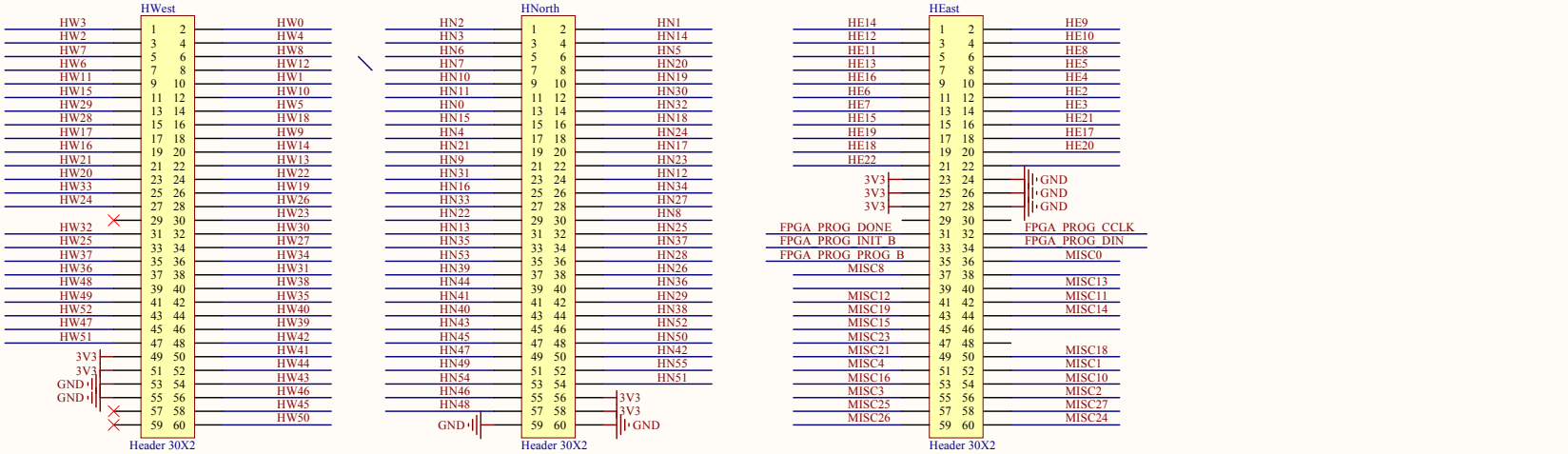
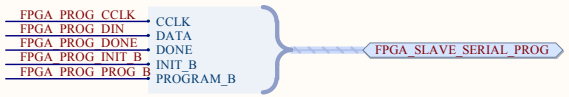
Toplevel Schematic for microcontroller - EFM32GG990



Header connectors - North West East





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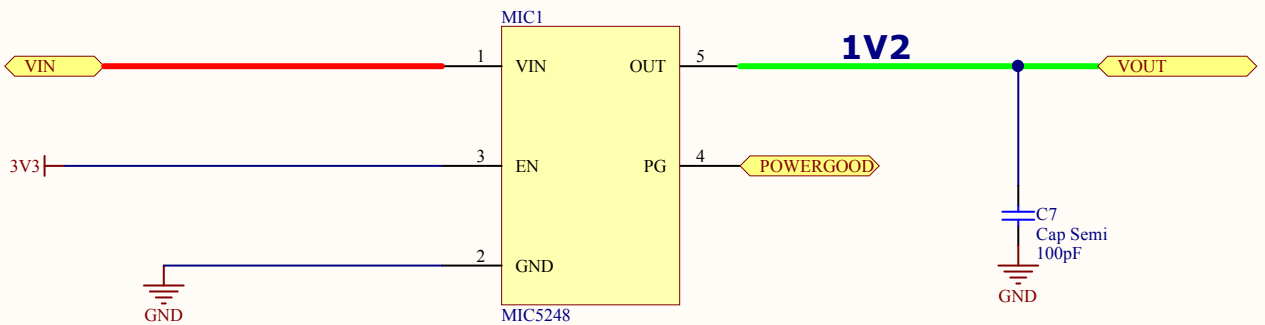
Schematic for 1V2 voltage regulator



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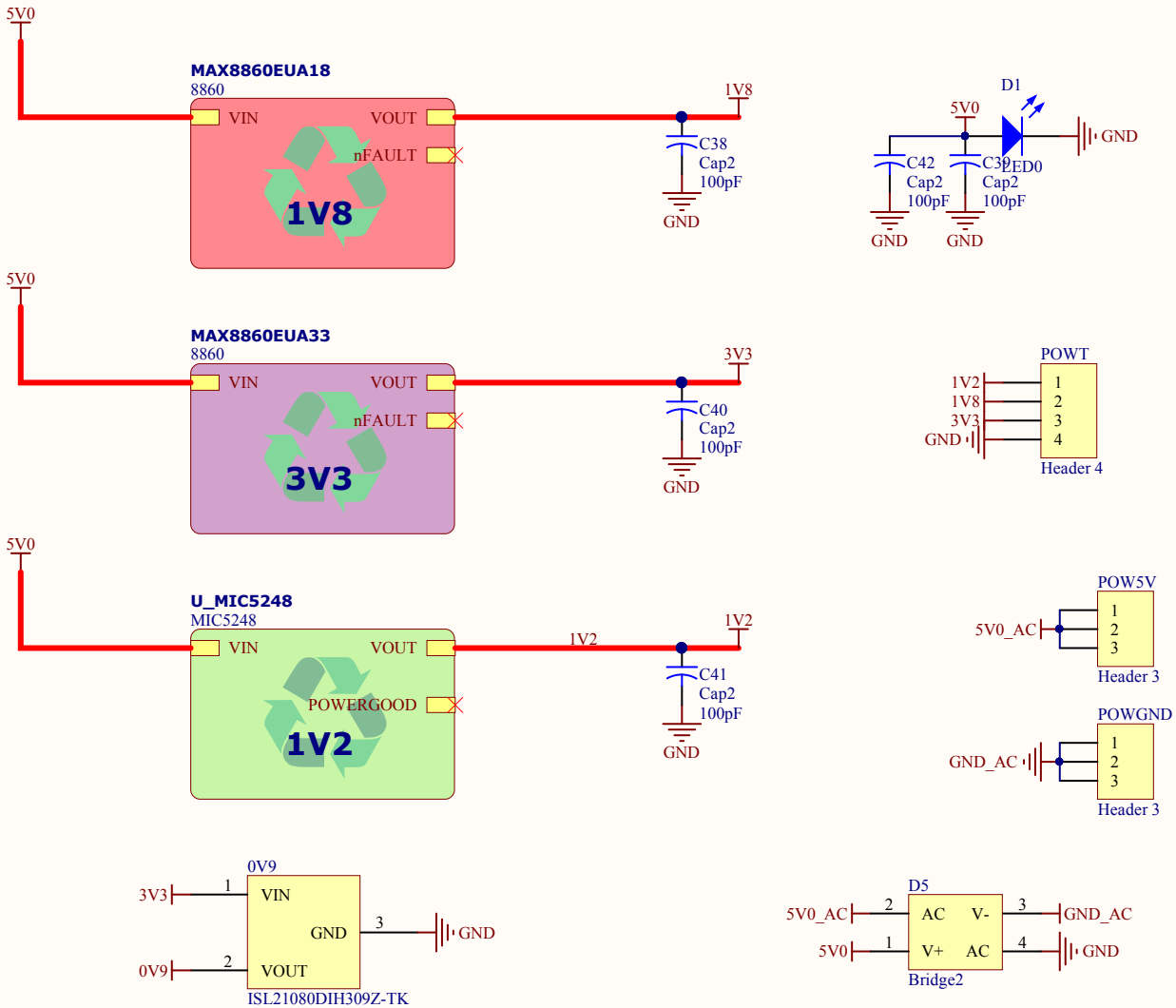
Toplevel Schematic for power supply unit



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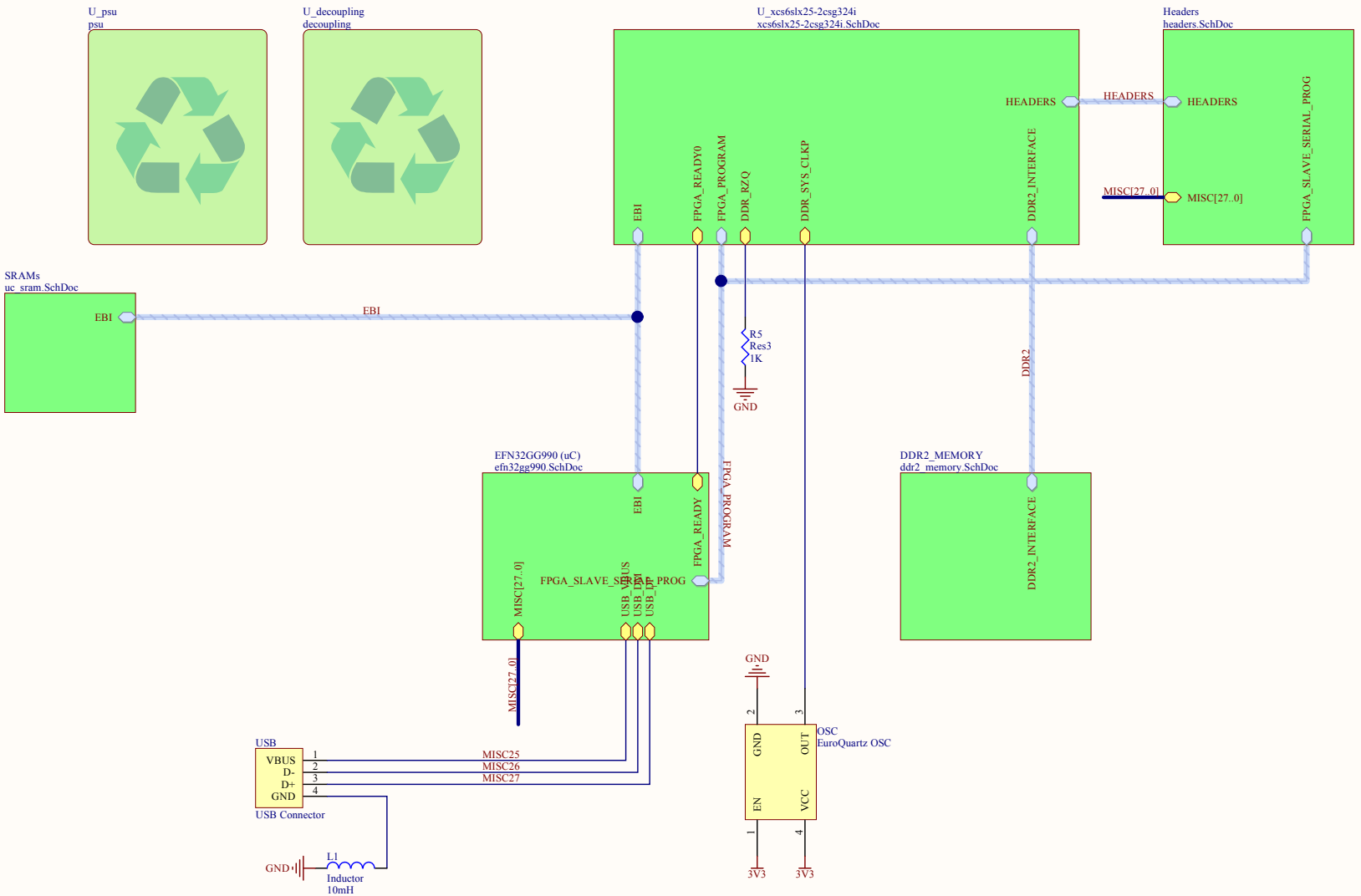
Toplevel Schematic for Motherboard



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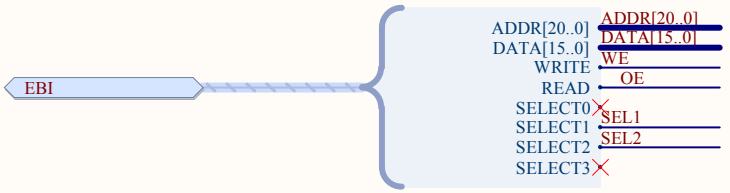
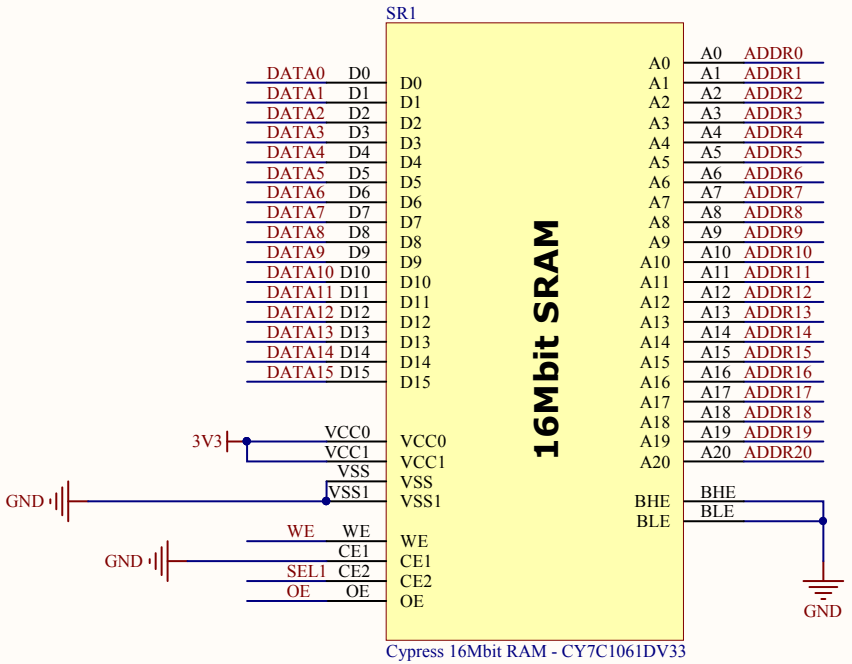
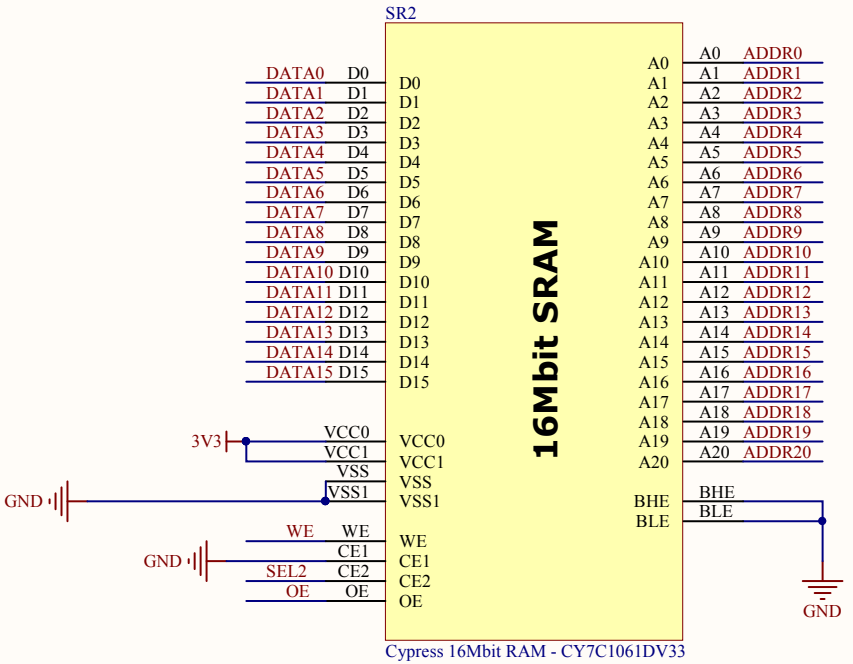
Schematic for SRAM chips







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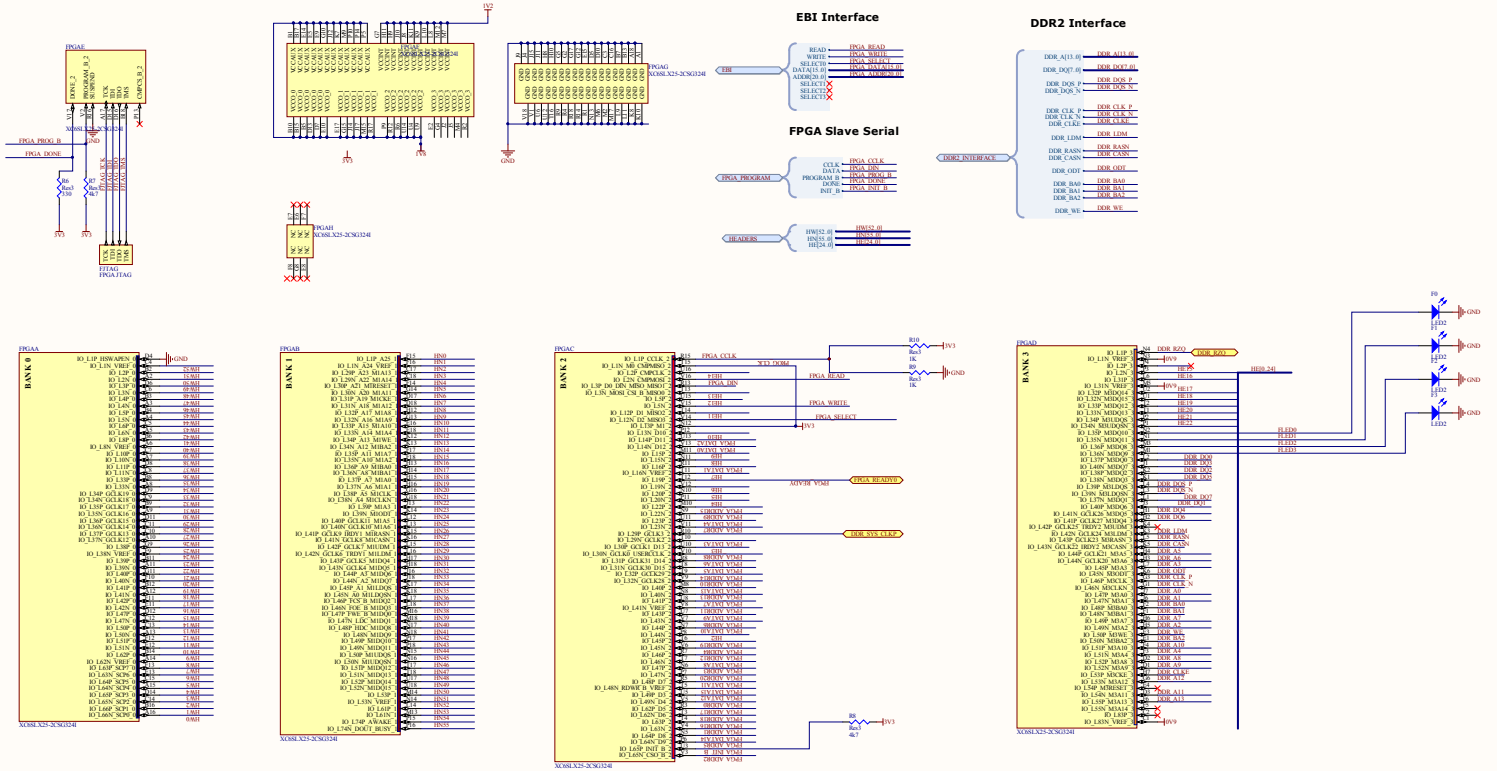
Toplevel Schematic for FPGA - Spartan 6 LXxx-2CSG324



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NASCENCE Mecobo v3.5 EU
OR
FPGA
DDR
USB
JTAG
HNorth
HWest
HEast
POW5V
POWGND
UCDBG
J_RESET
XC1
XC2
1V2
1V8
3V3
GND

Board S