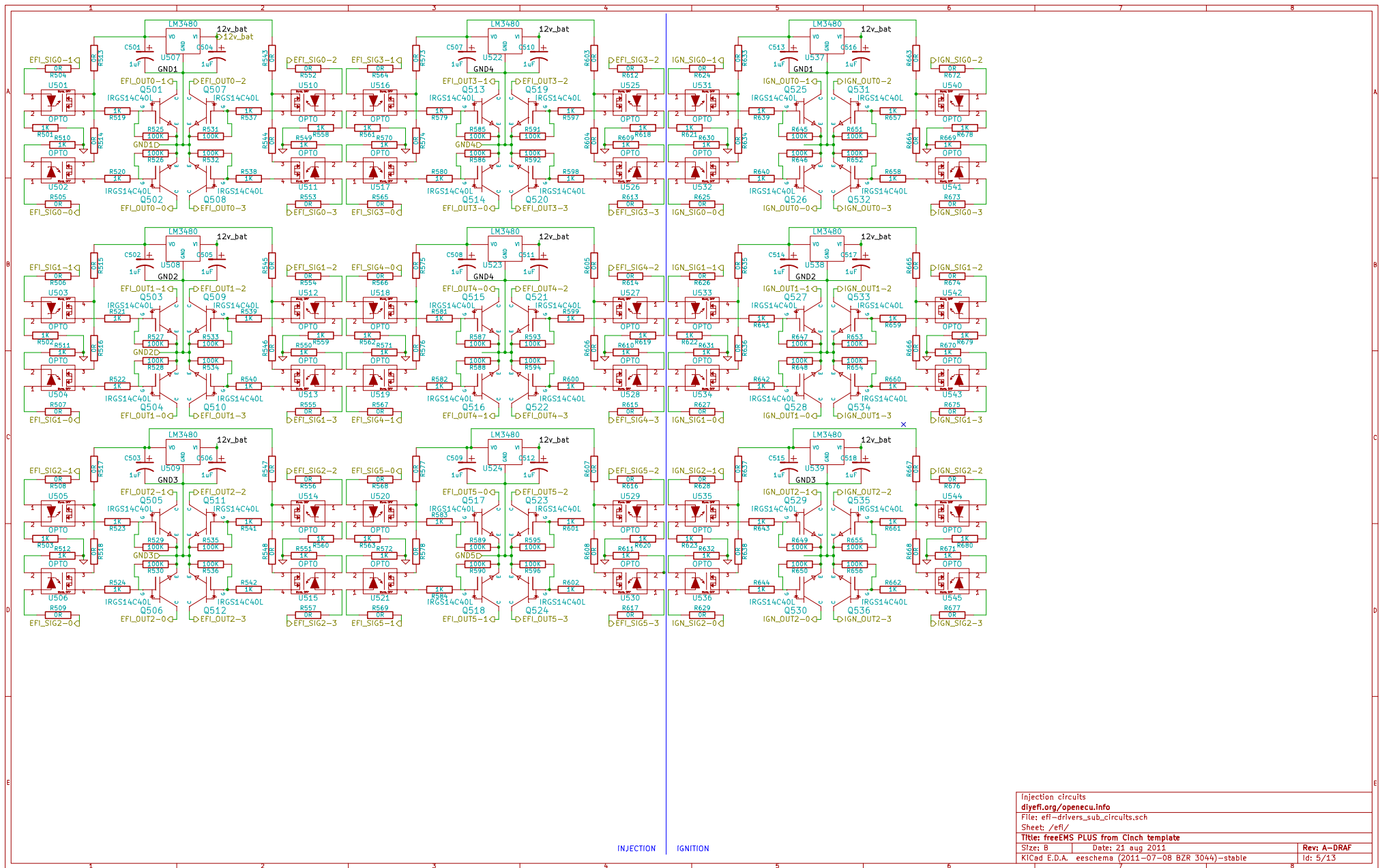
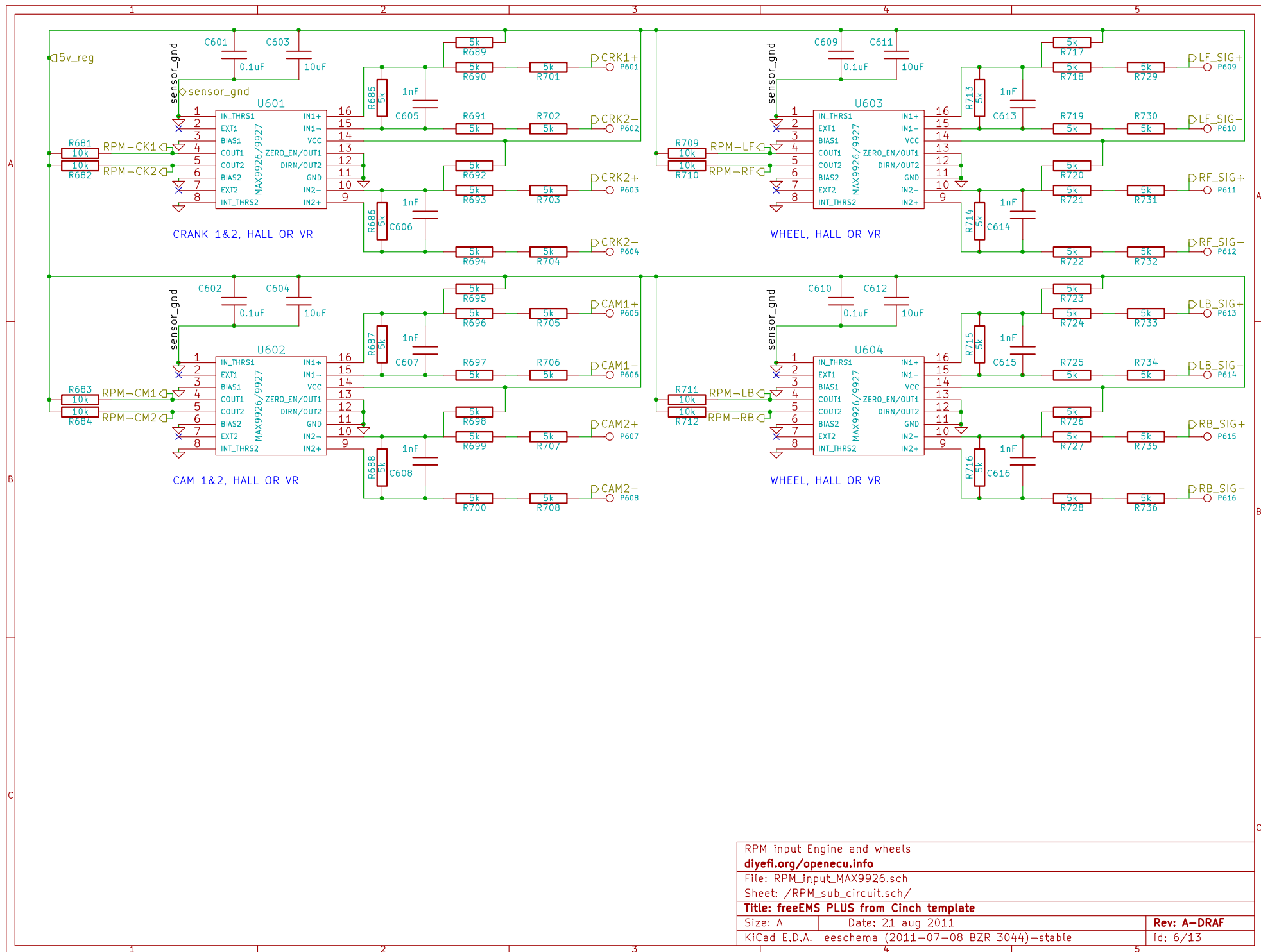


Github.org/spaceteam/bots	
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Sheets: /CPU/	
Notes freebies PLUS from Clock template	
Sheet D:	Date: 21 aug 2011
KXCD 1.0.0_A	eeshenka (2011-07-08 BZR 3544) -stable
	16/2/13







RPM input Engine and wheels

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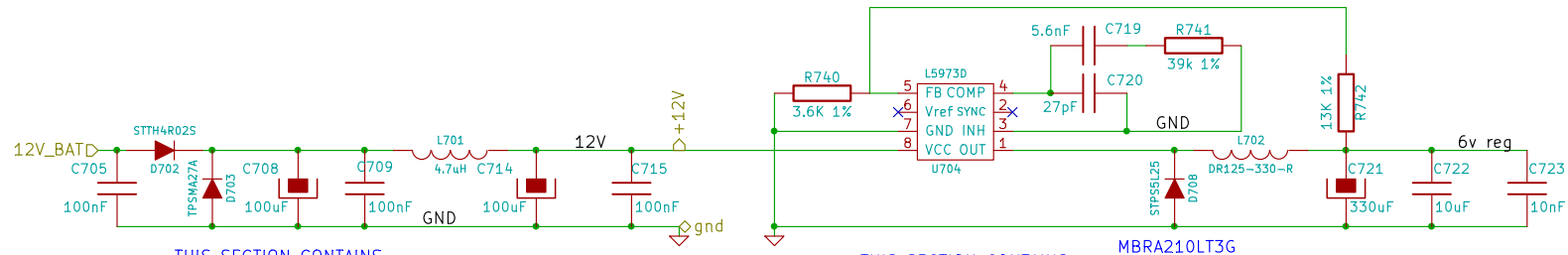
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Size: A Date: 21 aug 2011

KiCad E.D.A. eeschema (2011-07-08 BZR 3044)-stable

Rev: A-DRAF

Id: 6/13

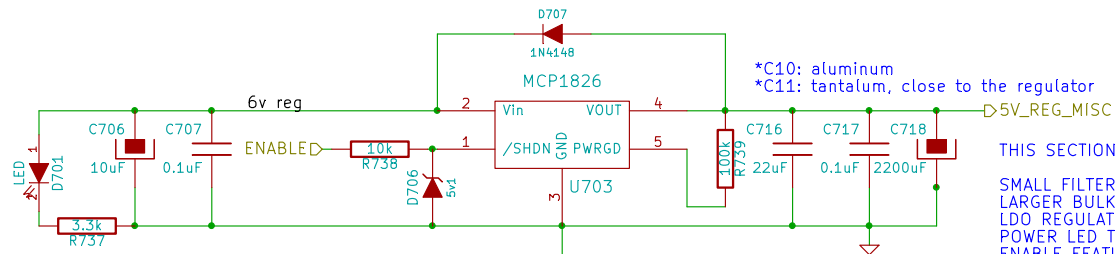


THIS SECTION CONTAINS

FILTER/BULK CAPS
REVERSE POLARITY PROTECTION DIODE
REVERSE POLARITY SPIKE PROTECTION
TRANSIENT INDUCTOR

THIS SECTION CONTAINS

A 2 AMP SWITCHING 6V REGULATOR. THIS PREVENTS THE LINEARS FROM HEATING UP IN EXCESS.

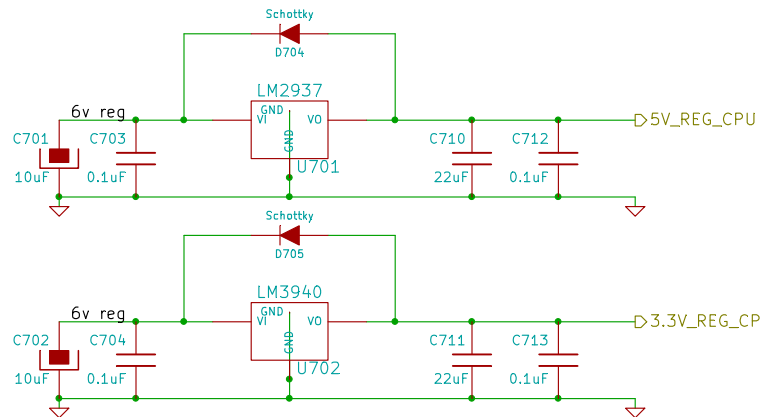


MCP regulator pin compatible with:
LP3852 or MIC29150 (26v-input capable)

*C10: aluminum
*C11: tantalum, close to the regulator

THIS SECTION CONTAINS

SMALL FILTERING CAPS TO PREVENT OSSILATION
LARGER BULK CAPS TO HANDLE THE BULK NEEDS
LDO REGULATOR TO GENERATE A 1 AMP ACCURATE 5V SOURCE GOOD FOR MISC NEEDS
POWER LED TO INDICATE IT'S POWRED
ENABLE FEATURE TO ALLOW MCU TO TURN THINGS OFF.
REVERSE POLARITY DIODE PROTECTING THE MCP1826



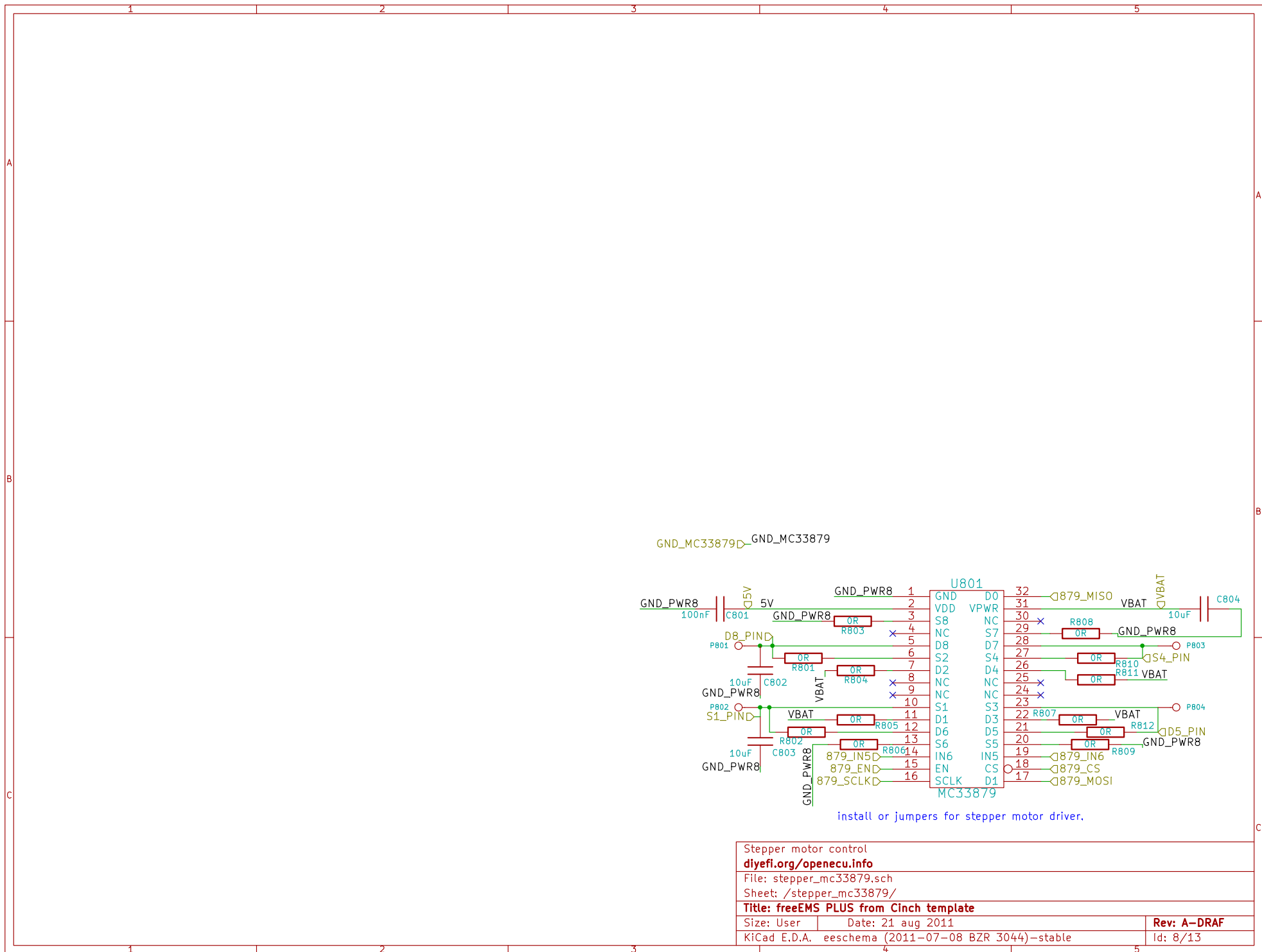
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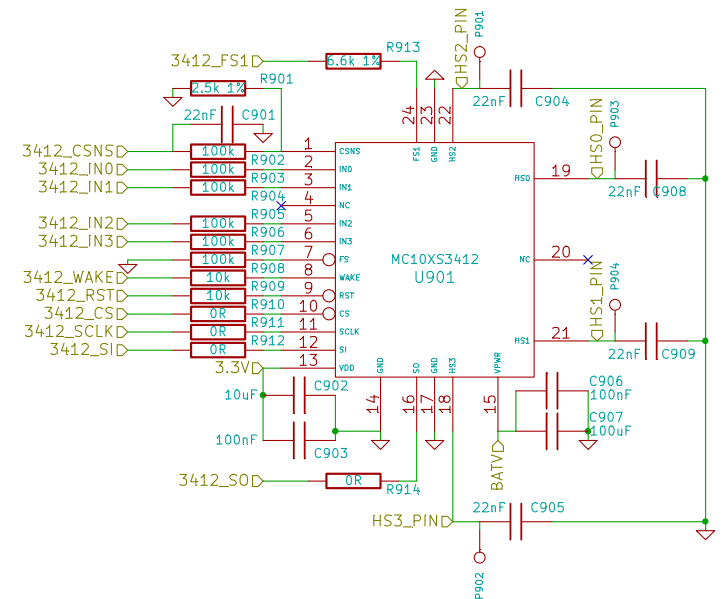
SMALL FILTERING CAPS TO PREVENT OSSILATION
LARGER BULK CAPS TO HANDLE THE BULK NEEDS
LDO REGULATOR TO GENERATE A 1 AMP 5V PERCISSION LOW RIPPLE SOURCE

THIS SECTION CONTAINS

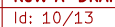
SMALL FILTERING CAPS TO PREVENT OSSILATION
LARGER BULK CAPS TO HANDLE THE BULK NEEDS
LDO REGULATOR TO GENERATE A 1 AMP 3.3V PERCISSION LOW RIPPLE SOURCE

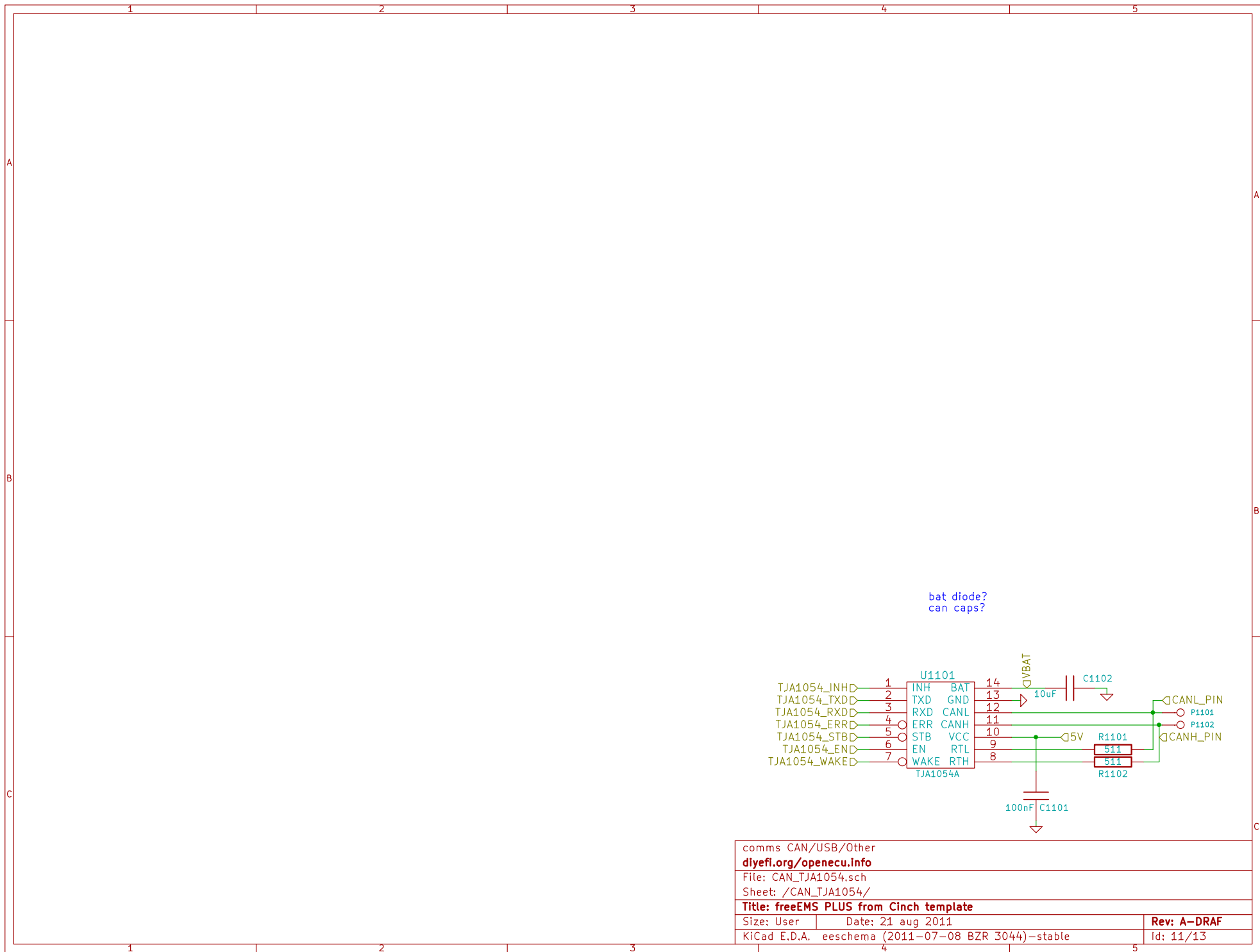
power regulator		
diyefi.org/openecu.info		
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Size: A	Date: 21 aug 2011	Rev: A-DRAF
KiCad E.D.A. eeschema (2011-07-08 BZR 3044)-stable		Id: 7/13

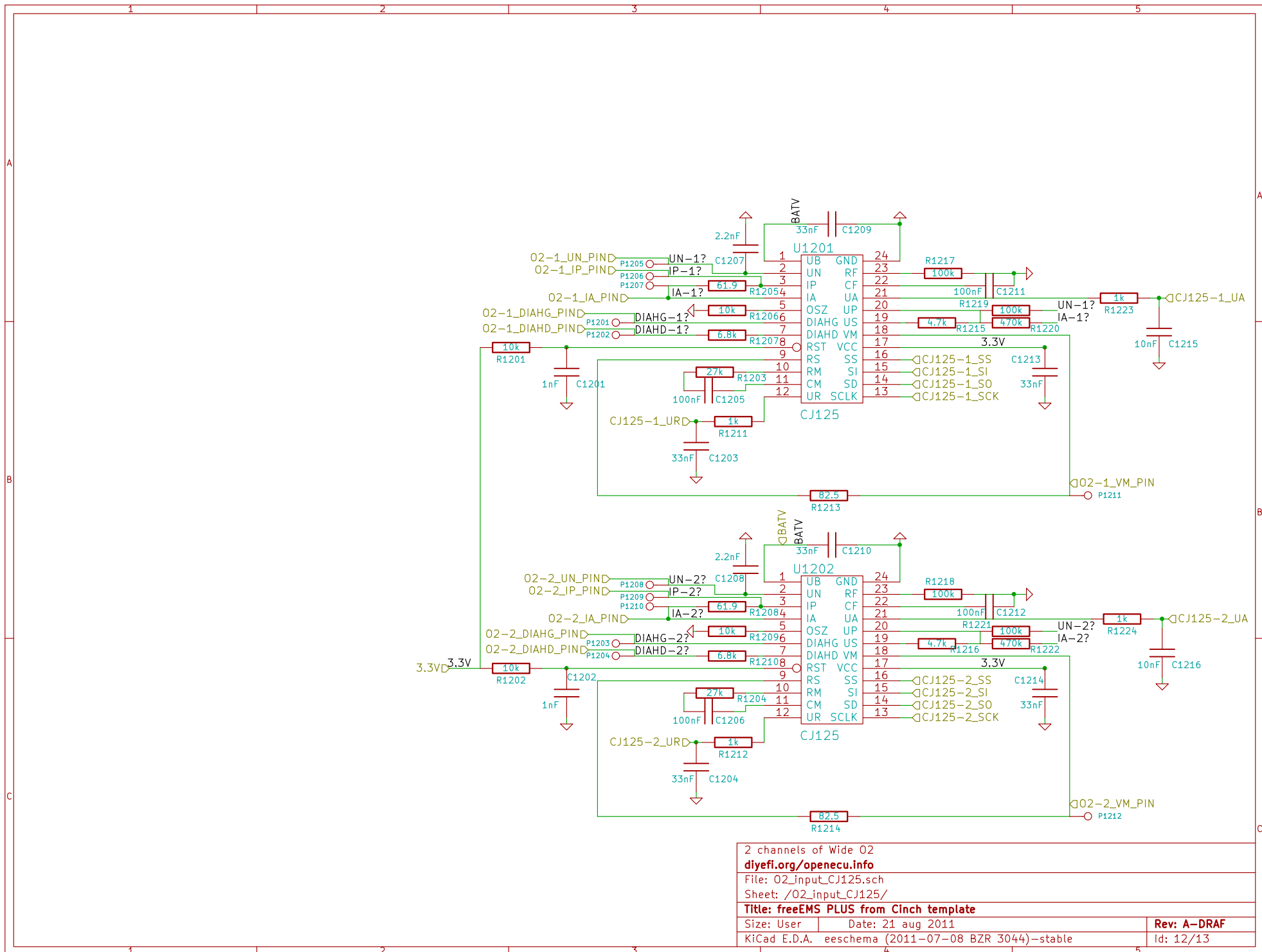




High side drive circuits	
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Title: freeEMS PLUS from Cinch template	
Size: A4	Date: 21 aug 2011
Rev: A-DRAF	
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Id: 9/13	







2 channels of Wide O2

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File: O2_input_CJ125.sch

Sheet: /O2_input_CJ125/

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Size: User Date: 21 aug 2011

KiCad E.D.A. eeschema (2011-07-08 BZR 3044)-stable

Rev: A-DRAF

Id: 12/13

