GATE 2023 Computer Science and Information Technology (CS)

November 10, 2023

1. The output of a 2-input multiplexer is connected back to one of its inputs as shown in the figure.
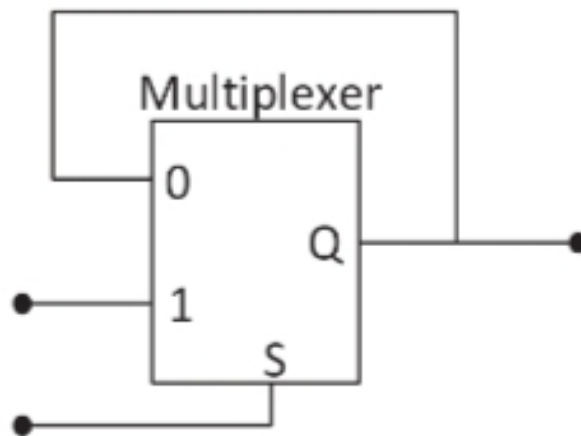

Figure 1: circuit

2. Match the functional equivalence of the circuit to one of the following options.

    (a) D Flip-flop
    (b) D Latch
    (c) Half-adder
    (d) Demultiplexer