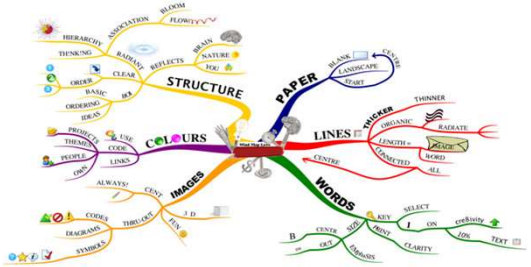
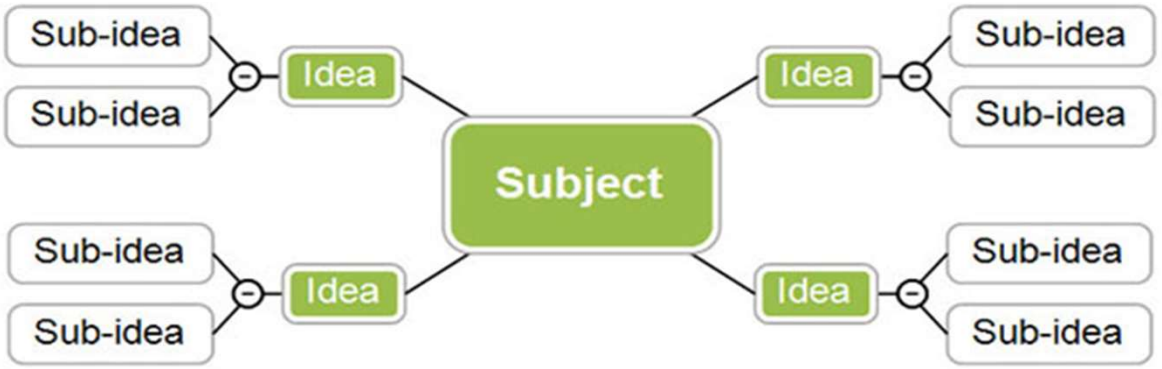
Mindmap	Subject for Final exam - one SSVV notion		
Scoring		75XP	75XP
20XP	3 topics (Idea) + 3 subtopics (sub-idea)		
15XP	The 4th topic (Idea) + 3 subtopics (sub-idea) for each topic		
15XP	The 5th topics (Ideas) + 3 subtopics (sub-idea) for each topic		
25XP	topics and subtopics - correct relation to the notion		
format	A4 paper size		
Mindmap type	**using a tool to draw it (bring it printed on paper)**		
	using regular (colored) pencil		





Nume		Prenume		SSVV notion for mindmap
931				
1	ABRUDAN	O.A.	ANDREI	white-box testing
2	ABRUDEAN	D	SERGIU-VALENTIN	unit testing
3	ACATRINEI	A.	ANDREEA-LAURA	system testing
4	ANDRASI	L	LADISLAU	function testing
5	BĂICAN	I	VLAD-ADRIAN	regression testing

6	BÂLC	M.	RADU	acceptance testing
7	BALEA	C.N.	DORIN CONSTANTIN	Fagan inspection
8	BANCIU	A	RAUL	Walkthrough
9	BĂRBOI	GH	GEORGE	Pair programming
10	BARCĂU	N	EMANUEL - TEODOR	Model checking
11	BENDIC	E	RADU	Symbolic execution
12	BERCIU	L. E.	LIVIU MARIAN	Equivalence classes (in testing)
13	BÎLC	H.	IRINA	boundary value analysis (in testing)
14	BIRO	L.	ANNA	Control flow graph (in testing)
15	BLAJ	D.S.	ANDREI-SORIN	Source code coverage criteria
16	BOANCA	A.S.	SARA	Floyd (correctness in testing)
17	BOB	M	ALEXANDRA-DIANA	Hoare's triples
18	BOBOC	P	BOGDAN	Dijkstra (in testing)
19	BOCA	A	MADALINA-ELENA	Levels of testing
20	BOCANICIU	GR	ANA-MARIA	Test case
21	BOIER	E	DANIEL	Software Verification
22	BORA	A	ANDREI	Software Validation
23	BRATA	L.	GEORGE	Quality control
24	CODREA	M.	CLAUDIU-MARIUS	Quality assurance
25	DELIBAȘ	I	ȘTEFAN	Quality of software
26	MACARIE	I.S.	IOANA-MARIA	Testing techniques
27	NĂSUI	V	DIANA-IOANA	Linear Time Logic
28	POPŞOR	V.B.	BOGDAN-ALEXANDRU	Computation Tree Logic
29	ROTARIU	I.	MIRELA-IONELA	Model cheker
30	RUSU	T.	DENISA	Verification techniques
31	SZUHAI	P.A.	IULIA MONICA	Types of errors
932				
1	ALB	M	MIRCEA-DAN	Symbolic execution tree
2	BUFTEA	I	MADALINA IOANA	RIMGEN
3	BUHAI	A.F.	ALEXANDRU	Replicate (RIMGEN)
4	BUMBAR	S	ANA-MARIA	Isolate (RIMGEN)
5	BUTNAR	T.G.	ADRIAN	Maximize (RIMGEN)
6	CATANA	M.A.	TUDOR	Generalize (RIMGEN)
7	CERNUŞCĂ	I	TUDOR	Externalize (RIMGEN)
8	CHERECHES	CA	SERGIU ALEXANDRU	Use a neutral tone (RIMGEN)
9	CHILOM	I,L,	MIRCEA-CONSTANTIN	Session Based Test Management
10	CHIRTES	N,	PAUL	Complete testing
11	CHIS	I.	IULIA-ȘTEFANIA	Interleaving semantics (Model checking topic lecture)

12	CIHODARU	D.	ALEXANDRU-CIPRIAN	[] (always, Model checking topic lecture)
13	CONEA	D	ALEXANDRA-IOANA	<>(eventually, Model checking topic lecture)
14	CORNEAN	E	DRAGOS-NICOLAE	safety property
15	COSTE	S.V.	MĂDĂLINA	liveness property
16	COTA	I.C.	IONAS-CALIN	deadlock
17	COTRAU	T	ANDREEA	Floyd - partial correctness
18	CRIȘAN	I	GABRIEL LUCIAN	Floyd - temination
19	CRIȘAN	V,	OANA-ALEXANDRA	Hoare - partial correctness
20	CSOKA	A.	ERVIN	Hoare - total correctness
21	CUIBUS	D	CIPRIAN	Rules of refinement
22	GHERMAN	B.A.	ALEXANDRU	Software correctness
23	MESAROS	V	SEBASTIAN-VASILE	Automation testing
24	POTRA	L.	VLAD-DIONISIE	Manual testing
25	RÜBL	C.R.D.	ERIC	Bug - life cycle
26	SIMION	G.	ALEXANDRA-MARIA	Continuous integration
27	SÎRB	D.	OVIDIU-DANIEL	Driver
28	SOREA	G D	SÂNZIANA	Sandwich testing
29	SPOIALĂ	O.	ANA-MARIA	Top-down integration
30	TODORAN	G	ANA-CORINA	Stub
933				
1	DEZSI	L	IMOLA-KATALIN	Big-bang integration
2	DOLOT	C. D.	DIANA - NICOLE	Incremental integration
3	DRAGODAN	A.	ALEXANDRA-ADRIANA	Pesticide paradox
4	DRAGOMIR	G	IOANA BIANCA	VerVal matrix
5	DUMA	EL	LAURENȚIU	Static verification techniques
6	DUMITRAȘCU	C	MIHAI RĂZVAN	Dynamic verification techniques
7	FARCAS	D	ALEXANDRU	Software Specification
8	FÂRTE	V.S.	RAZVAN-DAN	Precondition and postcondition
9	FOVAS	M.	DENIS-DANIEL-GEORGE	Test management tools
10	FRĂȚILĂ	M	NICOLAE	Software Bug
11	FULEA	N	RAZVAN-DOREL	Bug tracking tool
12	GAE	O.A.	ANDRADA MARIA	Bug report
13	GAL	F.O.	OSCAR	Design bug
14	GALBEN	P	CATALIN	Source code bug
15	GEORGESCU	I.G.	ȘTEFAN-PAUL	Missuse of interface
16	GERGELY	B.M.	KAROLY-BELA	Missunderstand of interface
17	GRAD	V	IONUT-ADRIAN	Software error
18	GRIGOR	I	SEBASTIAN ALEXANDRU	Giving feedback

19	GRIGORE	F.	DRAGOȘ-ALEXANDRU	Verification condition (Floyd)
20	GRIGOROVICI	C	MONICA MARIA	Termination condition (Floyd)
21	HANGAN	V	FLORIN	{P} S {Q}
22	HANGAN	V.	EMILIA	wp(if A then S else T end, R)
23	MOLDOVAN	I.I.	IOAN ADRIAN	Context (in testing)
24	NACU	M	CRISTIAN	Testing = search for information
25	RĂCHITAN	M	NICHITA MIHAI	regression testing
26	STAN	G.	ADELIN-ȘTEFAN	black-box testing
27	STANCIU	I.	ANA-MARIA	white-box testing
28	STANILA	I.	VLAD-IOAN	unit testing
29	TAMAȘ	Ș	IONUȚ-FLORIN	system testing
30	UNGUR	N.M	MARIA	function testing
934				
1	CODOREAN	C.	CARINA-BIANCA	acceptance testing
2	DUMITRESCU	I	DRAGOS-LUCIAN	Fagan inspection
3	GROZA	V.	DANIEL-VASILE	Walkthrough
4	HANEȘ	I.	CAMELIA-ANDREEA	Pair programming
5	HOJDA	PL	IULIAN IONUT	Model checking
6	ILINOIU	O	FLAVIUS - CATALIN	Symbolic execution
7	ILISEI	P.V.	DĂNUȚ	Equivalence classes (in testing)
8	IŞTOAN	V.	OVIDIU-TITUS	boundary value analysis (in testing)
9	KRISTO	B	BELA KRISZTIAN	Control flow graph (in testing)
10	LANGA	V	VALERIU CONSTANTIN	Source code coverage criteria
11	LAZĂR	G	IULIA-ANDREEA	Floyd (correctness in testing)
12	LAZĂR	N.E	MARIA	Hoare's triples
13	LEANCĂ	T	ADRIAN BOGDAN	Dijkstra (in testing)
14	LEITI	R	DÁVID	Levels of testing
15	LUCA	A	ALEX-AUGUSTIN	Test case
16	LUPAȘCU	C	ȘTEFAN	Software Verification
17	MACARIE	M.C.	CRISTIAN	Software Validation
18	MĂCIUCĂ	F	MARIA ALEXANDRA	Quality control
19	MAIER	R	BOGDAN	Quality assurance
20	MAIOR	D.A.	ALEXANDRA	Quality of software
21	MĂLAI	C	EMIL-SORIN	Testing techniques
22	MALI	D	IMRE-GERGELY	Linear Time Logic
23	MANAILA	M	ANDREI PETRUT	Isolate (RIMGEN)
24	MANCIU	V	DANIEL	Model cheker
25	MARUSCA	D	TABITA-CORA	Checklists (in inspection)

26	PERIȘANU	I.G.	DAVID	Types of errors
27	TANASE	C.L.	COSMIN	Verification techniques
28	TILI	A.D.	ADRIAN-FLORIN	Symbolic execution tree
29	TRIFON	I.S.	TITUS-TRAIAN	RIMGEN
30	TURCU	G. C.	EMA	Replicate (RIMGEN)
935				
1	DOGAR	A	ALEXANDRU	Maximize (RIMGEN)
2	FRENT	D.P.	GEORGE TUDOR	Generalize (RIMGEN)
3	ILIE	V	ANDREI-VASILE	Externalize (RIMGEN)
4	JUGARU	L.E.	ROBERT-GEORGE	Use a neutral tone (RIMGEN)
5	MICU	I	EMERSON GEORGE IONUT	Session Based Test Management
6	MIHALACHE	L.S	MIHAI-ALEXANDRU	Complete testing
7	MIHALIUC	F.S.	IOANA - ALEXANDRA	Interleaving semantics (Model checking topic lecture)
8	MIRCEA	D	SORIN-SEBASTIAN	[] (always, Model checking topic lecture)
9	MIRCEA	V.D.	MARIA-MĂDĂLINA	<>(eventually, Model checking topic lecture)
10	MISAN	L	ANDREI-LUCIAN	safety property
11	MOISUC	P	NAOMI	liveness property
12	MOLDOVAN	A-V	ALEXANDRU-VASILE	deadlock
13	MOLDOVANU	S	TUDOR	Floyd - partial correctness
14	MOLNÁR	A	ROLAND	Floyd - temination
15	MORARIU	O	ALEXANDRU-SIMION	Hoare - partial correctness
16	MUREŞAN	V.	ALEXANDRA-MIHAELA	Hoare - total correctness
17	MURG	A	DANIEL	Rules of refinement
18	NAGY	G	ALEXANDRU	Software correctness
19	NAZARIE	M	CIPRIAN-ALEXANDRU	Automation testing
20	NEAMȚ	L. I.	VLAD IOAN	Manual testing
21	NECHITA	GO	SEBASTIAN	Bug - life cycle
22	NECHITA	I.	IONUȚ-VASILE	Static verification techniques
23	NEGRU	D	SERGIU	Bottom up integration
24	NEMEȘ	C.C.	BIANCA-LAURA	Sandwich testing
25	OBREJA	C.	ELENA	Top-down integration
26	OLTEANU	D.A.	DAN-ANDREI	Stub
27	SUCIU	I	ȘTEFAN SEBASTIAN	Driver
28	USCAT	OT	MIHAI ANDREI	Big-bang integration
29	VARGA	I.V.	ALEXANDRU	Incremental integration
30	VIERIU	M G	DENIS-GABRIEL	Pesticide paradox
31	SZUHAI	P.A.	IULIA MONICA	VerVal matrix
936				

1	GAVRILĂ	O.E.	ANDREI-IONUȚ	Dynamic verification techniques
2	LUNG	I.	ANDREEA-CRISTINA	Software Specification
3	MUSCALA	S	ANDREI	Precondition and postcondition
4	OPRUȚA	F	DAVID	Test management tools
5	OROSZ	J	DENISE	Software Bug
6	OSIAN	G	MIHAI	Bug tracking tool
7	PAIUS	DC	TEODOR-MARIAN	Bug report
8	PANAITE	D	DORINEL	Design bug
9	PÎTEA	N.V.	NICOLAE-OCTAVIAN	Source code bug
10	PODARIU	H.I.	HOREA CATALIN	Missuse of interface
11	POP	F	CLAUDIA	Missunderstand of interface
12	POP	O	LORENA NOEMI	Software error
13	POP	P	PAUL DARIUS	Giving feedback
14	POPA	I.A.	CĂTĂLIN	Verification condition (Floyd)
15	PRISACARIU	C	ALEXANDRU	Termination condition (Floyd)
16	PRODAN	G.	BIANCA-MARIA	{P} S {Q}
17	RAȚIU	CI	COSMINA CRISTINA	wp(if A then S else T end, R)
18	ROBAȘ	R.D.	IULIA	Context (in testing)
19	ROMAN	I.A.	SERGIU-IOAN	Testing = search for information
20	SABADAS	R	OANA STEFANIA	Correctness (in software)
21	SABADÎȘ	V	ANDREEA-IOANA	Precondition and postcondition
22	SABOU	C	LORENA LIGIA	Incremental integration
23	SAS	S	INGRID-IULIA	Sandwich testing
24	SAVA	N	GHEORGHE	unit testing
25	VĂSIEȘ	LR	ANDREEA-LOUISA	Quality control
26	VELE	VM	RADU-GEORGE	Levels of testing
27	VETRO	I.	ANDREI	regression testing
28	VÎRVARA	P	ALEXANDRU	Static verification techniques
29	VITOC	DC	ALECSANDRU DANIEL	Dynamic verification techniques
30	ZĂPÎRȚAN	V	TATIANA	Software Specification

Others

1	Alonso		Lucia	Automation testing
2	Ayachi	Moha		Software Bug
3	Sabau		Liviu Dragos	Software correctness
4	Bocaniciu		Radu Teofil	Automation testing
5	Mehedeniuc		Gheorghe	Manual testing
6	Lucaciu		Catalin	Pesticide paradox