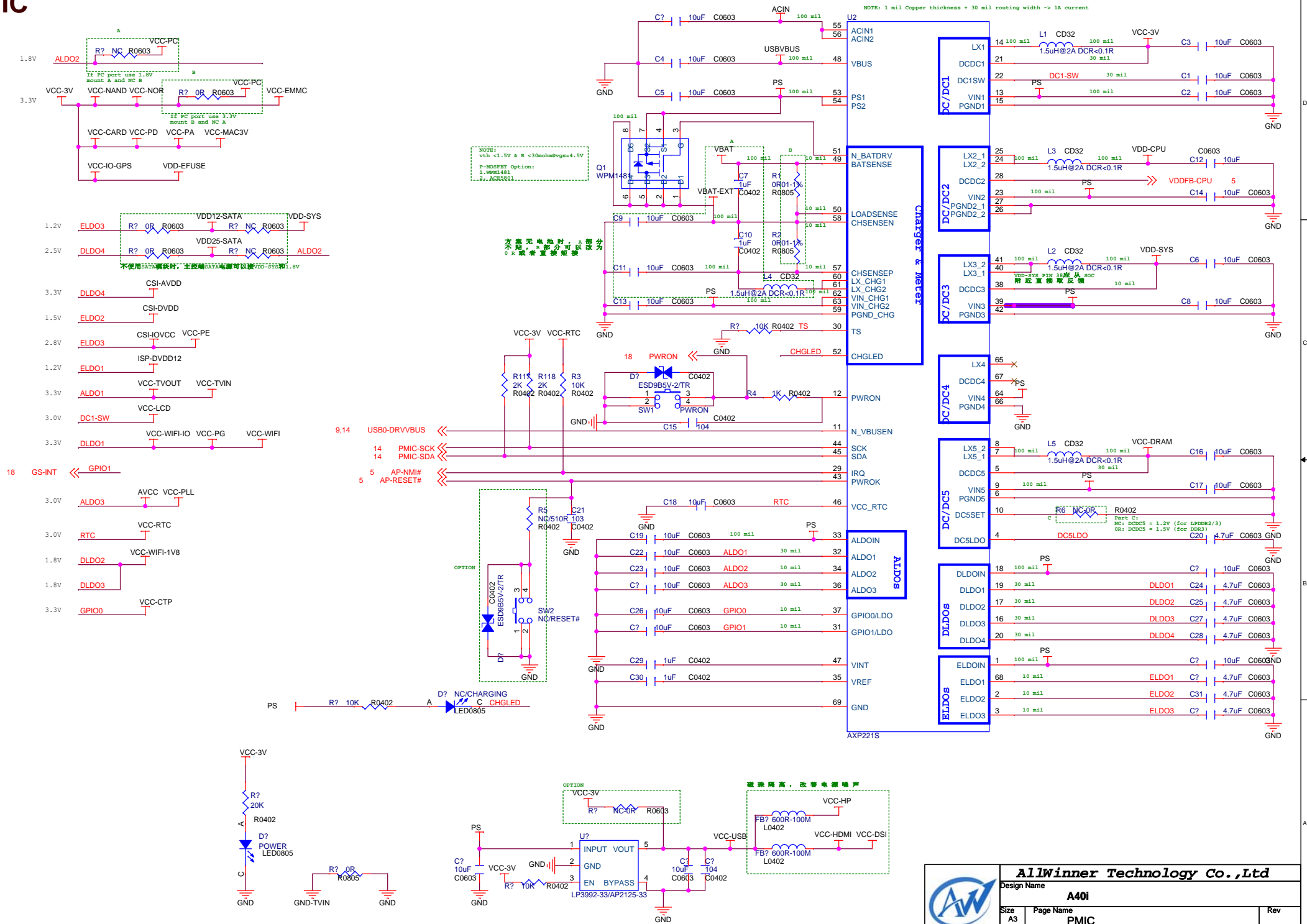


PMIC



54321

REVISION HISTORY

D

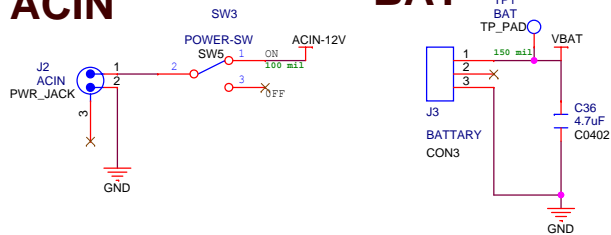
Schematics Index:

- C
- P01: REVISION HISTORY
 - P02: BLOCK
 - P03: POWER TREE
 - P04: GPIO ASSIGNMENT
 - P05: CPU
 - P06: DDR3_16X2
 - P07: PMIC
 - P08: POWER
 - P09: HDMI-USB0
 - P10: SATA-CARD0-CAMERA
 - P11: EMMC-NAND-NOR
 - P12: LVDS-RGB-MIPI DSI
 - P13: AUDIO
 - P14: MISC
 - P15: ETHERNET
 - P16: AV
 - P17: WIFI1
 - P18: SENSOR
 - P19: WIFI2
 - P20: 3G
 - P21: GPS
- B
- A

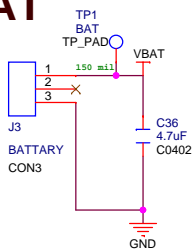
Revision	Description	Date	Drawn	Checked
	A40i_STD changelist.txt	2018-06-26		

POWER

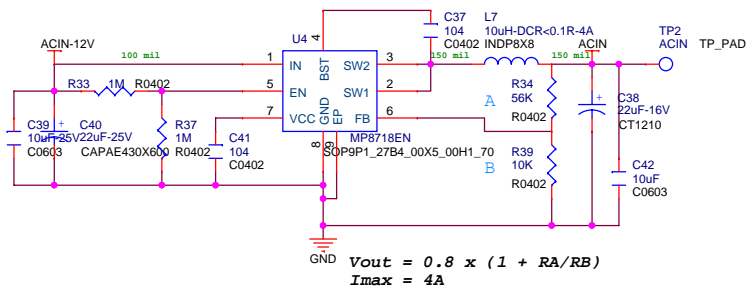
ACIN



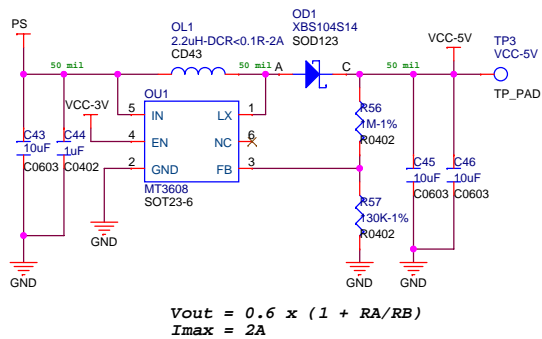
BAT



12V-5V



PS-5V



AllWinner Technology Co.,Ltd

Design Name

A40i

Size

Page Name

POWER

R

Date:

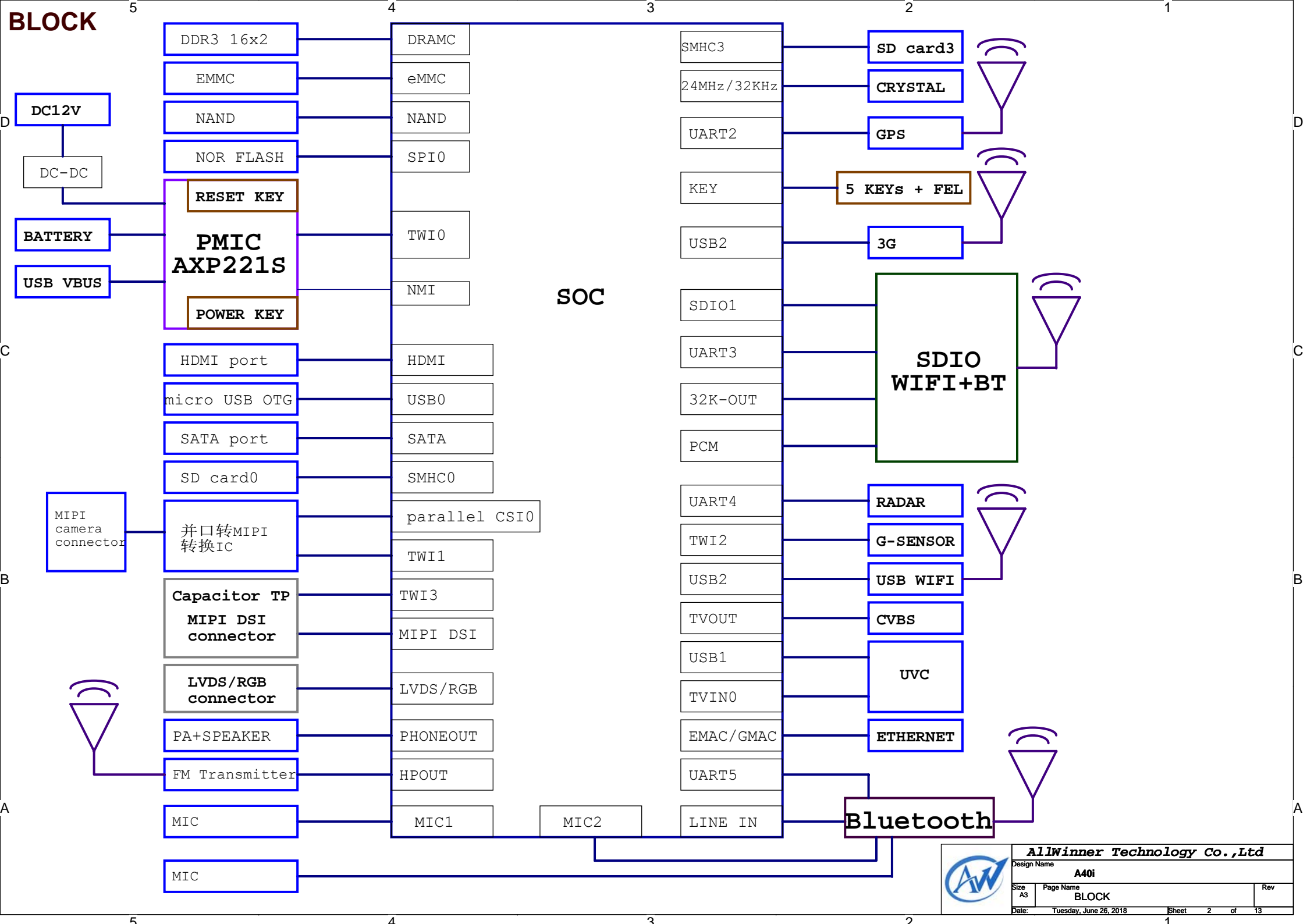
Tuesday, June 26, 2018

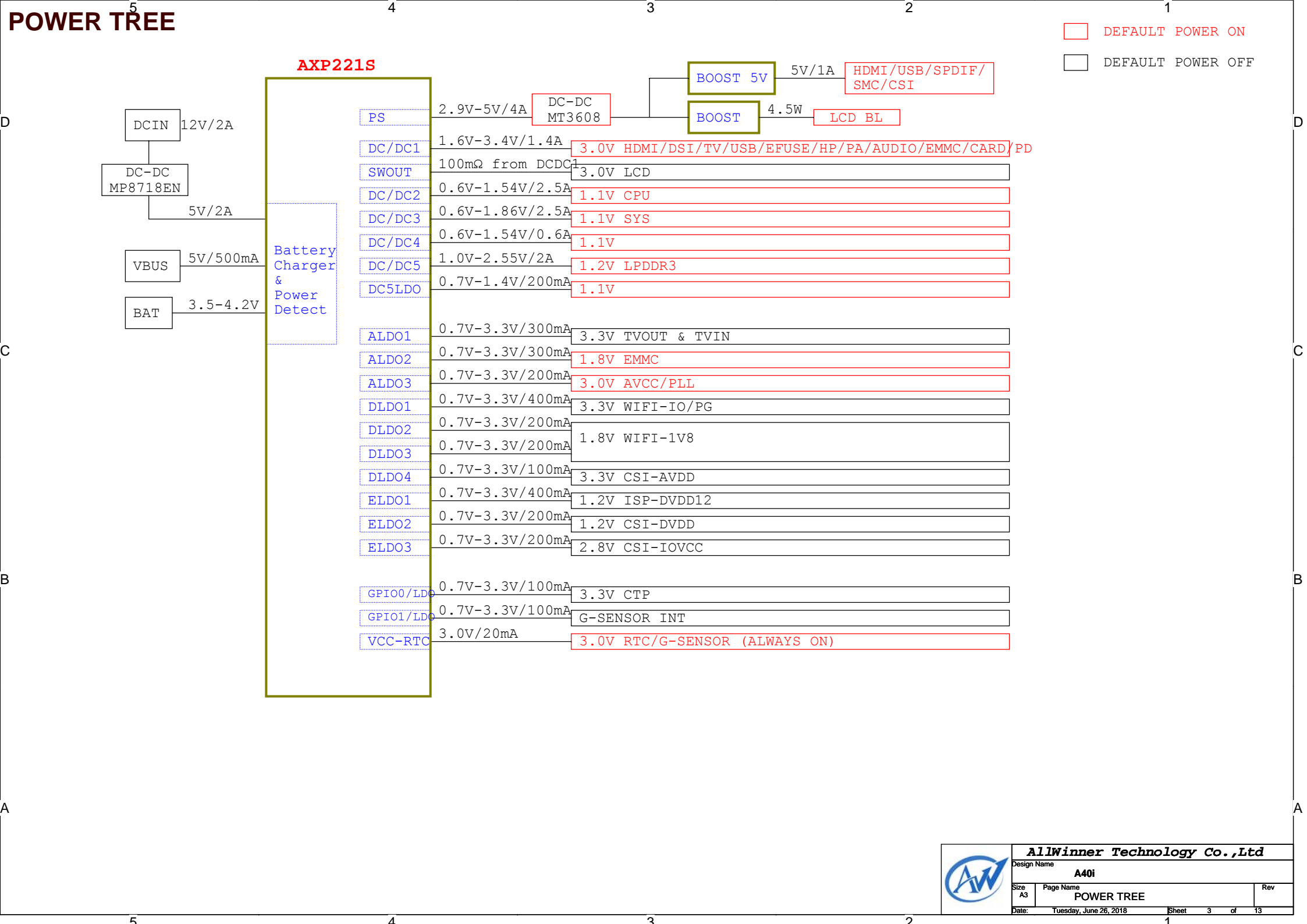
Shee

2

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8

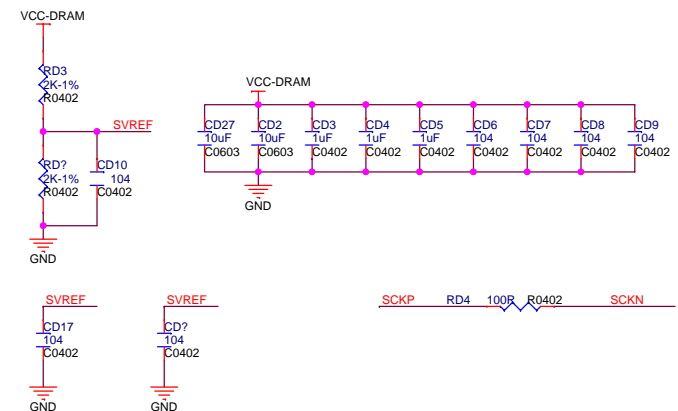
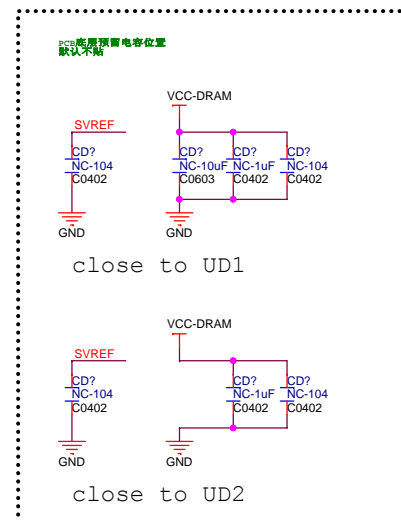
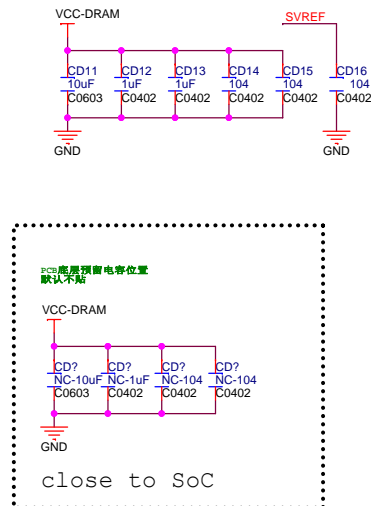
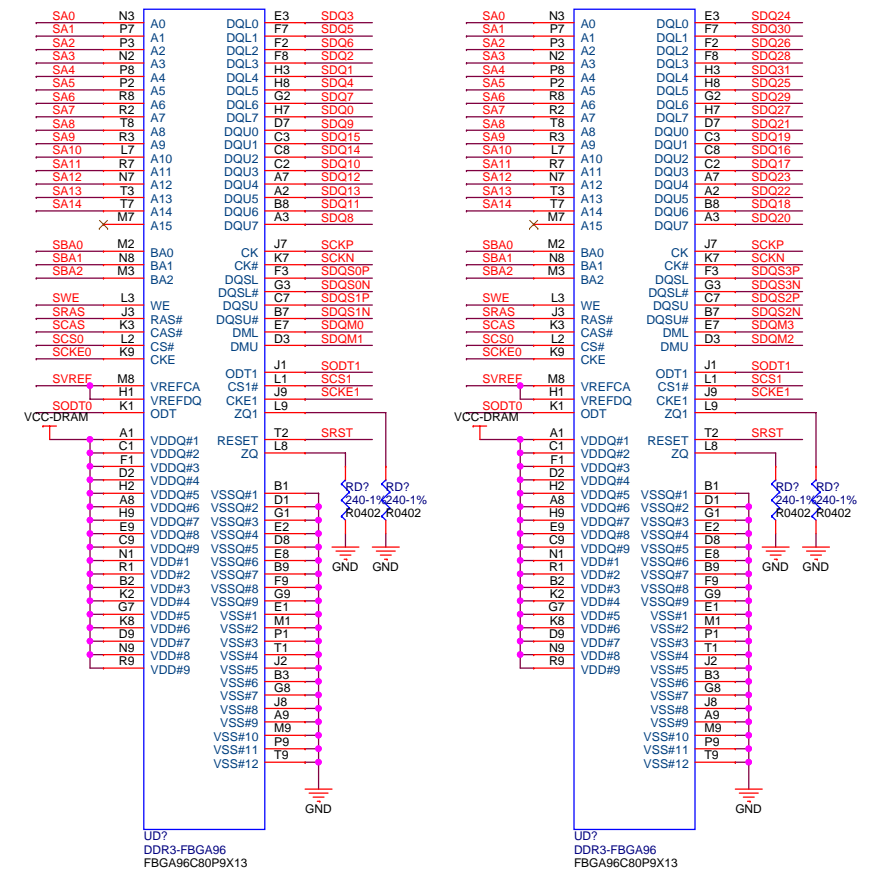
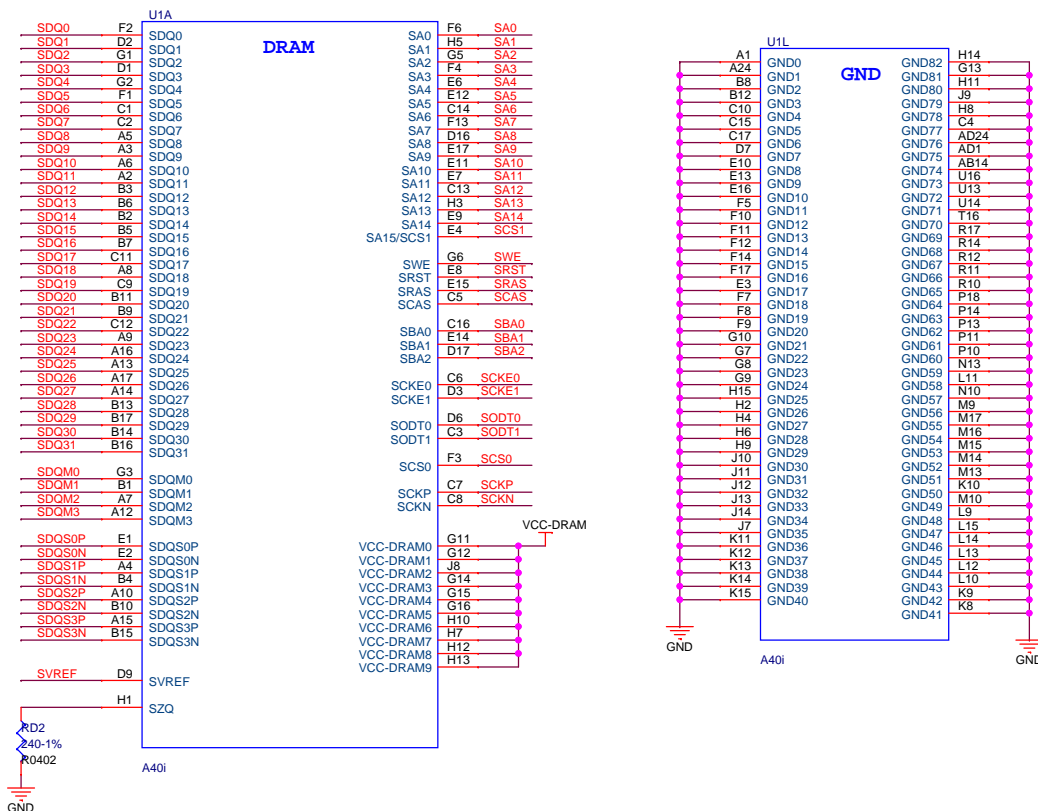




DEFAULT POWER ON

DEFAULT POWER OFF

DDR3_16X2



GPIO ASSIGNMENT

PIN	Define	CFG	Function
PA0	ERXD3	2	MAC
PA1	ERXD2	2	
PA2	ERXD1	2	
PA3	ERXD0	2	
PA4	ETXD3	2	
PA5	ETXD2	2	
PA6	ETXD1	2	
PA7	ETXD0	2	
PA8	ERXCLK	2	
PA9	ERXERR	2	
PA10	ERXDV	2	
PA11	EMDC	2	
PA12	EMDIO	2	
PA13	ETXEN	2	
PA14	ETXCK	2	
PA15	EGTXCLK	2	
PA16	ECLKIN	2	
PA17	TX-ER	2	

PIN	Define	CFG	Function
PB0	TWI-SCK	2	TWI0
PB1	TWI-SDA	2	
PB2	LCD-PWM	1	LCD
PB3			
PB4			
PB5	PA-SHDN	1	AUDIO
PB6	BCLK	2	PCM
PB7	SYNC	2	
PB8	DIN	2	
PB9	BL-EN	1	LCD
PB10	PIO0	1	
PB11			
PB12	DOUT	2	PCM
PB13			
PB14	TMS	3	JTAG
PB15	TCK	3	
PB16	TDO	3	
PB17	TDI	3	
PB18	SCK	2	TWI1
PB19	SDA	2	
PB20	SCK	2	TWI2
PB21	SDA	2	
PB22	TX	2	UART0
PB23	RX	2	

PIN	Define	CFG	Function
PC0	NWE	2	NAND
PC1	NALE	2	
PC2	NCLE	2	
PC3	NCE1	2	
PC4	NCE0	2	
PC5	DS	3	eMMC
PC6	CMD	3	
PC7	CLK		
PC8	D0	3	
PC9	D1	3	
PC10	D2	3	
PC11	D3	3	
PC12	D4	3	
PC13	D5	3	NAND
PC14	D6	3	
PC15	D7	3	
PC16	NWP	2	
PC17	NCE2	2	
PC18	NCE3	2	
PC19			
PC20			
PC21			
PC22			
PC23	SPI-CS	2	NOR
PC24	RST	3	eMMC

PIN	Define	CFG	Function
PD2	LCD-D2	2	LCD
PD3	LCD-D3	2	
PD4	LCD-D4	2	
PD5	LCD-D5	2	
PD6	LCD-D6	2	
PD7	LCD-D7	2	
PD10	LCD-D10	2	
PD11	LCD-D11	2	
PD12	LCD-D12	2	
PD13	LCD-D13	2	
PD14	LCD-D14	2	
PD15	LCD-D15	2	
PD18	LCD-D18	2	
PD19	LCD-D19	2	
PD20	LCD-D20	2	
PD21	LCD-D21	2	
PD22	LCD-D22	2	
PD23	LCD-D23	2	
PD24	LCD-CLK	2	
PD25	LCD-DE	2	
PD26	LCD-HSYNC	2	
PD27	LCD-VSYNC	2	

PIN	Define	CFG	Function
PE0	PCLK	3	CSI
PE1	MCLK	3	
PE2	HSYNC	3	
PE3	VSYNC	3	
PE4	D0	3	
PE5	D1	3	
PE6	D2	3	
PE7	D3	3	
PE8	D4	3	
PE9	D5	3	
PE10	D6	3	
PE11	D7	3	

PIN	Define	CFG	Function
PF0	D1	2	CARD0
PF1	D0	2	
PF2	CLK	2	
PF3	CMD	2	
PF4	D3	2	
PF5	D2	2	

PIN	Define	CFG	Function
PG0	WL-SDIO-CLK	4	WIFI+BT
PG1	WL-SDIO-CMD	4	
PG2	WL-SDIO-D0	4	
PG3	WL-SDIO-D1	4	
PG4	WL-SDIO-D2	4	
PG5	WL-SDIO-D3	4	
PG6	BT-UART-RX	4	
PG7	BT-UART-TX	4	
PG8	BT-UART-CTS	4	
PG9	BT-UART-RTS	4	
PG10			
PG11			

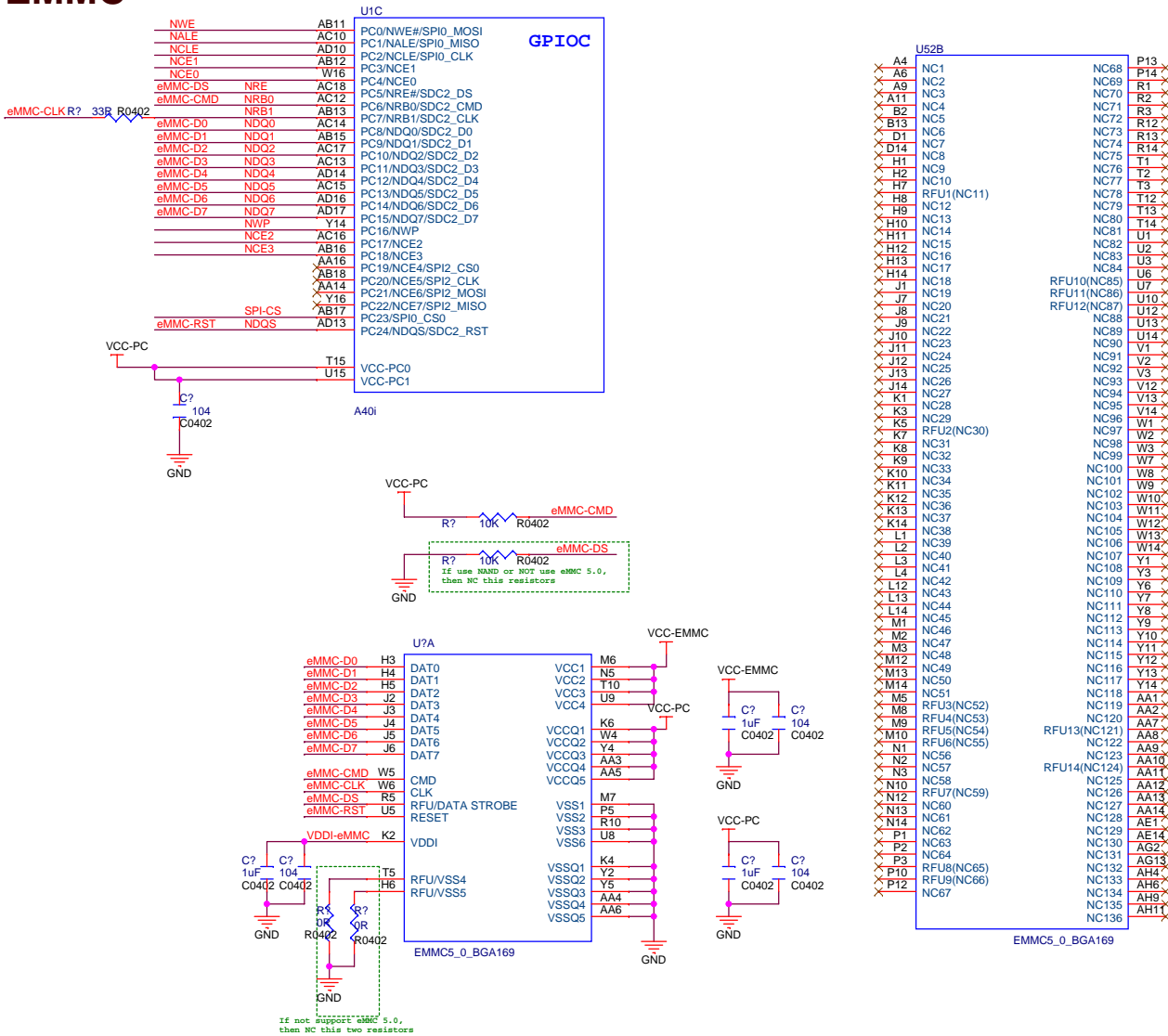
PIN	Define	CFG	Function
PH0	REV-DET-AP	0	ACC
PH1	ACC-INT	6	
PH2	LCD-PIO0	1	LCD
PH3	LCD-PIO1	1	
PH4	WL-REG-ON	1	WIFI
PH5	UART4-RX	3	RADAR
PH6	UART5-TX	3	OBD
PH7	UART5-RX	3	
PH8	AP-WAKE-BT	1	WIFI
PH9	WL-WAKE-AP	6	
PH10	RD-PWR	1	RADAR
PH11	BT-WAKE-AP	1	BT
PH12	BT-RST	1	
PH13			
PH14	CTP-RST	1	CTP
PH15	CTP-INT	6	
PH16	USB-ID	0	USB0
PH17	USB0-VBUSDET	0	
PH18	CSI-RST	1	CTP
PH19	GPS-STBY	1	GPS
PH20	GPS-RST	1	
PH21	FMT-PWR	1	FMT
PH22	AP-WAKE-3G	1	3G
PH23	3G-RST	1	
PH24	3G-ON-OFF	1	
PH25	BB-PWR-BAT	1	
PH26	BB-WAKE-AP	0	
PH27	CAM-EN	1	CAM

PIN	Define	CFG	Function
PI0	TWI3-SCK	2	TWI3
PI1	TWI3-SDA	2	
PI2	TWI4-SCK	2	TWI4
PI3	TWI4-SDA	2	
PI4	CMD	2	SDC3
PI5	CLK	2	
PI6	D0	2	
PI7	D1	2	
PI8	D2	2	UART5
PI9	D3	2	
PI10	UART5-TX	3	WIFI
PI11	UART5-RX	3	
PI12	32K OUT	4	USB0
PI13	DRVVBUS	1	
PI14	CARD0PWR	1	
PI15	CARD3PWR	1	
PI16	UART2-CTS	3	UART2
PI17	UART2-RTS	3	
PI18	UART2-RX	3	
PI19	UART2-TX	3	
PI20	SDC0-DET	0	SDC0
PI21	UART7-RX	3	UART7

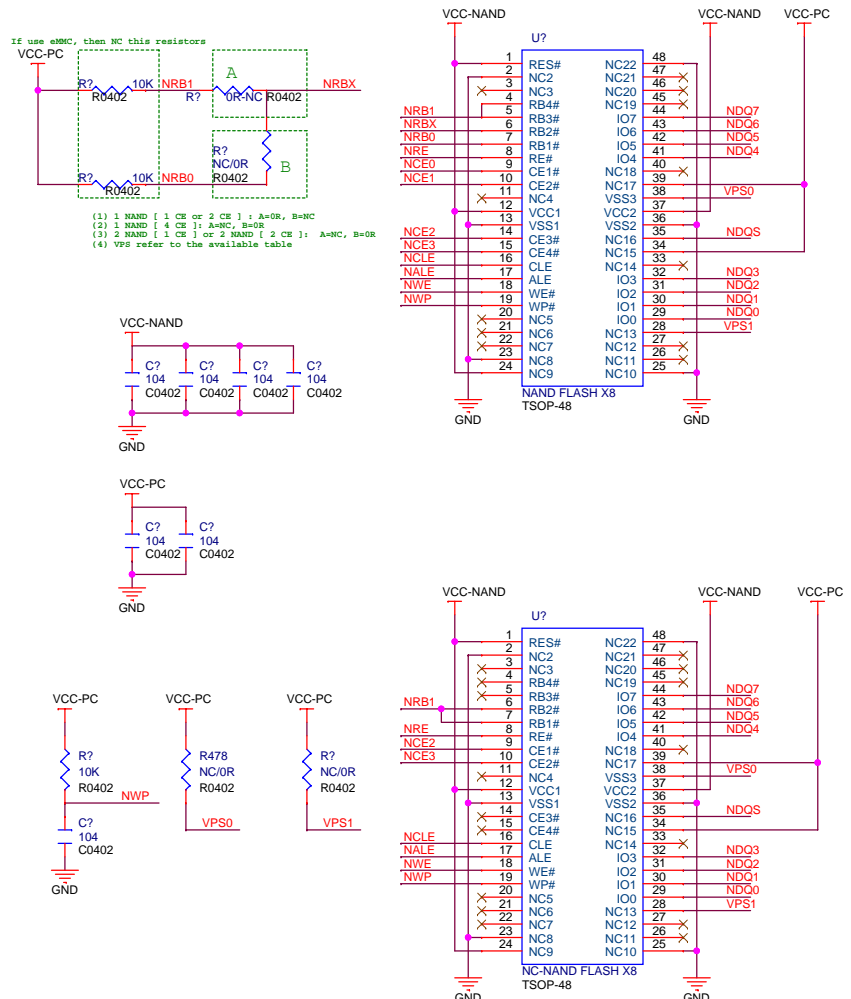
AXP221S

PIN	Define	Function
GPIO0	CTP	CTP
GPIO1	GS-INT	G-SENSOR

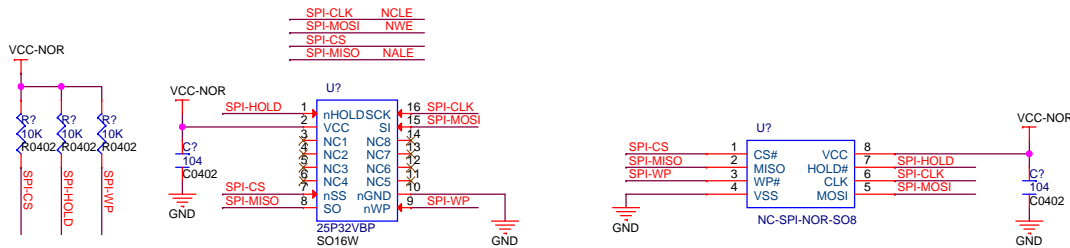
EMMC



NAND



NOR



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Design Name

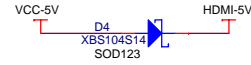
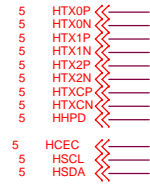
A40i

Size A3	Page Name EMMC-NAND-NOR
------------	----------------------------

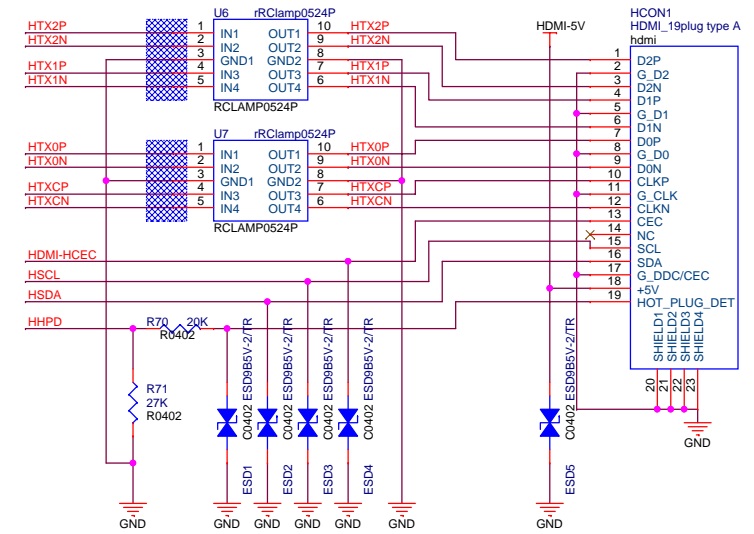
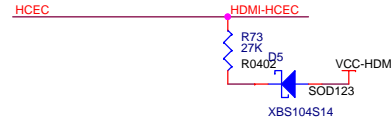
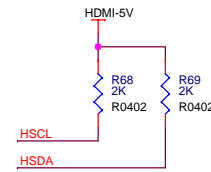
Date: Tuesday, June 26, 2018 Sheet 5 of 8

	1
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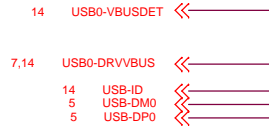
HDMI



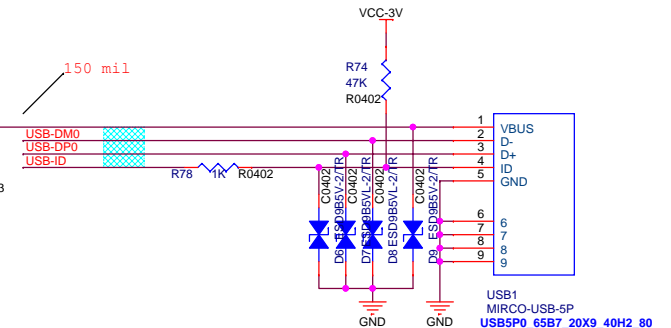
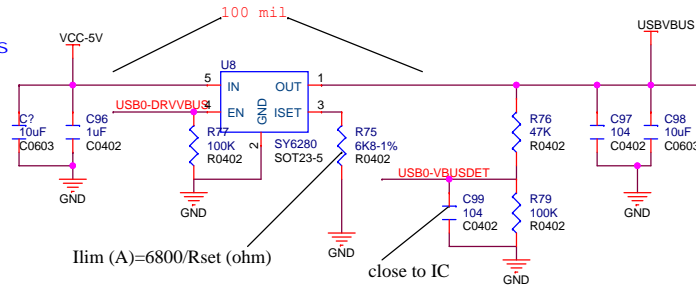
Differential pairs
Z0= 100 ohm



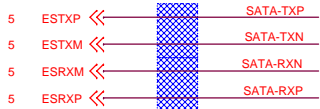
USB



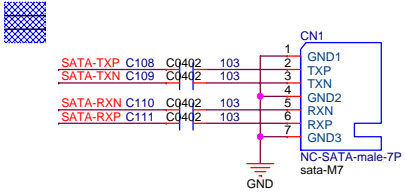
Differential pairs
Z0= 90 ohm



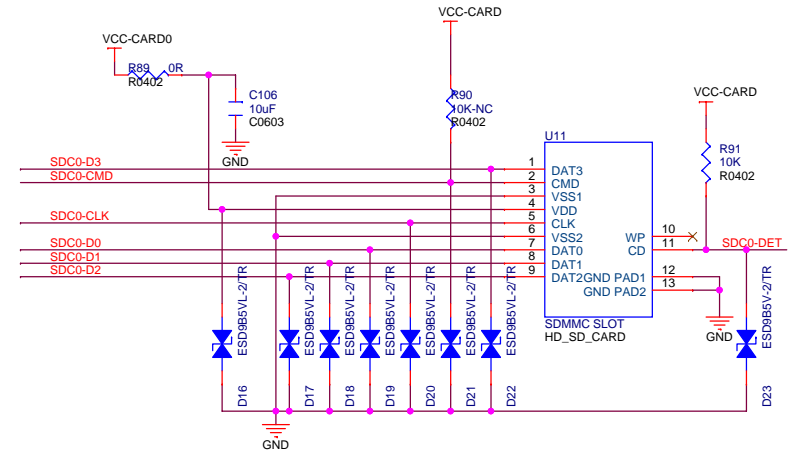
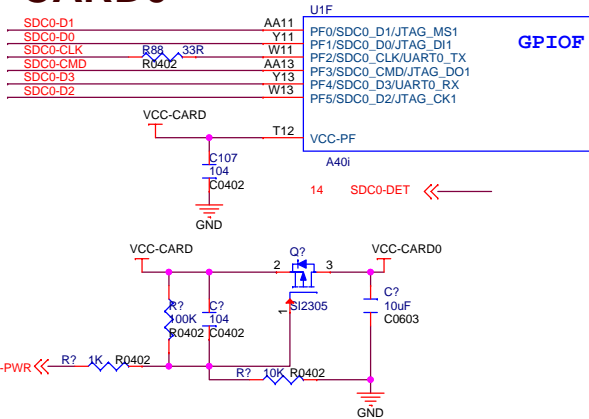
SATA



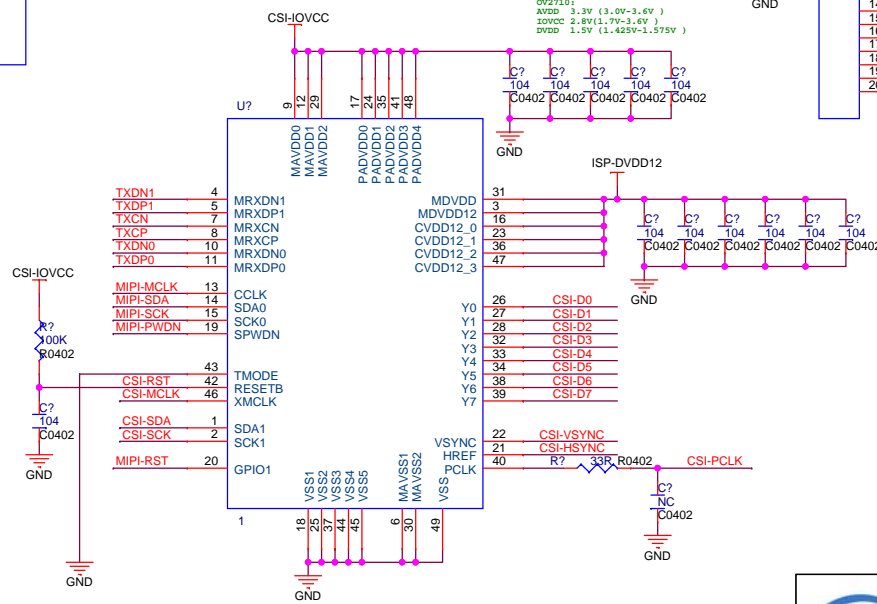
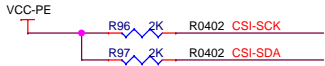
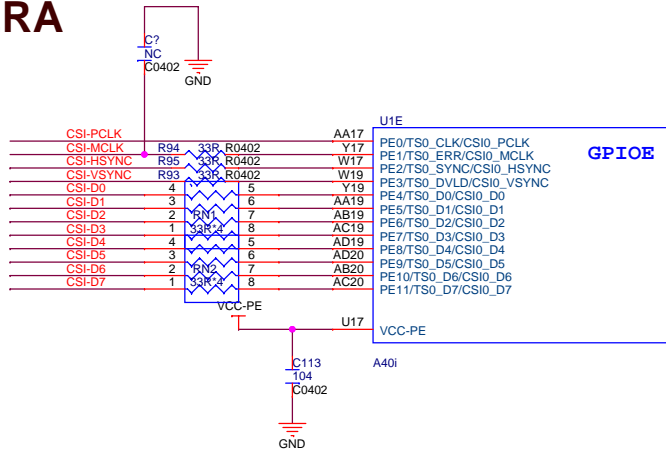
Differential pairs
Z0= 100 ohm



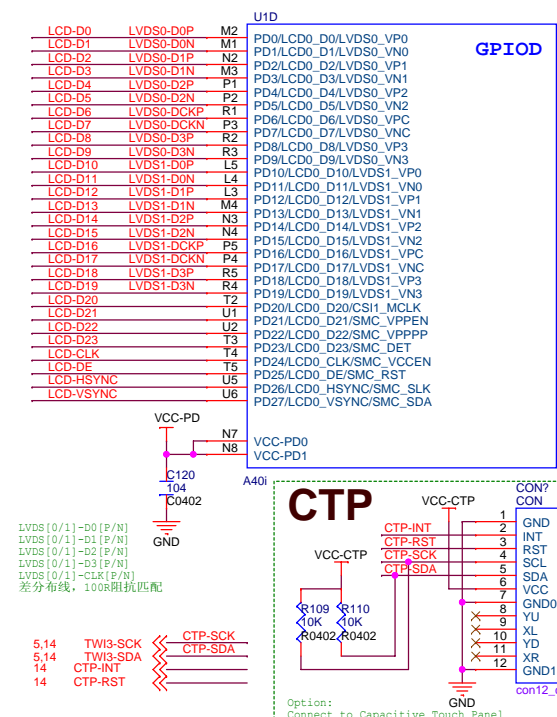
CARD0



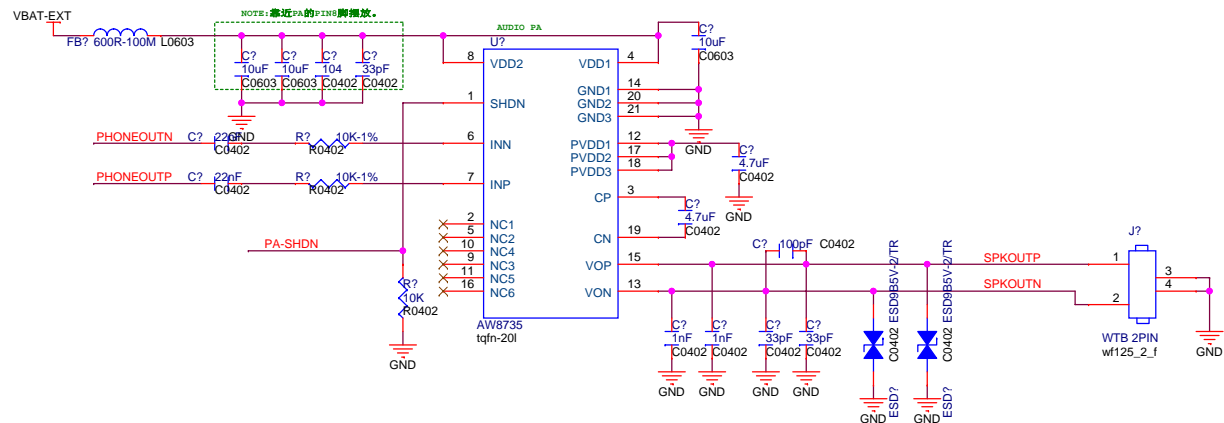
CAMERA



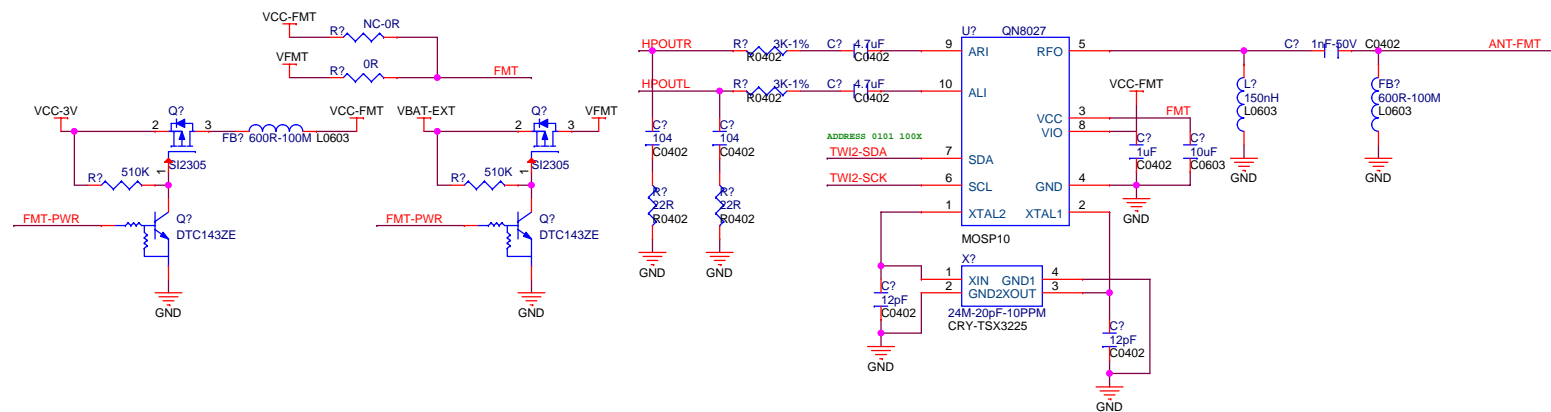
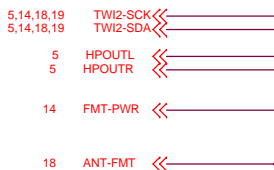
DISPLAY



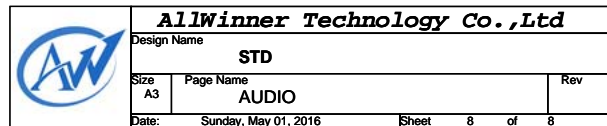
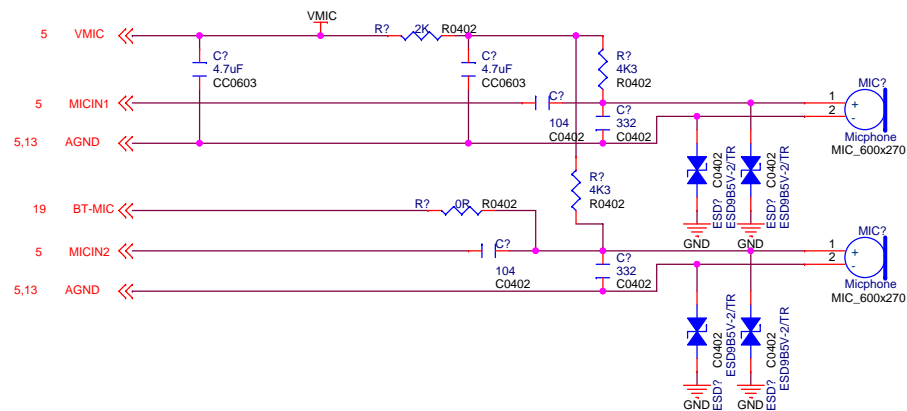
SPEAKER



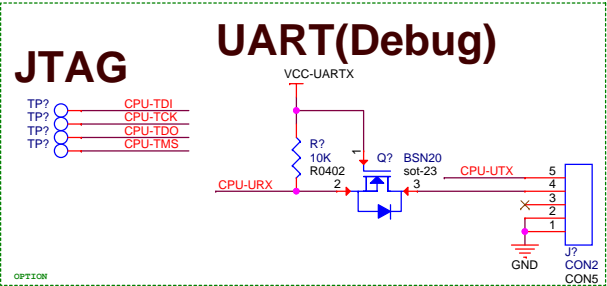
FM TRANSMIT



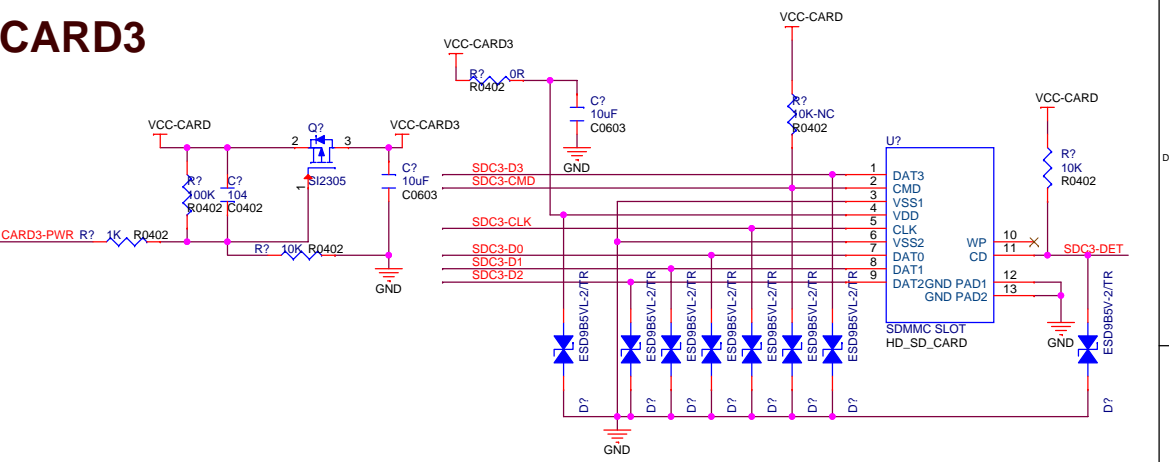
MIC



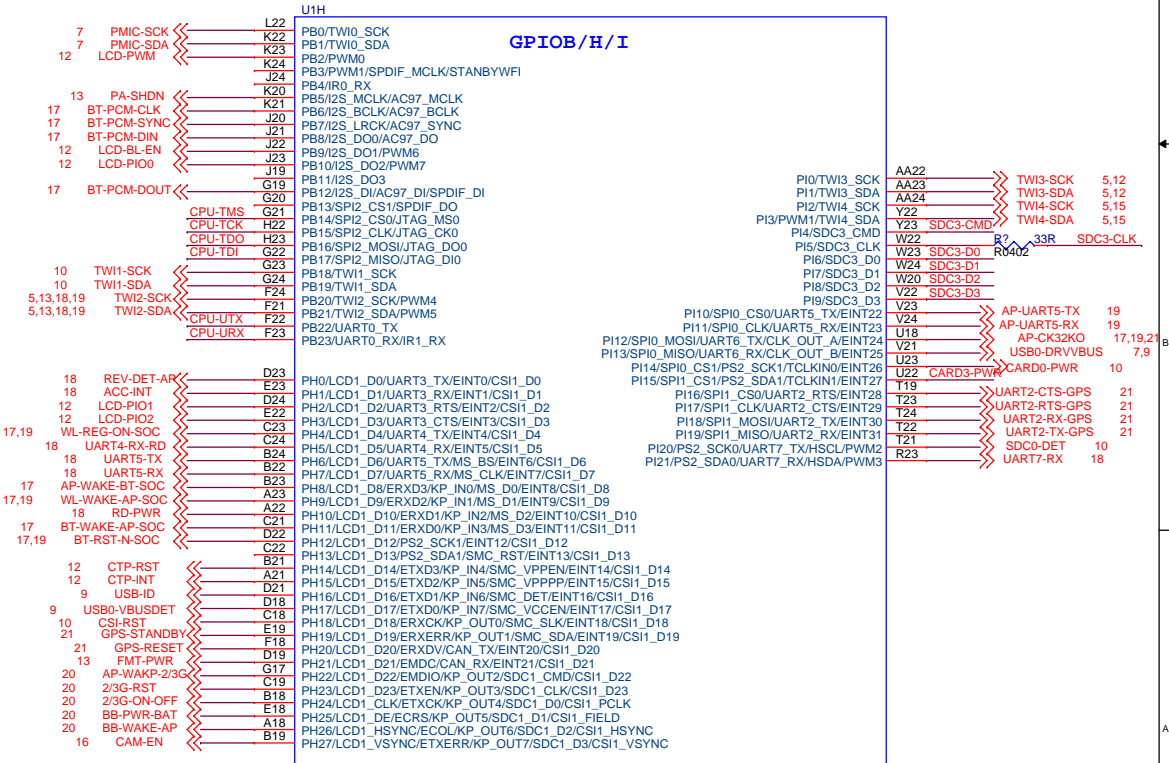
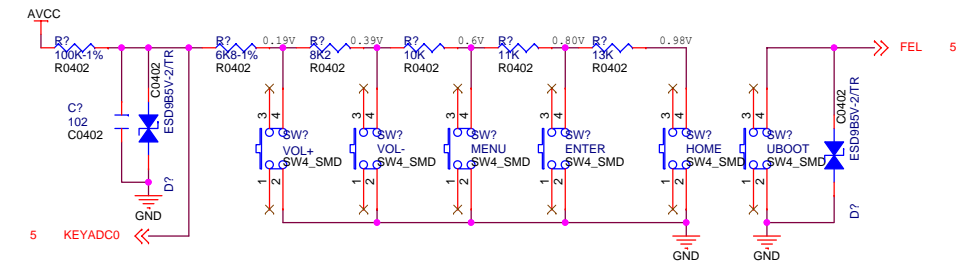
MISC



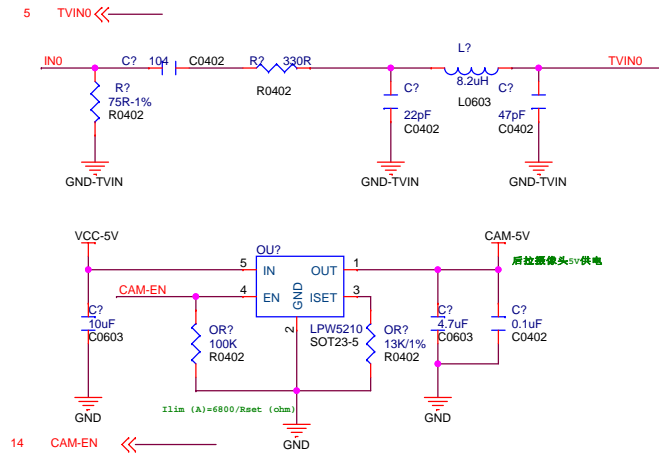
CARD3



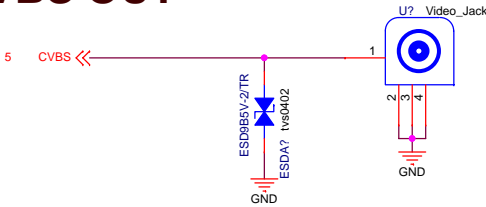
KEY



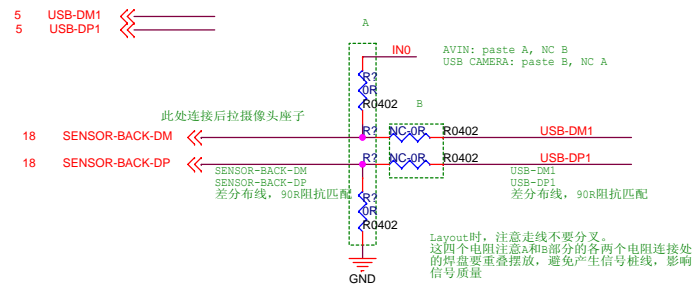
Sensor-back TVIN



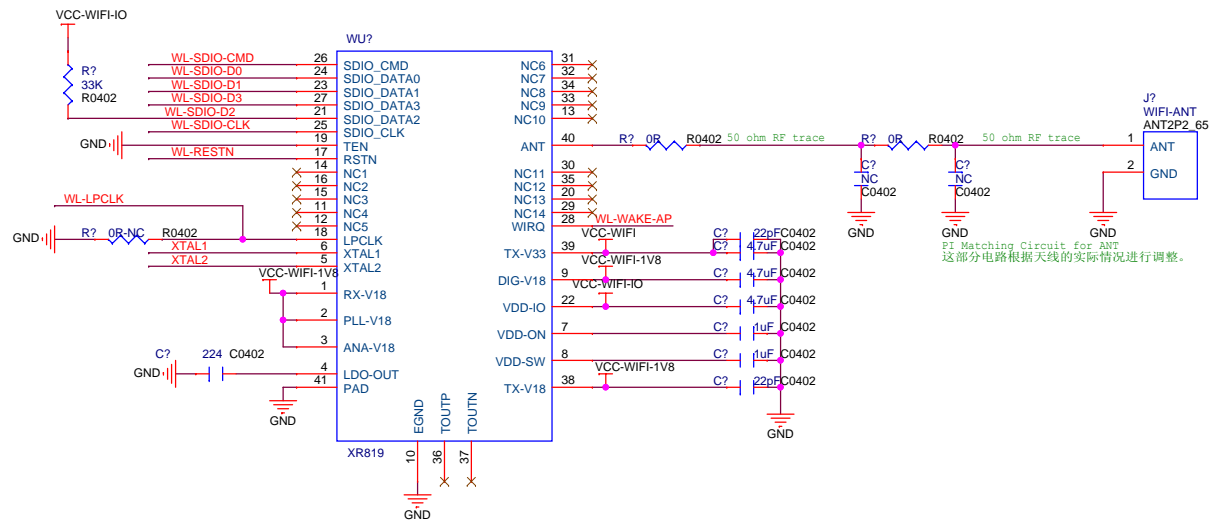
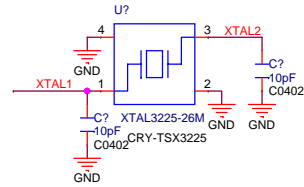
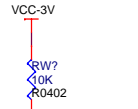
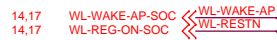
CVBS OUT



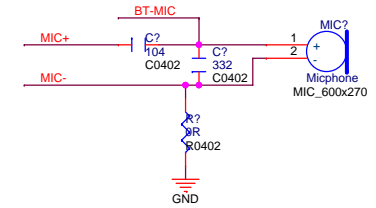
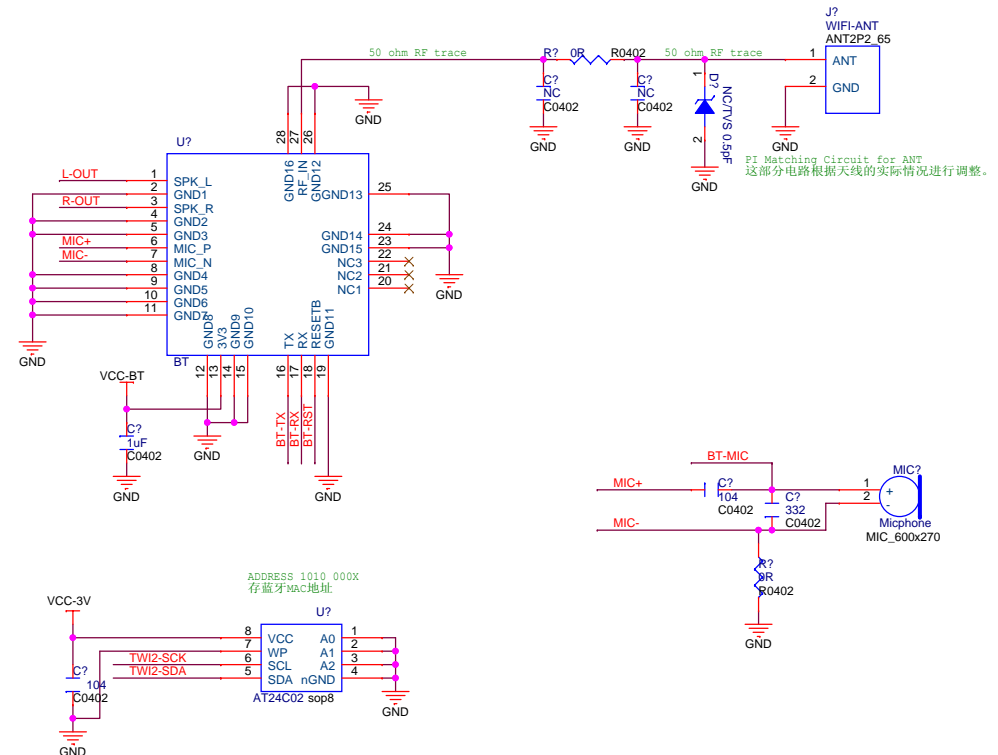
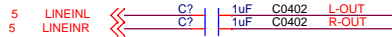
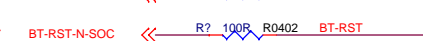
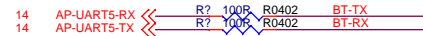
Sensor-back USB1



XR819 on board



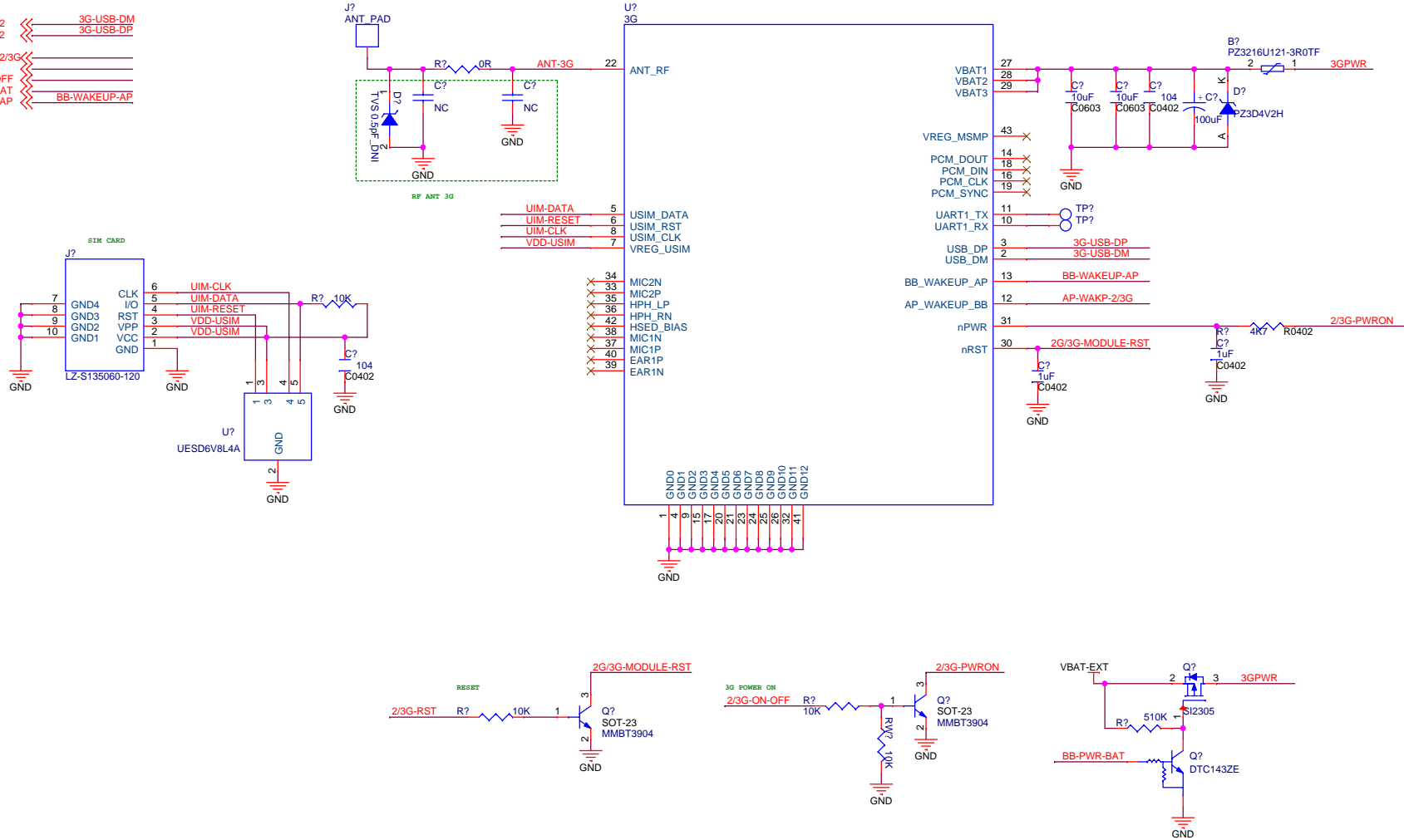
BT



3G Module

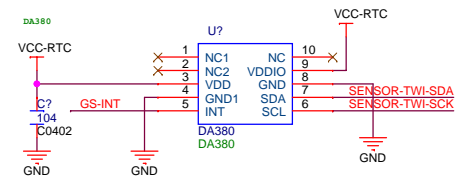
5,17 USB-DM2 3G-USB-DM
5,17 USB-DP2 3G-USB-DP

14 AP-WAKP-2/3G
14 2/3G-RST
14 2/3G-ON-OFF
14 BB-PWR-BAT
14 BB-WAKEUP-AP

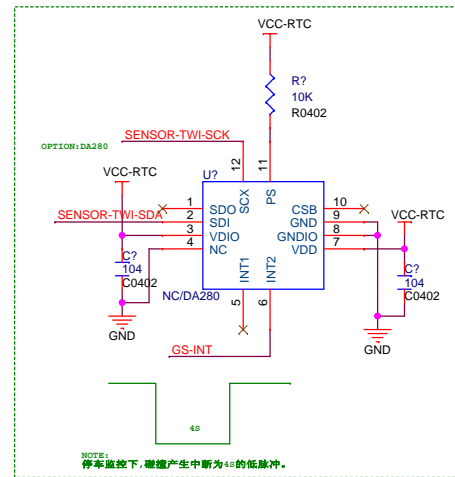


G-SENSOR

5,13,14,19 TWI2-SCK << SENSOR-TWI-SCK
5,13,14,19 TWI2-SDA << SENSOR-TWI-SDA
7 GS-INT <<



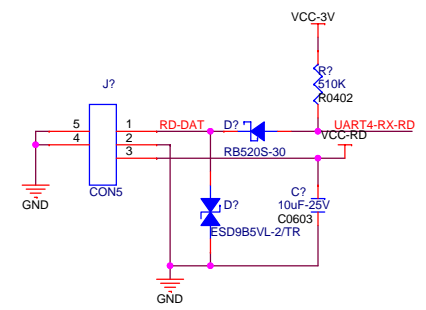
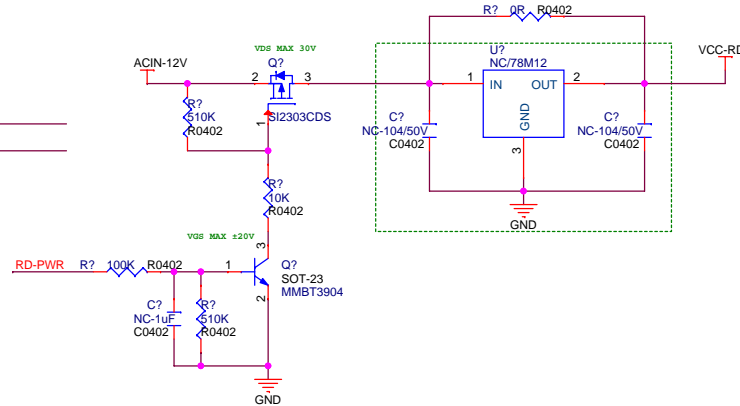
NOTE:
停车监控下,碰撞产生中断为4μs的低脉冲。



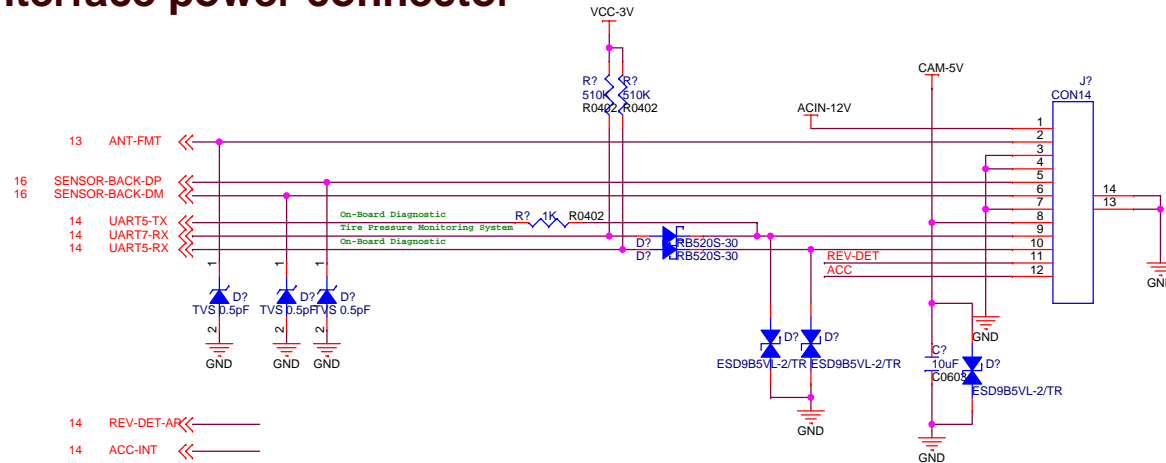
NOTE:
停车监控下,碰撞产生中断为4μs的低脉冲。

Radar

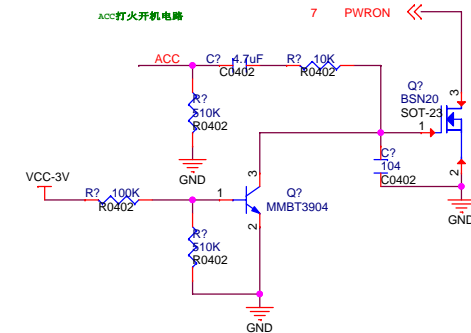
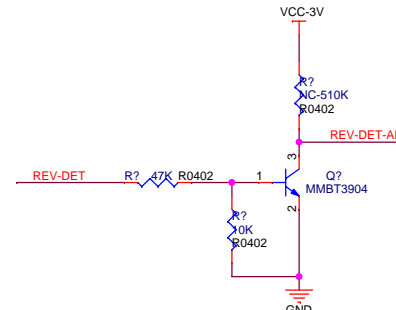
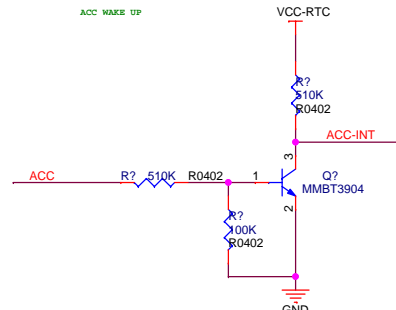
14 RD-PWR <<
14 UART4-RX-RD <<



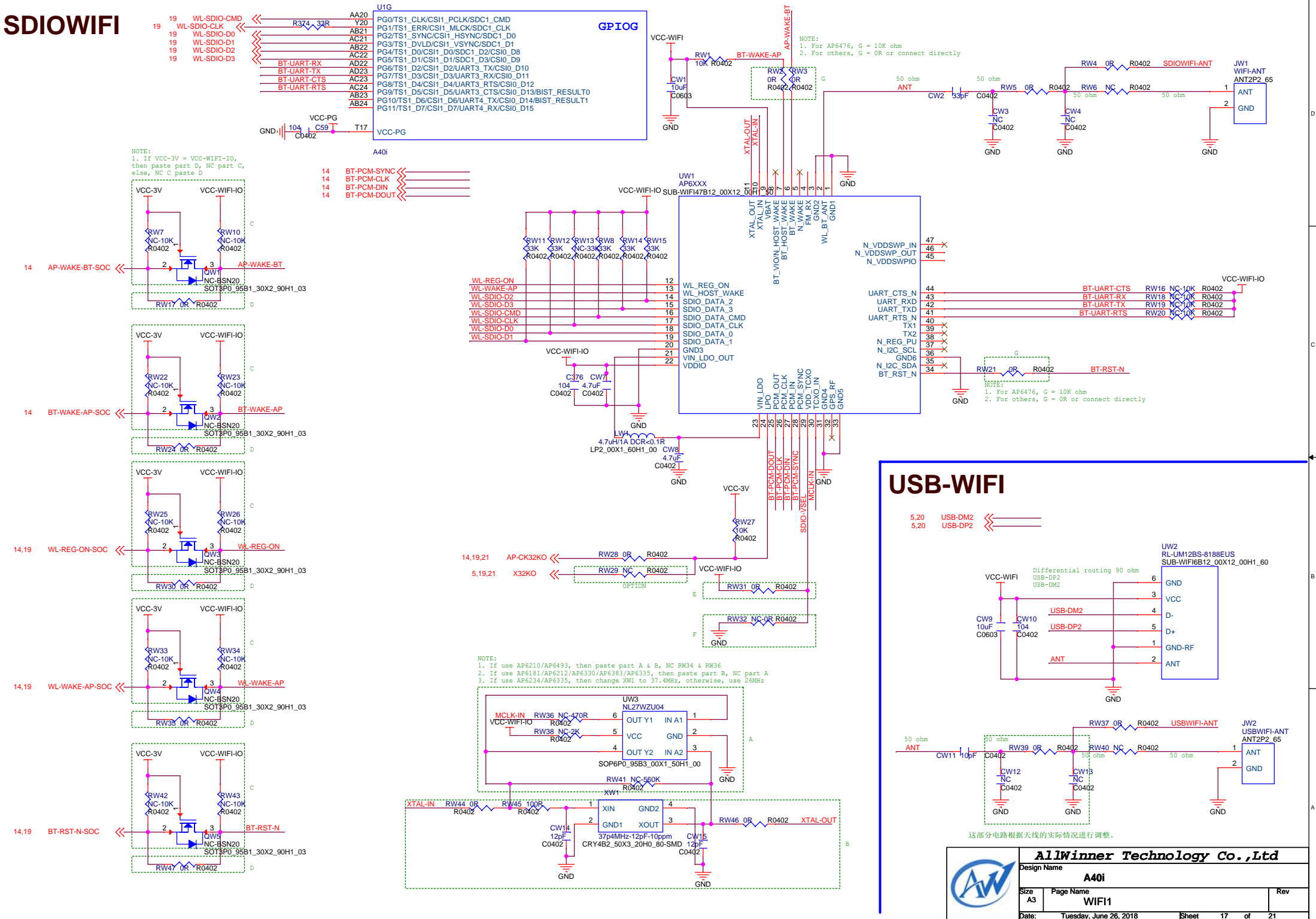
Interface power connector



14 REV-DET-AR <<
14 ACC-INT <<



SDIOWIFI



GPS

