The following sequence is given:

V dw 23456

....................

add ebx, v

sub ebx, 6

mov eax, ebx

Write one single instruction having a similar effect with the above sequence

LEA EAX,[EBX+V-6]

The following sequence is given:

add ebx, v

sub ebx, 6

mov eax, [ebx]

Write one single instruction having a similar effect with the above sequence

MOV EAX,[EBX+V-6]

The following sequence is given:

add ebx, [v]

sub ebx, 6

mov eax, ebx

Write one single instruction having a similar effect with the above sequence

WE CANNOT !!! Because all three of them refers to CONTENTS not to addresses !

The following sequence is given:

Xor edx, edx ; edx=0

Mov dl, 0fh ; EDX = 00 00 00 0f

Write an instruction or a sequence of instructions for performing the multiplication (EDX:EAX) \* 4

„SHL EDX:EAX, 2”

Shl eax,1 ; the bit 31 of EAX will be transferred into CF !!!!

Rcl edx,1 ; the value from CF is put as bit 0 into EDX

Shl eax,1 ; the bit 31 of EAX will be transferred into CF !!!!

Rcl edx,1 ; the value from CF is put as bit 0 into EDX

„Shl edx:eax, 2”

Task : the first 2 bits from the initial EAX must become the last 2 bits from EDX

Shl eax,1

Rcl edx,1

Shl eax,1

Rcl edx,1

shl eax, 1 ; bit 31 from EAX goes into CF

rcl edx, 1 ; the value from CF becomes the bit 0 from EDX

shl eax, 1 ; bit 31 from EAX goes into CF

rcl edx, 1 ; the value from CF becomes the bit 0 from EDX