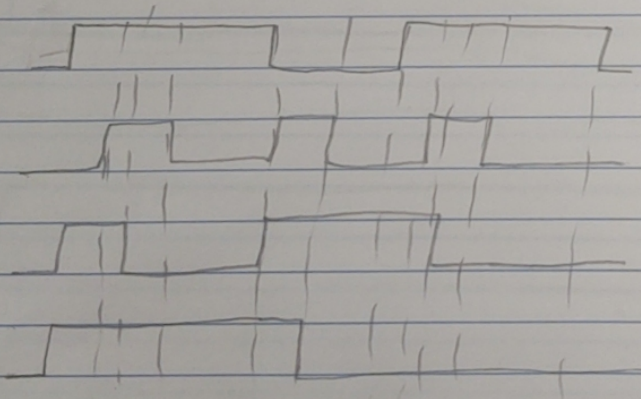
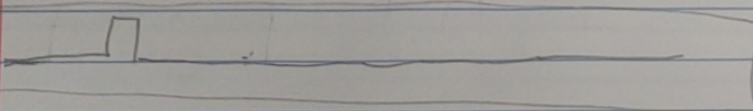


Justin Huckins
COSC330 9/23/21

section 3-2 #8

8.

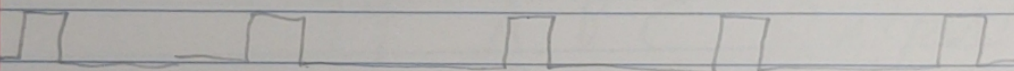
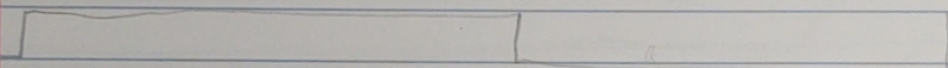
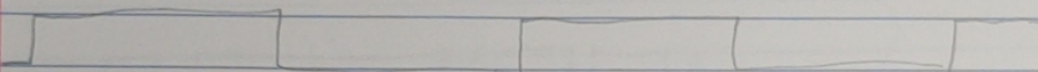
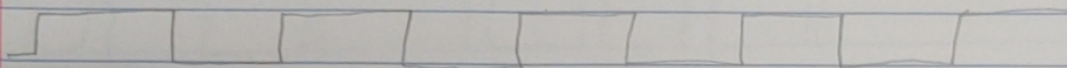
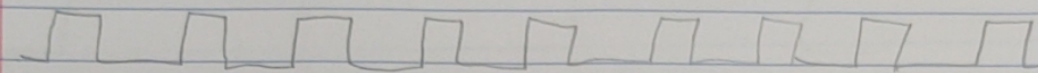


Output 

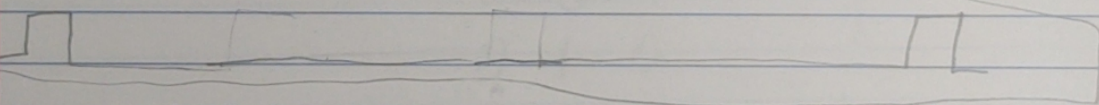
The output signal trace is a single horizontal line that starts at a low level, rises to a high level at the first vertical line, and remains high until the end of the diagram.

Section 3-3 #14

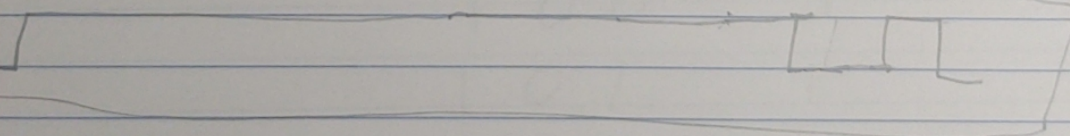
14,



Output
AND

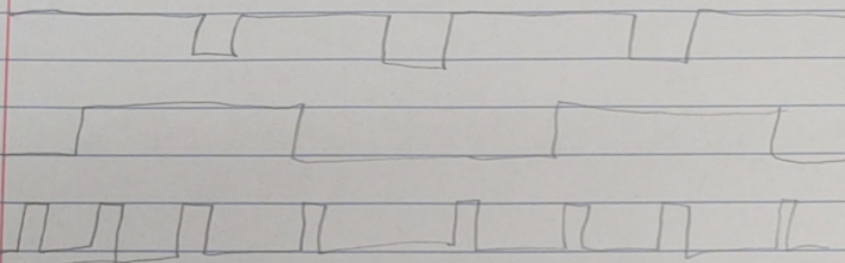


Output
OR



Section 3-4 #18

18.



Output
NAND

TT	A	B	C	output
	0	0	0	1
	0	0	1	1
	0	1	0	1
	0	1	1	1
	1	0	0	1
	1	0	1	1
	1	1	0	1
	1	1	1	0