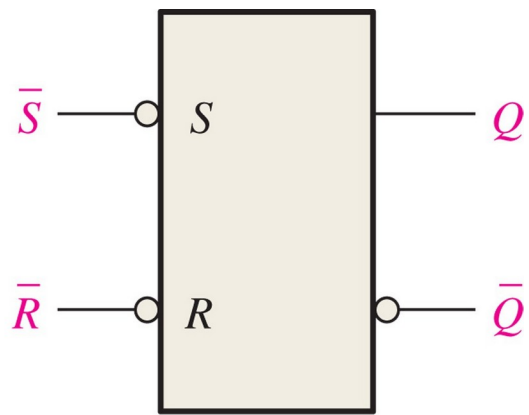
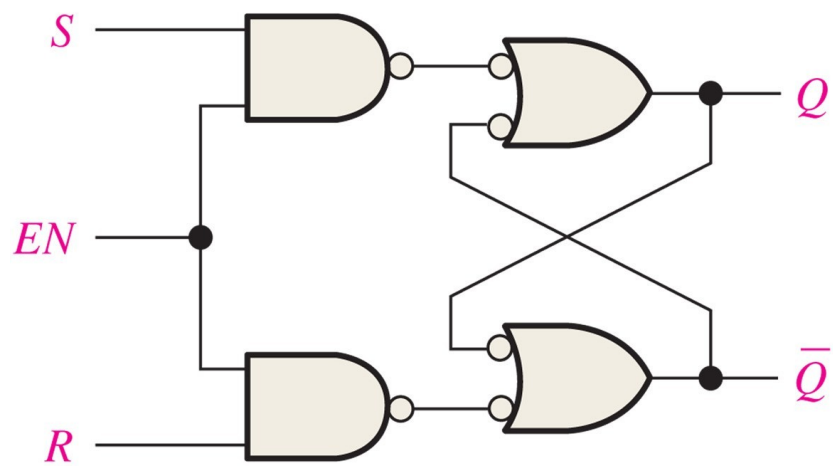


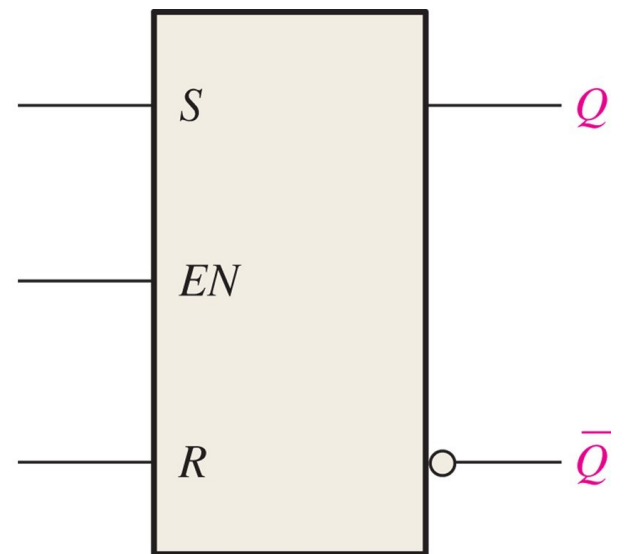
(a) Active-HIGH input  
S-R latch



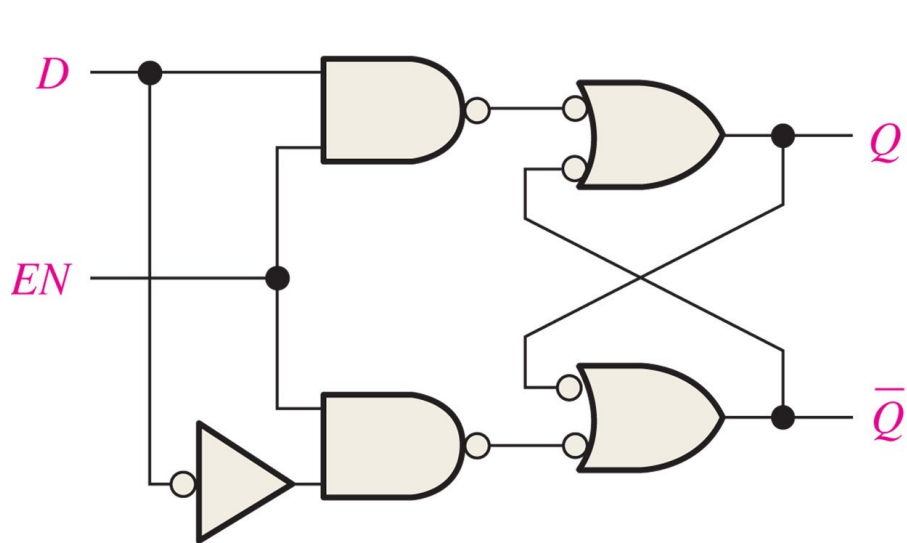
(b) Active-LOW input  
 $\bar{S}$ - $\bar{R}$  latch



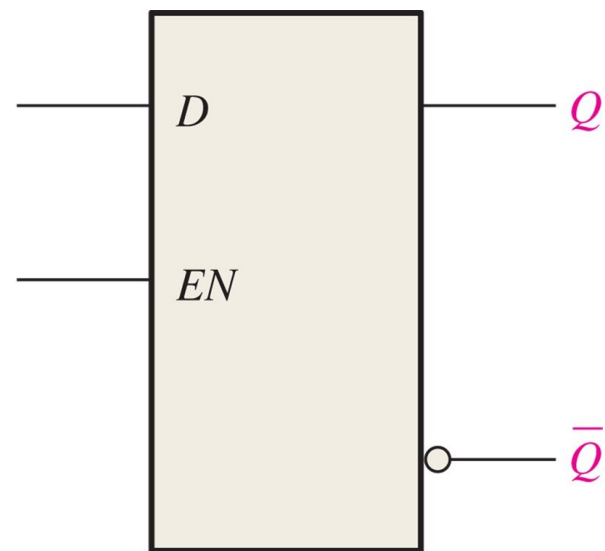
(a) Logic diagram



(b) Logic symbol



(a) Logic diagram



(b) Logic symbol