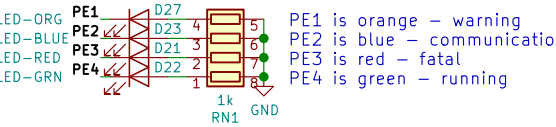
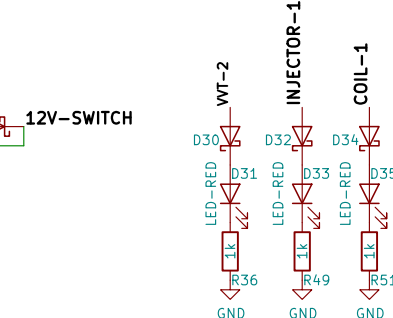
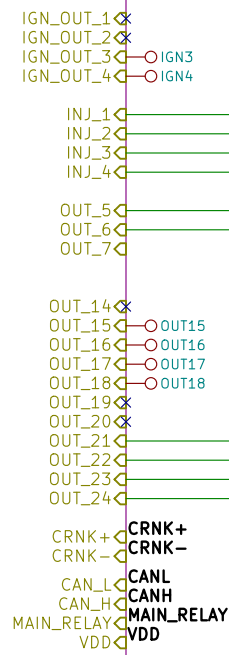
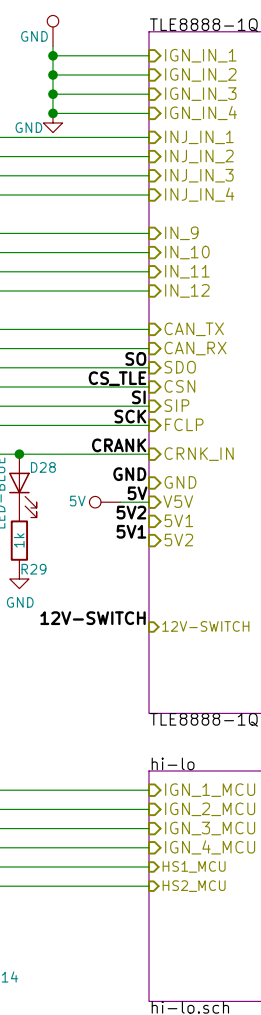
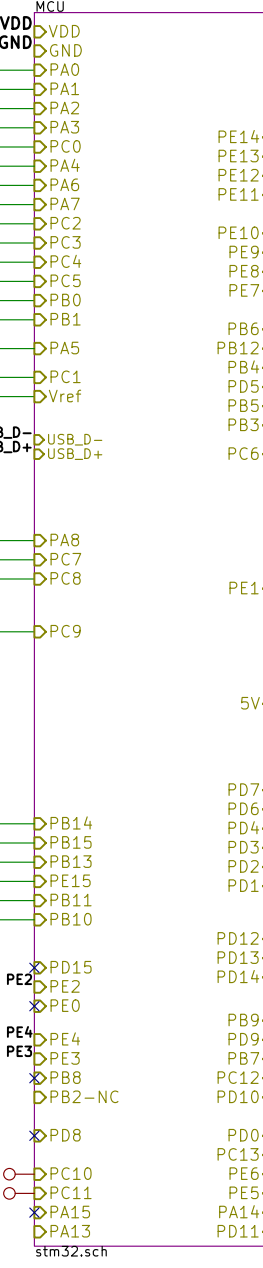
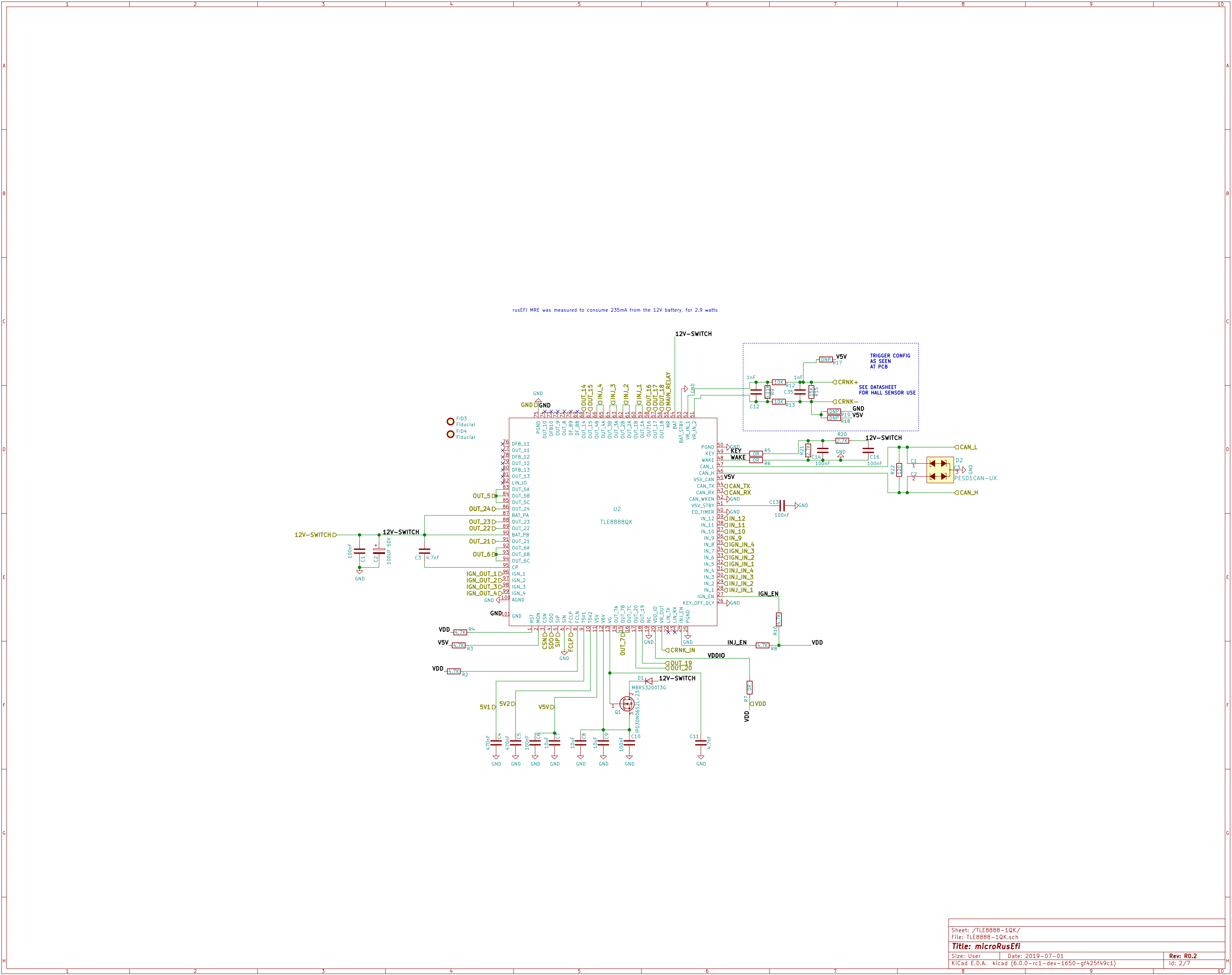
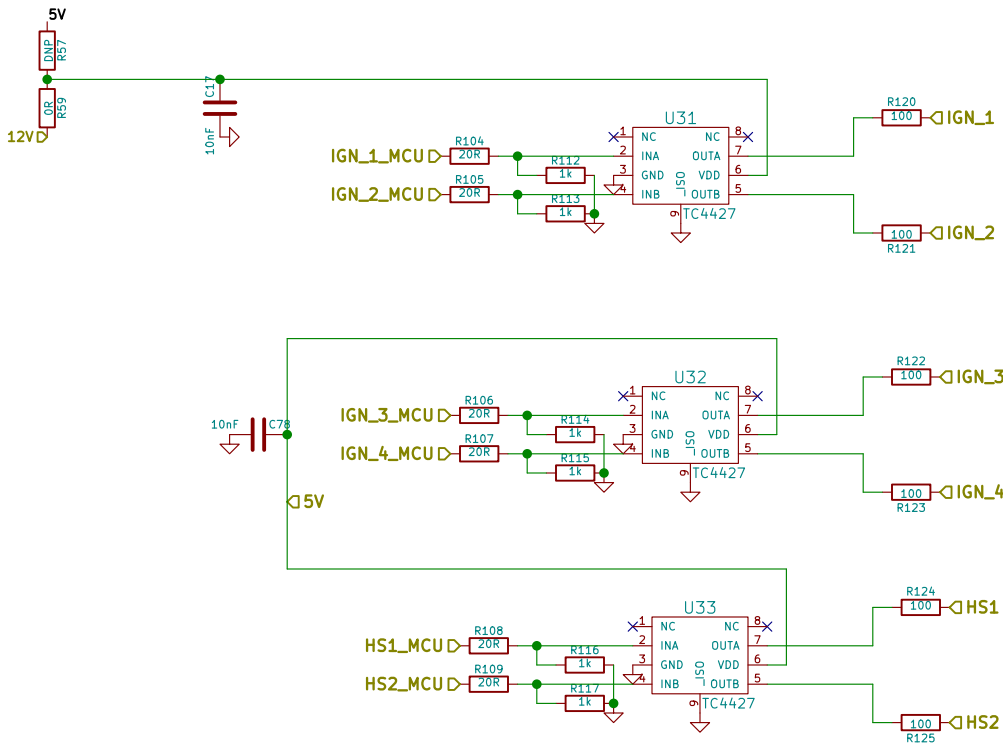


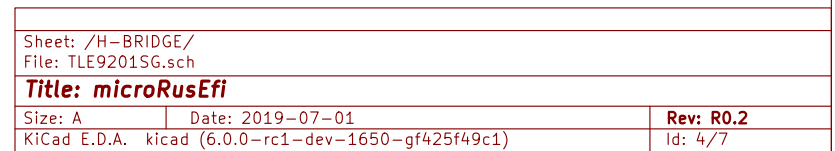
GP OUT 5 AND 6 ARE HIGH SIDE DRIVEN

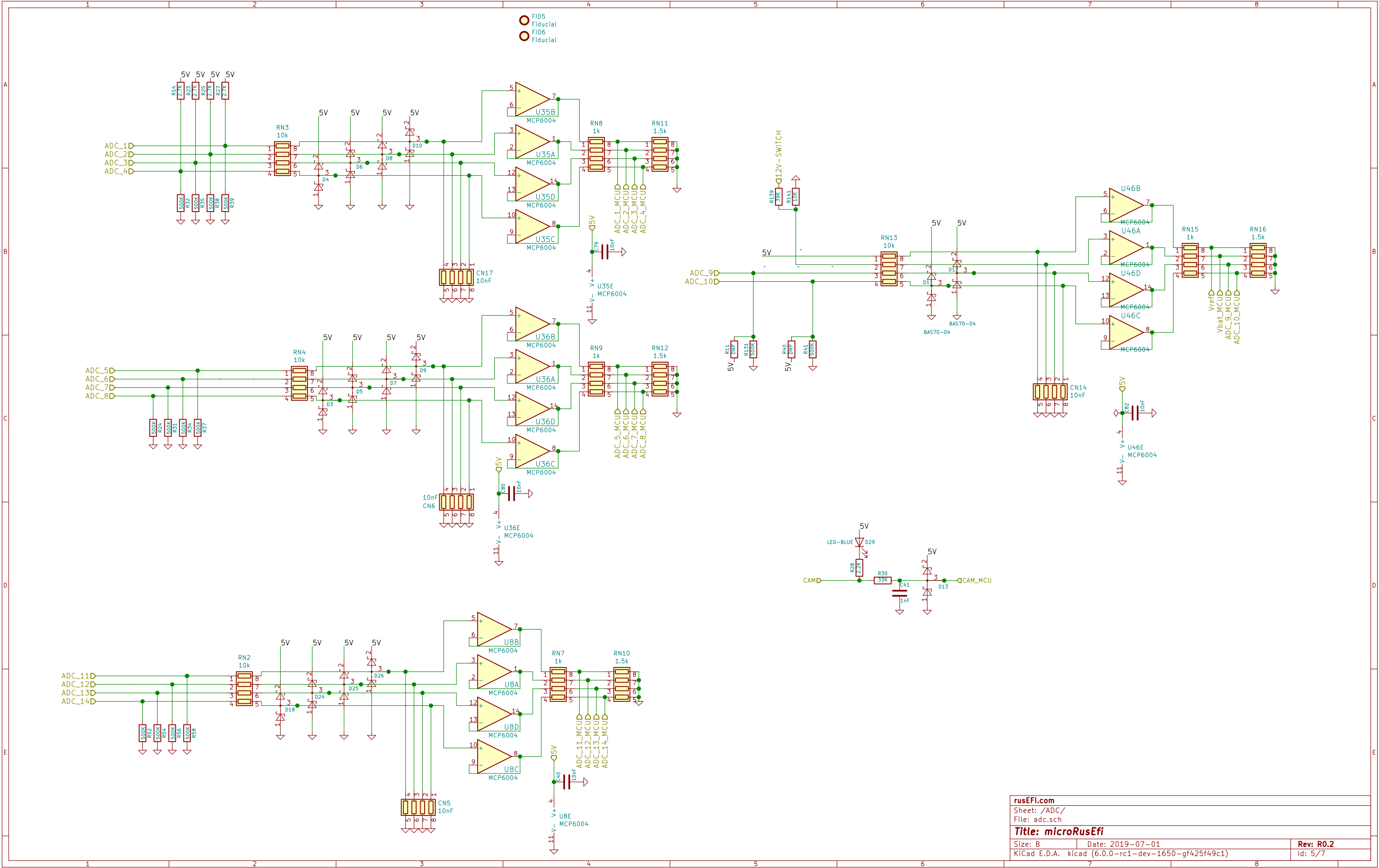


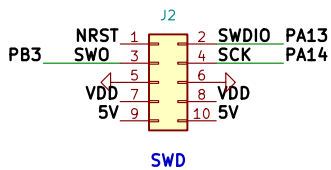
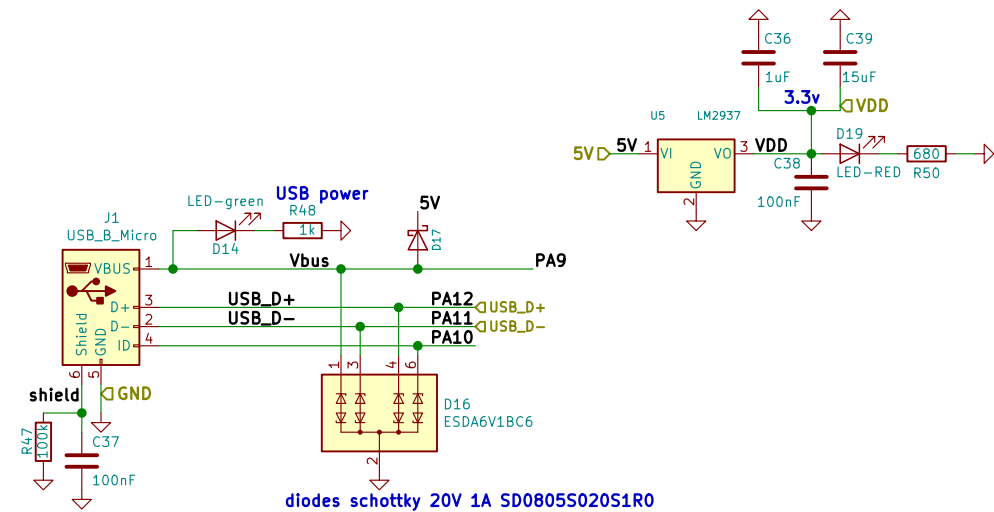
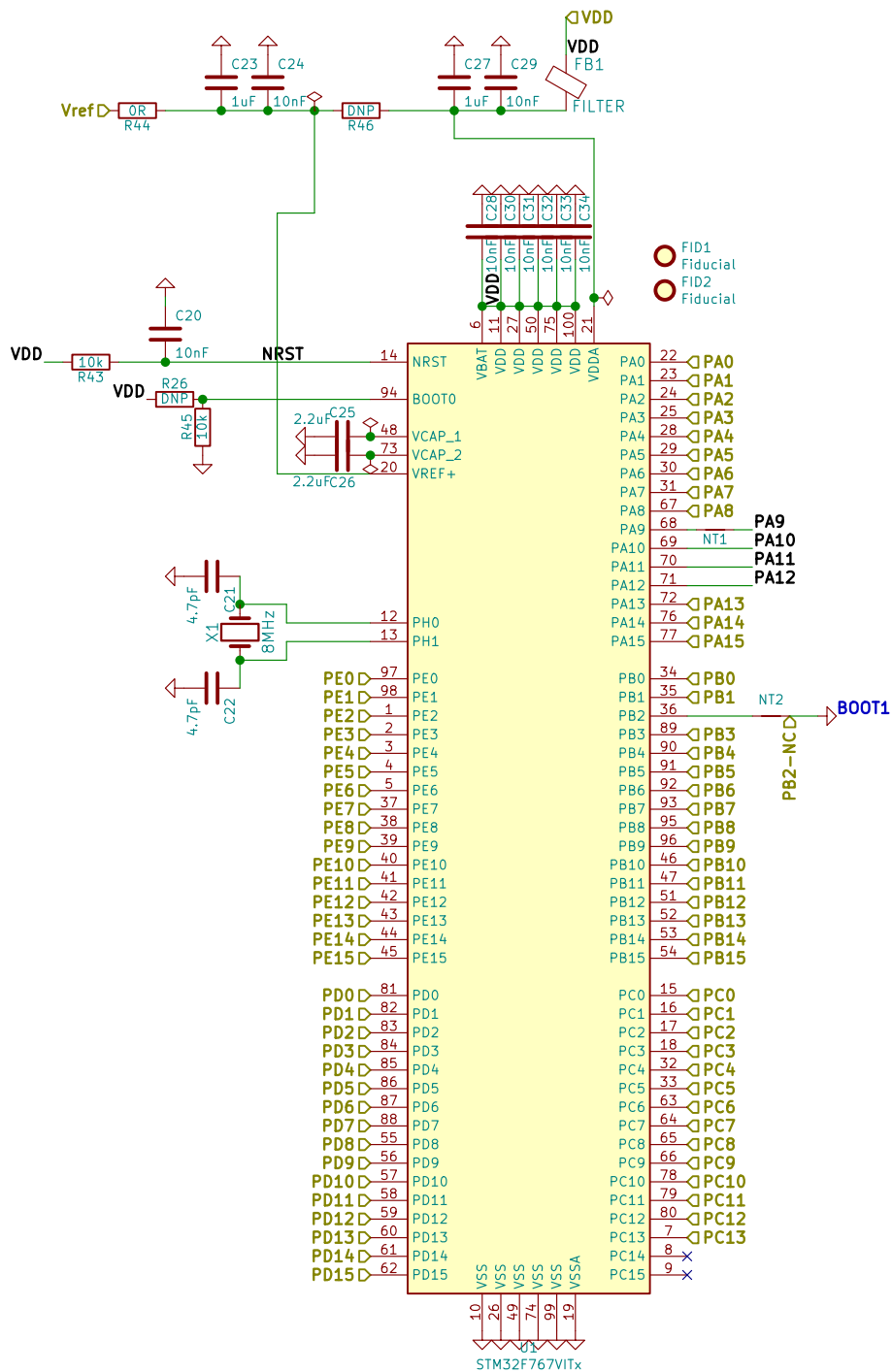


8 channel high / low side driver









<http://www.crystek.com/documents/apnnotes/Pierce-GateIntroduction.pdf>
 PCB per predictions with SaturnPCB has less then 3.5pF traces,
 STM32 pins assumed 5pF
 ESR = 80ohms max???
 Rf = 2meg could be between 1meg and 10meg.
 Cload should be 8pF per XTAL datasheet
 Cload = ((Cin+C1)[C2+Cout])/(Cin+C1+C2_Cout)+PCBstray
 Cload = ((5+4.7)[4.7+5])/(5+4.7+4.7+5)+3.5= 8.35pF
 C1=C2=C166=C167 = 4.7pF
 Rs = 1/(2pi*fC2) = 1/(2*pi*8MHz*4.7pF) = 4.2ohms.

Sheet: /MCU/		
File: stm32.sch		
Title: microRusEfi		
Size: B	Date: 2019-07-01	Rev: R0.2
KiCad E.D.A. kicad (6.0.0-rc1-dev-1650-gf425f49c1)		
Id: 6/7		

