

<i>Schematics</i>	<i>Revision</i>	<i>Date</i>	<i>Release Information</i>
SRT-96B-MEZ-FPGA	A	Feb 28 2018	first version release

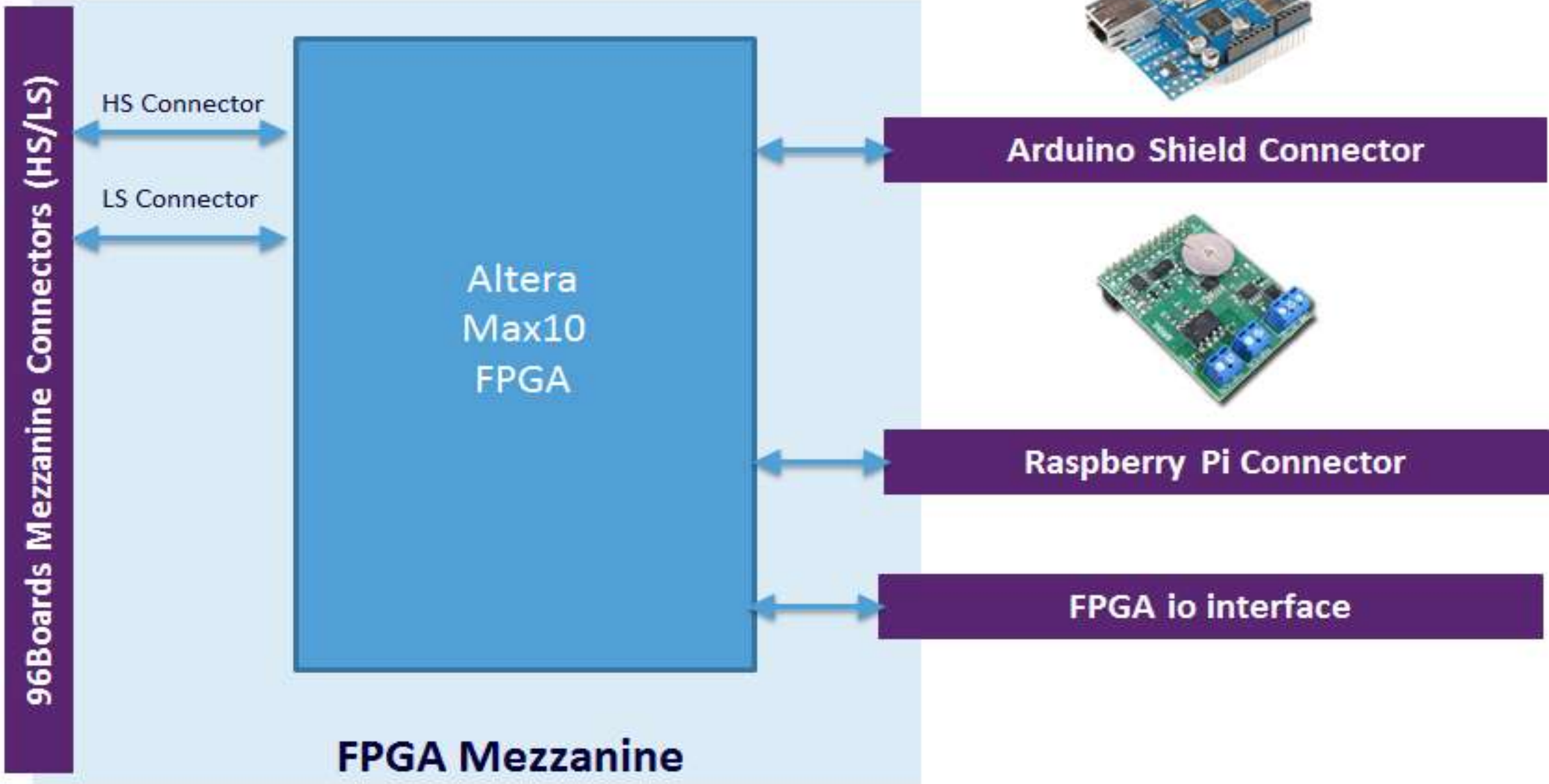
01

01	Revision History
02	Block Diagram
03	DXF
04	Stuck Up
05	Interafce_in_out-96board
06	max10-io
07	max10-power-misc
08	max10-programming
09	Raspberry Pi Connector
10	Arduino Shield Connectors
11	grove connectors
12	USB HUB + MISC

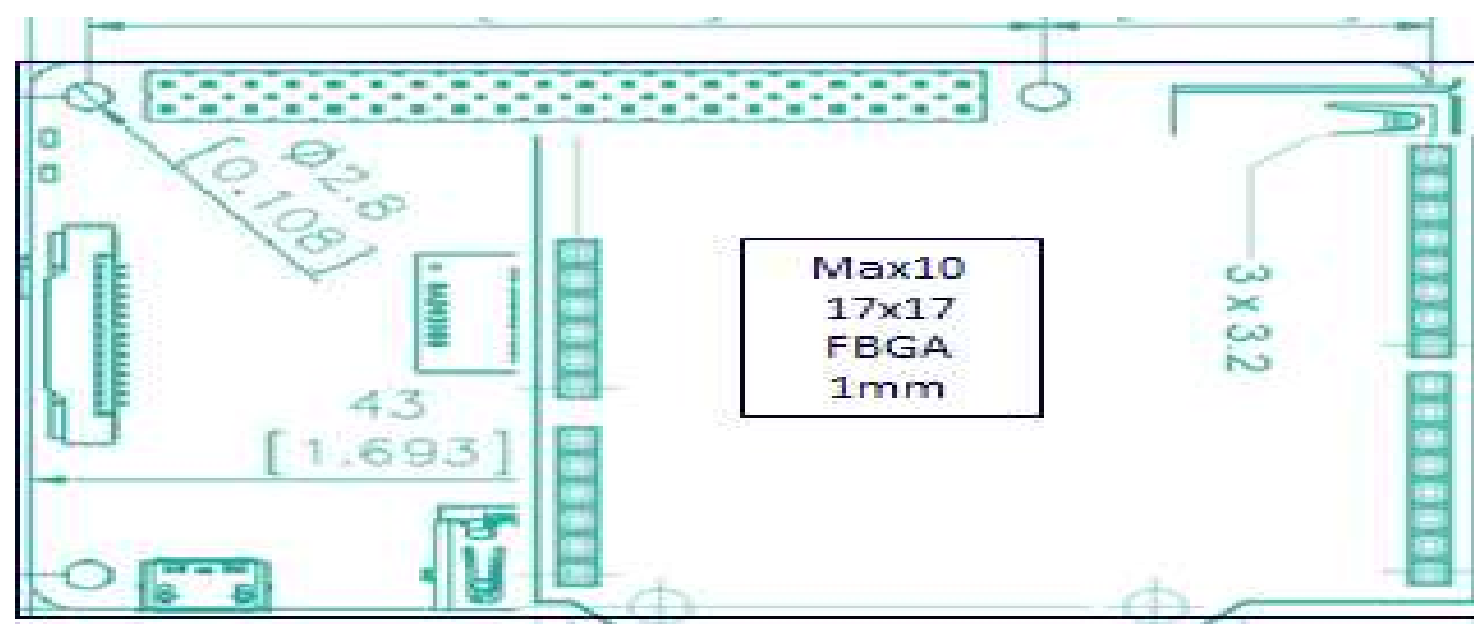
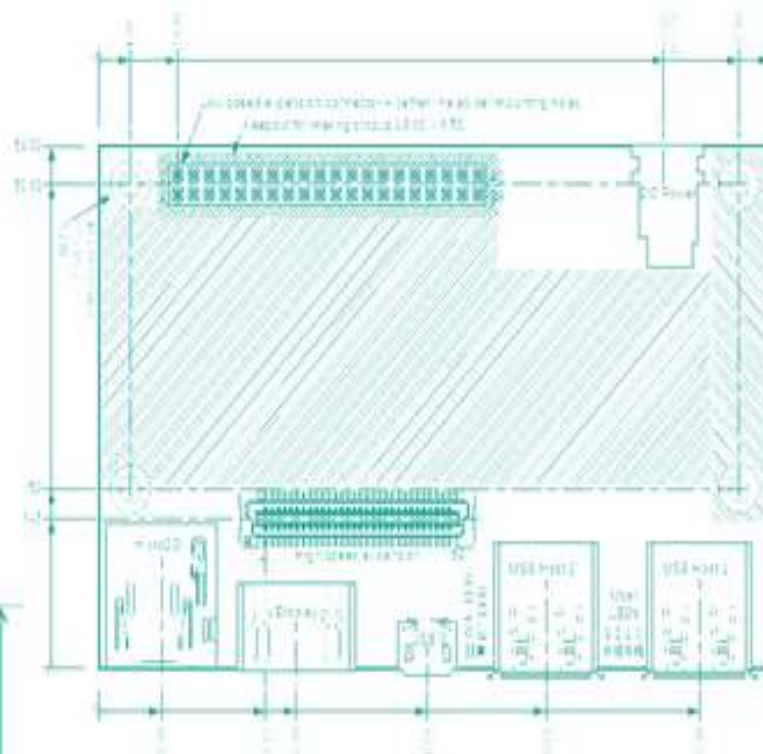
CPU
Connectivity

Programmable
Logic

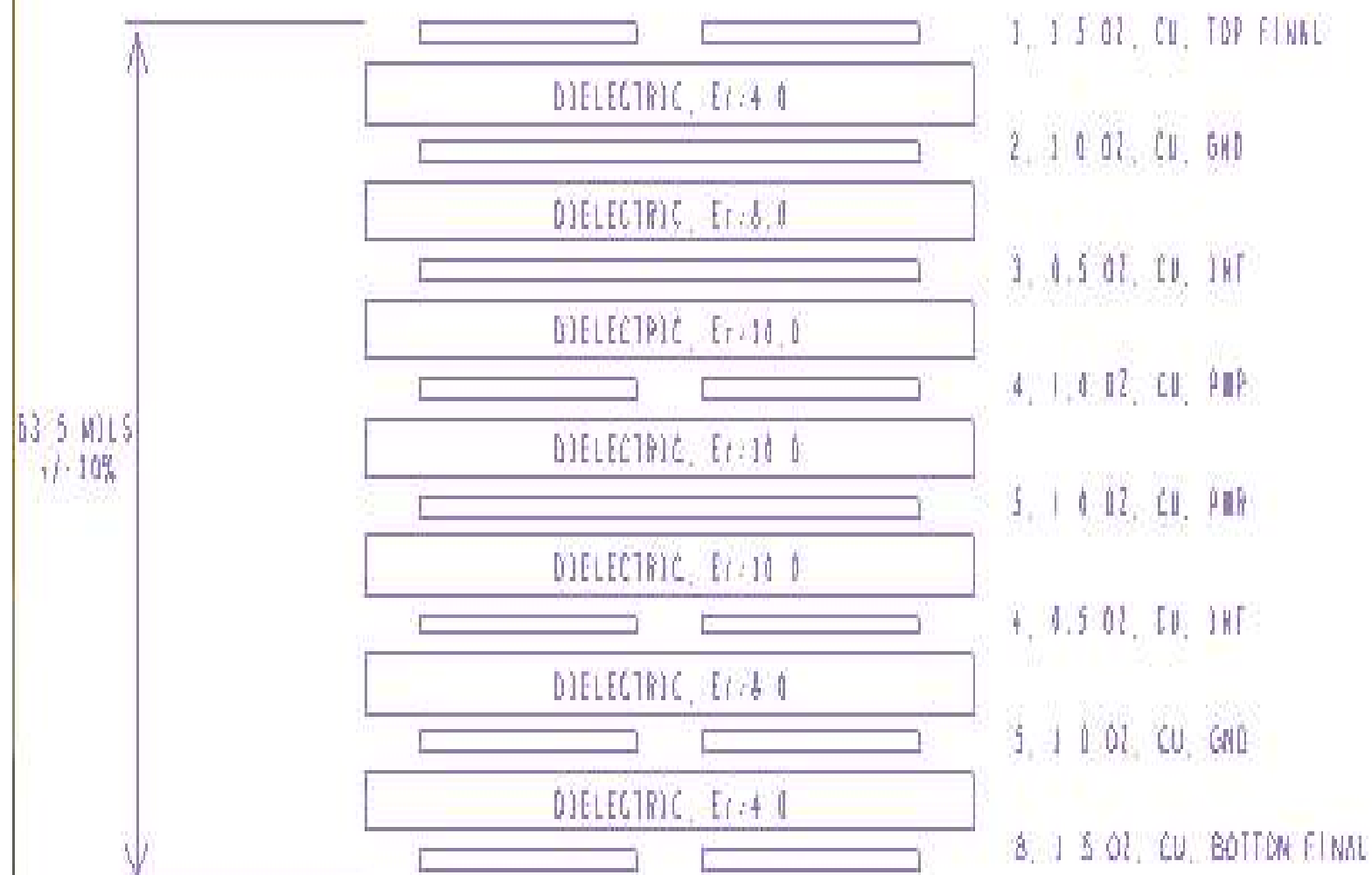
Sensors and
Actuators



Shiratech Solutions	Block Diagram			
	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A3	CAGE Code <Cage Code>	DWG NO A	Rev B
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B LAYERS CROSS SECTION
AND FILM SET

20-50 OHM +/-10%, WIDTH:7.18 MILS

Zdiff = 100 +/-10% WIDTH: 5 MIL DISTANCE : 4.57MIL

20-50 OHM +/-10%, WIDTH:8.0 MILS

Zdiff = 100 +/-10% WIDTH: 5 MIL DISTANCE : 6.0MIL

20-50 OHM +/-10%, WIDTH:8.0 MILS

Zdiff = 90 +/-10% WIDTH: 6 MIL DISTANCE : 5.0 MIL

Zdiff = 100 +/-10% WIDTH: 5 MIL DISTANCE : 6.0MIL

20-50 OHM +/-10%, WIDTH:7.18 MILS

Zdiff = 100 +/-10% WIDTH: 5 MIL DISTANCE : 4.57MIL

RECOMMENDED: STACKUP, FINAL STACKUP TO BE DETERMINED BY VENDOR

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Stack Up

SRT-96B-MEZ-FPGA

Drawn By Ishay Kokavka

Size
A3CAGE Code
<Cage Code>DWG NO
ARev
B

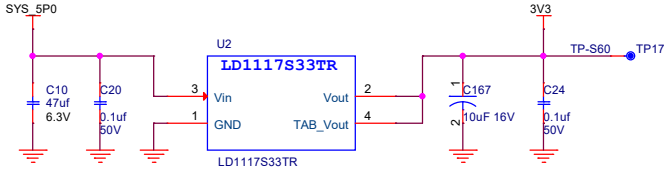
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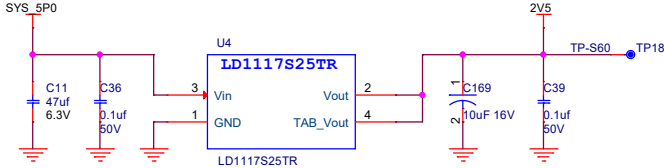
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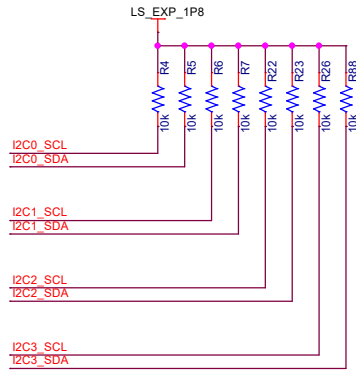
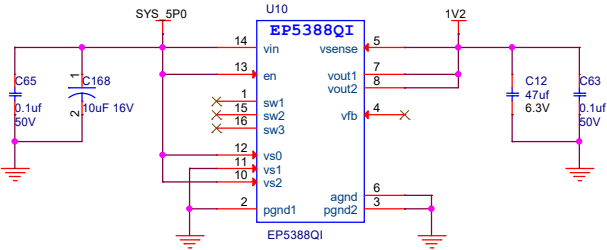
3V3-REG - 0.8A



2V5-REG - 0.8A

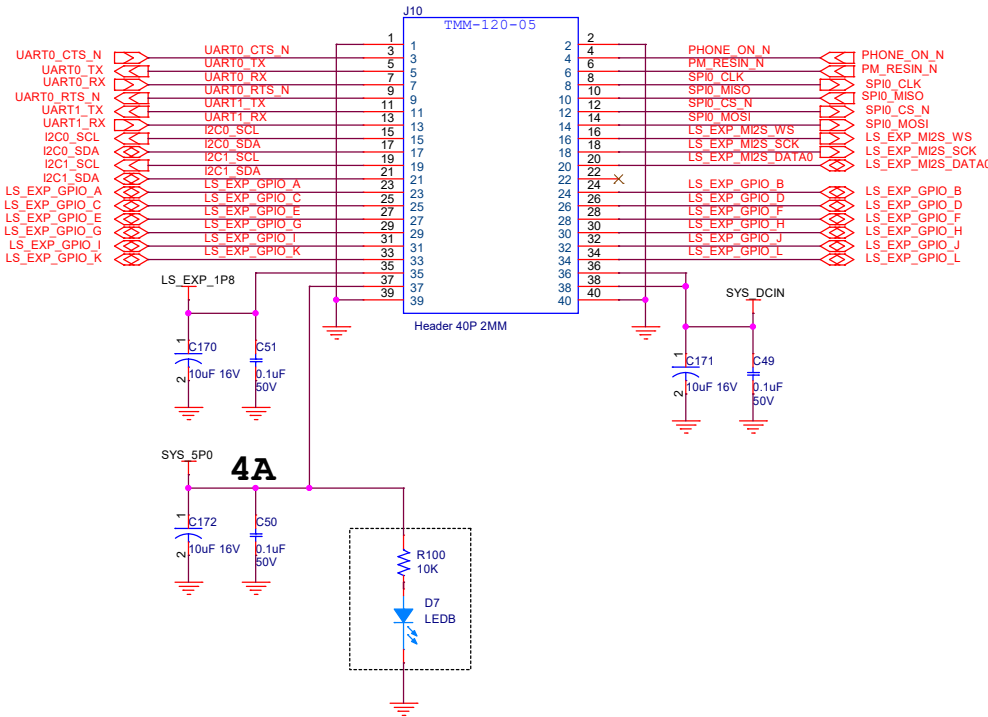


1V2-dc-dc - 0.8A

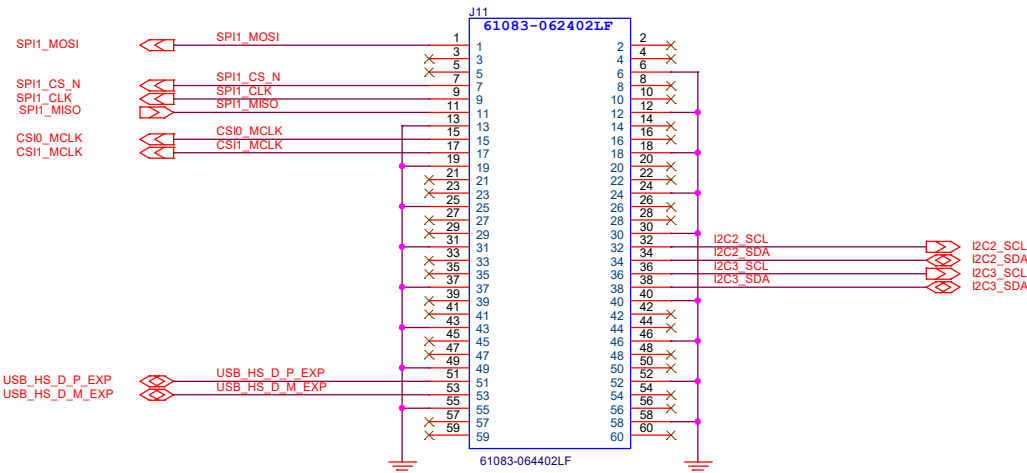


05

LS EXPANSION CONNECTOR
from galcom board
male connector (ps)



HS EXPANSION CONNECTOR
from galcom board



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Interface_in_out

SRT-96B-MEZ-FPGA

Drawn By Ishay Kokavka

Size
A2

CAGE Code
<Cage Code>

DWG NO
A

Rev
B

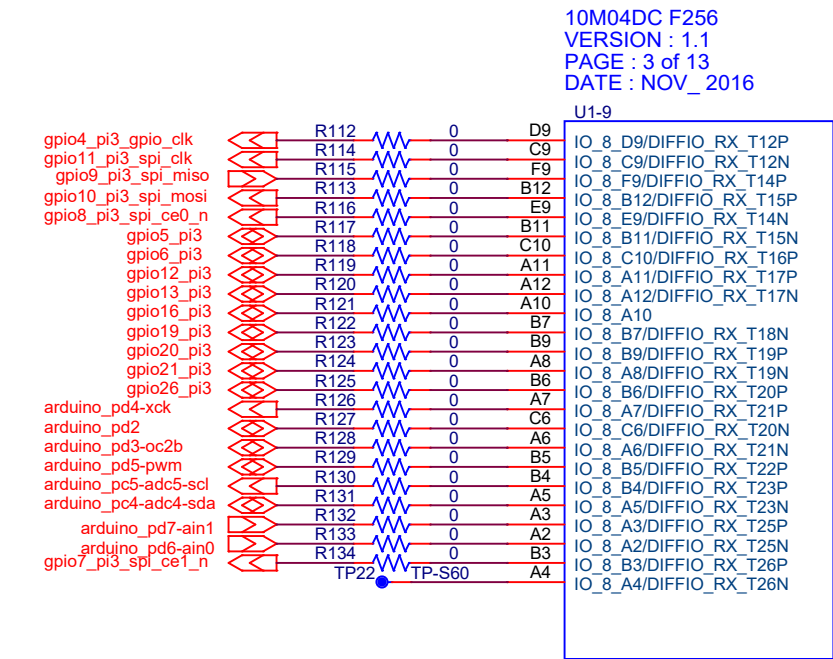
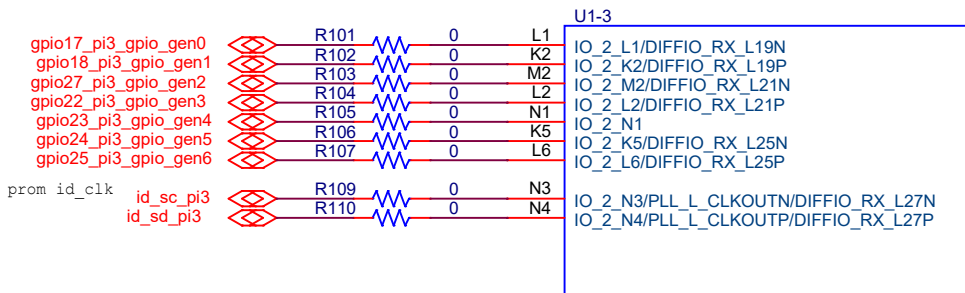
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Scale

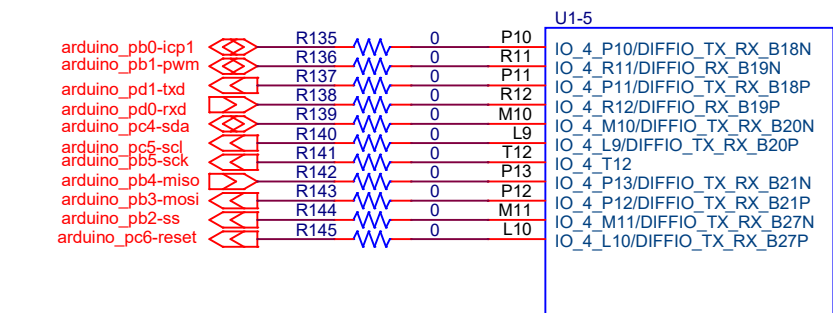
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raspberry pi3

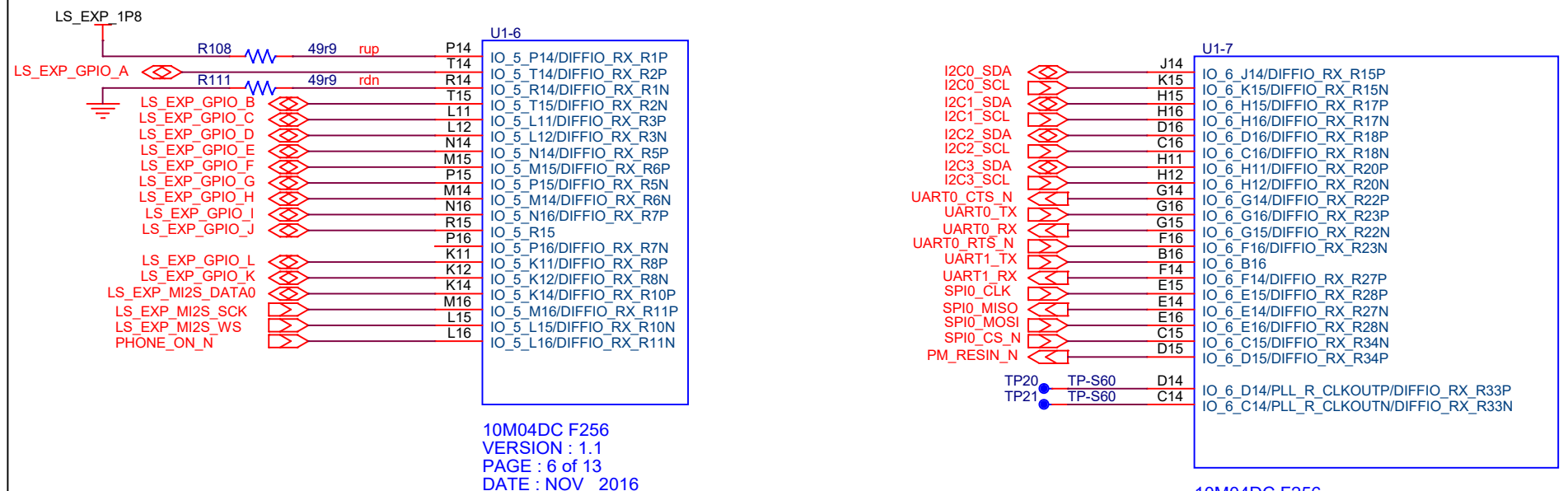


Arduino

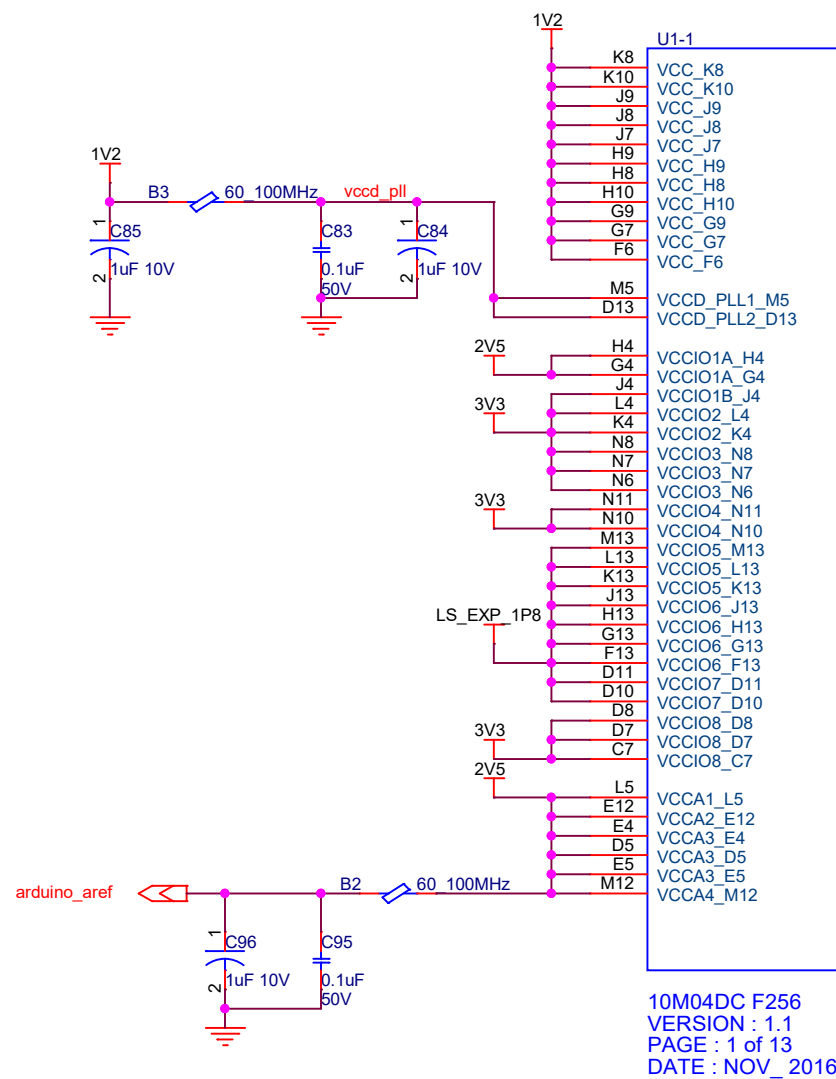
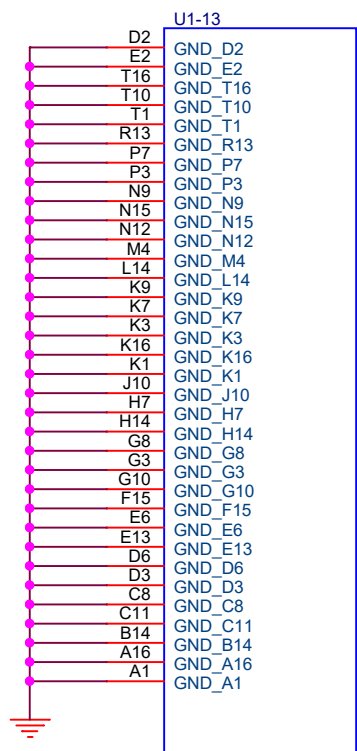
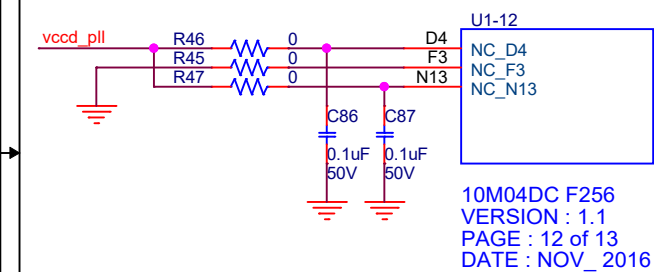
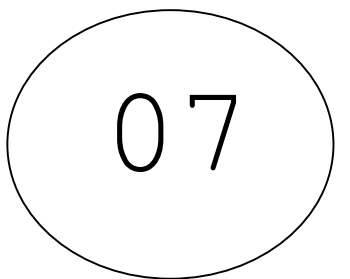
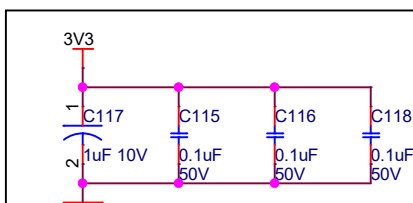
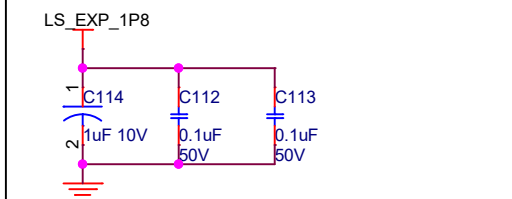
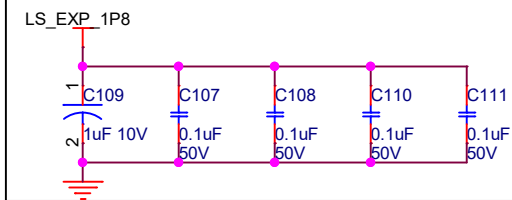
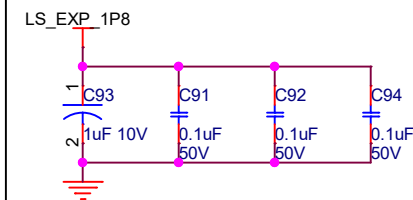
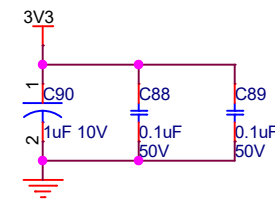
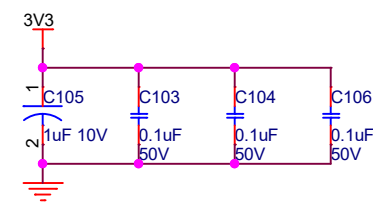
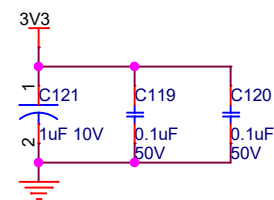
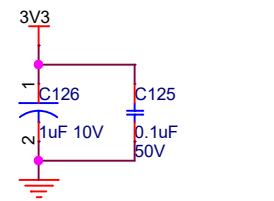
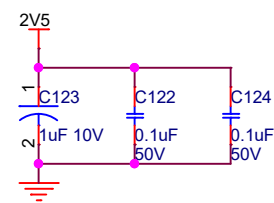
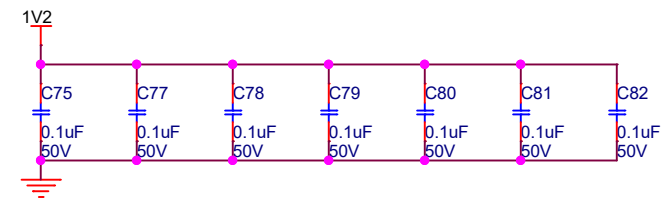
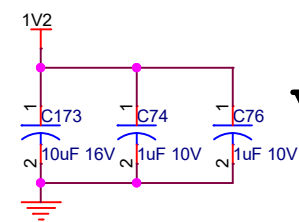


raspberry pi3 + Arduino EXTRA SIG

qualcomm "snap dragon" 410c (1v8)

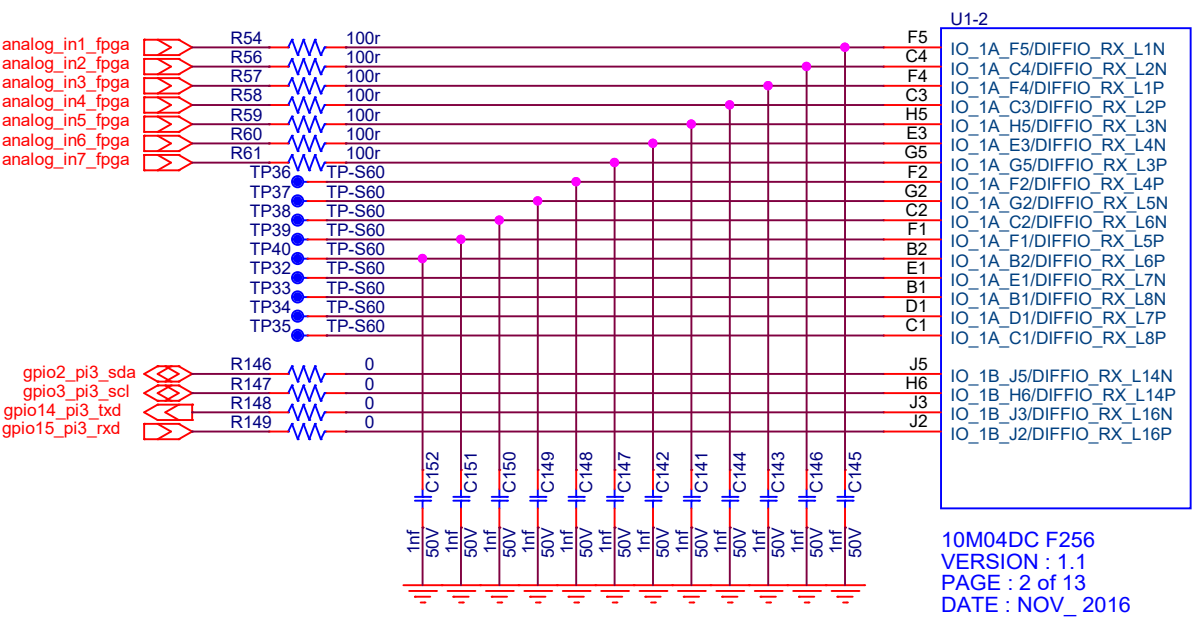


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	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A3	CAGE Code <Cage Code>	DWG NO A	Rev B
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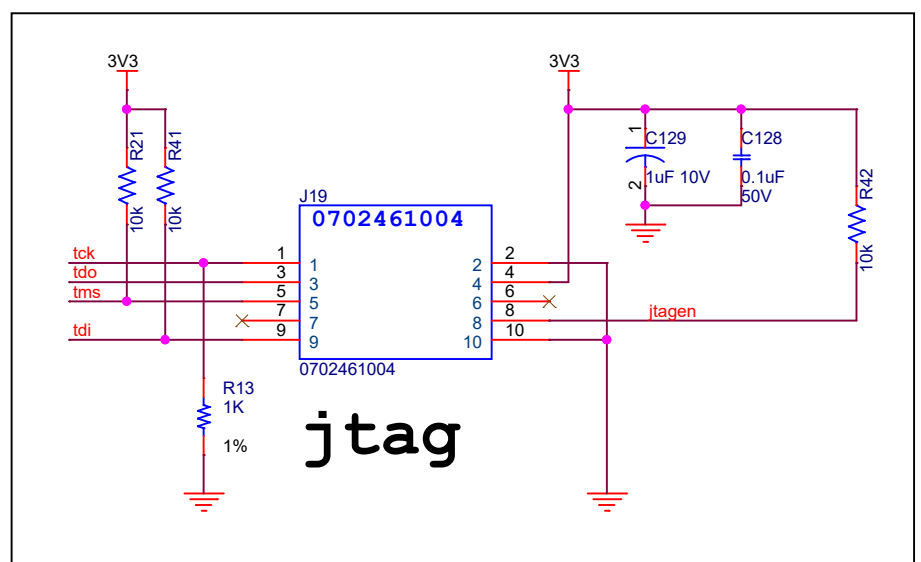
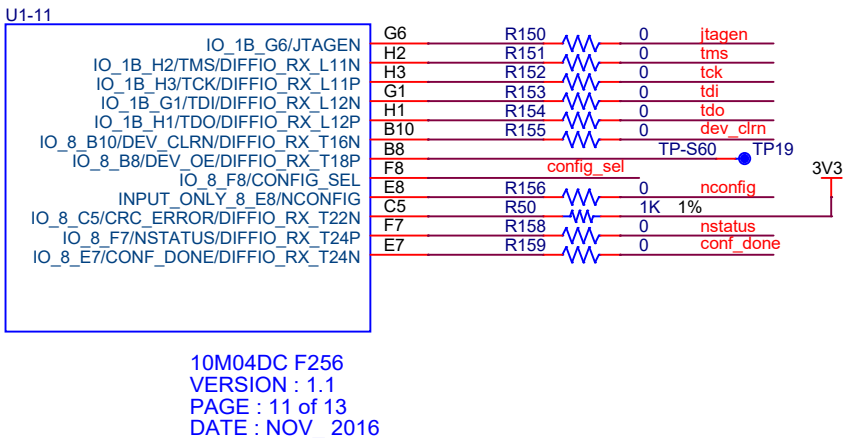
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	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A3	CAGE Code <Cage Code>	DWG NO A	Rev B
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Arduino

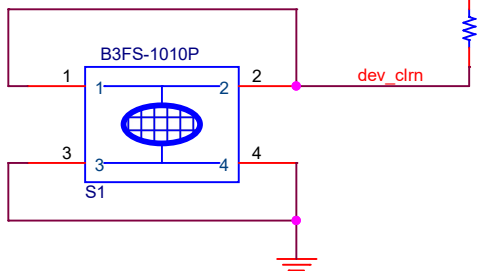


08

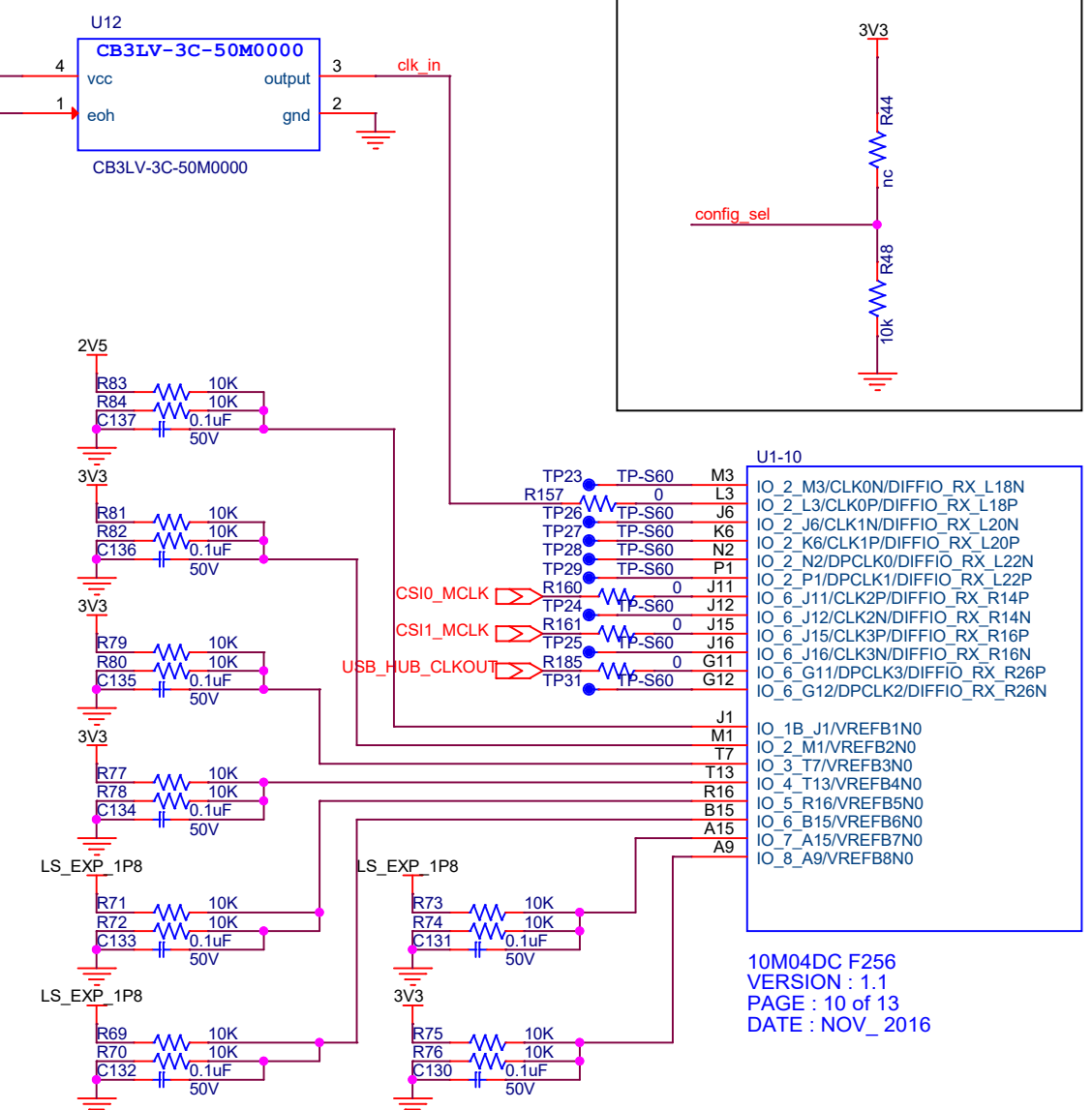
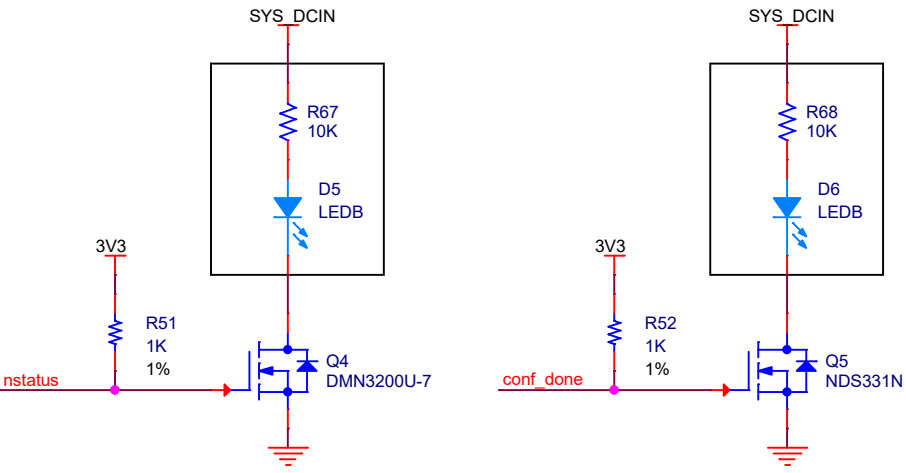
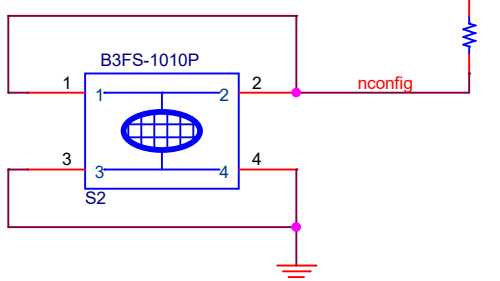
raspberry pi3



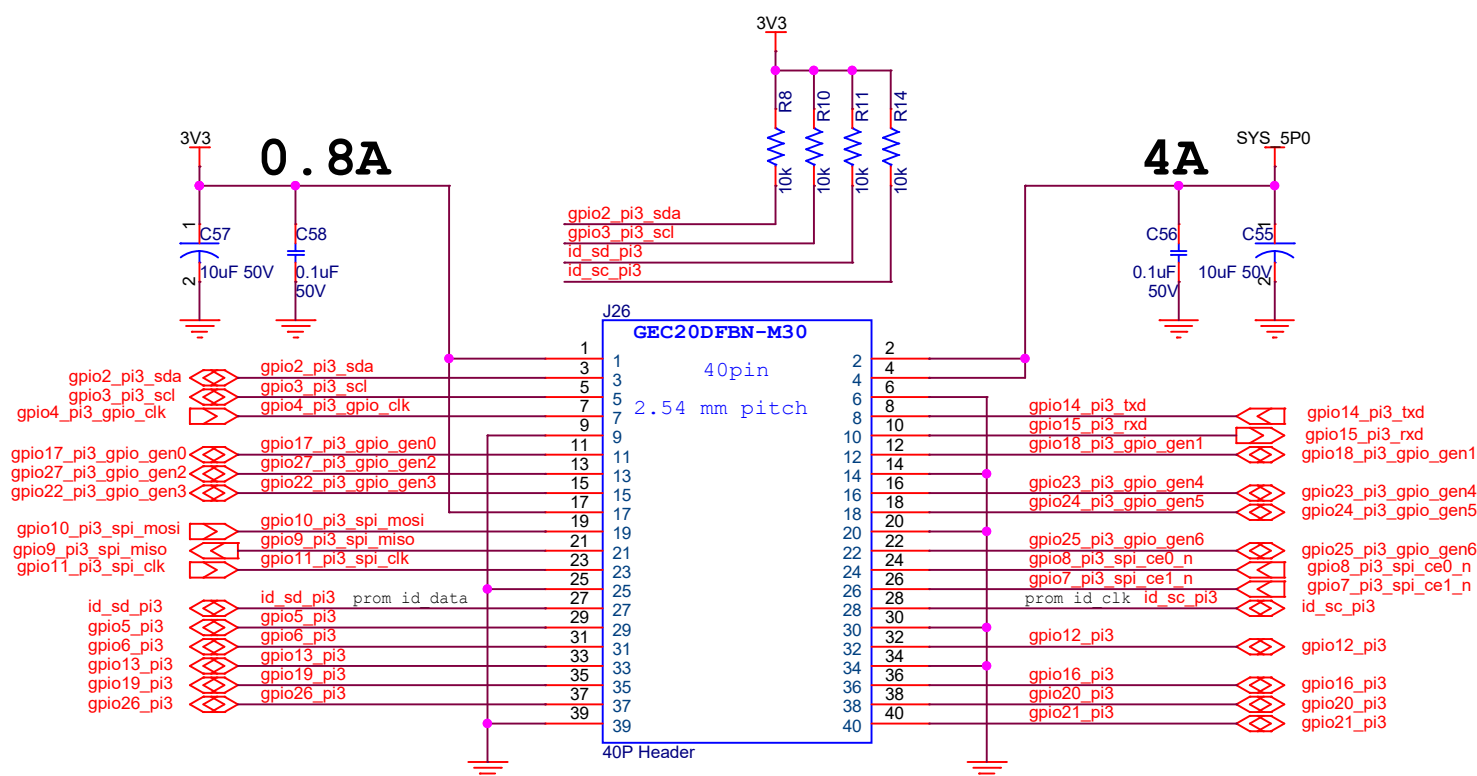
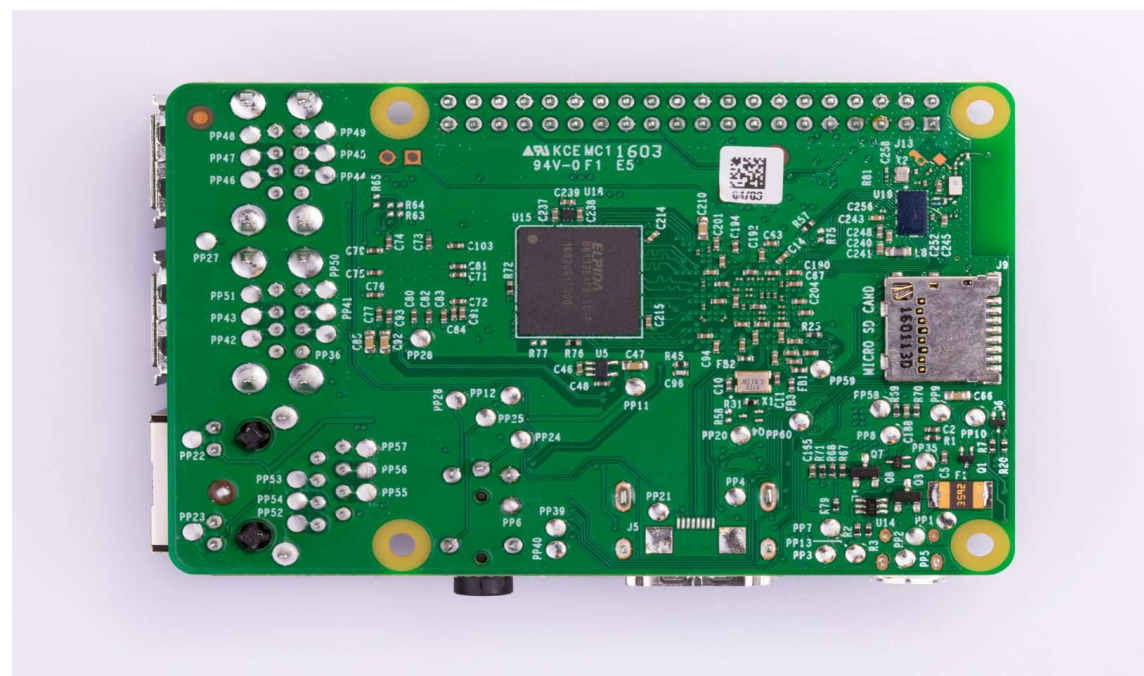
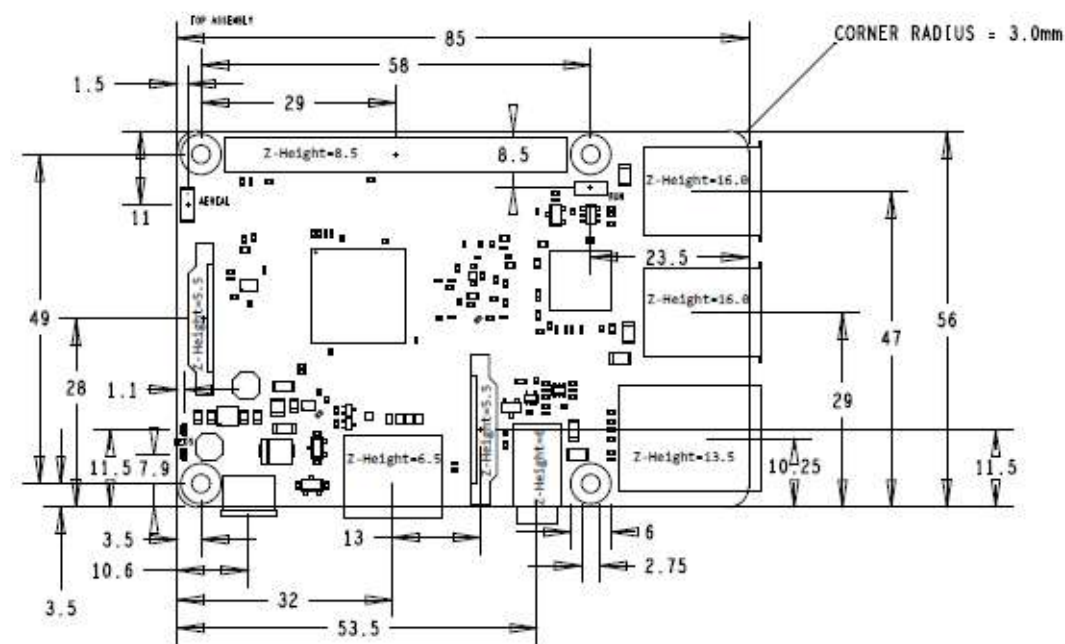
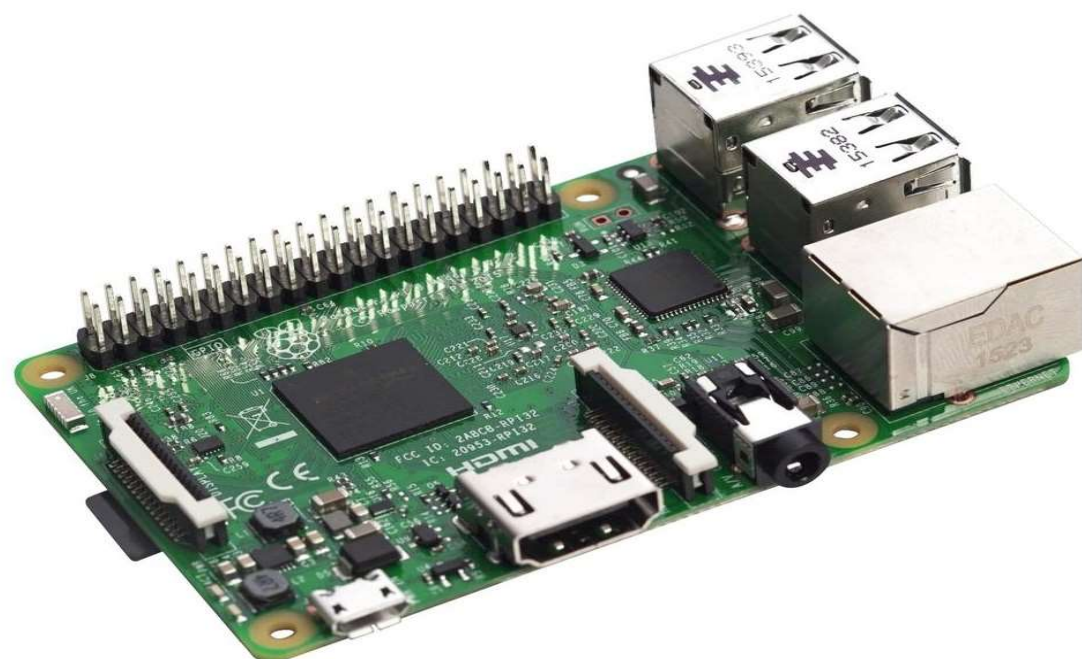
Reset Button



nconfig Button



Shiratech Solutions	max10-programming			
	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A3	CAGE Code <Cage Code>	DWG NO A	Rev B
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Saturday, September 29, 2018

Raspberry Pi Connector

SRT-96B-MEZ-FPGA

Size
A3

CAGE Code
<Cage Code>

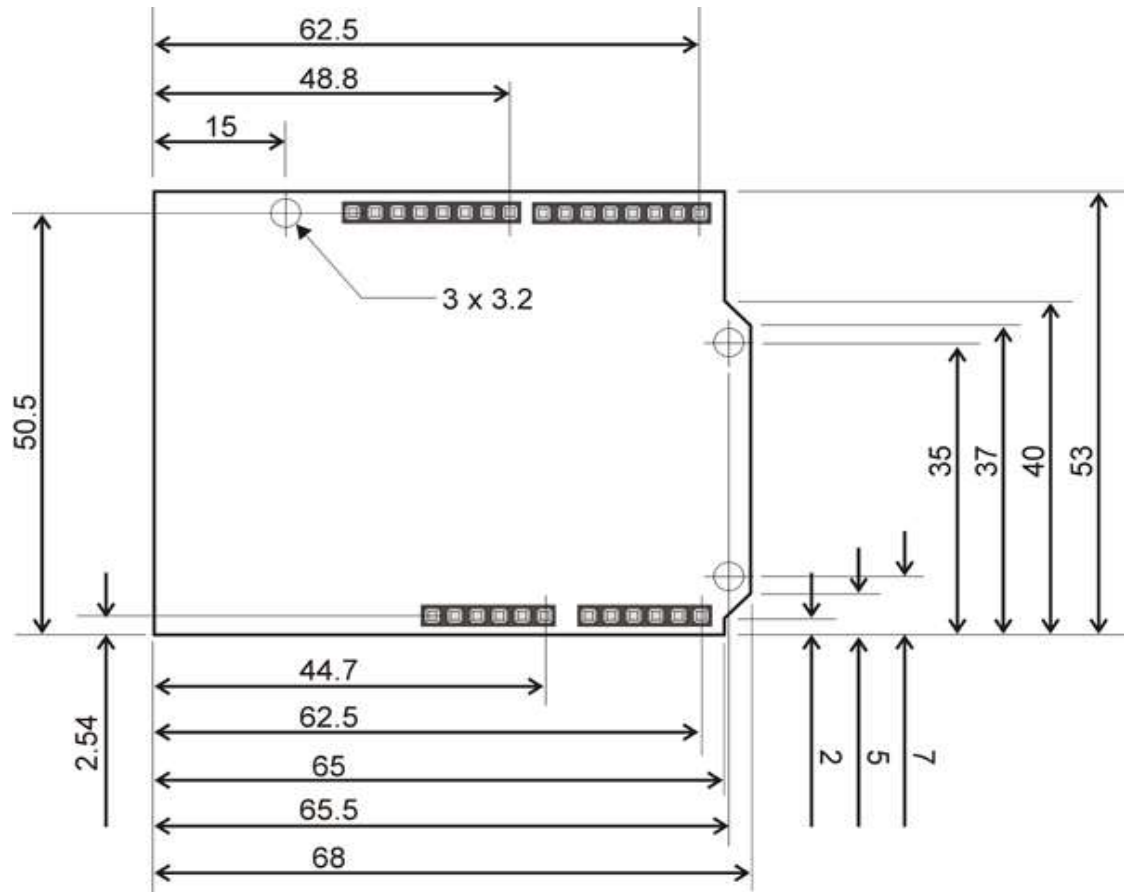
DWG NO
A

Rev
B

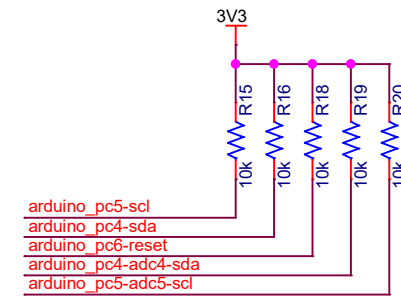
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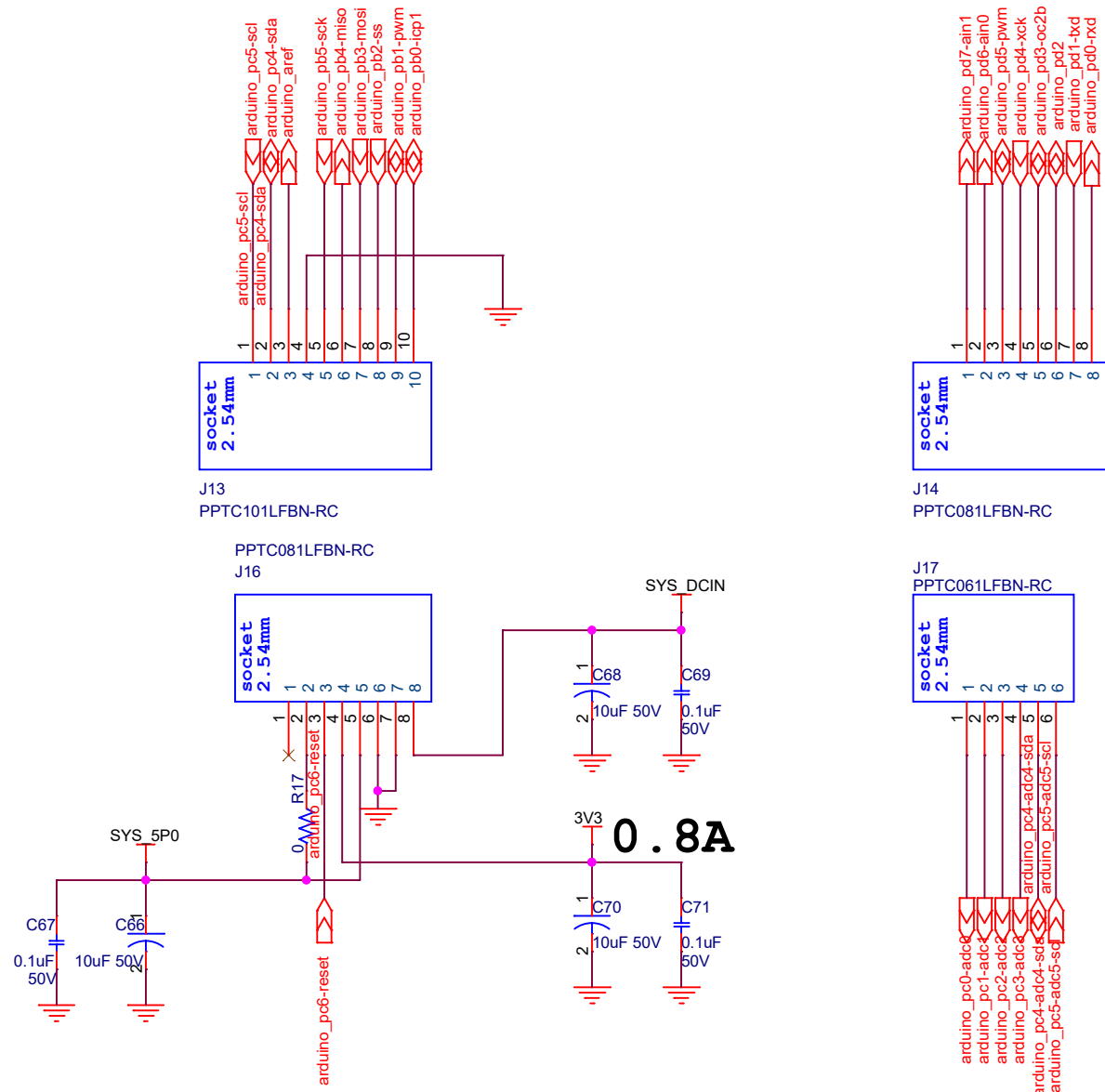
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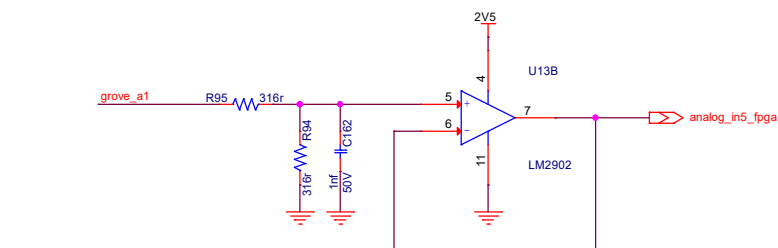
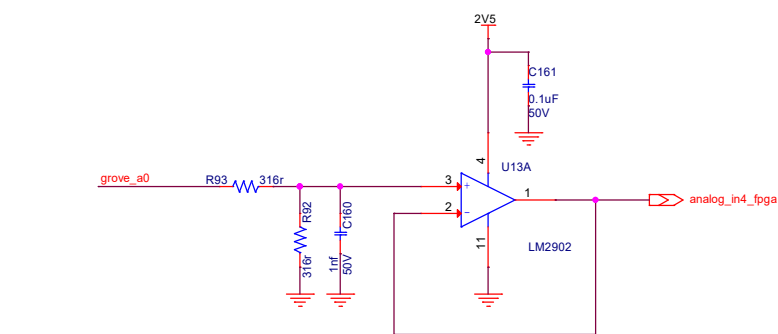
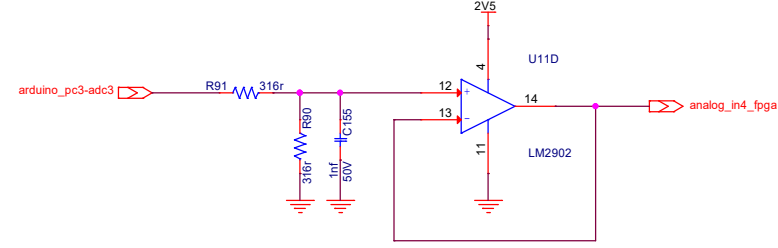
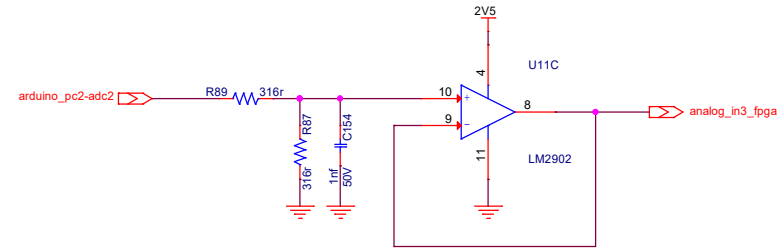
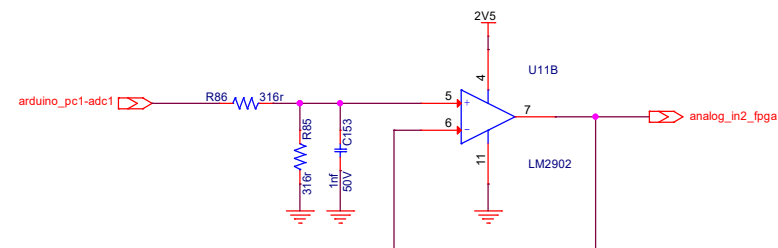
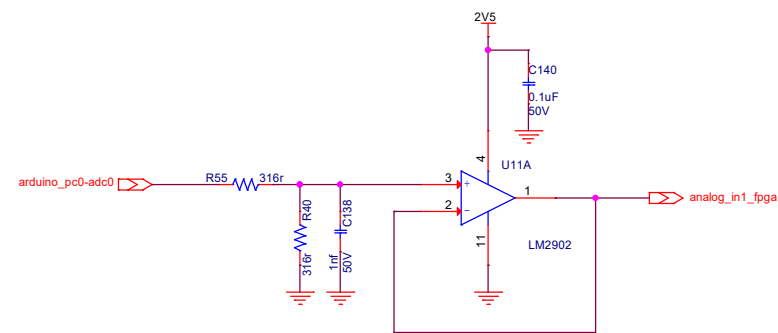
pull ups



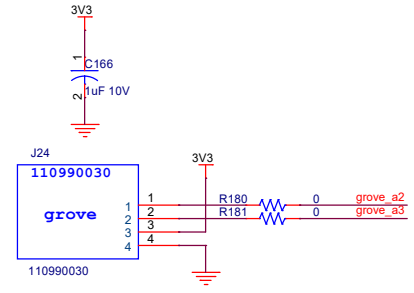
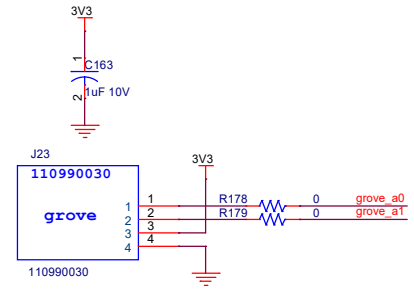
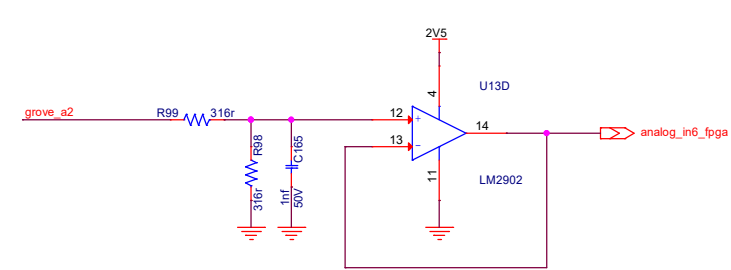
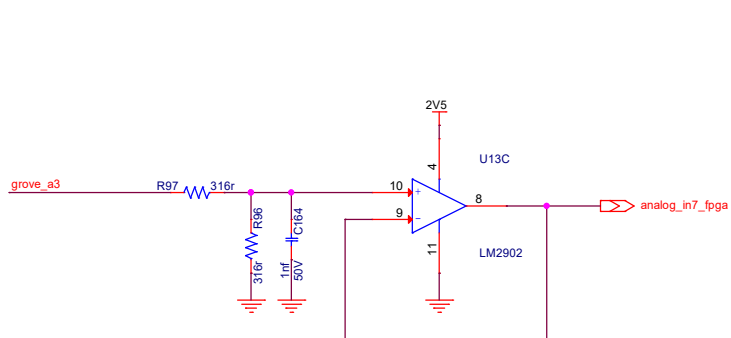
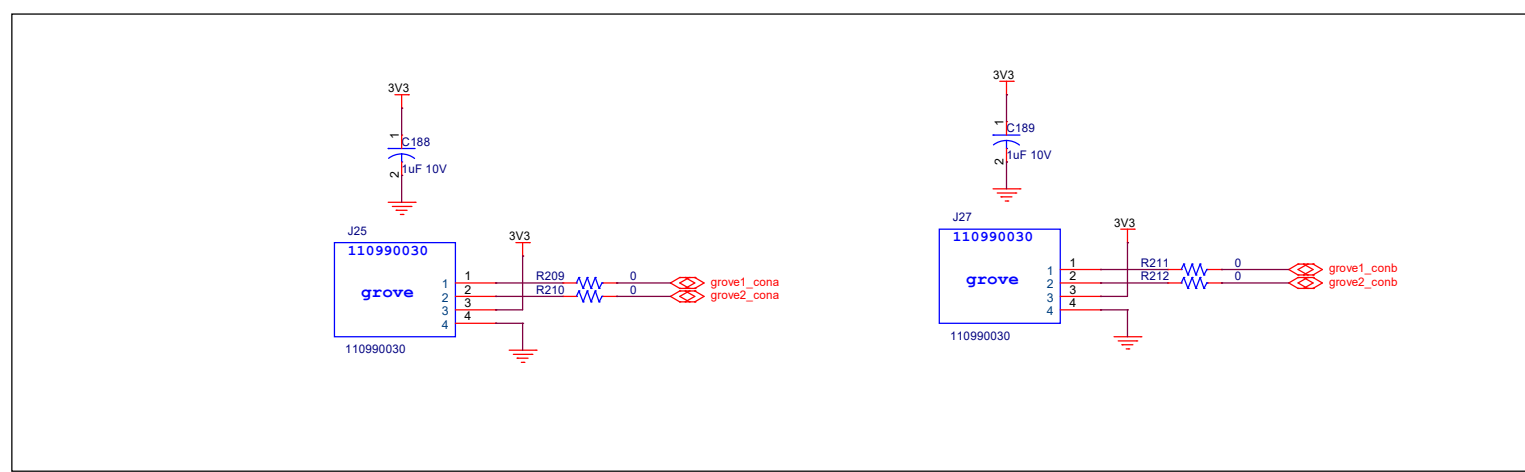
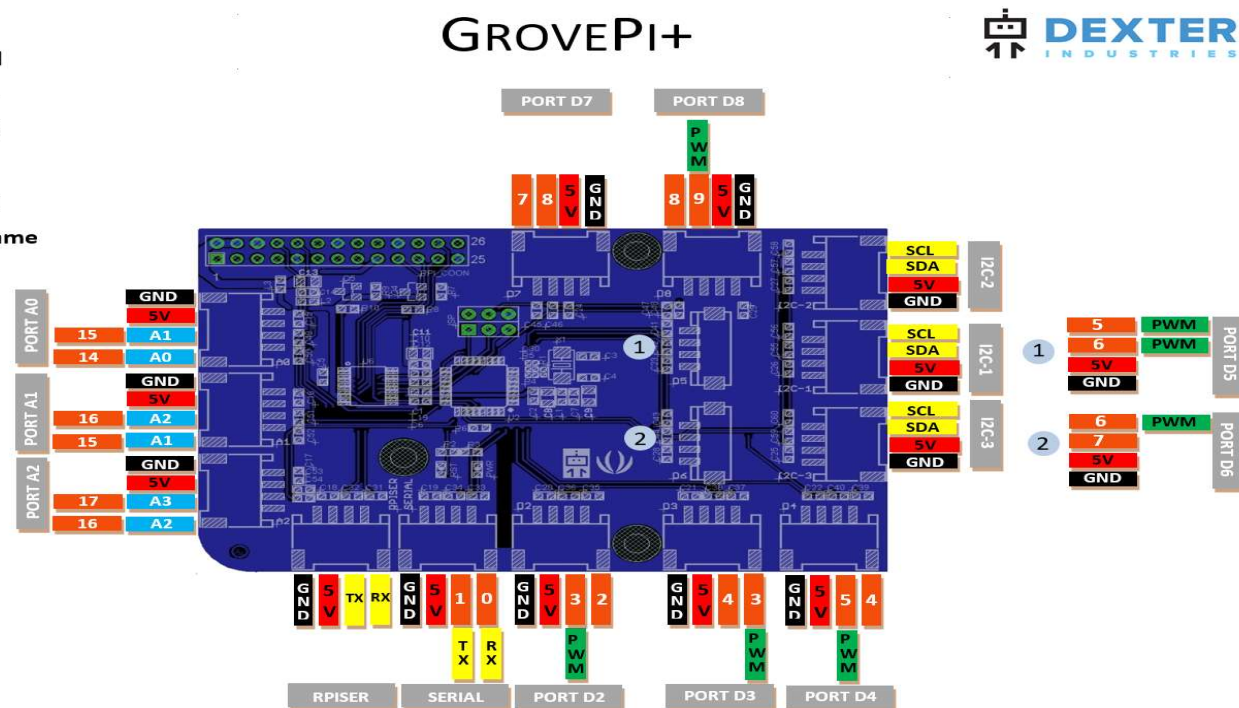
10



Shiratech Solutions	Arduino Shield Connectors			
	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A3	CAGE Code <Cage Code>	DWG NO A	Rev B
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- Power
- Ground
- Digital
- Analog
- PWM
- Control
- Port Name



Shiratech Solutions	grove connectors			
	SRT-96B-MEZ-FPGA			
Drawn By Ishay Kokavka	Size A2	CAGE Code <Cage Code>	DWG NO A	Rev B
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