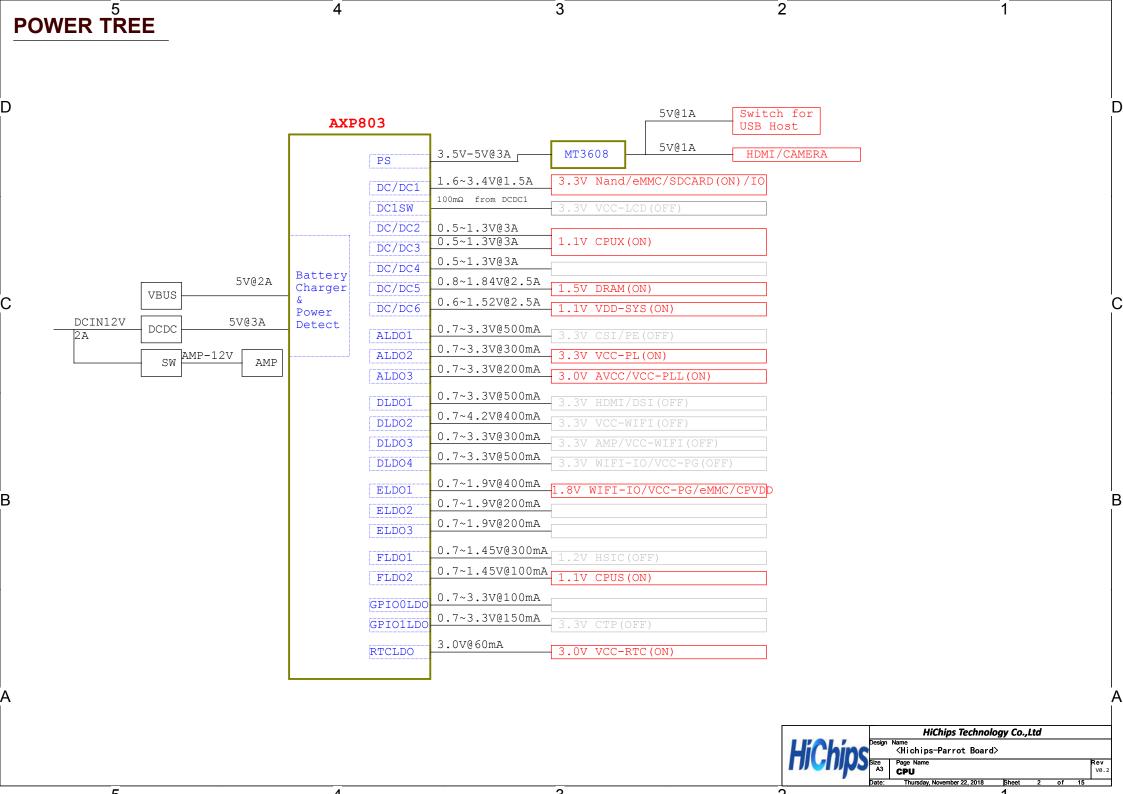
VERSION HISTORY

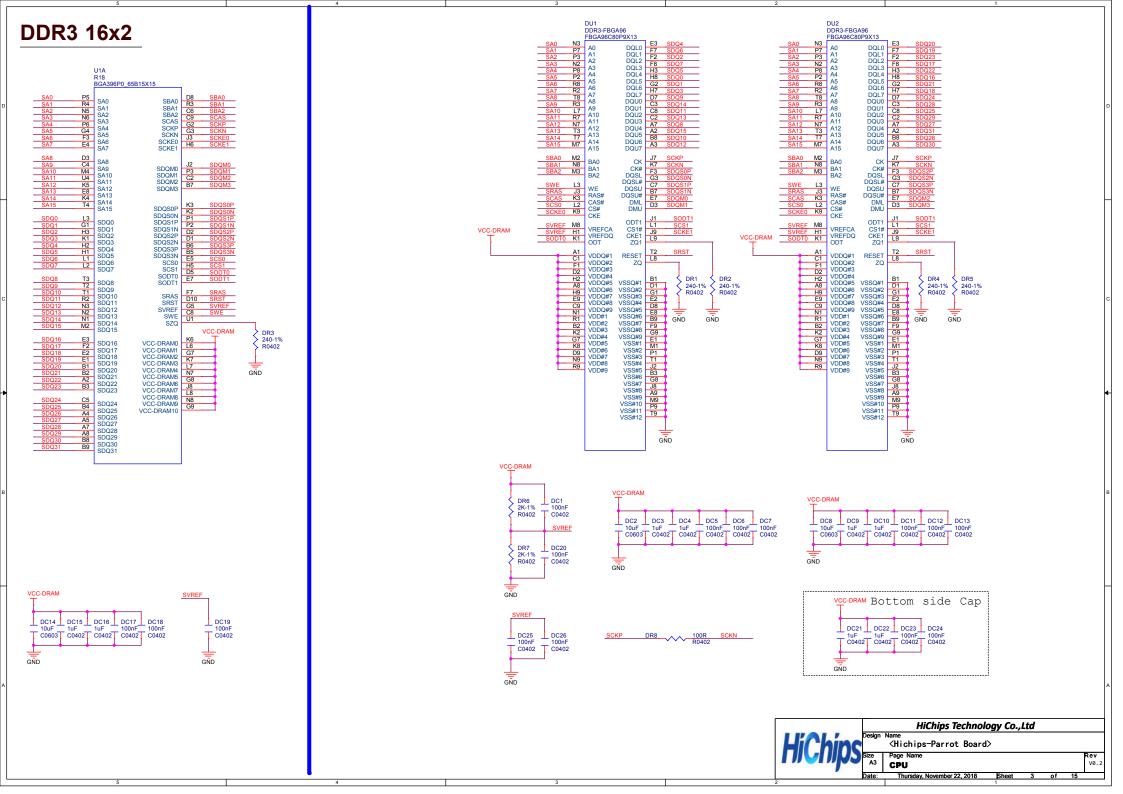
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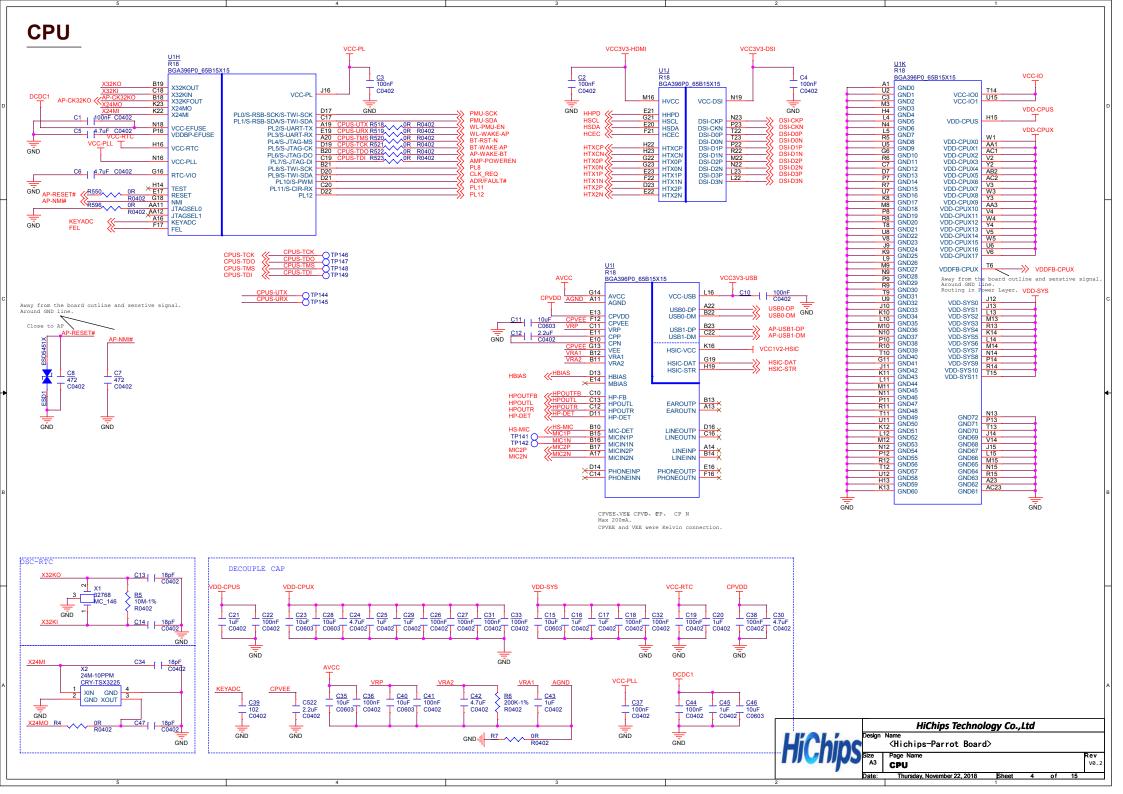
R18-SPEAKER-DEV 01 VERSION HISTORY 02 POWER TREE 03 DDR3 16X2 04 CPU 05 POWER1 06 POWER2 07 NAND/eMMC 08 MSIC 09 INTERFACE 10 HDMI 11 WIFI-BT 12 USB HUB 13 AMP 14 DEBUG&HEADPHONE 15 BLOCK

Revision	Description	Date	Drawn	Checked	Approved
Ver 0_1	Releas version	2018/5/10	LIN	Lee	GUO
Ver 0_2	Add diode D14 to control DCIN-12V current flow direction.	2018/9/27	LIN	Lee	GUO
Ver 0_2	Add a 2 pin male heade J308 to provide extra 12V output for 40 pin low profile female 2mm receptacle.	2018/9/27	LIN	Lee	GUO
Ver 0_2	The interface 60pin is updated to a 1.8V 100K pull up.	2018/9/27	LIN	Lee	GUO
Ver 0_2	GPIO-A increase voltage conversion circuit	2018/9/27	LIN	Lee	GUO
Ver 0_2	Add TP test point for UART2	2018/9/27	LIN	Lee	GUO
Ver 0_2	Remove NAND for EMMC	2018/9/27	LIN	Lee	GUO

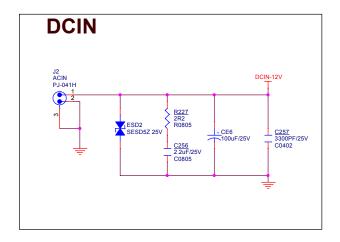
HiChips Technology Co.,Ltd vesign Name 〈Hichips-Parrot Board〉

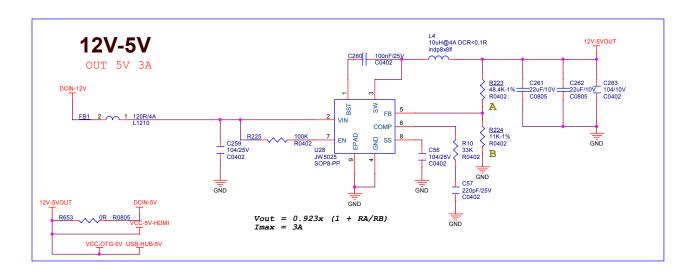


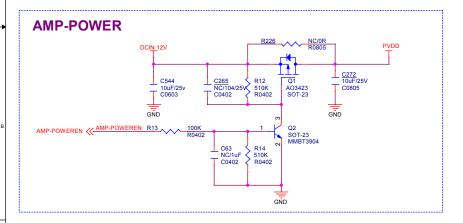


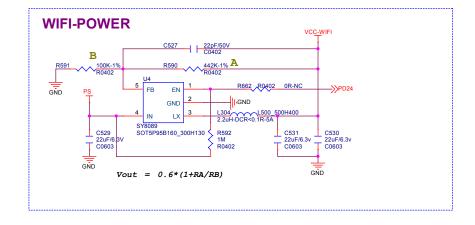


POWEER1









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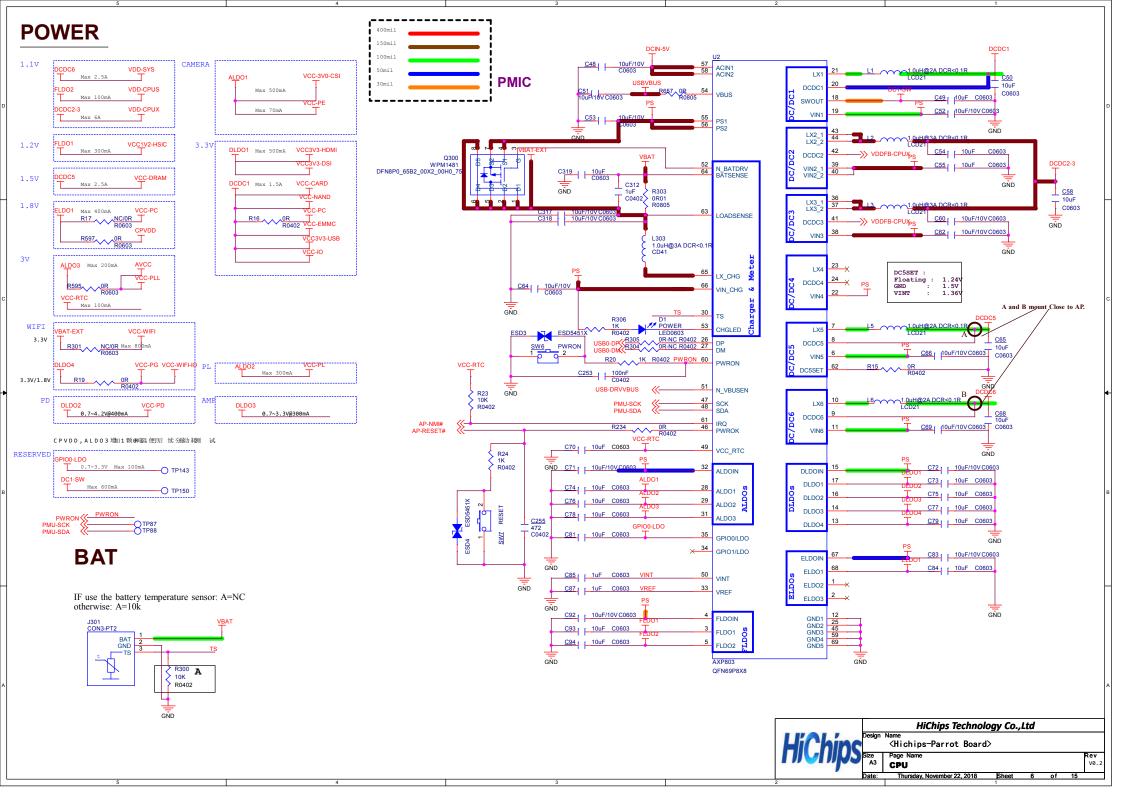
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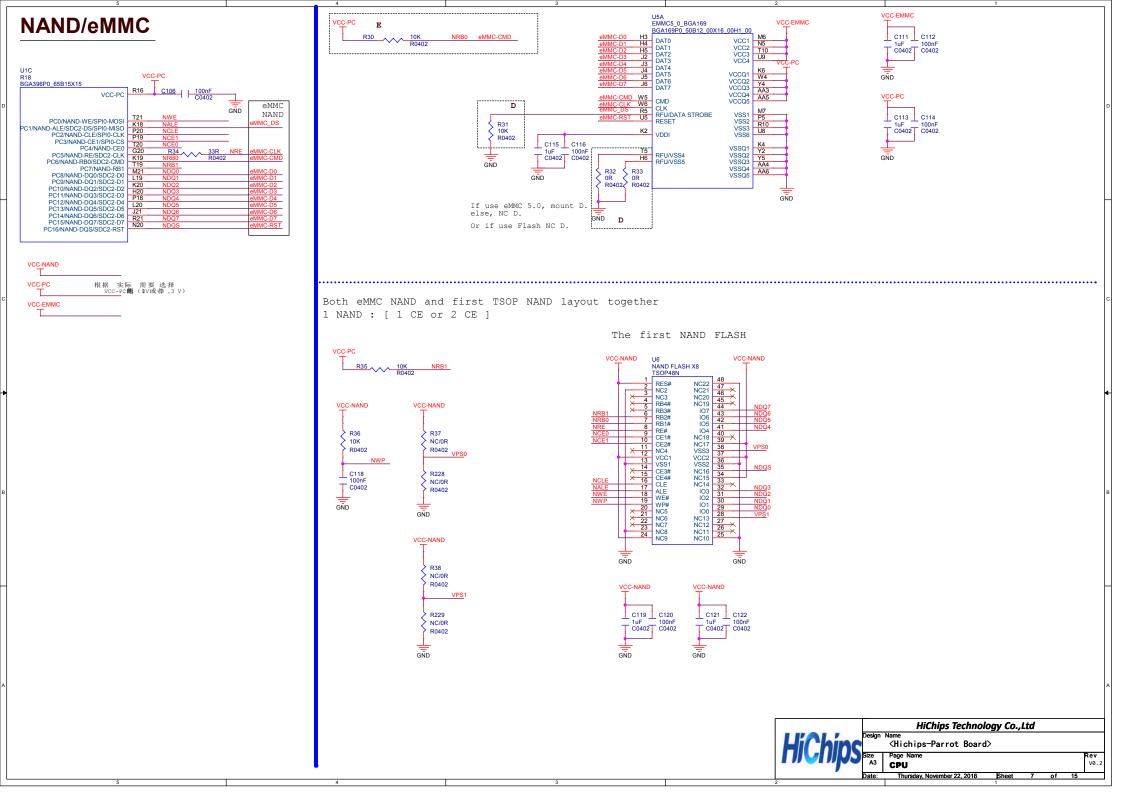
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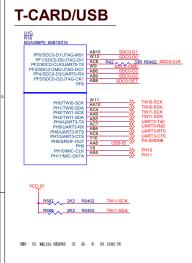
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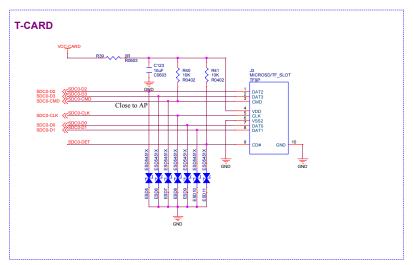
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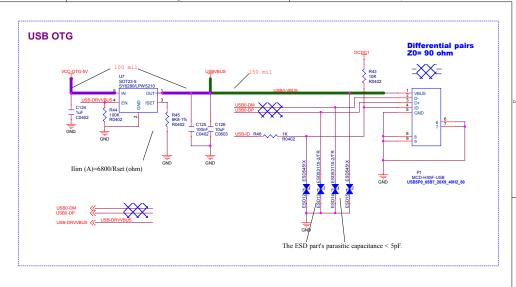
Date: Thursday, November 22, 2018 Sheet 5 of 15

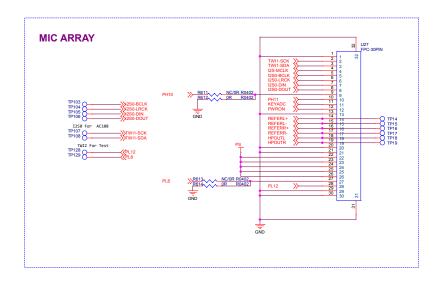


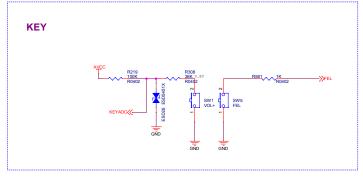




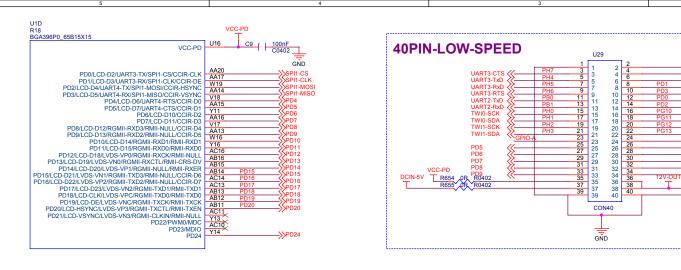


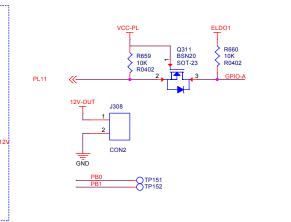












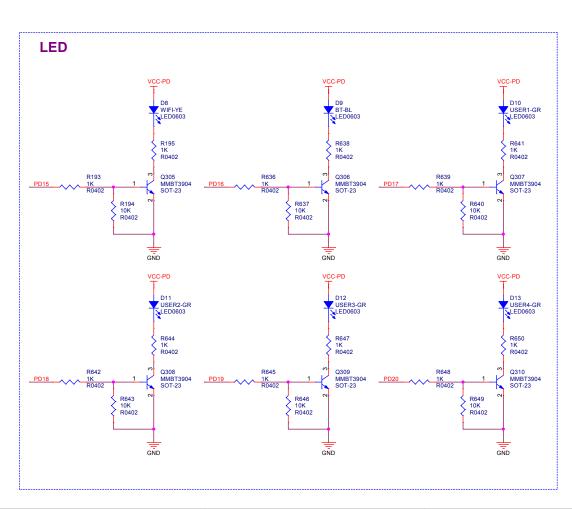
AP-RESET#

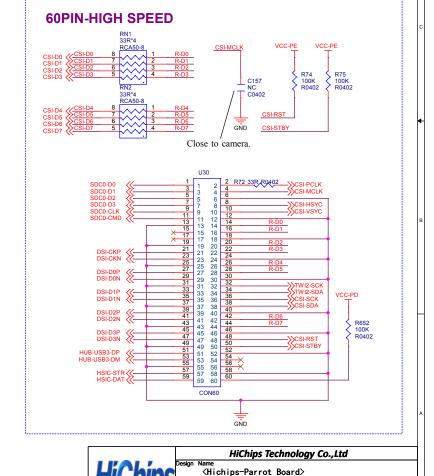
SPI1-CS SPI1-MOSI

AP-PCM-SYNC AP-PCM-BCLK

AP-PCM-DIN

PD13 PD14



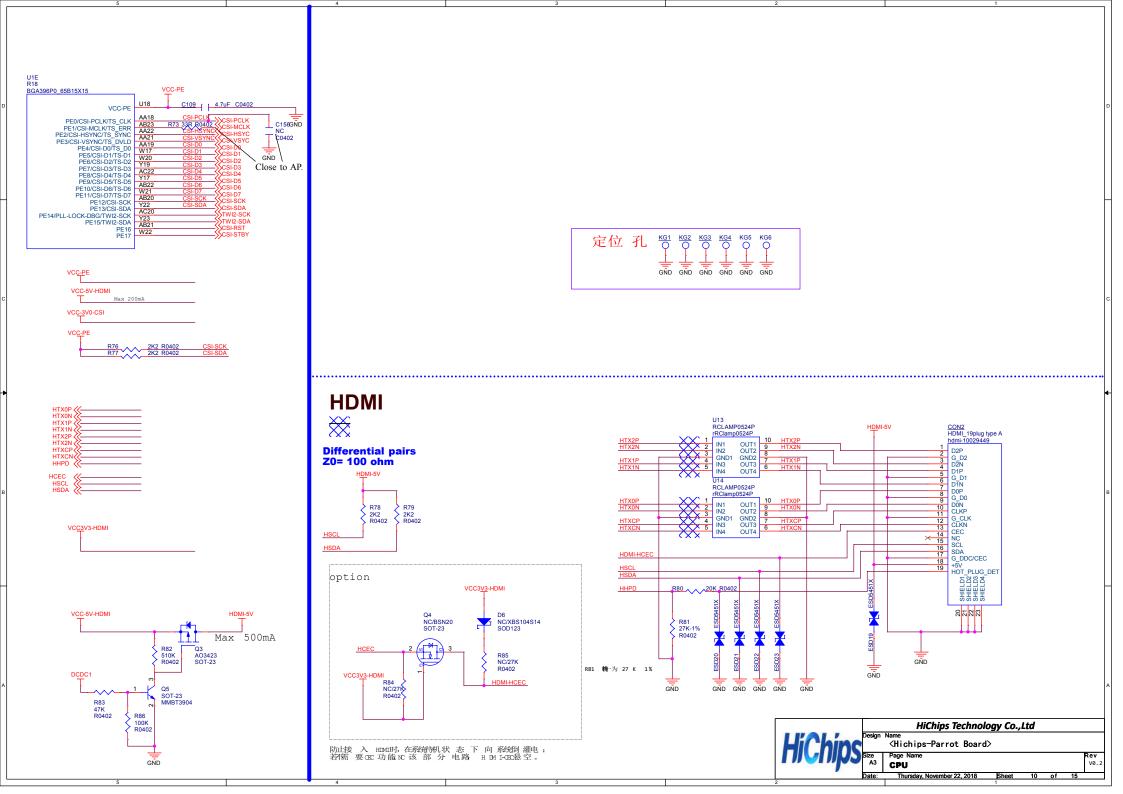


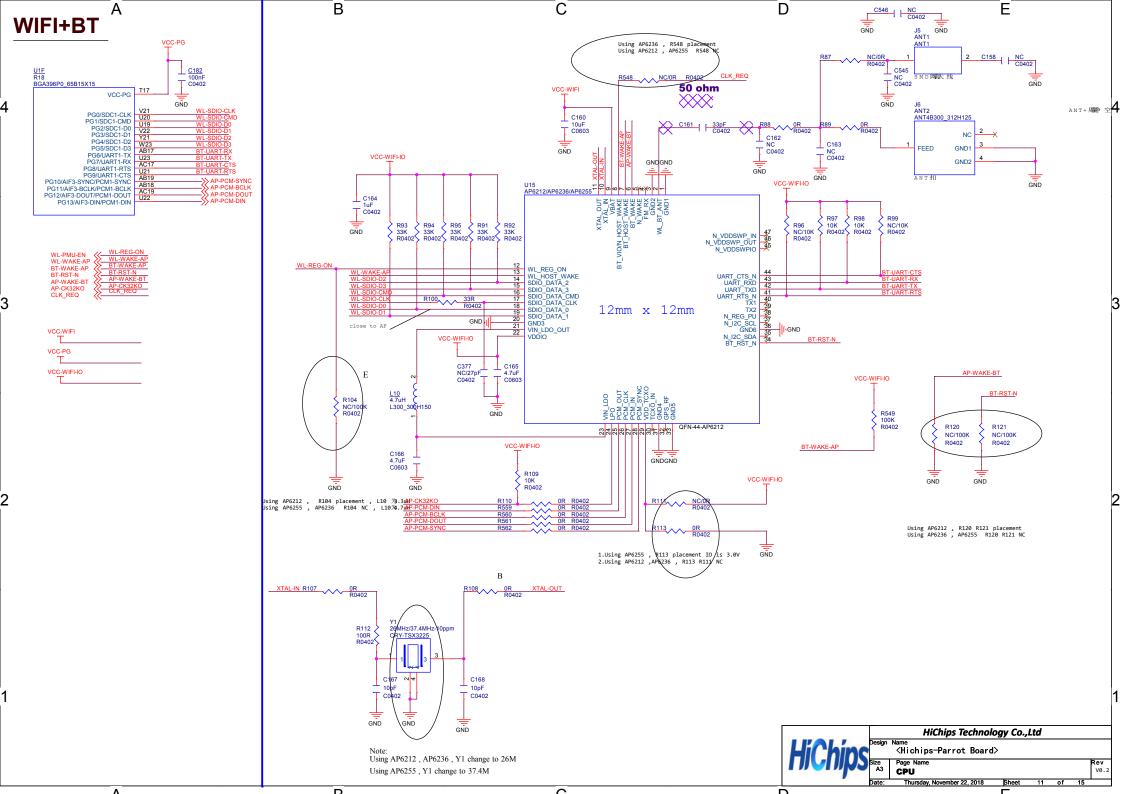
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Thursday, November 22, 2018

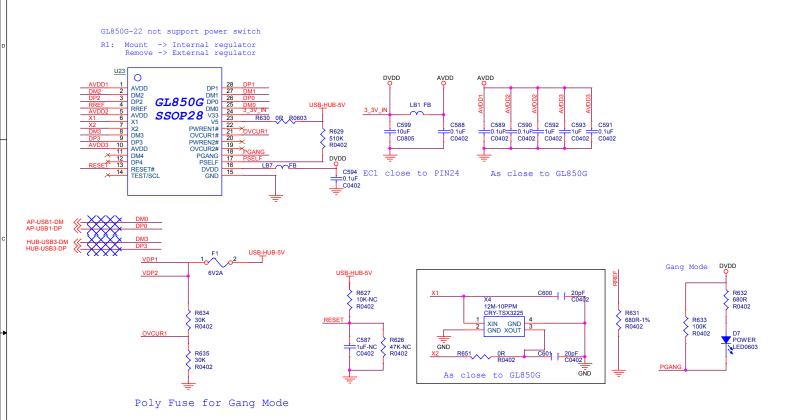
CPU

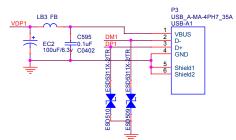
₹ev ∨0.2



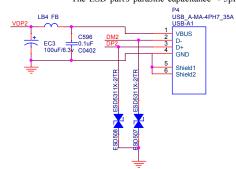


USB HUB



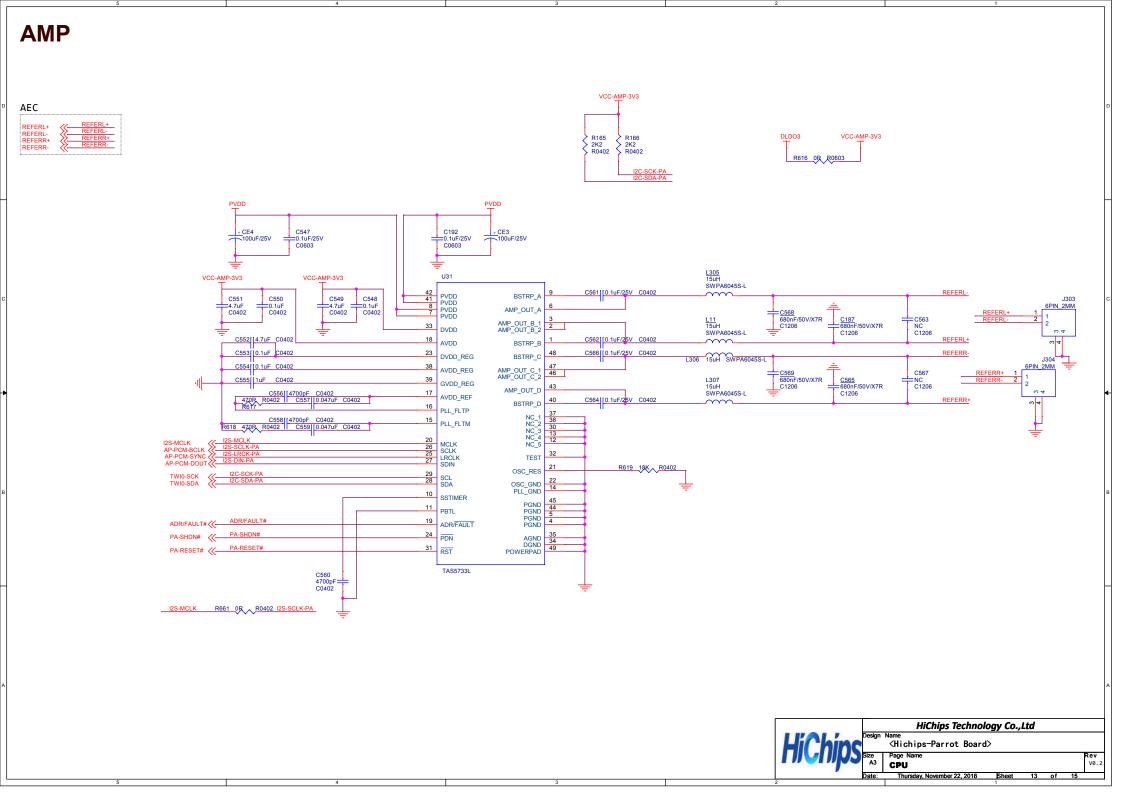


The ESD part's parasitic capacitance < 5pF.

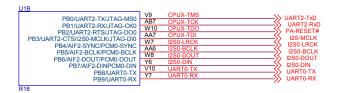


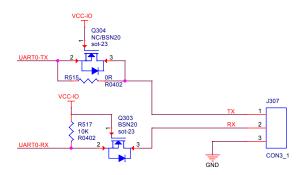
The ESD part's parasitic capacitance < 5pF.











HiChips Technology Co.,Ltd

Rev V0.2

Design Name 〈Hichips-Parrot Board〉

Thursday, November 22, 2018

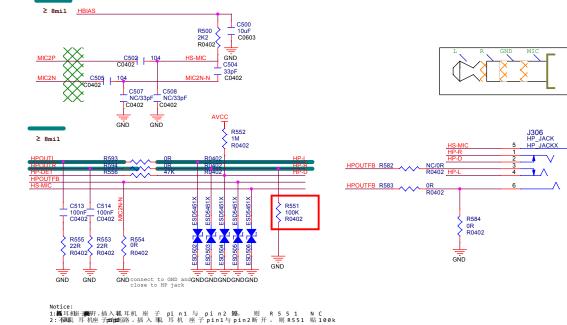
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CPUX-TCK CPUX-TDO CPUX-TMS CPUX-TDI CPUX-TDI CPUX-TDI CPUX-TDI TP94 TP95 TP96 TP97

Headphone



HP_JACK



BLOCK

