


PROJECT - MIDWAY

PCB specification:

- 8 CHANNEL 24V OUTPUT
- 5 CHANNEL AC220V SSR OUTPUT
- 4 CHANNEL TC (KTYPE) INPUT
- 4 CHANNEL RTD (PT100) INPUT
- 2 CHANNEL PRESSURE (0-5V) INPUT
- 2 CHANNEL RS232 COMMUNICATE
- 1 CHANNEL RS485 COMMUNICATE
- 1 CHANNEL DEBUG (TTL) IF
- 1 CHANNEL USB (DEBUG)

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Ref DWG		DO NOT SCALE DWG		Drawn By Date jackchen 2020-03-11		Note		WrtTitleNote	
SCH CIC0004339-A1		MATL		Eng/Des jackchen 2020-03-11		P/N		Drawing Number	
PCB CIC0004337-A1		FINISH		First Used On		CIC0004339		CIC0004339	
ASSY CIC0004338-A1		WrtFinish		Filename 1-Block Diagram.SchDoc		Scale		Sheet 1 of 10	
								Rev. A1	

A

B

C

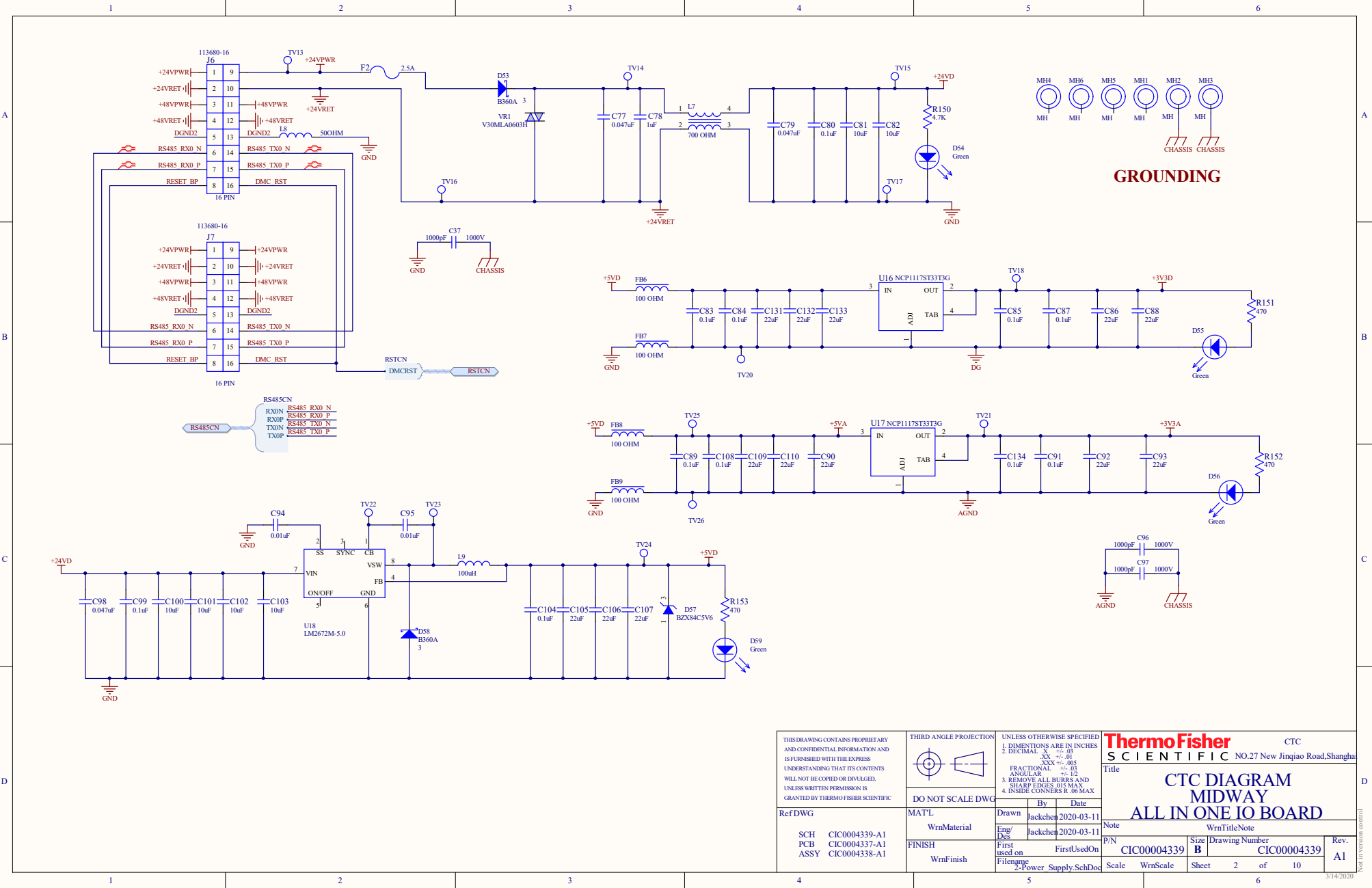
D

A

B

C

D



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		DO NOT SCALE DWG		Drawn By Date Jackchen 2020-03-11		Title CTC DIAGRAM MIDWAY ALL IN ONE BOARD		Note WrtTitleNote	
Ref DWG		MATL		Eng/Des Jackchen 2020-03-11		P/N		Size	
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PCB CIC0004337-A1		WrtFinish		File name		CIC00004339		2 of 10	
ASSY CIC0004338-A1		2-Power Supply.SchDoc		Scale		WrtScale		Rev.	
								A1	

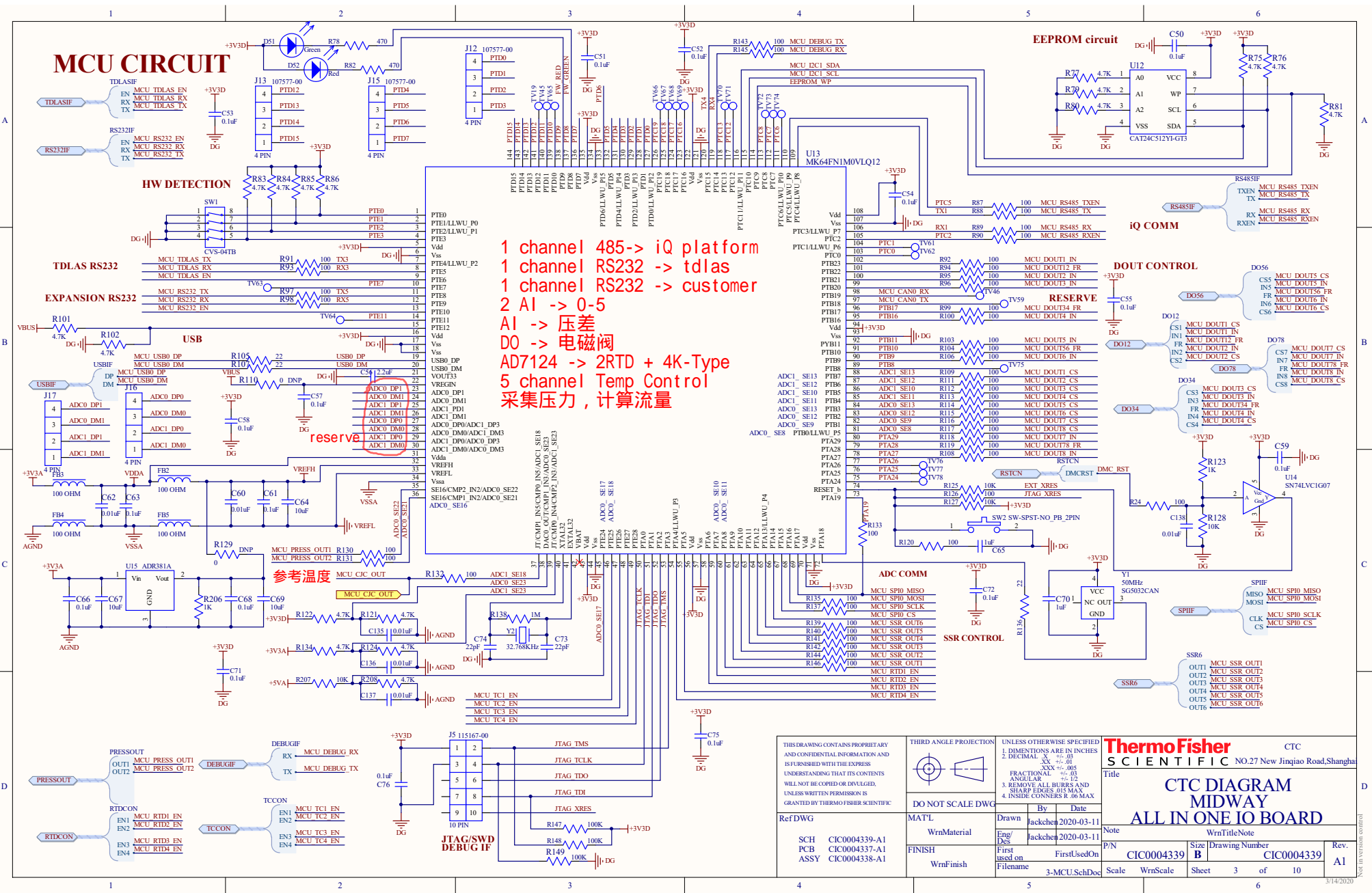
MCU CIRCUIT

1 channel 485-> iQ platform
1 channel RS232 -> tdlas
1 channel RS232 -> customer
2 AI -> 0-5
AI -> 压差
DO -> 电磁阀
AD7124 -> 2RTD + 4K-Type
采集压力, 计算流量

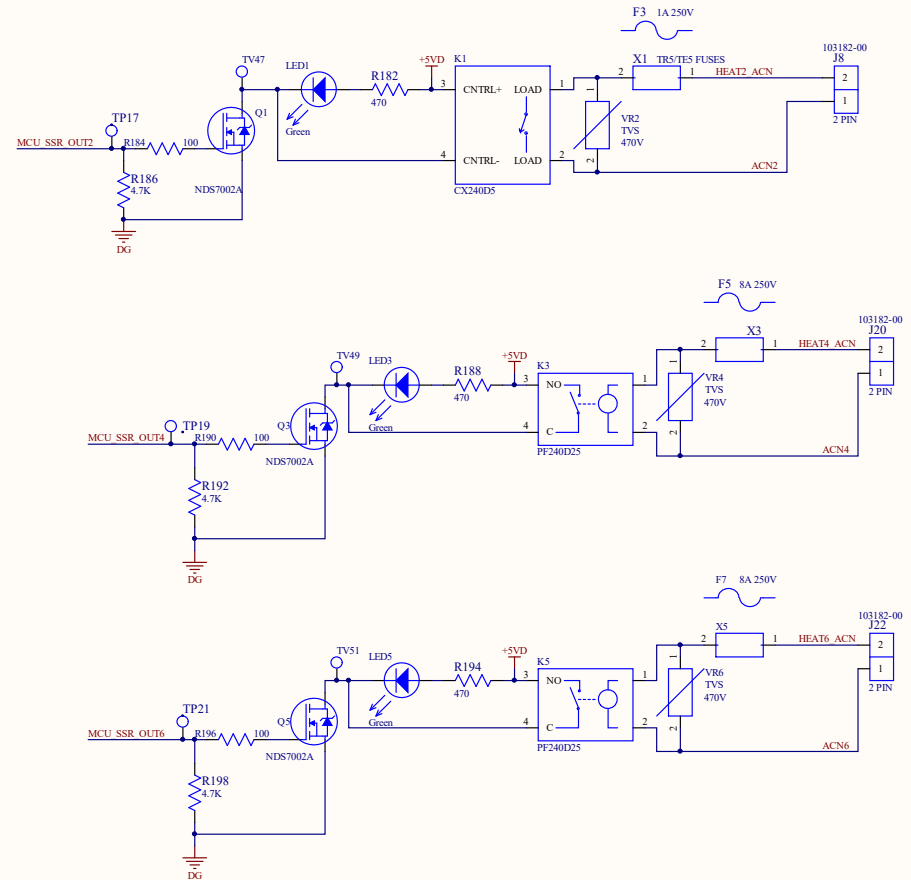
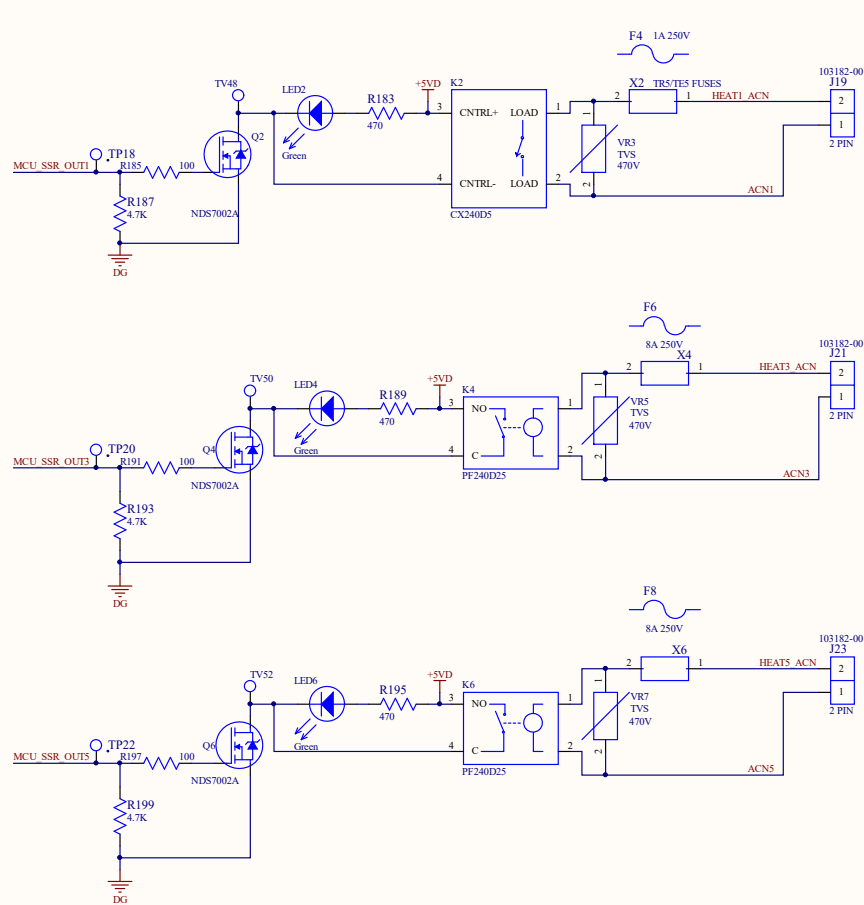
CTC
SCIENTIFIC NO.27 New Jinqiao Road, Shanghai

CTC DIAGRAM
MIDWAY
ALL IN ONE IO BOARD

Ref DWG	SCH PCB ASSY	CIC0004339-A1 CIC0004337-A1 CIC0004338-A1	Drawn Eng/Des First Used on Filename	jackchen 2020-03-11 jackchen 2020-03-11 3-MCU_SchDoc	Date	By	WnTitle	WnScale	Scale	Sheet	3	of	10	Rev.	A1
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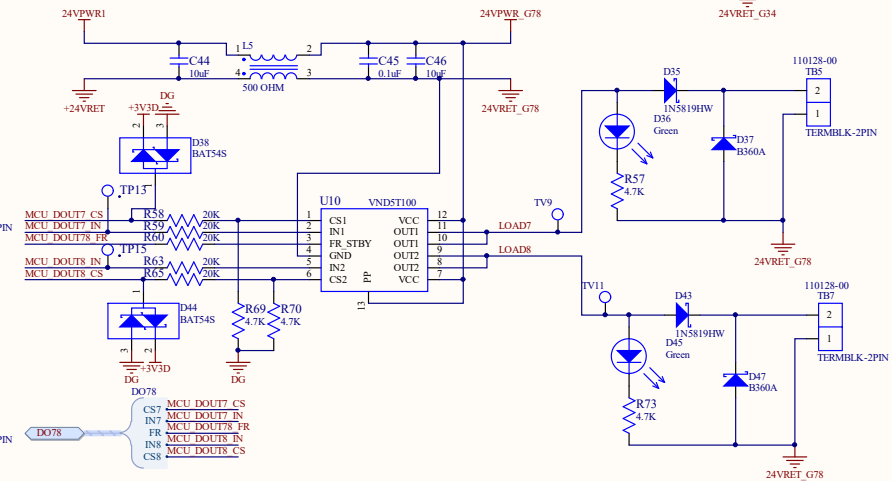
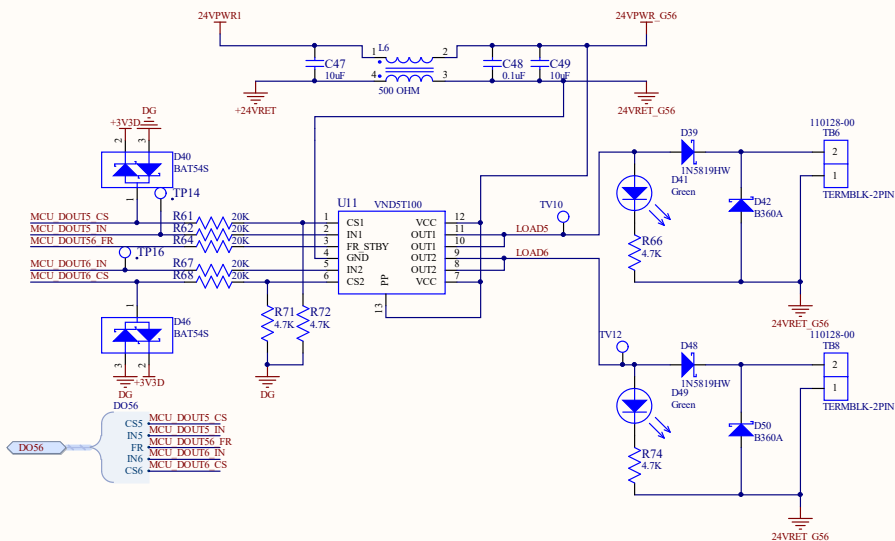
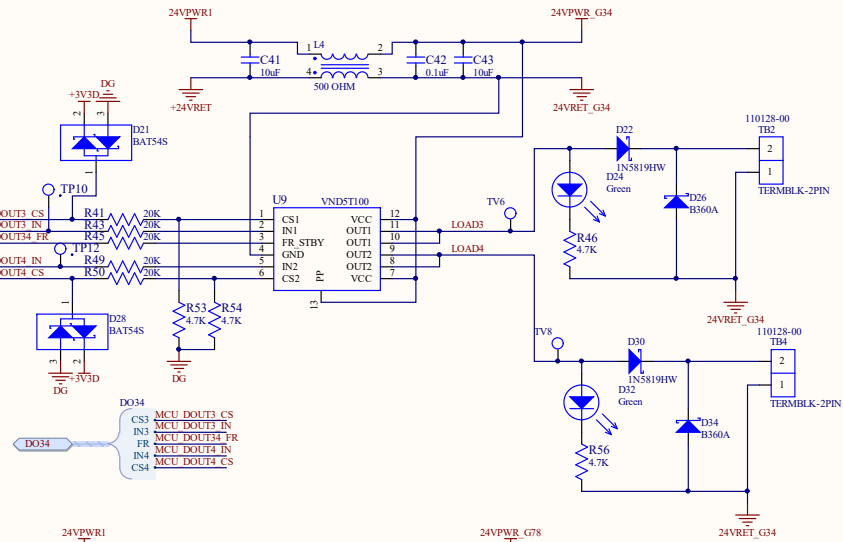
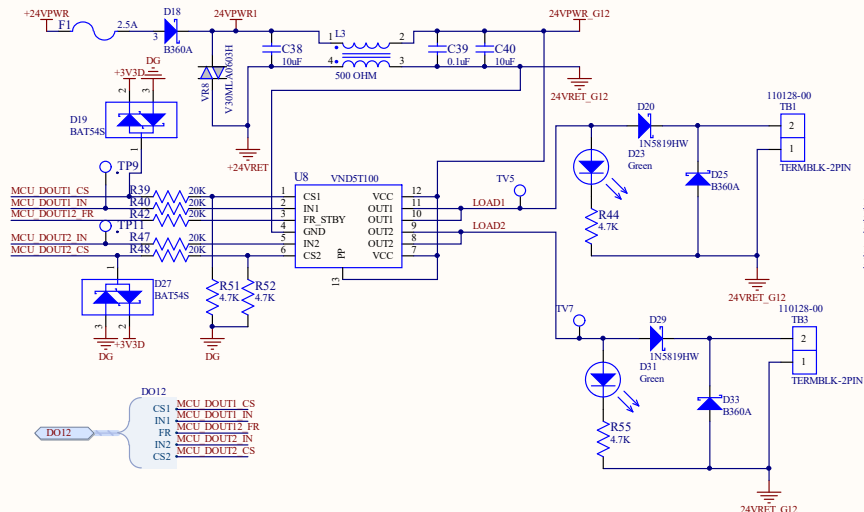


6 CHANNEL SSR CIRCUIT



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					Title		CTC DIAGRAM MIDWAY		
	DO NOT SCALE DWG				Note		WrtNtlnote		
	Ref/DWG		MAT'L		Drawn By Date		P/N		
	SCH CIC0004339-A1 PCB CIC0004337-A1 ASSY CIC0004338-A1		WrmMaterial		Eng/Des By Date jackchen 2020-03-11 jackchen 2020-03-11		Size B Drawing Number CIC0004339		Rev. A1
		FINISH		First used on File Name: _SSR_OUT.SchDoc		Scale WrmScale		Sheet 4 of 10	
		WrmFinish							

8 CHANNEL 24VOUT CIRCUIT



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RefDWG	
SCH	CIC0004339-A1
PCB	CIC0004337-A1
ASSY	CIC0004337-A1

DO NOT SCALE DWG

MATL	WrnMaterial
FINISH	WrnFinish

UNLESS OTHERWISE SPECIFIED

1. DIMENSIONS ARE IN INCHES

2. DECIMAL .X +/- .03
.XX +/- .01
.XXX +/- .005
FRACTIONAL +/- .03
ANGULAR +/- 1/2

3. REMOVE ALL BURRS AND SHARP EDGES .015 MAX

4. INSIDE CONNERS R .06 MAX

	By	Date
Drawn	Jackchen	2020-03-11
Eng/Des	Jackchen	2020-03-11
First used on	FirstUsedOn	

ThermoFisher CTC
SCIENTIFIC NO.27 New Jinqiao Road, Shanghai

Title	CTC DIAGRAM MIDWAY ALL IN ONE IO BOARD	D
1		strol

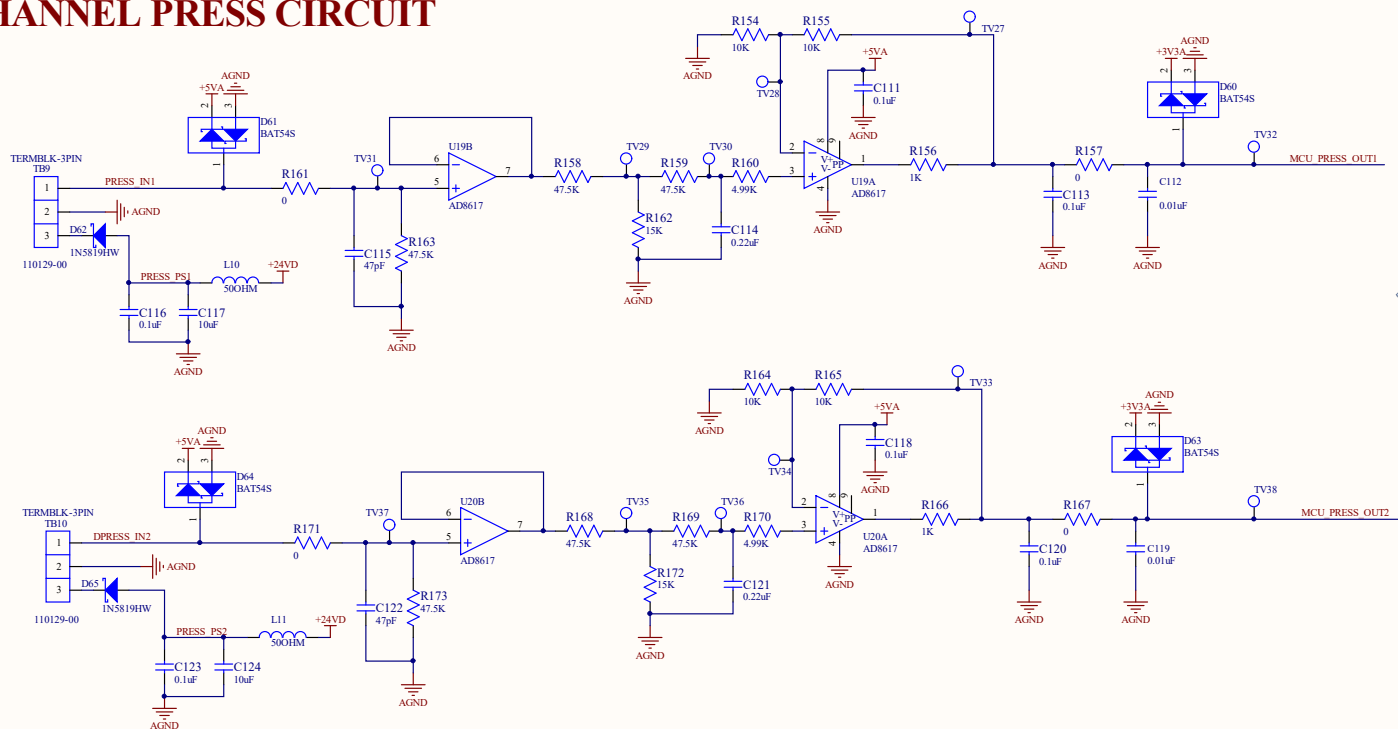
Note	WrnTitleNote				
P/N	CIC0004339	Size B	Drawing Number CIC0004339		Rev. A1
Scale	WrnScale	Sheet 5	of 10		


Not in version control

Diagram illustrating the connection of the PRESSOUT signal to the MCU:

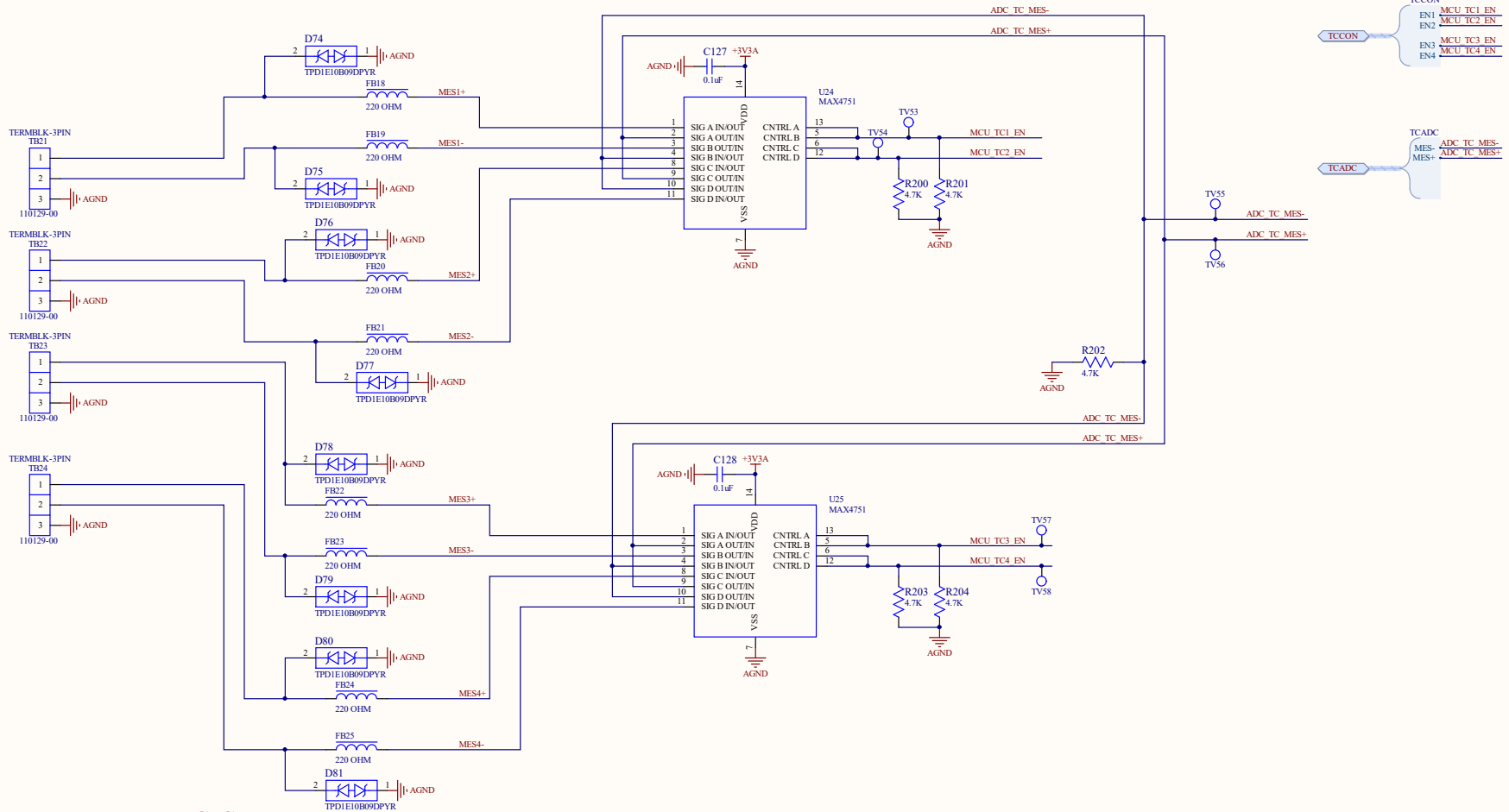
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    graph LR
    PRESSOUT --> OUT1
    PRESSOUT --> OUT2
    OUT1 -- "MCU PRESS OUT1" --> MCU
    OUT2 -- "MCU PRESS OUT2" --> MCU
  
```

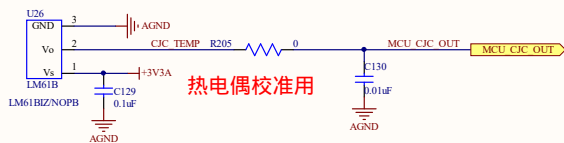


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		DO NOT SCALE DWG					
Ref/DWG		MAT'L	Drawn	By	Date	Title	
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SCH PCB ASSY		CIC0004339-A1 CIC0004337-A1 CIC0004338-A1	Eng/ Des	Jackchen	2020-03-11	Note	
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			Scale	WrnScale	Sheet	6 of 10	Rev. A1

4 CHANNEL TC CIRCUIT



CJC

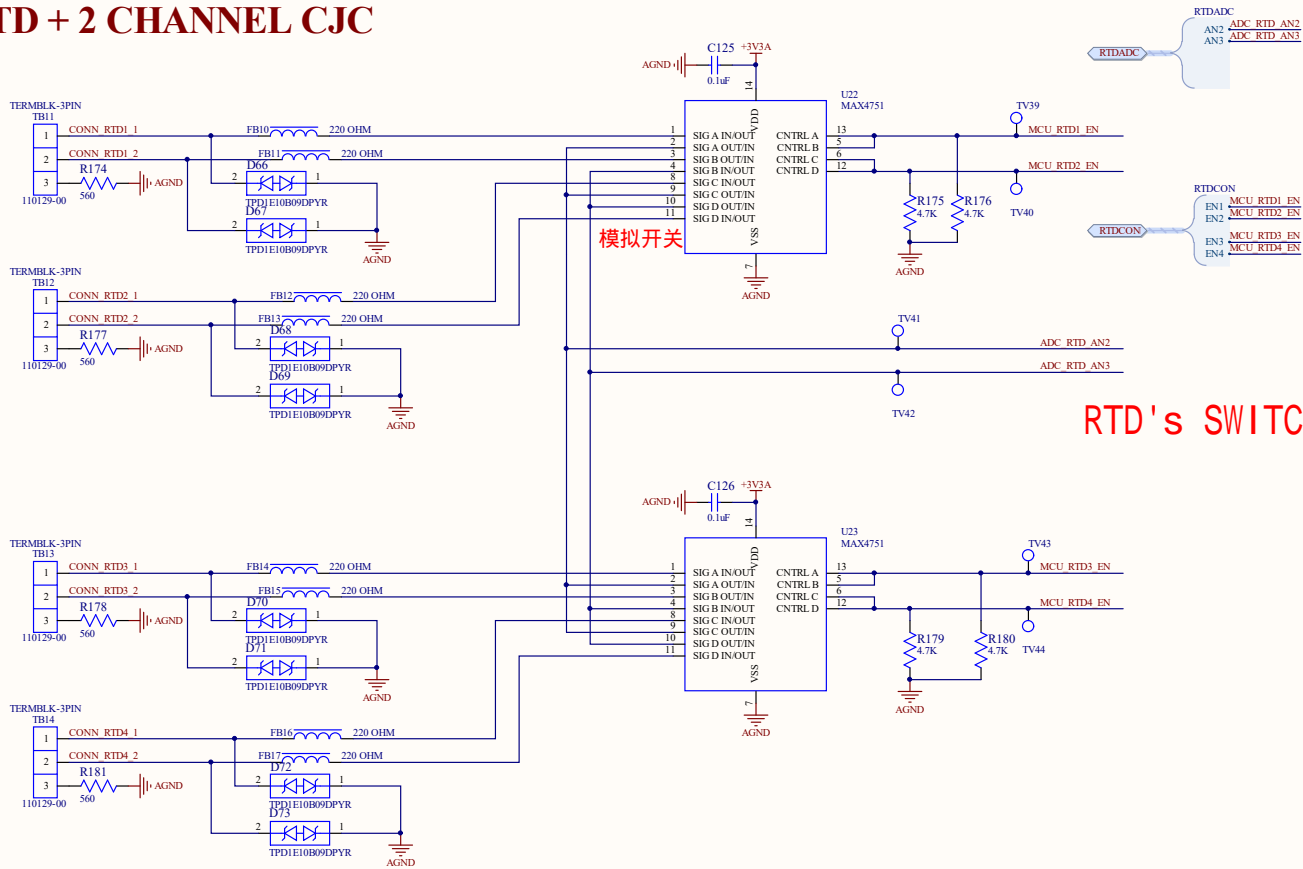


热电偶校准用


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						Title				
		DO NOT SCALE DWG				CTC DIAGRAM MIDWAY				
						ALL IN ONE IO BOARD				
Ref DWG	MAT'L	Drawn	By	Date	Note	P/N	Size	B	WrmTitleNote	Rev.
SCH PCB ASSY	CIC0004339-A1 CIC0004337-A1 CIC0004338-A1	WrmMaterial	Jackchen	2020-03-11		CIC0004339	Sheet	7	of 10	A1
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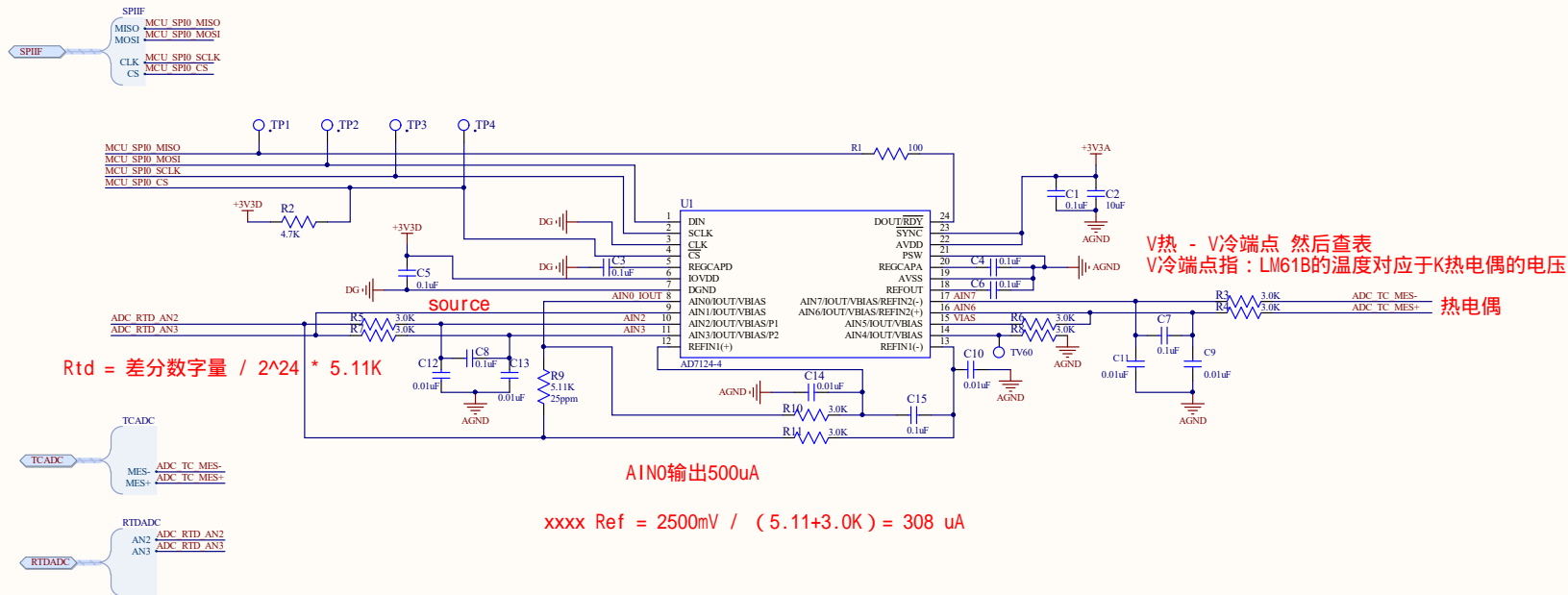
NOT REVISIONS REQUIRED

2 CHANNEL RTD + 2 CHANNEL CJC



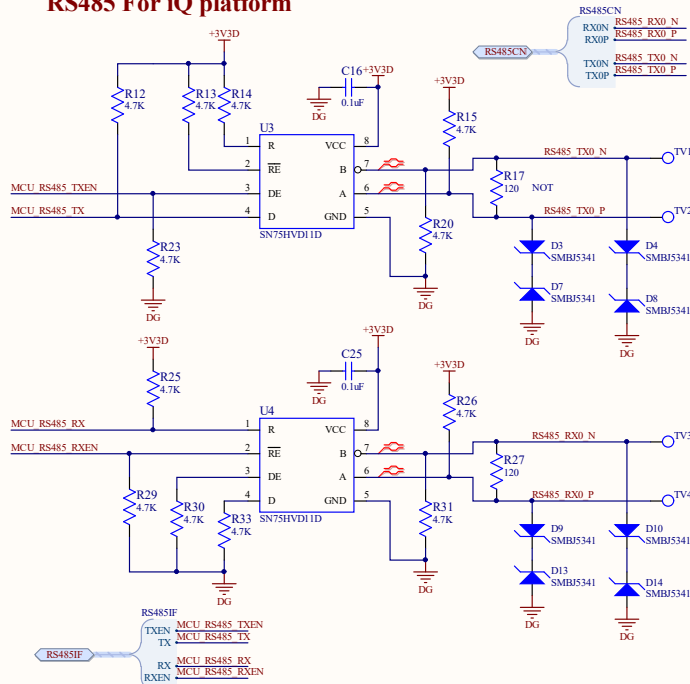
RTD's SWITCH 开关

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DO NOT SCALE DWG		MATL		Drawn By Date jackchen 2020-03-11		Title CTC DIAGRAM MIDWAY ALL IN ONE IO BOARD	
Ref DWG SCH CIC0004339-A1 PCB CIC0004337-A1 ASSY CIC0004338-A1		WrmMaterial FINISH WrmFinish		Eng/Des FirstUsedOn jackchen 2020-03-11		Note WrmTitleNote P/N CJC0004339 Size B Drawing Number CJC0004339	
Filename 8-RTD Switch.SchDoc		Scale WrmScale		Sheet 8 of 10		Rev. A1	

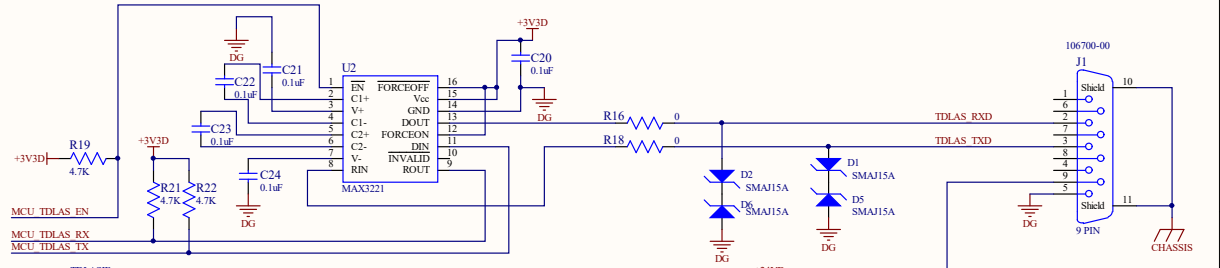


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					Title		CTC DIAGRAM MIDWAY		
	DO NOT SCALE DWG				By Date		ALL IN ONE IO BOARD		
	Ref/DWG				MAT'L		Note		WmTitleNote
SCH PCB ASSY	CIC0004339-A1 CIC0004337-A1 CIC0004338-A1	WmMaterial	Drawn Eng/Des	Jackchen Jackchen	2020-03-11	P/N	Size B	Drawing Number	Rev.
FINISH		WmFinish	First Used On	FirstUsedOn		CIC0004339	B	CIC0004339	A1
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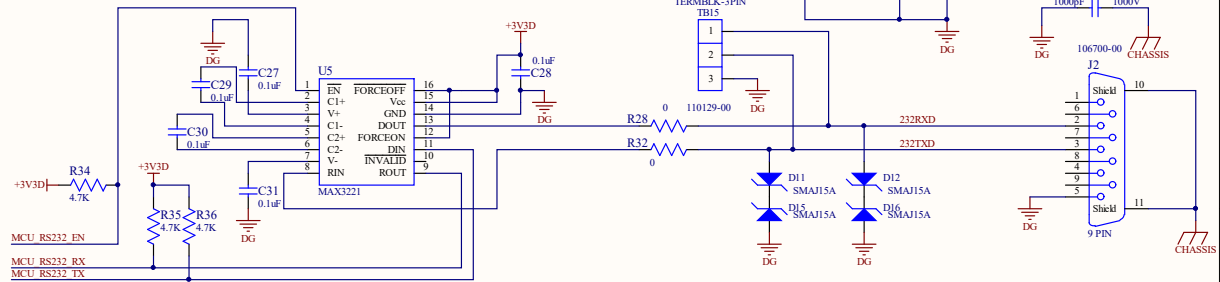
RS485 For iQ platform



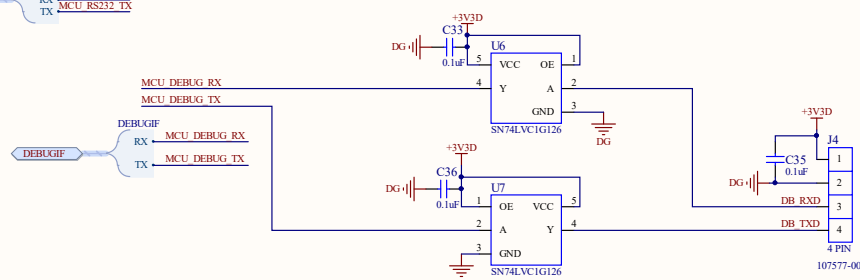
RS232 For TDLAS module



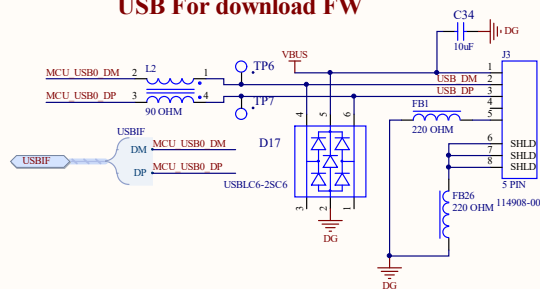
RS232 For Costumer Expansion




UART for FW debug



USB For download FW



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					Title		
	DO NOT SCALE DWG				CTC DIAGRAM MIDWAY		
					ALL IN ONE IO BOARD		
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