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1. GENERAL DESCRIPTION

NTA92C is a LCD monitor control board, the resolution is up to 1920×1200. It supports LED control board adopting single/dual LVDS connector. Its color reach 24bit potentially. H-Frequency signal range from 30 to 80 KHz, V-Frequency signal 56 to 75 Hz.

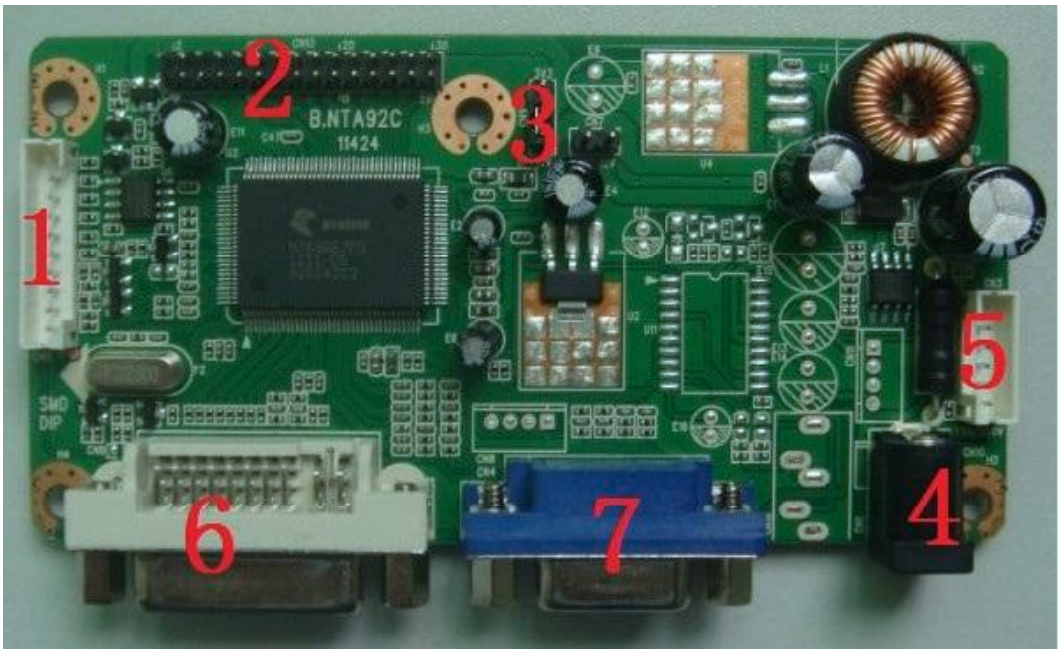
NTA92C supports HDCP(Optional), synchronize automatic detection and Dynamic Contrast Control, apply to Sync Separator for extracting Hsync and Vsync.

2. FEATURES

CHIPSET	NT68667(FG/HFG)/NT68667(UFG)	
PANEL	Interface	Dual LVDS
	Resolution	1680×1050(FG/HFG)/1920×1200(UFG)
POWER	Requirement	5V, 12V
	To Panel	3.3V(Max 700mA), 5V, 12V
	Management	Low power consumption mode, standby
	standby	<1W
OSD LANGUAGE	Simple Chinese、Traditional Chinese、English、French、German，Italian、Spanish、Portuguese、Japanese、Korean	
KEY FUNCTION	POWER, MENU, +/-, AUTO/EXIT	

3. FUNCTION LAYOUT

TOP VIEW OF LCD CONTROL BOARD

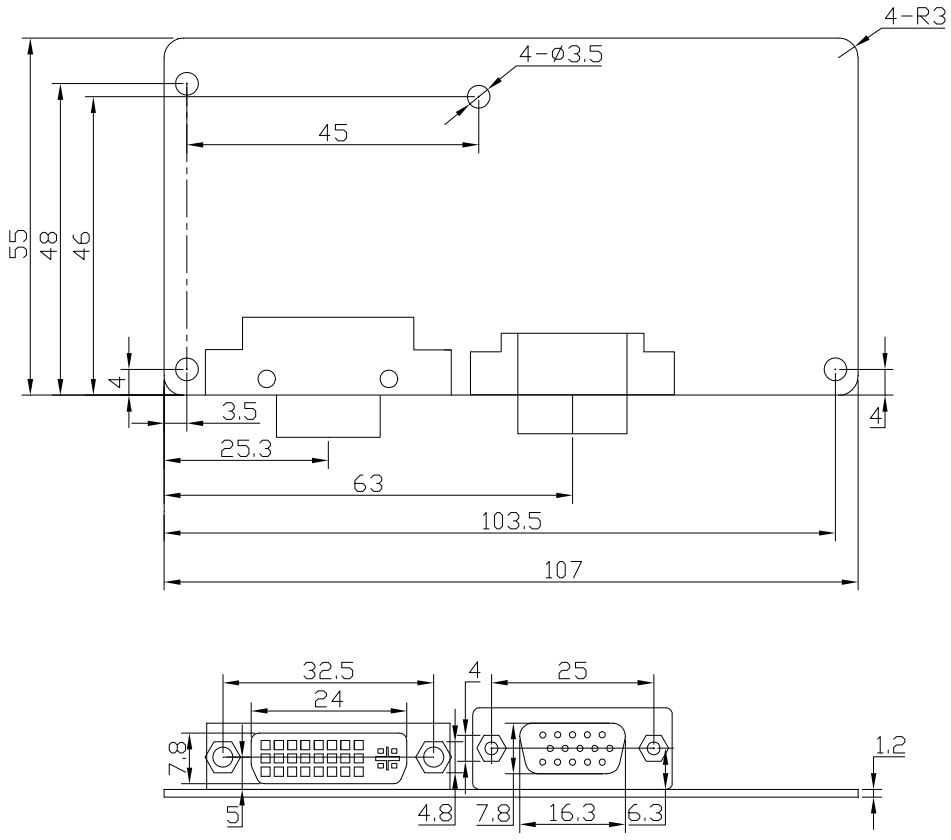


1.KeyBoard 2.LVDS interface 3.LCD Voltage Jumper (3.3V 5V 12V for LCD Panel)

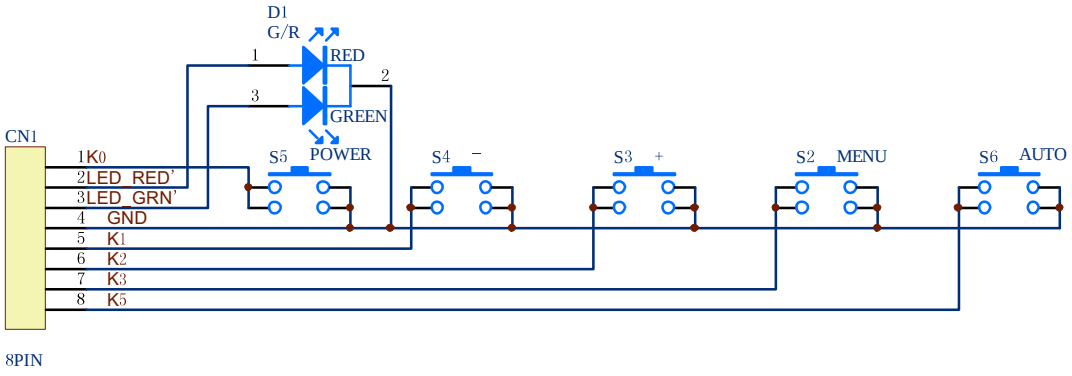
4.Powersupply(12V) 5.Inverter 6.DVI Input 7.VGA input

4. PCB DIMENSION

The overall height of NTA92C is 19.0mm.



5. KEY BOARD SCHEMATIC DIAGRAM



6. INTERFACE DEFINITION

◆ CN3(6PIN/2.0): INVERTER CONNECTOR

NO.	SYMBOL	DESCRIPTION
1	12V	+12V DC Power Supply
2	12V	
3	BLON	Back-Light ON/OFF Control for Panel
4	ADJ	Brightness Adjustment for Panel
5	GND	Ground
6	GND	

◆ CN5(10PIN/2.0): KEY BOARD CONNECTOR

NO.	SYMBOL	DESCRIPTION
1	K0	Key 0
2	RED	Red Indicator
3	GRN	Green Indicator
4	GND	Ground
5	K1	Key 1
6	K2	Key 2
7	K3	Key 3
8	K4	Key 4
9	K5	Key 5
10	K6	Key 6

◆ **CN1:** POWER SUPPLY CONNECTOR

NO.	SYMBOL	DESCRIPTION
1	GND	Ground (Side)
2	12V	+12V DC Power Supply (Center)

◆ CN13(2× 15PIN/2.0): LVDS INTERFACE

NO.	SYMBOL	DESCRIPTION
1	VSEL	Power Supply for panel
2	VSEL	
3	VSEL	
4	GND	Ground
5	GND	
6	GND	
7	RXO0-	LVDS ODD 0- Signal
8	RXO0+	LVDS ODD 0+ Signal
9	RXO1-	LVDS ODD 1- Signal
10	RXO1+	LVDS ODD 1+ Signal
11	RXO2-	LVDS ODD 2- Signal
12	RXO2+	LVDS ODD 2+ Signal
13	GND	Ground
14	GND	
15	RXOC-	LVDS ODD Clock- Signal
16	RXOC+	LVDS ODD Clock+ Signal
17	RXO3-	LVDS ODD 3- Signal
18	RXO3+	LVDS ODD 3+ Signal
19	RXE0-	LVDS EVEN 0- Signal
20	RXE0+	LVDS EVEN 0+ Signal
21	RXE1-	LVDS EVEN 1- Signal
22	RXE1+	LVDS EVEN 1+ Signal
23	RXE2-	LVDS EVEN 2- Signal
24	RXE2+	LVDS EVEN 2+ Signal
25	GND	Ground
26	GND	
27	RXEC-	LVDS EVEN Clock- Signal
28	RXEC+	LVDS EVEN Clock+ Signal
29	RXE3-	LVDS EVEN 3- Signal
30	RXE3+	LVDS EVEN 3+ Signal

7. CONFIGURATION & GENERAL PRECAUTIONS

- Relative Humidity：≤80％
- Storage Temperature：-10～+60℃
- Operation Temperature：0～+40℃
- Keep away from static places and water
- Must not be pressed and distorted
- Do not disassemble the module.
- Protect it away from contacting with metal or other electric substance.