**Requirements:**

|  |  |
| --- | --- |
| **ID** | **Descriptions** |
| H\_01 | To perform basic logic gates operations |
| H\_01\_L\_01 | Perform NOT Gate |
| H\_02 | Operations of 2 Input Gates |
| H\_02\_L\_01 | Perform 2 Input AND Gate |
| H\_02\_L\_02 | Perform 2 Input OR Gate |
| H\_02\_L\_03 | Perform 2 Input NAND Gate |
| H\_02\_L\_04 | Perform 2 Input NOR Gate |
| H\_02\_L\_05 | Perform 2 Input XOR Gate |
| H\_02\_L\_06 | Perform 2 Input XNOR Gate |
| H\_03 | Operations of 3 Input Gates |
| H\_03\_L\_01 | Perform 3 Input AND Gate |
| H\_03\_L\_02 | Perform 3 Input OR Gate |
| H\_03\_L\_03 | Perform 3 Input NAND Gate |
| H\_03\_L\_04 | Perform 3 Input NOR Gate |
| H\_03\_L\_05 | Perform 3 Input XOR Gate |