./

Learning Report –

Embedded C

Course Code: <CODE>



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| **Ver. Rel. No.** | **Release Date** | **Prepared. By** | **Reviewed By** | **Approved By** | **Remarks/Revision Details** |
| 1 | 30th Sept 2020 | Amit Das |  |  | MCU specific header files for GPIO and RCC |
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**Document History**

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# Activity 1: Linker Script

[Link](https://github.com/99002591/GENESIS_WEEK3/blob/master/STM32_Linker_example.txt)

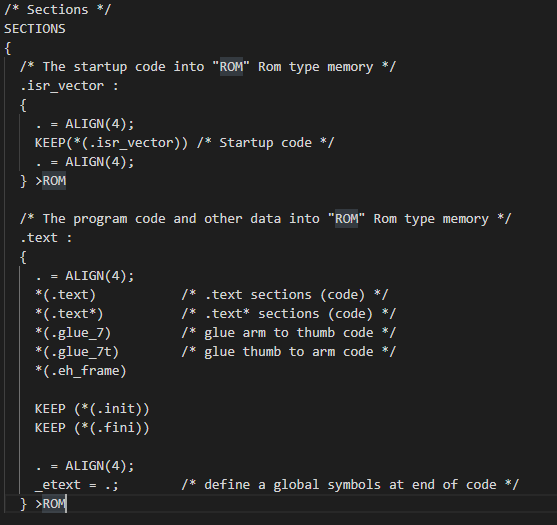


Figure : Linker section

# Activity 2: Semi Hosting

[Link](https://github.com/99002591/GENESIS_WEEK3/tree/master/Learn01)

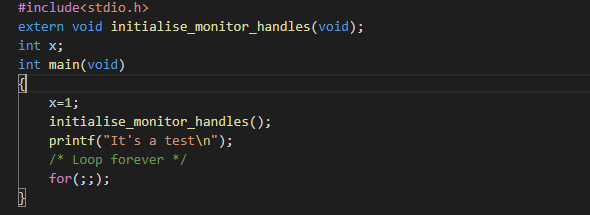


Figure : Code Snippet for semi hosting

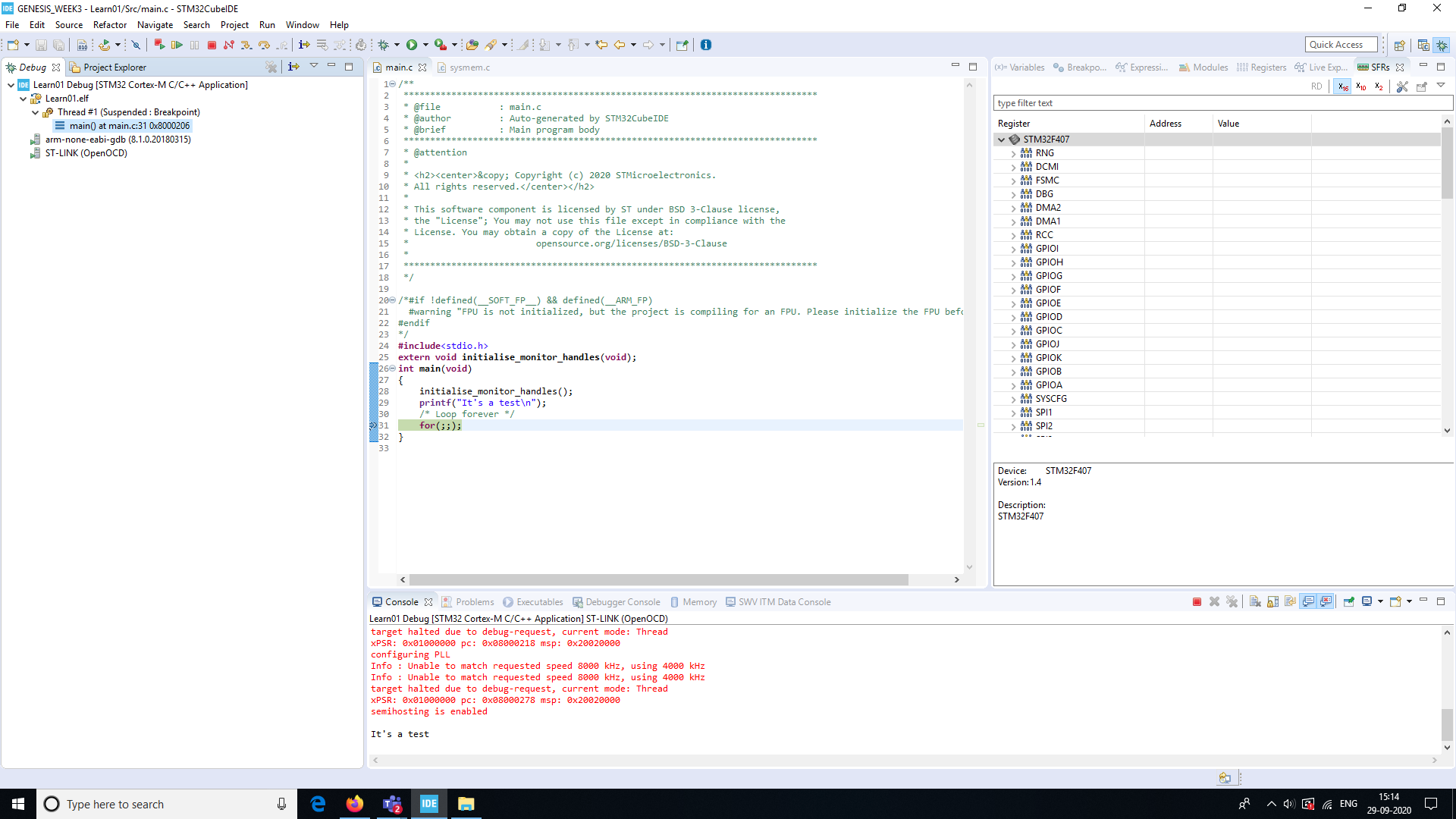


Figure : Semi hosting enabled

# Activity 3: Changing bit state of SPI1 SPE

[Link](https://github.com/99002591/GENESIS_WEEK3/tree/master/Activity001)

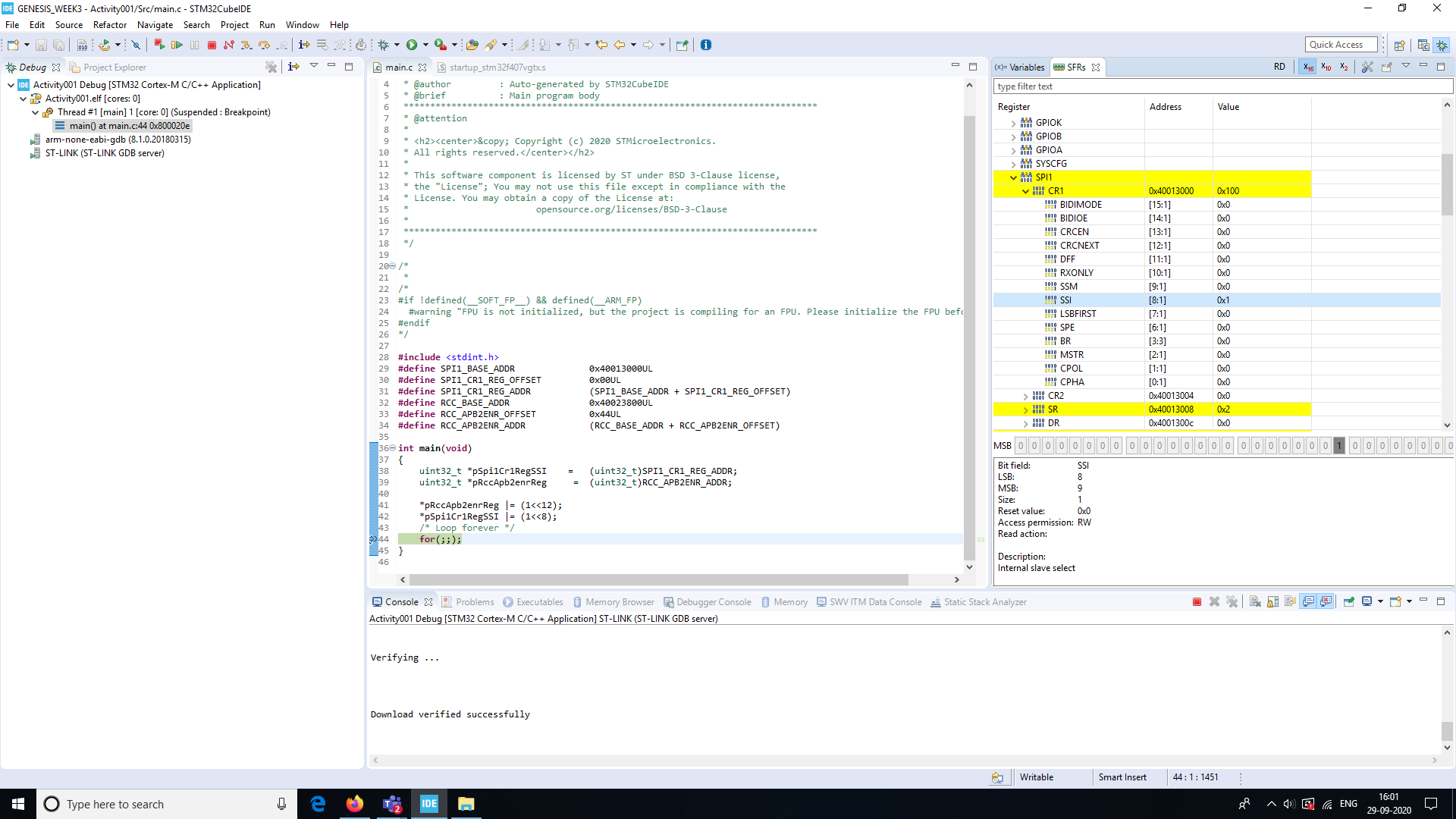


Figure : Bit modification of SPI1 SSI

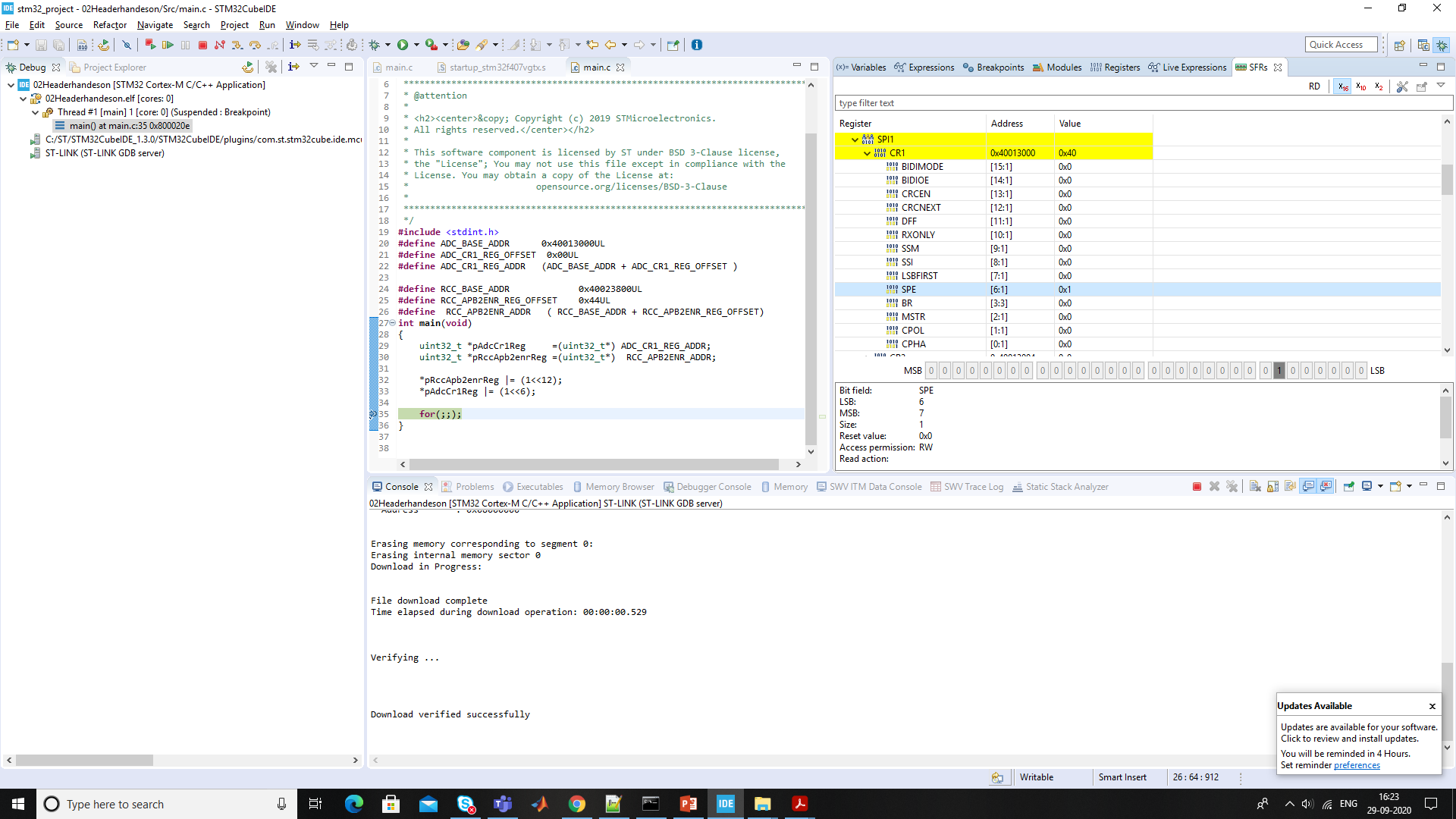


Figure : Bit modification of SPI1 SPE

# Activity 4: Debugging techniques

## Serial wire viewer and data tracing

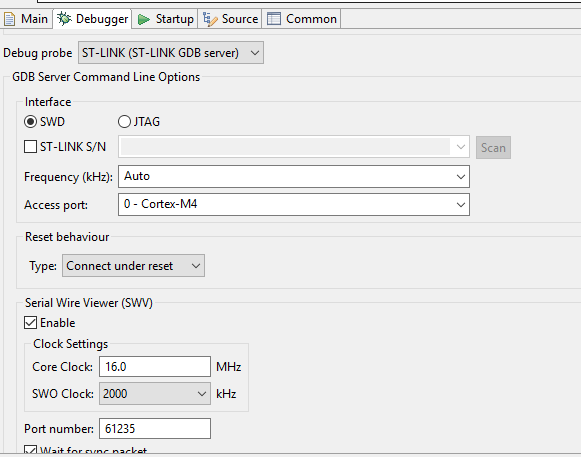


Figure : Enable serial wire viewer

## Single stepping, stepping over and stepping out

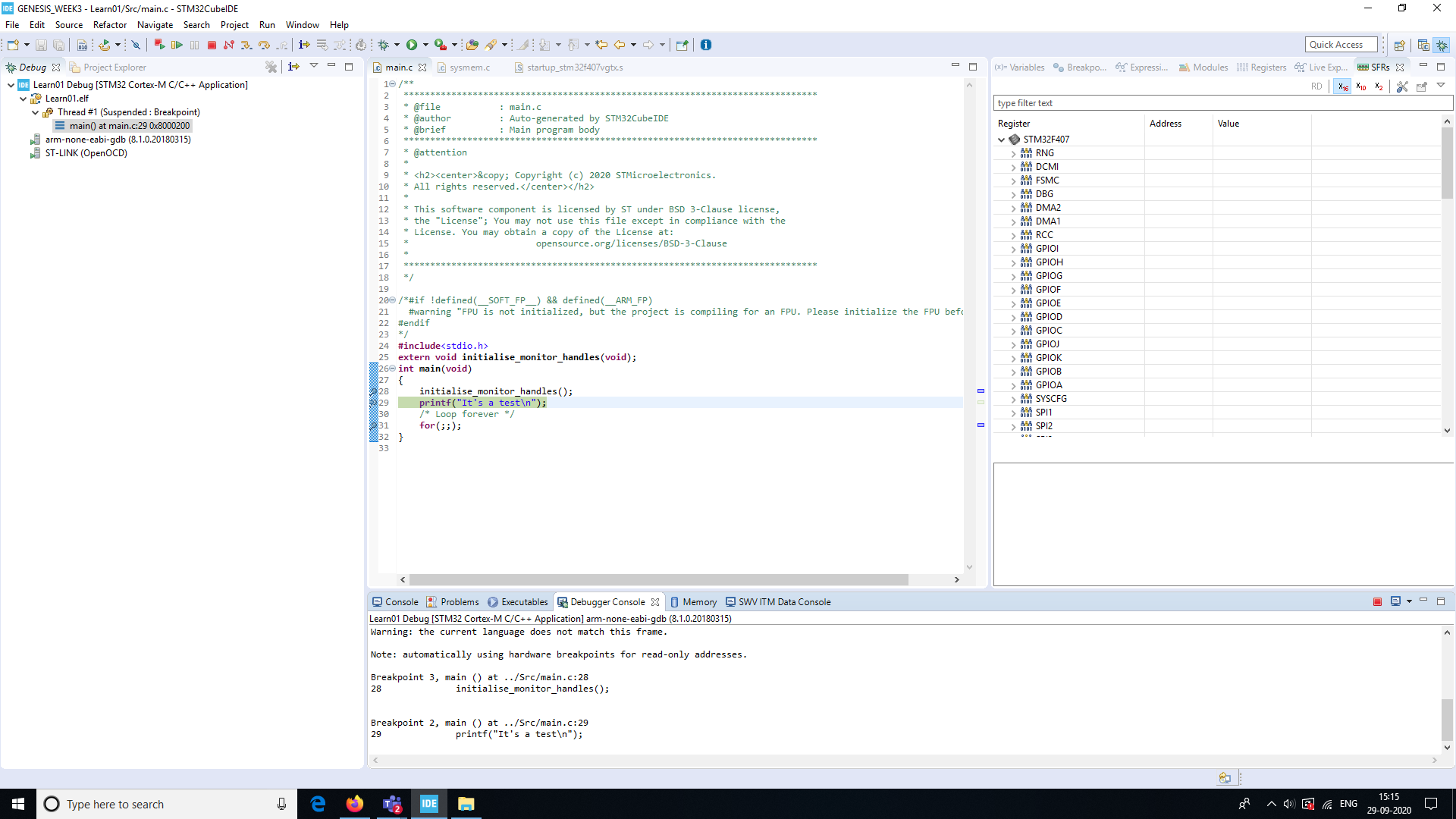


Figure : Breakpoint stepping

## Breakpoints

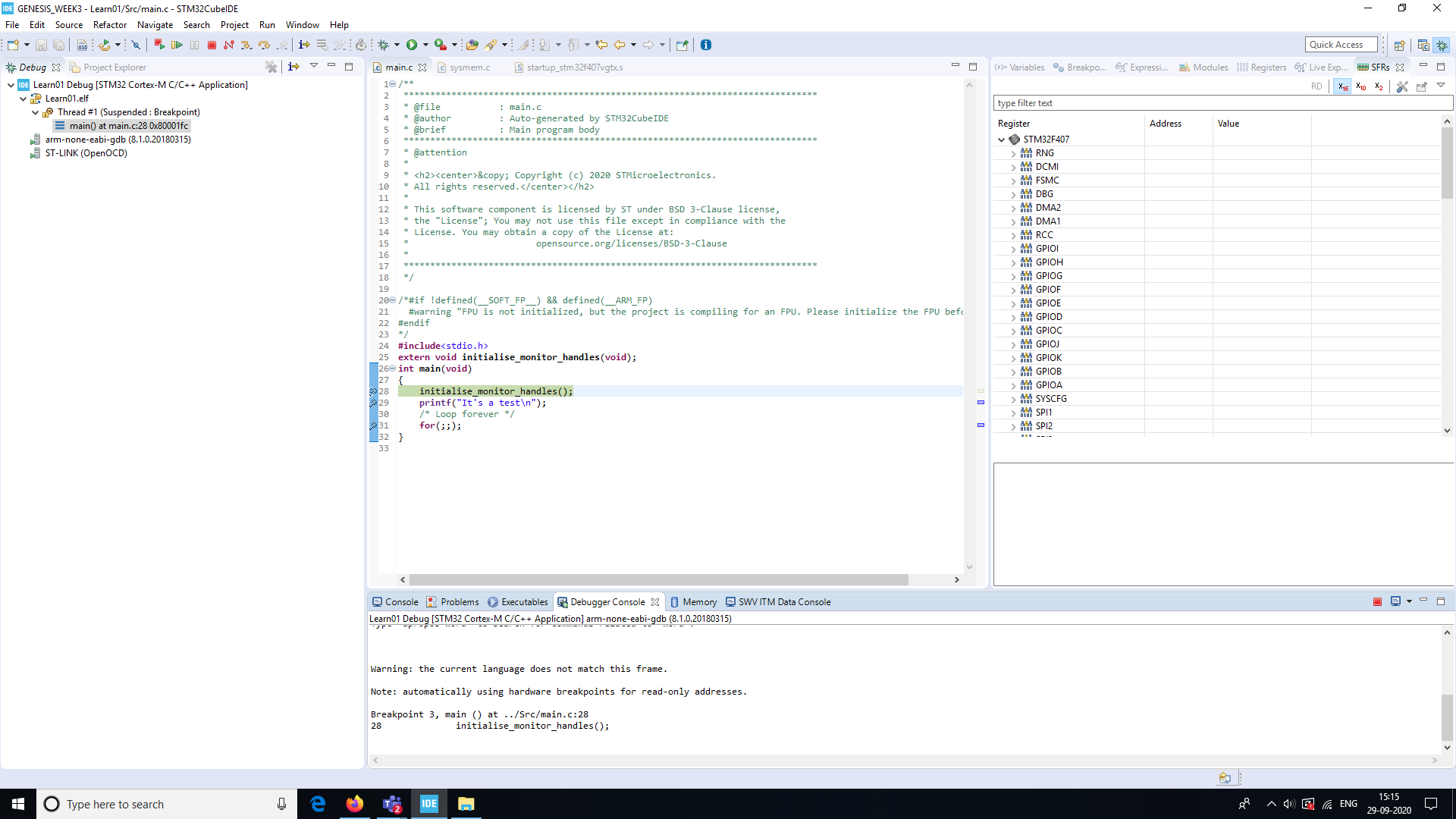


Figure : Multiple breakpoints

## Call stack (Static stack analyzer)

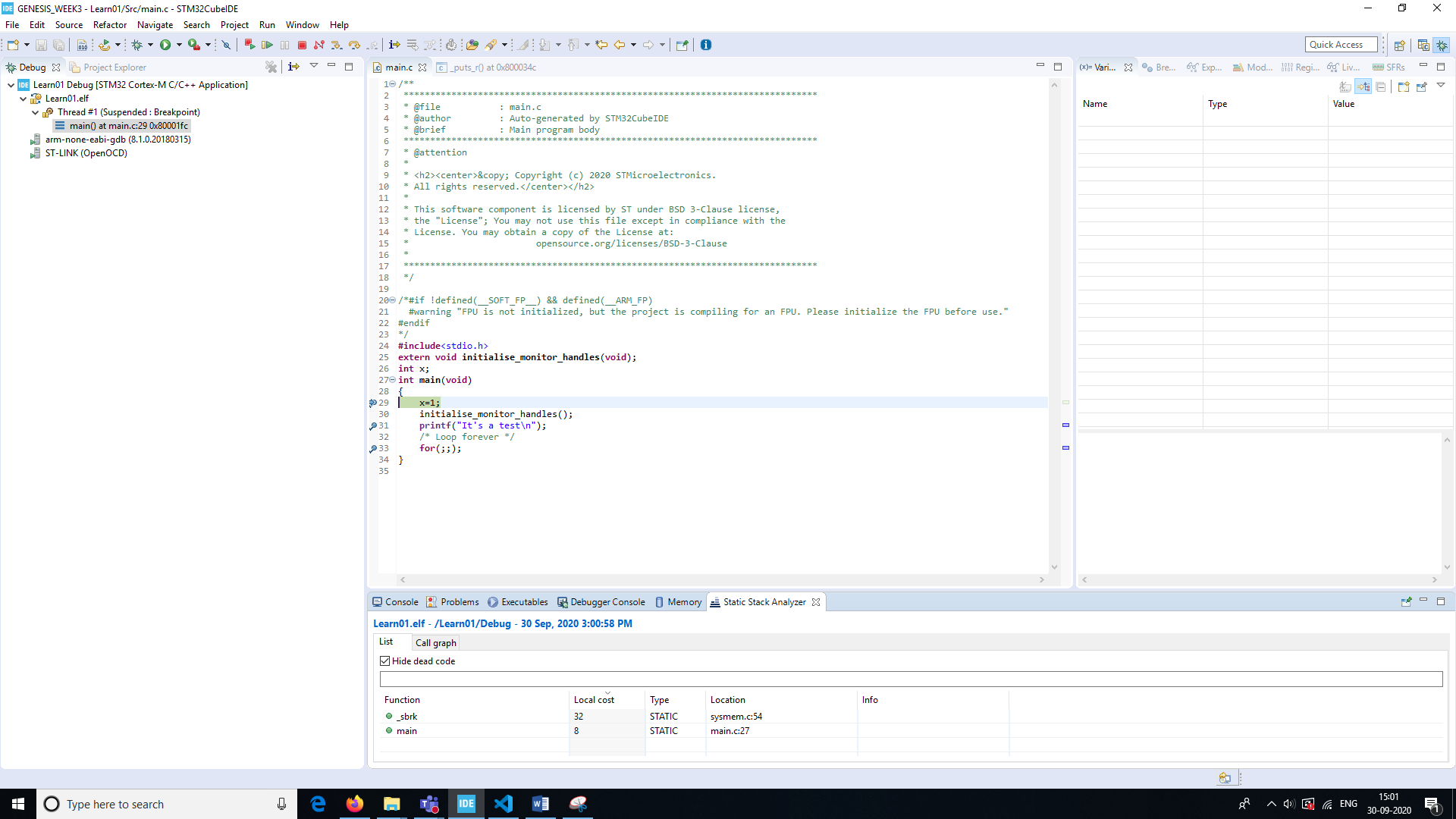


Figure : Static stack analyzer

## Expression and Variable window

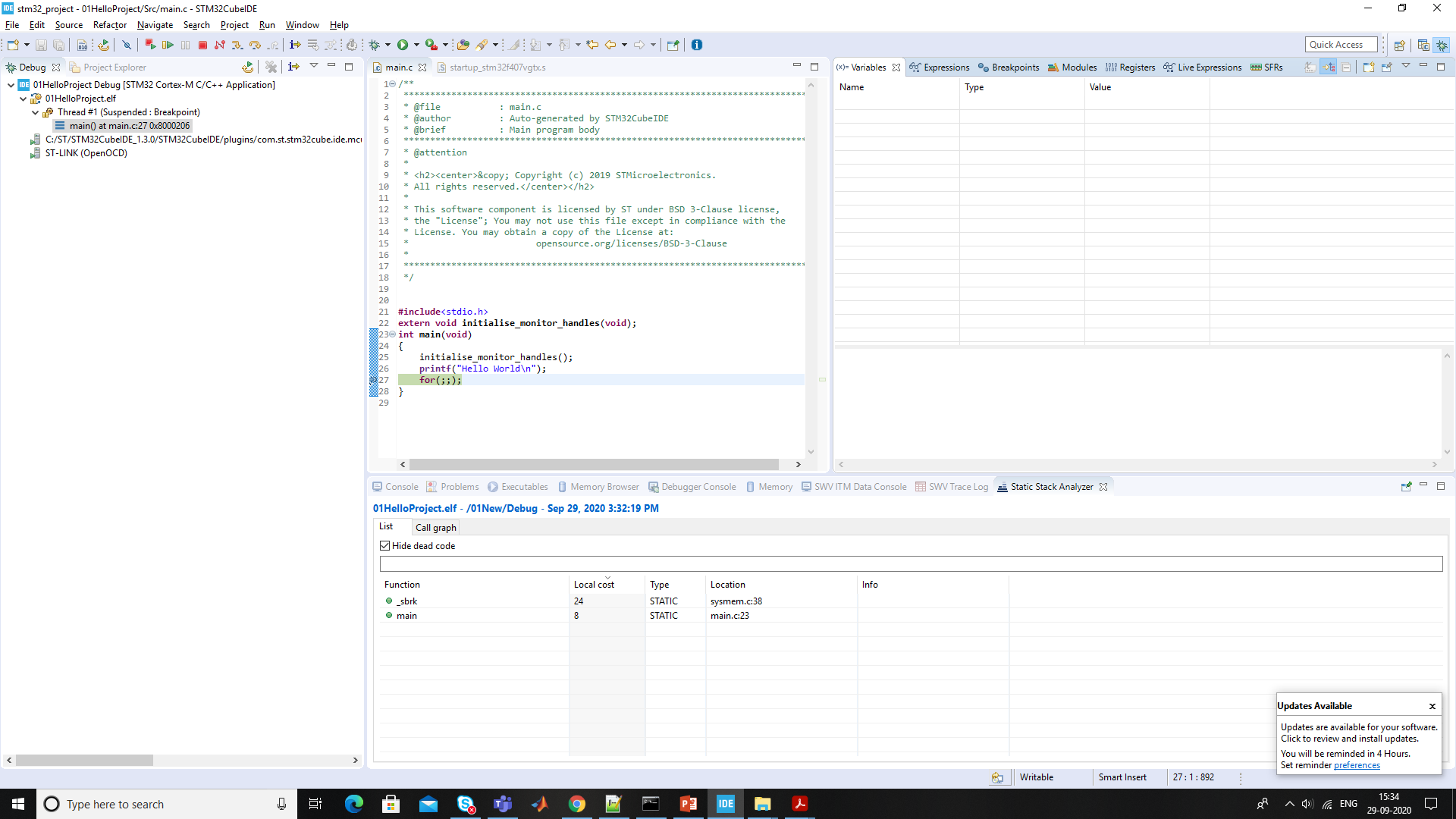


Figure : Variable window

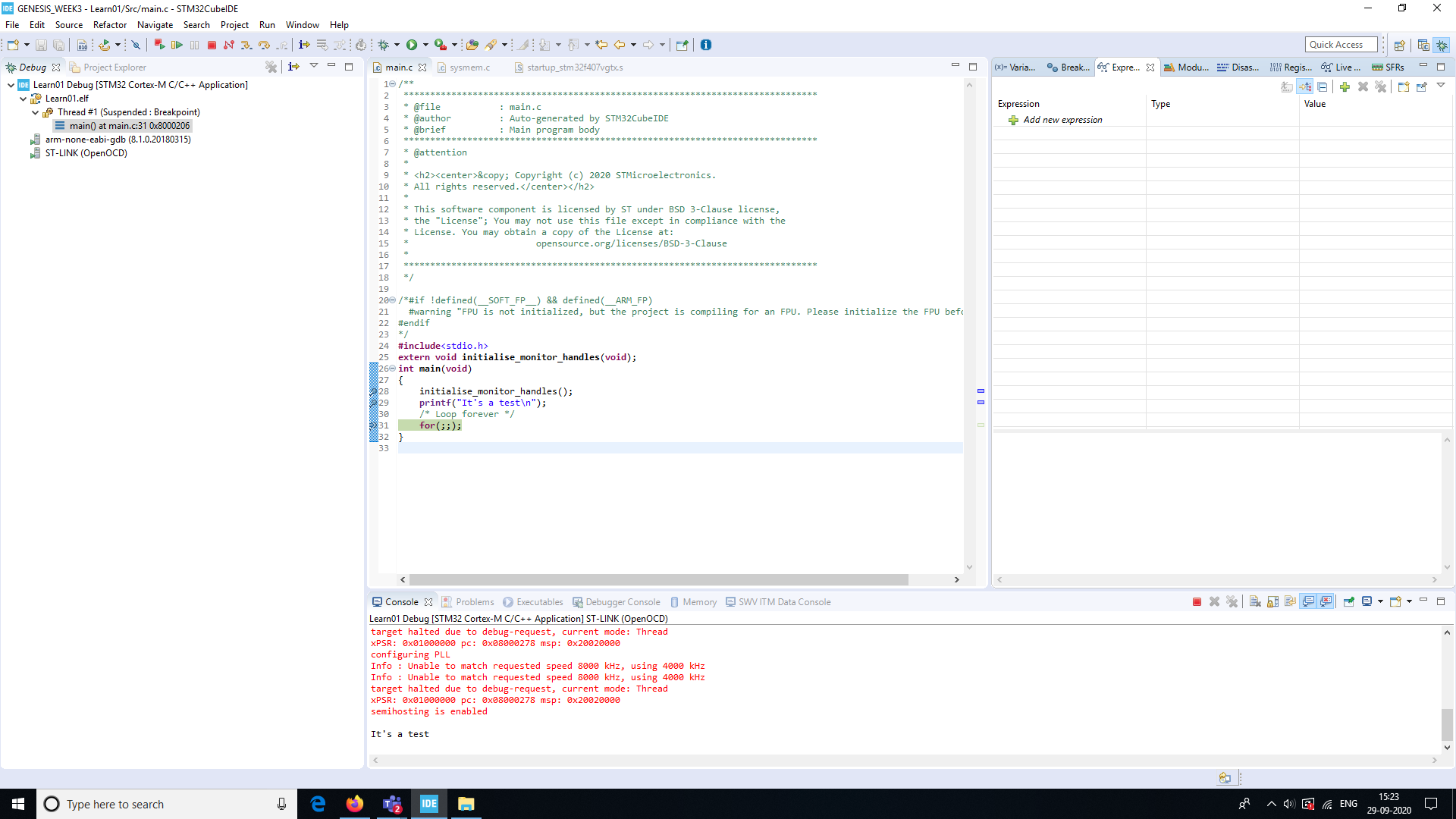


Figure : Expression window

## Memory browser

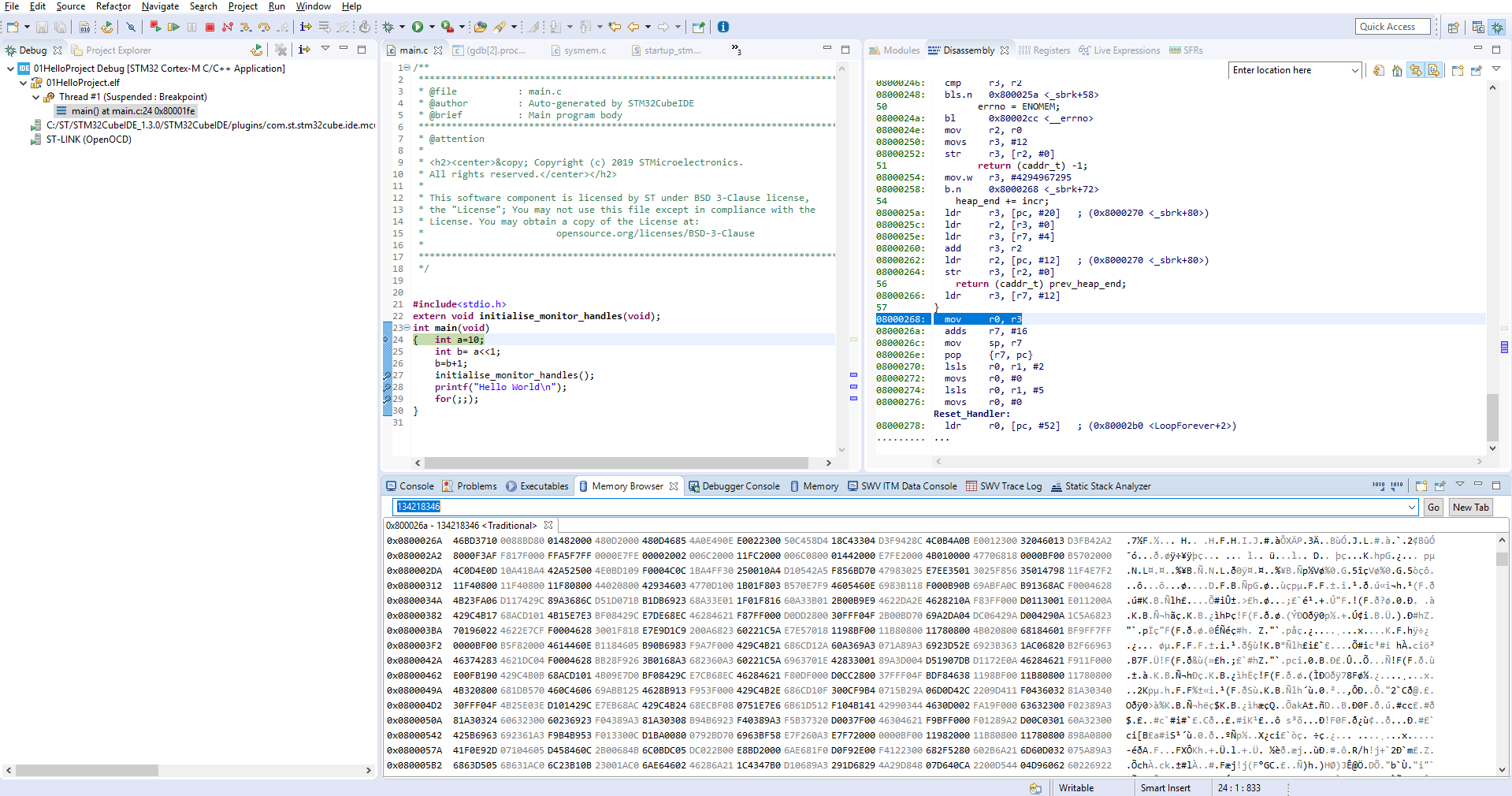


Figure : Memory browser

## Data watch points

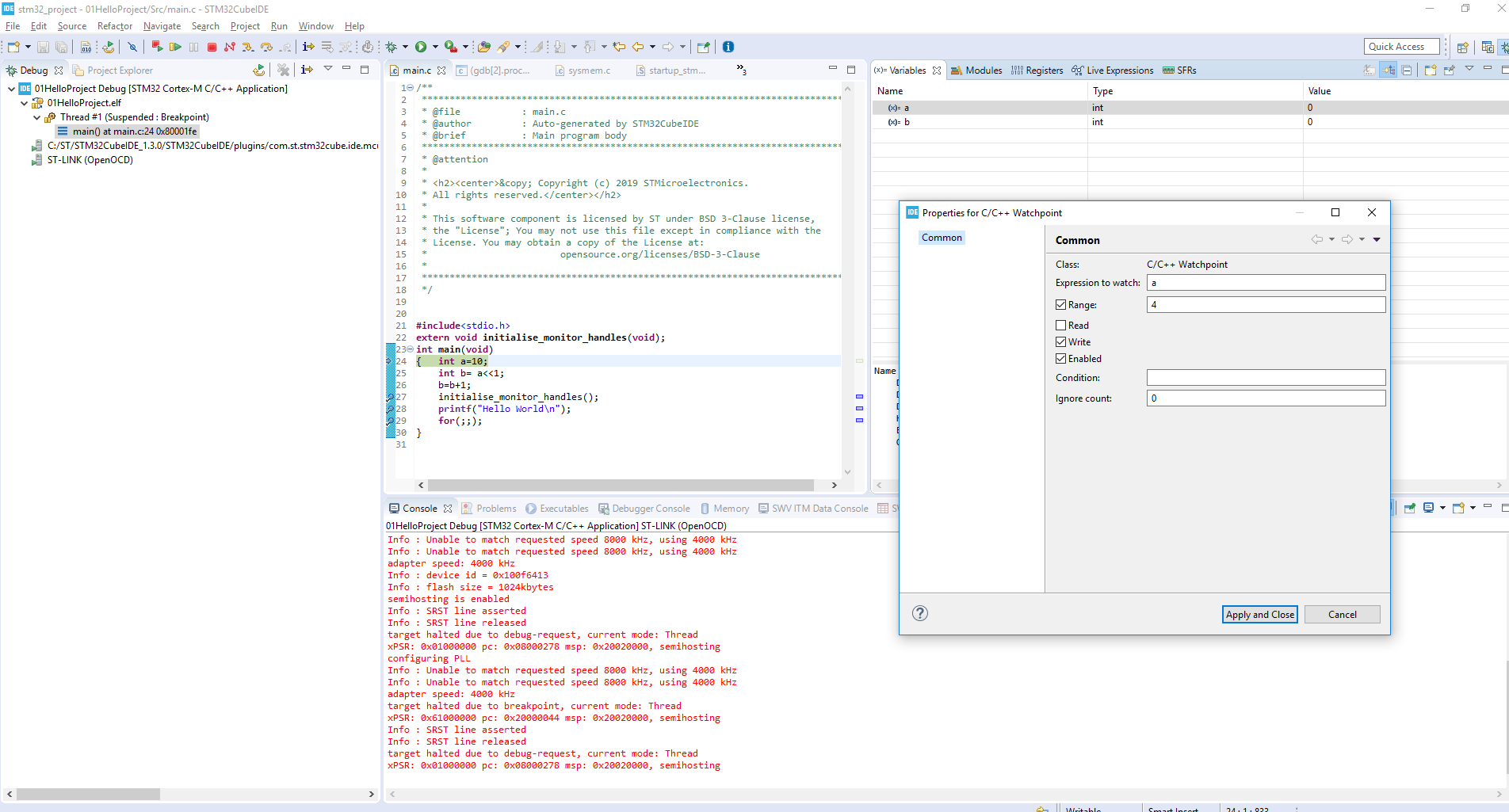


Figure : Assigning watch-point

## Disassembly

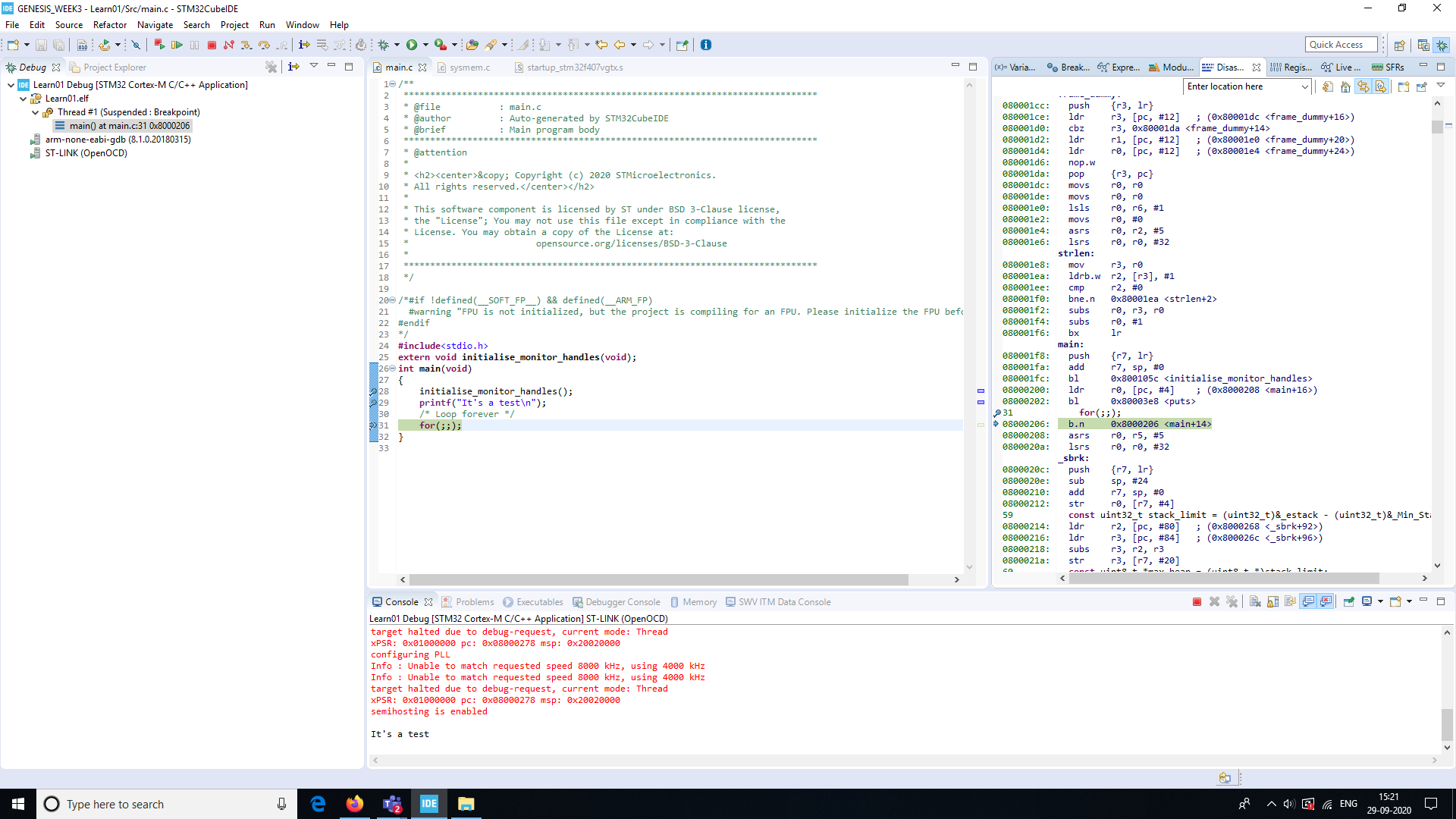


Figure : Disassembly window

# Activity 5: MCU specific header file

## Link to repository: [Link](https://github.com/99002591/GENESIS_WEEK3/tree/master/Learn03)