

GENESIS

Learning Report on

Embedded C – Hardware + Programming + Testing



LTTTS
GLOBAL
ENGINEERING
ACADEMY



Details

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ACTIVITY 1

CREATE A MAKEFILE

1.1 MAKEFILE

Make is UNIX utility that is designed to start execution of a make file. A makefile is a special file, containing shell commands that user creates and name makefile (or Makefile depending upon the system). While in the directory containing this makefile, user needs to type *make* and the commands in the makefile will be executed.

Make keeps track of the last time files (normally object files) were updated and only updates those files which are required (ones containing changes) to keep the source file up-to-date. If you have a large program with many source and/or header files, when you change a file on which others depend, you must recompile all the dependent files. Without a makefile, this is an extremely time-consuming task.

As a makefile is a list of shell commands, it must be written for the shell which will process the makefile. A makefile that works well in one shell may not execute properly in another shell.

The makefile contains a list of *rules*. These rules tell the system what commands you want to be executed. Most times, these rules are commands to compile (or recompile) a series of files. The rules, which must begin in column 1, are in two parts. The first line is called a *dependency* line and the subsequent line(s) are called *actions* or *commands*. The action line(s) must be indented with a tab.

1.2 CREATING A MAKEFILE:

Procedure done:

1. Dividing the program into multi files (.c and .h)
2. Object file designed by the command : -o
3. The command : arm-none-eabi-gcc main.c -o main.o -
It takes the input file main.c and creates the relocation object file (main.o).
4. arm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -O0 main.c -o main.o -
This removes the error in assembler stage.

The makefile command:

```
C:\Users\Training\Downloads\baremetalembdedded-master\baremetalembdedded-master\source>make -f Makefile.mak
arm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -O0 main.c -o main.o
arm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -O0 led.c -o led.o
```

Figure 1: Makefile command

```
CC=arm-none-eabi-gcc
MACH=cortex-m4
CFLAGS= -c -mcpu=$(MACH) -mthumb -std=gnu11 -Wall -o0
LDFLAGS= -nostdlib -T stm32_ls.ld -Wl,-Map=final.map

all:main.o sort.o stm32_startup.o final.elf

main.o:main.c
    $(CC) $(CFLAGS) -o $$ $^

sort.o:sort.c
    $(CC) $(CFLAGS) -o $$ $^

stm32_startup.o:stm32_startup.c
    $(CC) $(CFLAGS) -o $$ $^

final.elf: main.o sort.o stm32_startup.o
    $(CC) $(LDFLAGS) -o $$ $^

clean:
    rm -rf *.o *.elf
```

Figure 2: Code snippet of Makefile

ACTIVITY 2

CREATE A STARTUP FILE

2.1 START UP FILE:

A start up file is a piece of code written in assembly or C language that executes before the main () function of our embedded application. It performs various initialization steps by setting up the hardware of the microcontroller so that the user application can run. Therefore, a start-up file always runs before the main () code of our embedded application.

2.2 CREATING A START UP FILE:

Following are the main functions of a start-up file:

1. Disable all interrupts.
2. Copying initialized global, global static, and local static variable data from flash to .data section RAM memory of a microcontroller
3. Copying uninitialized global, global static, and local static variable data from flash to .bss section of RAM memory and initialize .bss section of RAM to zero.
4. Allocate space for the stack and initialize the stack pointer

5. It also contains an array of function pointers (interrupt vector table) that point to various interrupt vector routines such as interrupts and exceptions. The start-up file also contains definitions of these interrupt or exception routines such as reset handler, NMI handler, bus fault handler, etc.
6. Enable interrupts
7. Calls the main function

```
#include<stdint.h>

#define SRAM_START    0x20000000U
#define SRAM_SIZE     (128U * 1024U) //128KB
#define SRAM_END      ((SRAM_START) + (SRAM_SIZE))

#define STACK_START   SRAM_END

extern uint32_t _etext;
extern uint32_t _edata;
extern uint32_t _edata;
extern uint32_t _la_data;

extern uint32_t _sbss;
extern uint32_t _ebss;

//prototype of main
int main(void);

/* function prototypes of STM32F407x system exception and IRQ handlers */

void Reset_Handler(void);

void NMI_Handler              (void) __attribute__((weak, alias("Default_Handler")));
void HardFault_Handler        (void) __attribute__((weak, alias("Default_Handler")));
void MemManage_Handler        (void) __attribute__((weak, alias("Default_Handler")));
void BusFault_Handler         (void) __attribute__((weak, alias("Default_Handler")));
void UsageFault_Handler       (void) __attribute__((weak, alias("Default_Handler")));
void SVC_Handler              (void) __attribute__((weak, alias("Default_Handler")));
void DebugMon_Handler         (void) __attribute__((weak, alias("Default_Handler")));
void PendSV_Handler           (void) __attribute__((weak, alias("Default_Handler")));
void SysTick_Handler          (void) __attribute__((weak, alias("Default_Handler")));
void WWDG_IRQHandler           (void) __attribute__((weak, alias("Default_Handler")));
void PVD_IRQHandler            (void) __attribute__((weak, alias("Default_Handler")));
void TAMP_STAMP_IRQHandler     (void) __attribute__((weak, alias("Default_Handler")));
void RTC_WKUP_IRQHandler       (void) __attribute__((weak, alias("Default_Handler")));
void RCC_IRQHandler            (void) __attribute__((weak, alias("Default_Handler")));
void EXTI0_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void EXTI1_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void EXTI2_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void EXTI3_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void EXTI4_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream0_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream1_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream2_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream3_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream4_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream5_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void DMA1_Stream6_IRQHandler    (void) __attribute__((weak, alias("Default_Handler")));
void ADC_IRQHandler            (void) __attribute__((weak, alias("Default_Handler")));
void CAN1_TX_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void CAN1_RX0_IRQHandler       (void) __attribute__((weak, alias("Default_Handler")));
void CAN1_RX1_IRQHandler       (void) __attribute__((weak, alias("Default_Handler")));
void CAN1_SCE_IRQHandler       (void) __attribute__((weak, alias("Default_Handler")));
void EXTI9_5_IRQHandler        (void) __attribute__((weak, alias("Default_Handler")));
void TIM1_BRK_TIM9_IRQHandler   (void) __attribute__((weak, alias("Default_Handler")));
void TIM1_UP_TIM10_IRQHandler   (void) __attribute__((weak, alias("Default_Handler")));
void TIM1_TRG_COM_TIM11_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
void TIM1_CC_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void TIM2_IRQHandler           (void) __attribute__((weak, alias("Default_Handler")));
void TIM3_IRQHandler           (void) __attribute__((weak, alias("Default_Handler")));
void TIM4_IRQHandler           (void) __attribute__((weak, alias("Default_Handler")));
void I2C1_EV_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void I2C1_ER_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void I2C2_EV_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void I2C2_ER_IRQHandler         (void) __attribute__((weak, alias("Default_Handler")));
void SPI1_IRQHandler            (void) __attribute__((weak, alias("Default_Handler")));
void SPI2_IRQHandler            (void) __attribute__((weak, alias("Default_Handler")));
void USART1_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void USART2_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
void USART3_IRQHandler          (void) __attribute__((weak, alias("Default_Handler")));
```



```
uint32_t vectors[] __attribute__((section(".isr_vector"))) = {
    STACK_START,
    (uint32_t)Reset_Handler,
    (uint32_t)NMI_Handler,
    (uint32_t)HardFault_Handler,
    (uint32_t)MemManage_Handler,
    (uint32_t)BusFault_Handler,
    (uint32_t)UsageFault_Handler,
    0,
    0,
    0,
    0,
    0,
    (uint32_t)SVC_Handler,
    (uint32_t)DebugMon_Handler,
    0,
    (uint32_t)PendSV_Handler,
    (uint32_t)SysTick_Handler,
    (uint32_t)WWDG_IRQHandler,
    (uint32_t)FVD_IRQHandler,
    (uint32_t)TAMP_STAMP_IRQHandler,
    (uint32_t)RTC_WKUP_IRQHandler,
    0,
    (uint32_t)RCC_IRQHandler,
    (uint32_t)EXTI0_IRQHandler,
    (uint32_t)EXTI1_IRQHandler,
    (uint32_t)EXTI2_IRQHandler,
    (uint32_t)EXTI3_IRQHandler,
    (uint32_t)EXTI4_IRQHandler,
    (uint32_t)DMA1_Stream0_IRQHandler,
    (uint32_t)DMA1_Stream1_IRQHandler,
    (uint32_t)DMA1_Stream2_IRQHandler,
    (uint32_t)DMA1_Stream3_IRQHandler,
    (uint32_t)DMA1_Stream4_IRQHandler,
    (uint32_t)DMA1_Stream5_IRQHandler,
    (uint32_t)DMA1_Stream6_IRQHandler,
    (uint32_t)ADC_IRQHandler,
    (uint32_t)CAN1_TX_IRQHandler,
    (uint32_t)CAN1_RX0_IRQHandler,
    (uint32_t)CAN1_RX1_IRQHandler,
    (uint32_t)CAN1_SCE_IRQHandler,
    (uint32_t)EXTI9_5_IRQHandler,
    (uint32_t)TIM1_BRK_TIM9_IRQHandler,
    (uint32_t)TIM1_UP_TIM10_IRQHandler,
    (uint32_t)TIM1_TRG_COM_TIM11_IRQHandler,
    (uint32_t)TIM1_CC_IRQHandler,
    (uint32_t)TIM2_IRQHandler,
    (uint32_t)TIM3_IRQHandler,
    (uint32_t)TIM4_IRQHandler,
    (uint32_t)I2C1_EV_IRQHandler,
    (uint32_t)I2C1_ER_IRQHandler,
    (uint32_t)I2C2_EV_IRQHandler,
    (uint32_t)I2C2_ER_IRQHandler,
    (uint32_t)SPI1_IRQHandler,
    (uint32_t)SPI2_IRQHandler,
    (uint32_t)USART1_IRQHandler,
    (uint32_t)USART2_IRQHandler,
    (uint32_t)USART3_IRQHandler,
    (uint32_t)EXTI15_10_IRQHandler,
    (uint32_t)RTC_Alarm_IRQHandler,
    (uint32_t)OTG_FS_WKUP_IRQHandler,
    (uint32_t)TIM9_BRK_TIM12_IRQHandler,
    (uint32_t)TIM9_UP_TIM13_IRQHandler,
    (uint32_t)TIM9_TRG_COM_TIM14_IRQHandler,
};

void Default_Handler(void)
{
    while(1);
}

void Reset_Handler(void)
{
    //copy .data section to SRAM
    uint32_t size = (uint32_t)&edata - (uint32_t)&sdata;

    uint8_t *pDst = (uint8_t*)&sdata; //sram
    uint8_t *pSrc = (uint8_t*)&_la_data; //flash

    for(uint32_t i = 0 ; i < size ; i++)
    {
        *pDst++ = *pSrc++;
    }

    //Init. the .bss section to zero in SRAM
    size = (uint32_t)&ebss - (uint32_t)&sbss;
    pDst = (uint8_t*)&sbss;
    for(uint32_t i = 0 ; i < size ; i++)
    {
        *pDst++ = 0;
    }

    main();
}
```

Figure 3: Code snippet of start up file

ACTIVITY 3

CREATE A LINKER SCRIPT

3.1 LINKER SCRIPT:

The Linker Script is a text file made up of a series of Linker directives which tell the Linker where the available memory is and how it should be used. Thus, they reflect exactly the memory resources and memory map of the target microcontroller.

3.2 CREATING A LINKER SCRIPT :

```
ENTRY(Reset_Handler)

MEMORY
{
    FLASH(rx) : ORIGIN = 0x08000000, LENGTH = 1024K
    SRAM(rw) : ORIGIN = 0x20000000, LENGTH = 128K
}

SECTIONS
{
    .text :
    {
        *(.isr_vector)
        *(.text)
        *(.text.*)
        *(.init)
        *(.fini)
        *(.rodata)
        *(.rodata.*)
        . = ALIGN(4);
        _etext = .;
    } > FLASH

    _la_data = LOADADDR(.data);

    .data :
    {
        _sdata = .;
        *(.data)
        *(.data.*)
        . = ALIGN(4);
        _edata = .;
    } > SRAM AT> FLASH

    .bss :
    {
        _sbss = .;
        _bss_start__ = _sbss;
        *(.bss)
        *(.bss.*)
        *(COMMON)
        . = ALIGN(4);
        _ebss = .;
        _bss_end__ = _ebss;
        . = ALIGN(4);
        end = .;
        _end = .;
    } > SRAM
```

Figure 4: Code snippet of Linker script

ACTIVITY 4

DEBUGGING TECHNIQUES

Debugging is the process of detecting and removing of existing and potential errors (also called as ‘bugs’) in a software code that can cause it to behave unexpectedly or crash. To prevent incorrect operation of a software or system, debugging is used to find and resolve bugs or defects. When various subsystems or modules are tightly coupled, debugging becomes harder as any change in one module may cause more bugs to appear in another.

Executable Download:

Connect the target to the PC by debug adapter, instituting programming. The host protocol to the native target protocol.

Programming Adapters:

It is used to get access to debug interface of the target with native protocol. It helps to download and debug code.

Eg: SEGGER - JLINK EDU

Open OCD:

Procedure:

1. Open OCD connects to debug adopter over its driver.
2. Open OCd sends USB packet as a target interface.
3. You can use JTAG pins or SWD. SWD has two pins. i.e., Clock and IO pins. IO is a data line> It is controlled by debug adopter and it bidirectional.

Various bus interface can be accessed such as ABP. There are various access points in the processors to talk to memory and core.

ACTIVITY 5

TOGGLE LED

5.1 LED Toggle using Push Pull Configuration on STM32 Board.

```
1  /*
2   * 001led_toggle.c
3   *
4   * Created on: 20-Feb-2021
5   * Author: Training
6   */
7
8 #include "stm32f4xx.h"
9 #include "stm32f407xx_gpio_driver.h"
10
11 void delay(void)
12 {
13     for(uint32_t i=0; i<50000;i++);
14 }
15
16 int main(void)
17 {
18     GPIO_Handle_t GpioLed;
19
20     GpioLed.pGPIOx = GPIOD;
21     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
22     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
23     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
24     GpioLed.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PD;
25     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_PP;
26
27     //GPIO initialization
28
29     GPIO_PericlockControl(GPIOD, ENABLE);
30     GPIO_Inint(&GpioLed);
31
32     while(1)
33     {
34         GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_12);
35         delay();
36     }
37
38     return 0;
39 }
40
```

Figure 1: LED toggle code

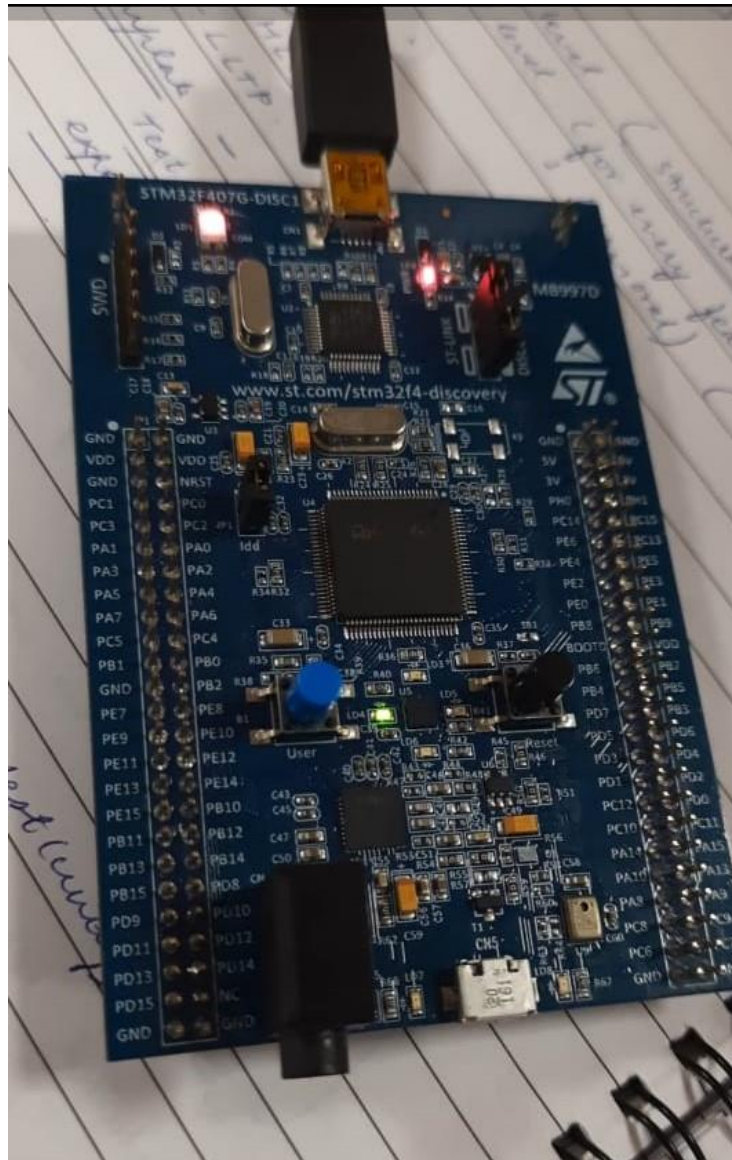


Figure 2: LED toggle output

5.2 LED Toggle using Open Drain configuration and enabling Internal Pull Up on STM32 Board.

```

1  /*
2   * opendrain_internalpullup.c
3   *
4   * Created on: 20-Feb-2021
5   * Author: Training
6   */
7
8
9  #include "stm32f4xx.h"
10 #include "stm32f407xx_gpio_driver.h"
11
12 void delay(void)
13 {
14     for(uint32_t i=0; i<50000;i++);
15 }
16
17 int main(void)
18 {
19     GPIO_Handle_t GpioLed;
20
21     GpioLed.pGPIOx = GPIOD;
22     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
23     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
24     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
25     GpioLed.GPIO_PinConfig.GPIO_PinPuDControl = GPIO_NO_PU;
26     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_OD;
27
28     //GPIO initialization
29
30     GPIO_PerioClockControl(GPIOD, ENABLE);
31     GPIO_Init(&GpioLed);
32
33     while(1)
34     {
35         GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_12);
36         delay();
37     }
38
39     return 0;
40 }
41
42

```

Figure 3: open drain internal pull up code

```

#include "stm32f4xx.h"
#include "stm32f407xx_gpio_driver.h"

void delay(void){
    for(uint32_t i=0;i<50000;i++);
}

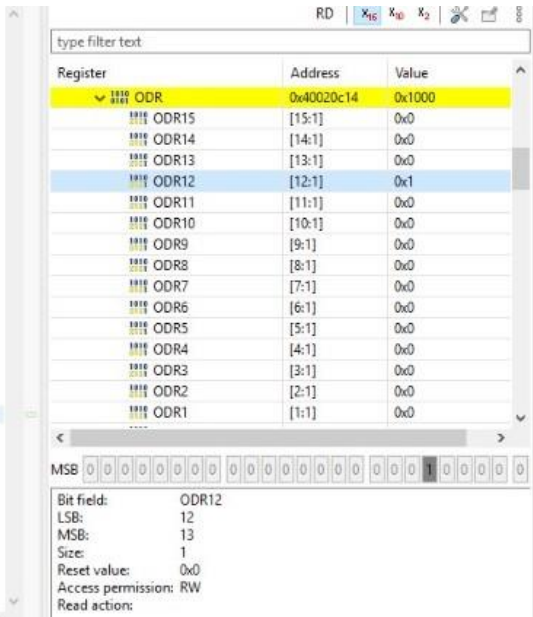
int main(void){

    Gpio_Handle_t GpioLed;
    //led
    GpioLed.pGPIOx = GPIOD;
    GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
    GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
    GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_FAST;
    GpioLed.GPIO_PinConfig.GPIO_PinPuDControl = GPIO_PUPDR_PU;
    GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_OD;
    GPIO_PerioClockControl(GPIOD, ENABLE);
    GPIO_Init(&GpioLed);

    while(1){
        GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_12);
        delay();
    }

    return 0;
}

```



Register	Address	Value
ODR15	[15:1]	0x0
ODR14	[14:1]	0x0
ODR13	[13:1]	0x0
ODR12	[12:1]	0x1
ODR11	[11:1]	0x0
ODR10	[10:1]	0x0
ODR9	[9:1]	0x0
ODR8	[8:1]	0x0
ODR7	[7:1]	0x0
ODR6	[6:1]	0x0
ODR5	[5:1]	0x0
ODR4	[4:1]	0x0
ODR3	[3:1]	0x0
ODR2	[2:1]	0x0
ODR1	[1:1]	0x0

MSB: 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0

Bit field: ODR12
 LSB: 12
 MSB: 13
 Size: 1
 Reset value: 0x0
 Access permission: RW
 Read action:

Figure 4: open drain internal pull up output

5.3 LED Toggle using open drain configuration and external pull up on STM32 Board.

```
1  /*
2   * externalpullup.c
3   *
4   * Created on: 24-Feb-2021
5   * Author: Training
6   */
7
8  #include "stm32f4xx.h"
9  #include "stm32f407xx_gpio_driver.h"
10
11 void delay(void)
12 {
13     for(uint32_t i=0; i<50000;i++);
14 }
15
16 int main(void)
17 {
18     GPIO_Handle_t GpioLed;
19
20     GpioLed.pGPIOx = GPIOD;
21     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_11;
22     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
23     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
24     GpioLed.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PUPD;
25     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_OD;
26
27     //GPIO initialization
28
29     GPIO_PerioClockControl(GPIOD, ENABLE);
30     GPIO_Inint(&GpioLed);
31
32     while(1)
33     {
34         GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_11);
35         delay();
36     }
37
38     return 0;
39 }
40
```

Figure 5: open drain external pull up code

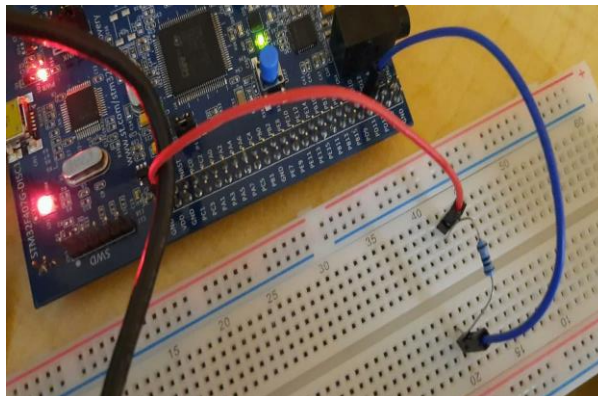


Figure 6: open drain external pull up output

5.4 Toggle External LED using push pull configuration on STM32 Board.

```
1 /*
2  * pushpull_externalLED.c
3  *
4  * Created on: 23-Feb-2021
5  * Author: Training
6  */
7
8
9 #include "stm32f4xx.h"
10 #include "stm32f407xx_gpio_driver.h"
11
12 void delay(void)
13 {
14     for(uint32_t i=0; i<5000;i++);
15 }
16
17 int main(void)
18 {
19     GPIO_Handle_t GpioLed;
20
21     GpioLed.pGPIOx = GPIOD;
22     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
23     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
24     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
25     GpioLed.GPIO_PinConfig.GPIO_PinPuDControl = GPIO_NO_PD;
26     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_PP;
27
28     //GPIO initialization
29     GPIO_PerClockControl(GPIOD, ENABLE);
30     GPIO_Inint(&GpioLed);
31
32     while(1)
33     {
34         GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_12);
35         delay();
36     }
37
38     return 0;
39 }
40 }
```

Figure 7: External LED Push pull code

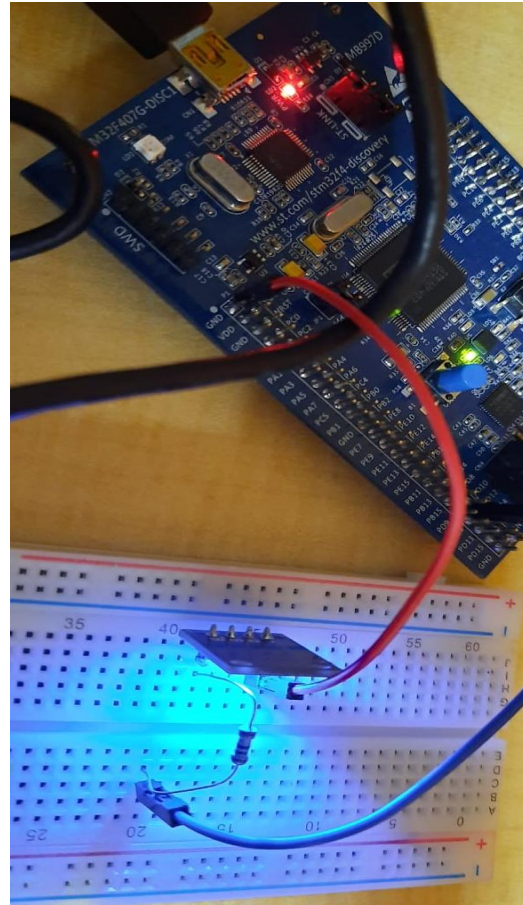


Figure 8: External LED Push Pull output

5.4 Enable internal button and toggle the LED using STM32 Board.

```
1  /*
2   * button.c
3   *
4   * Created on: 20-Feb-2021
5   * Author: Training
6   */
7
8
9  #include "stm32f4xx.h"
10 #include "stm32f407xx_gpio_driver.h"
11
12 #define HIGH      1
13 #define BTN_PRESSED HIGH
14
15 void delay(void)
16 {
17     for(uint32_t i=0; i<50000/2;i++);
18 }
19
20 int main(void)
21 {
22     GPIO_Handle_t GpioLed;
23     GPIO_Handle_t GpioBtn;
24     //LED
25     GpioLed.pGPIOx = GPIOD;
26     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
27     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
28     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
29     GpioLed.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PD;
30     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_PP;
31
32     //GPIO initialization
33
34     GPIO_PericlockControl(GPIOD, ENABLE);
35     GPIO_Inint(&GpioLed);
36     //Button
37     GpioBtn.pGPIOx = GPIOA;
38     GpioBtn.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_0;
39     GpioBtn.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_IN;
40     GpioBtn.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
41     GpioBtn.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PD;
42 }
```

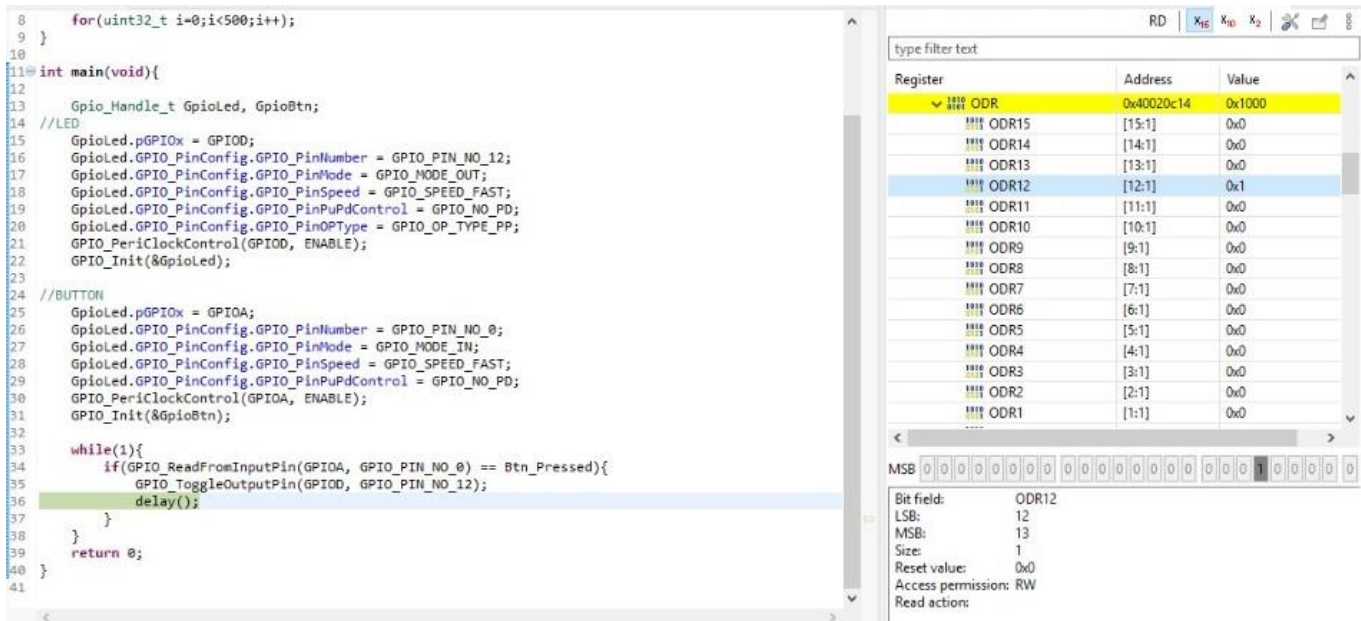
Figure 9: internal button led toggle code

```

42
43
44     //GPIO initialization
45
46     GPIO_PerioClockControl(GPIOA, ENABLE);
47     GPIO_Init(&GpioBtn);
48
49     while(1)
50     {
51         if(GPIO_ReadFromInputPin(GPIOA, GPIO_PIN_NO_0) == BTN_PRESSED)
52         {
53             GPIO_ToggleOutputPin(GPIOD, GPIO_PIN_NO_12);
54             delay();
55         }
56     }
57
58     return 0;
59 }
60

```

Figure 10: internal button led toggle code



The screenshot displays a C code editor on the left and a hardware register view on the right. The code initializes GPIOA for an input button (GPIO_PIN_NO_0) and GPIOD for an output LED (GPIO_PIN_NO_12). It includes a while loop that toggles the LED state when the button is pressed. The register view on the right shows the Output Data Register (ODR) for the GPIOD peripheral. The ODR12 register is highlighted, showing its address (0x40020c14) and value (0x1000). The bit field for ODR12 is shown as 1, indicating the LED is turned on. The bit field details for ODR12 are: Bit field: ODR12, LSB: 12, MSB: 13, Size: 1, Reset value: 0x0, Access permission: RW, Read action: Read action.

Figure 11: internal button led toggle output

5.6 Use external switch to toggle the LED on STM32 Board.

```
1  /*
2   * ExternalSwitch_LED.c
3   *
4   * Created on: 23-Feb-2021
5   * Author: Training
6   */
7
8  #include "stm32f4xx.h"
9  #include "stm32f407xx_gpio_driver.h"
10
11 #define HIGH          1
12 #define BTN_PRESSED   HIGH
13
14 void delay(void)
15 {
16     for(uint32_t i=0; i<50000/2;i++);
17 }
18
19 int main(void)
20 {
21     GPIO_Handle_t GpioLed;
22     GPIO_Handle_t GpioBtn;
23     //LED
24     GpioLed.pGPIOx = GPIOD;
25     GpioLed.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_12;
26     GpioLed.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_OUT;
27     GpioLed.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
28     GpioLed.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PD;
29     GpioLed.GPIO_PinConfig.GPIO_PinOPType = GPIO_OP_TYPE_PP;
30
31     //GPIO initialization
32
33     GPIO_PericlockControl(GPIOD, ENABLE);
34     GPIO_Inint(&GpioLed);
35     //Button
36     GpioBtn.pGPIOx = GPIOA;
37     GpioBtn.GPIO_PinConfig.GPIO_PinNumber = GPIO_PIN_NO_0;
38     GpioBtn.GPIO_PinConfig.GPIO_PinMode = GPIO_MODE_IN;
39     GpioBtn.GPIO_PinConfig.GPIO_PinSpeed = GPIO_SPEED_HIGH;
40     GpioBtn.GPIO_PinConfig.GPIO_PinPuPdControl = GPIO_NO_PUPD;
41 }
```

Figure 12: external switch led toggle

type filter text

Register	Address	Value
> GPIOE		
> GPIOD		
> MODER	0x40020c00	0x1000000
> OTYPER	0x40020c04	0x0
> OSPEEDR	0x40020c08	0x20
> PUPDR	0x40020c0c	0x0
> IDR	0x40020c10	0x9fef
> ODR	0x40020c14	0x1000
ODR15	[15:1]	0x0
ODR14	[14:1]	0x0
ODR13	[13:1]	0x0
ODR12	[12:1]	0x1
ODR11	[11:1]	0x0
ODR10	[10:1]	0x0
ODR9	[9:1]	0x0
ODR8	[8:1]	0x0
ODR7	[7:1]	0x0
ODR6	[6:1]	0x0
ODR5	[5:1]	0x0
ODR4	[4:1]	0x0
ODR3	[3:1]	0x0
ODR2	[2:1]	0x0
ODR1	[1:1]	0x0
ODR0	[0:1]	0x0

MSB 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 1 0 0 0 0 0 0 0 0 0 0 0 0

Bit field: ODR12
 LSB: 12
 MSB: 13
 Size: 1
 Reset value: 0x0
 Access permission: RW
 Read action:

Description:
 Port output data (y = 0..15)

Figure 13: external switch led toggle code

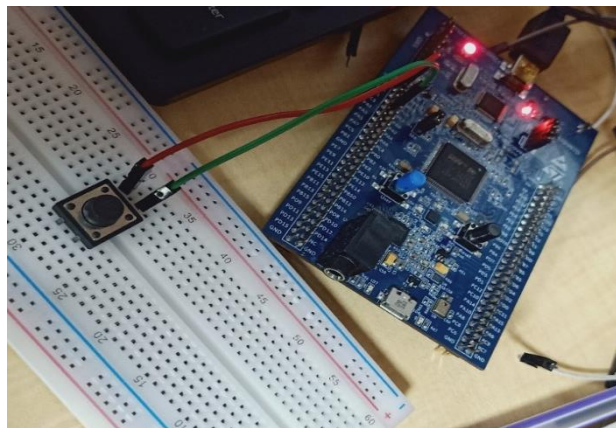


Figure 14: external switch led toggle output

5.7 HAL INTERRUPT

```

1  /* USER CODE BEGIN Header */
2  /**
3   *
4   * @file      : main.c
5   * @brief     : Main program body
6   *
7   * @attention
8   *
9   * <h2><center>&copy; Copyright (c) 2021 STMicroelectronics.
10  * All rights reserved.</center></h2>
11  *
12  * This software component is licensed by ST under BSD 3-Clause license,
13  * the "License"; You may not use this file except in compliance with the
14  * License. You may obtain a copy of the license at:
15  *      opensource.org/licenses/BSD-3-Clause
16  *
17  */
18  /*
19  */
19  /* USER CODE END Header */
20  /* Includes -----*/
21  #include "main.h"
22
23  /* Private includes -----*/
24  /* USER CODE BEGIN Includes */
25
26  /* USER CODE END Includes */
27
28  /* Private typedef -----*/
29  /* USER CODE BEGIN PTD */
30
31  /* USER CODE END PTD */
32
33  /* Private define -----*/
34  /* USER CODE BEGIN PD */
35  /* USER CODE END PD */
36
37  /* Private macro -----*/
38  /* USER CODE BEGIN PM */
39
40  /* USER CODE END PM */
41
42  /* Private variables -----*/

```

Figure 15: HAL interrupt

```

43
44  /* USER CODE BEGIN PV */
45  volatile uint8_t flag=0;
46
47  /* USER CODE END PV */
48
49  /* Private function prototypes -----*/
50  void SystemClock_Config(void);
51  static void MX_GPIO_Init(void);
52  /* USER CODE BEGIN PFP */
53
54  /* USER CODE END PFP */
55
56  /* Private user code -----*/
57  /* USER CODE BEGIN 0 */
58
59  /* USER CODE END 0 */
60
61  /**
62   * @brief The application entry point.
63   * @retval int
64   */
65  int main(void)
66  {
67      /* USER CODE BEGIN 1 */
68
69      /* USER CODE END 1 */
70
71      /* MCU Configuration-----*/
72
73      /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
74      HAL_Init();
75
76      /* USER CODE BEGIN Init */
77
78      /* USER CODE END Init */
79
80      /* Configure the system clock */
81      SystemClock_Config();
82
83      /* USER CODE BEGIN SysInit */
84
85      /* USER CODE END SysInit */

```

Figure 16: HAL interrupt

```

79
80 /* Configure the system clock */
81 SystemClock_Config();
82
83 /* USER CODE BEGIN SysInit */
84
85 /* USER CODE END SysInit */
86
87 /* Initialize all configured peripherals */
88 MX_GPIO_Init();
89 /* USER CODE BEGIN 2 */
90
91 /* USER CODE END 2 */
92
93 /* Infinite loop */
94 /* USER CODE BEGIN WHILE */
95 while (1)
96 {
97     /* USER CODE END WHILE */
98     if(1==flag)
99     {
100         HAL_Delay(1000);
101         HAL_GPIO_TogglePin(GREEN_LED_GPIO_Port, GREEN_LED_Pin);
102         flag=0;
103     }
104     /* USER CODE BEGIN 3 */
105 }
106 /* USER CODE END 3 */
107 }
108
109 /**
110  * @brief System Clock Configuration
111  * @retval None
112  */
113 void SystemClock_Config(void)
114 {
115     RCC_OscInitTypeDef RCC_OscInitStruct = {0};
116     RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
117
118     /** Configure the main internal regulator output voltage
119     */
120     __HAL_RCC_PWR_CLK_ENABLE();

```

Figure 17: HAL interrupt

```

118 /** Configure the main internal regulator output voltage
119 */
120 __HAL_RCC_PWR_CLK_ENABLE();
121 __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
122 /** Initializes the RCC Oscillators according to the specified parameters
123  * in the RCC_OscInitTypeDef structure.
124 */
125 RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
126 RCC_OscInitStruct.HSIState = RCC_HSI_ON;
127 RCC_OscInitStruct.HSICalibrationValue = RCC_HSICALIBRATION_DEFAULT;
128 RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
129 if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
130 {
131     Error_Handler();
132 }
133 /** Initializes the CPU, AHB and APB buses clocks
134 */
135 RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
136 |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
137 RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_HSI;
138 RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
139 RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;
140 RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;
141
142 if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_0) != HAL_OK)
143 {
144     Error_Handler();
145 }
146 }
147
148 /**
149  * @brief GPIO Initialization Function
150  * @param None
151  * @retval None
152  */
153 static void MX_GPIO_Init(void)
154 {
155     GPIO_InitTypeDef GPIO_InitStruct = {0};
156
157     /* GPIO Ports Clock Enable */
158     __HAL_RCC_GPIOA_CLK_ENABLE();
159     __HAL_RCC_GPIOB_CLK_ENABLE();

```

Figure 18: HAL interrupt

```

160
161 /*Configure GPIO pin Output Level */
162 HAL_GPIO_WritePin(GREEN_LED_GPIO_Port, GREEN_LED_Pin, GPIO_PIN_RESET);
163
164 /*Configure GPIO pin : BLUE_BTN_Pin */
165 GPIO_InitStruct.Pin = BLUE_BTN_Pin;
166 GPIO_InitStruct.Mode = GPIO_MODE_IT_FALLING;
167 GPIO_InitStruct.Pull = GPIO_NOPULL;
168 HAL_GPIO_Init(BLUE_BTN_GPIO_Port, &GPIO_InitStruct);
169
170 /*Configure GPIO pin : GREEN_LED_Pin */
171 GPIO_InitStruct.Pin = GREEN_LED_Pin;
172 GPIO_InitStruct.Mode = GPIO_MODE_OUTPUT_PP;
173 GPIO_InitStruct.Pull = GPIO_NOPULL;
174 GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
175 HAL_GPIO_Init(GREEN_LED_GPIO_Port, &GPIO_InitStruct);
176
177 /* EXTI interrupt init*/
178 HAL_NVIC_SetPriority(EXTI0_IRQn, 0, 0);
179 HAL_NVIC_EnableIRQ(EXTI0_IRQn);
180
181 }
182
183 /* USER CODE BEGIN 4 */
184 void HAL_GPIO_EXTI_Callback(uint16_t GPIO_Pin)
185 {
186     flag=1;
187 }
188 /* USER CODE END 4 */
189
190 /**
191  * @brief This function is executed in case of error occurrence.
192  * @retval None
193  */
194 void Error_Handler(void)
195 {
196     /* USER CODE BEGIN Error_Handler_Debug */
197     /* User can add his own implementation to report the HAL error return state */
198
199     /* USER CODE END Error_Handler_Debug */
200 }
201

```

Figure 19: HAL interrupt

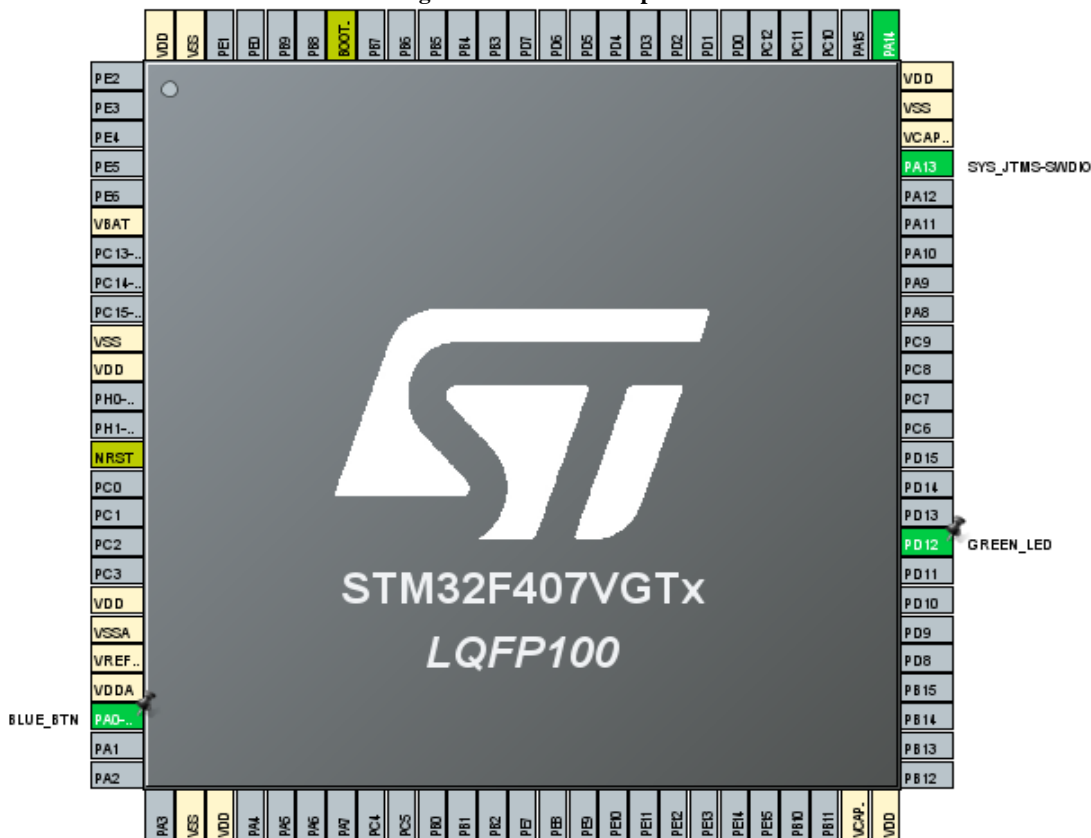


Figure 20: HAL interrupt

5.8 HAL ADC

```

46
47 /* USER CODE END PV */
48
49 /* Private function prototypes -----*/
50 void SystemClock_Config(void);
51 static void MX_GPIO_Init(void);
52 static void MX_ADC1_Init(void);
53 /* USER CODE BEGIN PFP */
54
55 /* USER CODE END PFP */
56
57 /* Private user code -----*/
58 /* USER CODE BEGIN 0 */
59 uint32_t adcVal1, adcVal2;
60 ADC_ChannelConfTypeDef sConfig ;
61 /* USER CODE END 0 */
62
63 /**
64  * @brief The application entry point.
65  * @retval int
66  */
67 int main(void)
68 {
69     /* USER CODE BEGIN 1 */
70
71     /* USER CODE END 1 */
72
73     /* MCU Configuration-----*/
74
75     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
76     HAL_Init();
77
78     /* USER CODE BEGIN Init */
79
80     /* USER CODE END Init */
81
82     /* Configure the system clock */
83     SystemClock_Config();
84
85     /* USER CODE BEGIN SysInit */
86
87     /* USER CODE END SysInit */
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```

Figure 21: HAL ADC

```

79
80 /* USER CODE END Init */
81
82 /* Configure the system clock */
83 SystemClock_Config();
84
85 /* USER CODE BEGIN SysInit */
86
87 /* USER CODE END SysInit */
88
89 /* Initialize all configured peripherals */
90 MX_GPIO_Init();
91 MX_ADC1_Init();
92 /* USER CODE BEGIN 2 */
93
94 /* USER CODE END 2 */
95
96 /* Infinite loop */
97 /* USER CODE BEGIN WHILE */
98 while (1)
99 {
100     /* USER CODE END WHILE */
101
102     /* USER CODE BEGIN 3 */
103     HAL_ADC_Start(&hadc1);
104     if (HAL_ADC_PollForConversion(&hadc1, 5) == HAL_OK)
105     {
106         adcVal0 = HAL_ADC_GetValue(&hadc1);
107     }
108     HAL_Delay(50);
109 }
110 /* USER CODE END 3 */
111 }
112
113 /**
114  * @brief System Clock Configuration
115  * @retval None
116  */
117 void SystemClock_Config(void)
118 {
119     RCC_OscInitTypeDef RCC_OscInitStruct = {0};
120     RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
121
122     /* Configure the main internal regulator output voltage
123     */
124     __HAL_RCC_PWR_CLK_ENABLE();
125     __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
126     /* Initializes the RCC Oscillators according to the specified parameters
127     * in the RCC_OscInitTypeDef structure.
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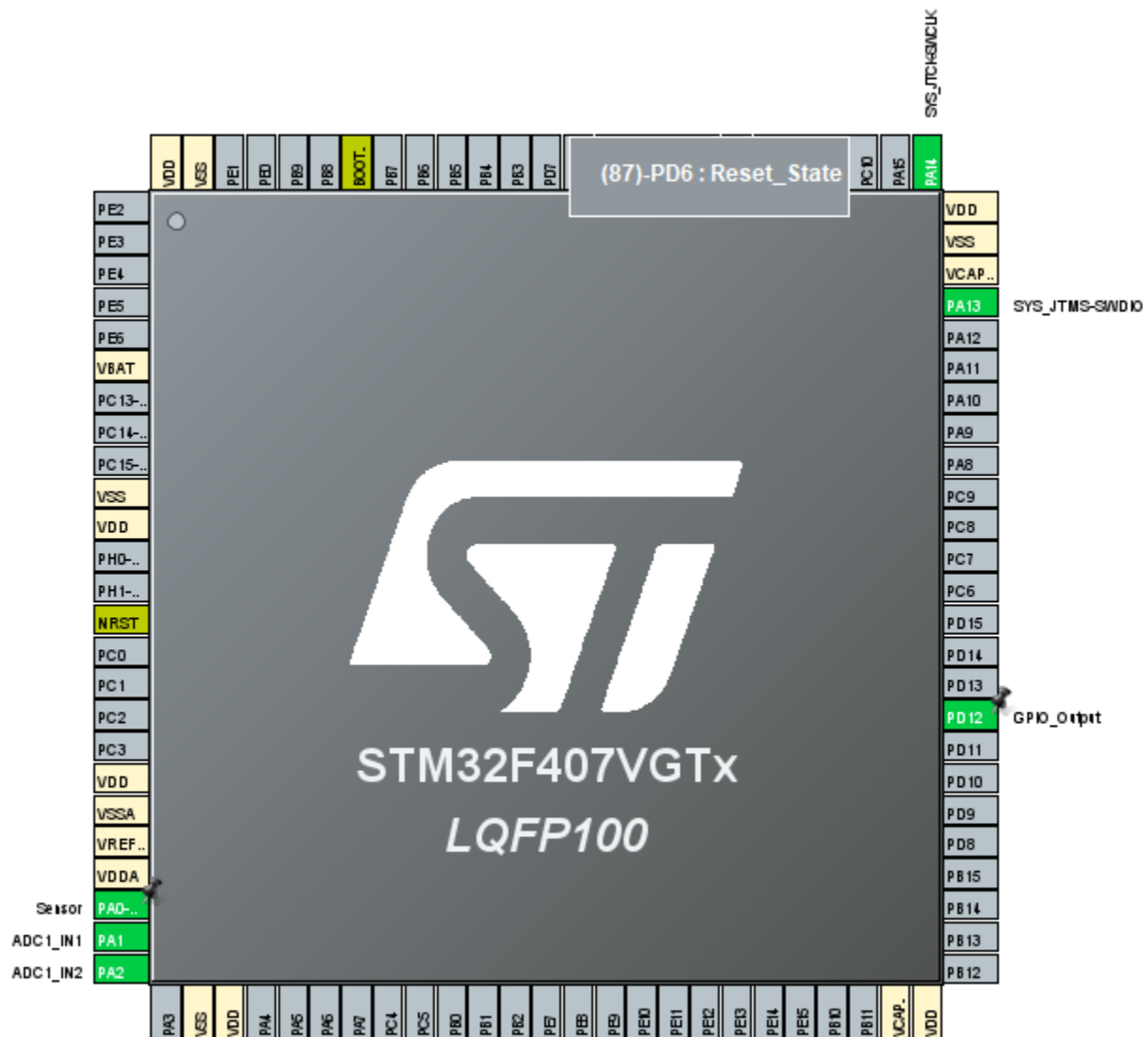


Figure 23: HAL ADC

5.9 HAL SPI

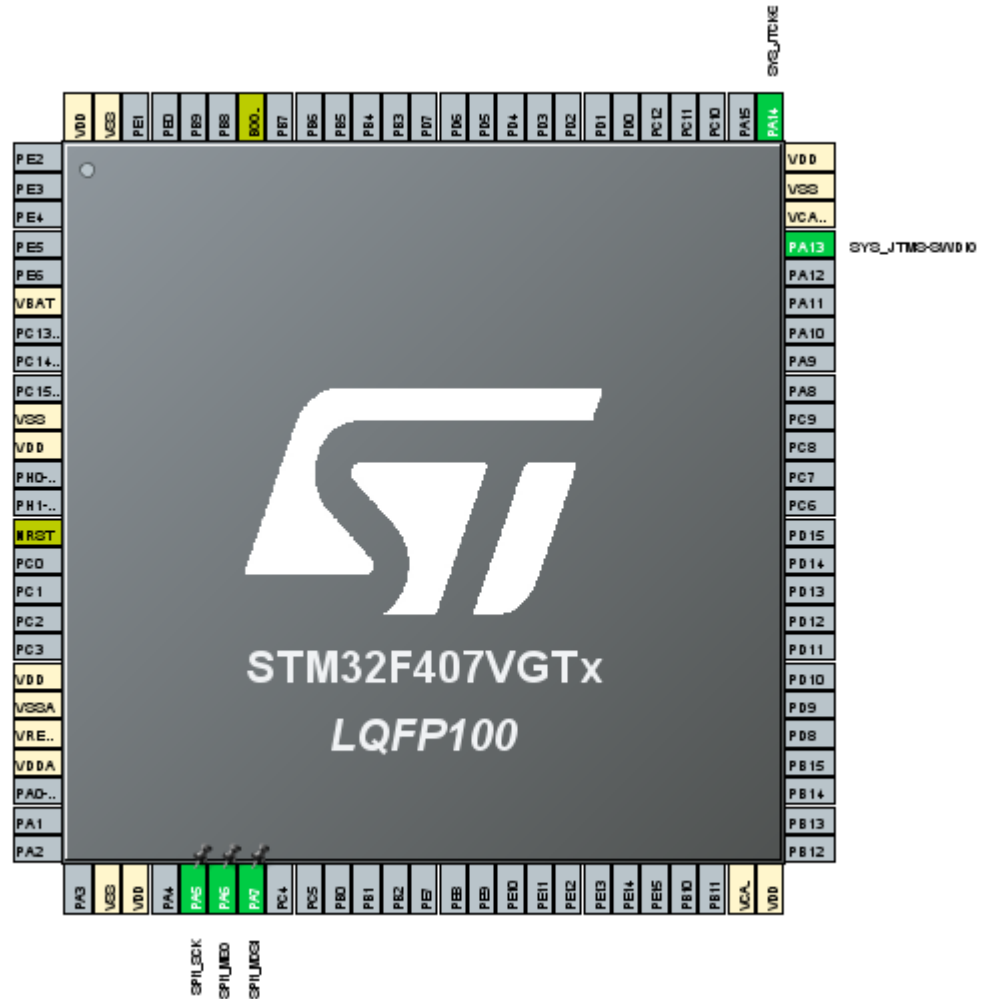


Figure 24: HAL SPI

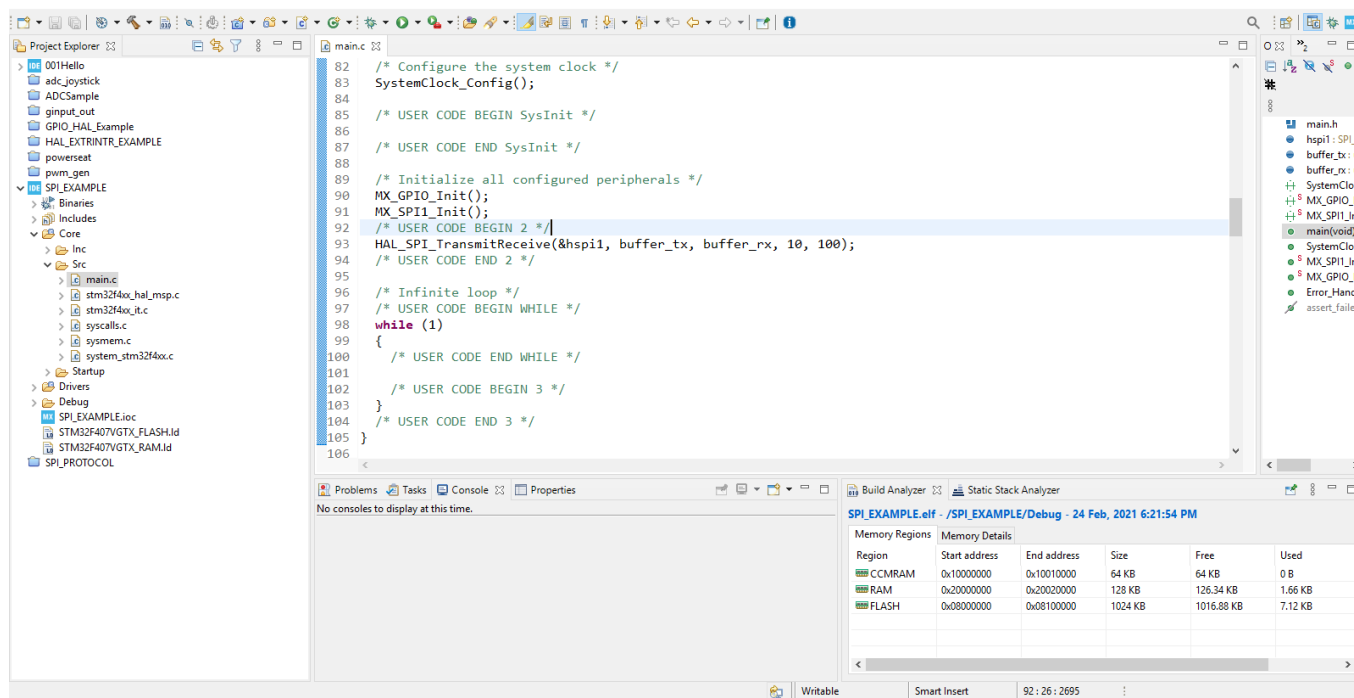


Figure 25: Output of SPI

5.10 HAL UART

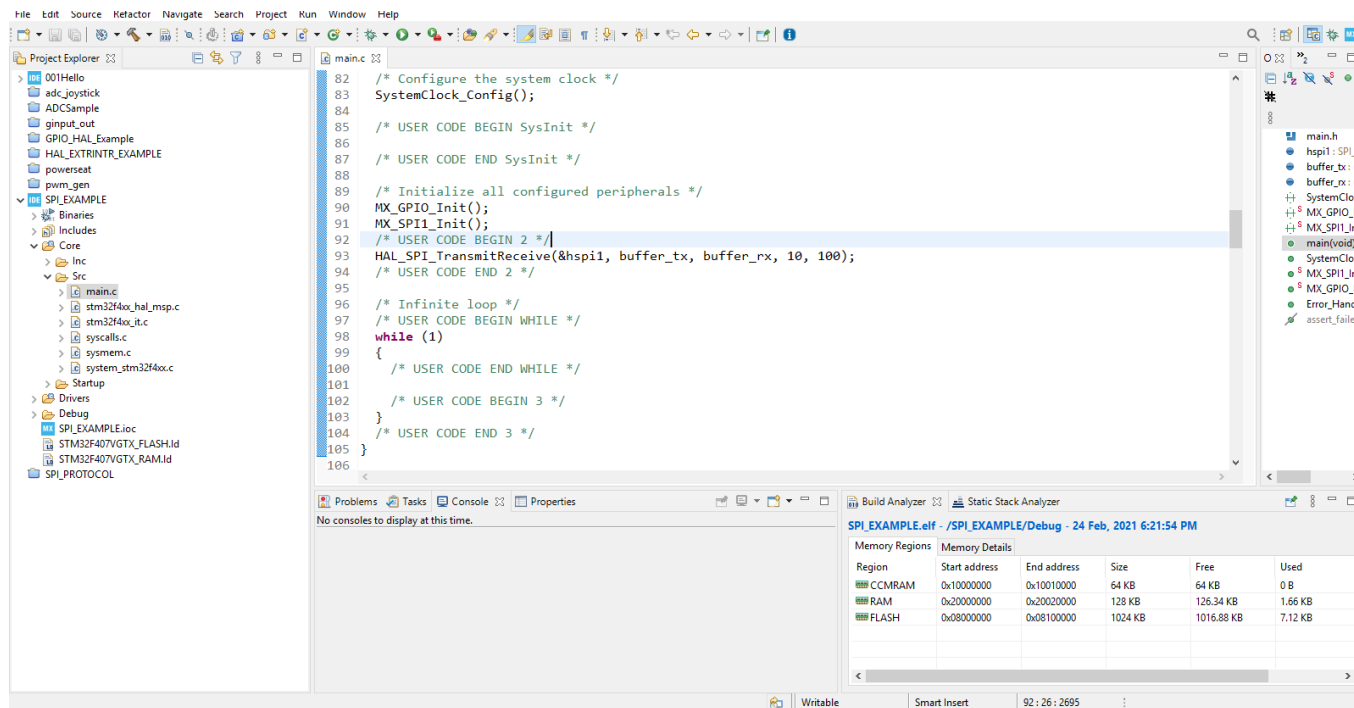


Figure 26: Output of UART