Coverage Report for Automotive_model2

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Analysis Information

Coverage Data Information

Collected in version	(R2020b)

Model Information

Model version	1.29
Author	99003757
Last saved	Wed Mar 24 17:52:33 2021

Simulation Optimization Options

Default parameter behavior	tunable
Block reduction	forced off
Conditional branch optimization	on

Coverage Options

Analyzed model	Automotive_model2
Logic block short circuiting	off
MCDC mode	masking
Filter name(s):	Untitled

Objects Filtered from Coverage Analysis

Filter Untitled

File	C:\Users\99003761\Downloads\slcov_output\Automotive_model2\Untitled.cvf
Description	N/A

Filtered Model Object	Rationale
J1. SubSystem block "BCM_Door_system"	none
J2. SubSystem block "decision_Box"	none
J3. SubSystem block "Chart1"	none
J4. SubSystem block "Chart"	none
J5. SubSystem block "Chart"	none

Aggregated Tests

Run	Test Name	Date
Model: "Automotive_model2"		
T1	Run 1	24-Mar-2021 18:01:31

T2	Run 2	24-Mar-2021 18:02:34
T3	Run 3	24-Mar-2021 18:09:18
T4	Run 4	24-Mar-2021 18:12:14

Summary

Model Hierarchy/Complexity

		Decision	Condition	MCDC	Execution
1. Automotive_model2	461	97%	78%	71%	100%
2. . . . BCM_SAFETY_MODULE	460	97%	78%	71%	100%
3. . . . . BCM_AIRBAGSYSTEM	167	82%	64%	45%	100%
4. . . . . BCM_AB_CheckCrashType	3	100%	100%	100%	100%
5. . . . . ActivateAirBagSensor	3	100%	NA	NA	NA
6. . . . . BCM_AB_FrontAirBagActivation	82	83%	65%	53%	100%
7. . . . . BCM_AB_Compare_Driver		NA	50%	NA	100%
8. . . . . BCM_AB_Compare_Front		NA	100%	NA	100%
9. . . . . BCM_AB_Compare_Left_Rear		NA	100%	NA	100%
10. . . . . BCM_AB_Compare_Right_Rear		NA	100%	NA	100%
11. . . . . BCM_AB_SIDE_WeightSensor	76	77%	59%	53%	NA
12. . . . . BCM_AB_Side_AB_Activation	82	80%	59%	33%	100%
13. . . . . BCM_AB_Decision_Driver		NA	50%	NA	100%
14. . . . . BCM_AB_Decision_LFront		NA	100%	NA	100%
15. . . . . BCM_AB_Decision_LRear		NA	100%	NA	100%
16. . . . . BCM_AB_Decision_RRear		NA	100%	NA	100%
17. . . . . BCM_AB_WeightSensor	76	74%	51%	33%	NA
18. . . . . BCM_Powerwindow_and_Door_operation	267	100%	100%	100%	100%
19. . . . . BCM_Door_system	4	100%	100%	100%	100%
20. . . . . BCM_Automatic_Door_lock	2	100%	100%	100%	100%
21. . . . . BCM_Delay_System_door lock and BCM_interior light subsystem	2	100%	100%	100%	100%
22. . . . . Compare To Constant		NA	100%	NA	100%
23. . . . . Repeating Sequence Stair		NA	NA	NA	100%
24. . . . . LimitedCounter		NA	NA	NA	100%
25. . . . . Compare To Constant		NA	100%	NA	100%
26. . . . . Front_left_subsystem	43	100%	100%	100%	100%
27. . . . . decision_Box	39	100%	100%	100%	NA
28. . . . . SF: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box	38	100%	100%	100%	NA
29. . . . . SF: Normal	9	100%	NA	NA	NA
30. . . . . SF: Normal2	16	100%	100%	100%	NA
31. . . . . Rear_right_Subsystem	74	100%	100%	100%	100%
32. . . . . Chart1	70	100%	100%	100%	NA
33. . . . . SF: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1	69	100%	100%	100%	NA
34. . . . . SF: Normal	9	100%	NA	NA	NA
35. . . . . SF: Normal2	43	100%	100%	100%	NA
36. . . . . driver_control_Subsystem	72	100%	100%	100%	NA
37. . . . . Chart	70	100%	100%	100%	NA
38. . . . . SF: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart	69	100%	100%	100%	NA
39. . . . . SF: Normal	9	100%	NA	NA	NA
40. . . . . SF: Normal2	43	100%	100%	100%	NA
41. . . . . rear_left_Subsystem	74	100%	100%	100%	100%
42. . . . . Chart	70	100%	100%	100%	NA
43. . . . . SF: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart	69	100%	100%	100%	NA
44. . . . . SF: Normal	9	100%	NA	NA	NA
45. . . . . SF: Normal2	43	100%	100%	100%	NA

46. . . . . BCM_WC_CONTROL	2	100%	68%	50%	100%
47. . . . . BCM_WC_FR_Automodes		NA	67%	NA	100%
48. . . . . BCM_FR_AUTOMODE0		NA	100%	NA	100%
49. . . . . BCM_FR_AUTOMODE1		NA	100%	NA	100%
50. . . . . BCM_FR_AUTOMODE2		NA	50%	NA	100%
51. . . . . BCM_FR_AUTOMODE3		NA	50%	NA	100%
52. . . . . BCM_FR_AUTOMODE4		NA	50%	NA	100%
53. . . . . BCM_FR_AUTOMODE5		NA	50%	NA	100%
54. . . . . BCM_WC_FR_manual	1	100%	50%	0%	100%
55. . . . . BCM_FRM_COND		NA	50%	NA	100%
56. . . . . BCM_FRM_COND1		NA	50%	NA	100%
57. . . . . BCM_FRM_COND2		NA	50%	NA	100%
58. . . . . BCM_FRM_COND3		NA	50%	NA	100%
59. . . . . LessThan30		NA	50%	NA	100%
60. . . . . BCM_WC_Rear_windshield		NA	100%	100%	100%
61. . . . . BCM_WC_COMP		NA	100%	NA	100%
62. . . . . TP_Monitoring	24	100%	NA	NA	100%
63. . . . . IndicateHigh1	2	100%	NA	NA	100%
64. . . . . IndicateHigh2	2	100%	NA	NA	100%
65. . . . . IndicateHigh3	2	100%	NA	NA	100%
66. . . . . IndicateHigh4	2	100%	NA	NA	100%
67. . . . . IndicateLow1	2	100%	NA	NA	100%
68. . . . . IndicateLow2	2	100%	NA	NA	100%
69. . . . . IndicateLow3	2	100%	NA	NA	100%
70. . . . . IndicateLow4	2	100%	NA	NA	100%

Details

1. Model "Automotive_model2"

Child Systems: BCM_SAFETY_MODULE

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	461
Condition	NA	78% ((333+130)/592) condition outcomes
Decision	NA	97% ((300+134)/449) decision outcomes
MCDC	NA	71% ((88+86)/246) conditions reversed the outcome
Execution	NA	100% (160/160) objective outcomes

2. SubSystem block "BCM_SAFETY_MODULE"

Justify or Exclude

Parent: /Automotive_model2

Child Systems: BCM_AIRBAGSYSTEM, BCM_Powerwindow_and_Door_operation, BCM_WC_CONTROL, TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	460
Condition	NA	78% ((333+130)/592) condition outcomes
Decision	NA	97% ((300+134)/449) decision outcomes
MCDC	NA	71% ((88+86)/246) conditions reversed the outcome
Execution	NA	100% (160/160) objective outcomes

3. SubSystem block "BCM_AIRBAGSYSTEM"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE

Child Systems: BCM_AB_CheckCrashType, BCM_AB_FrontAirBagActivation, BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	167
Condition	NA	64% (202/318) condition outcomes
Decision	NA	82% (70/85) decision outcomes
MCDC	NA	45% (56/124) conditions reversed the outcome
Execution	NA	100% (82/82) objective outcomes

Full Coverage

Model Object	Metric
Sum block "BCM_AB_DD"	Execution
RelationalOperator block "BCM_AB_SD"	Condition, Execution
Constant block "BCM_AB_ConstantVS"	Execution
Constant block "BCM_AB_Value"	Execution
Constant block "BCM_Airbag_Val_Side"	Execution

4. SubSystem block "BCM_AB_CheckCrashType"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM
Child Systems: ActivateAirBagSensor

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	3
Condition	NA	100% (12/12) condition outcomes
Decision	NA	100% (3/3) decision outcomes
MCDC	NA	100% (4/4) conditions reversed the outcome
Execution	NA	100% (5/5) objective outcomes

Full Coverage

Model Object	Metric
Logic block "AND"	Condition, MCDC, Execution
Logic block "AND1"	Condition, MCDC, Execution
RelationalOperator block "CheckYawRateForFront"	Condition, Execution
RelationalOperator block "CheckYawRateForSide"	Condition, Execution
Constant block "Val_Front"	Execution

5. MATLAB Function "ActivateAirBagSensor"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_CheckCrashType

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	3
Decision	NA	100% (3/3) decision outcomes

Full Coverage

Model Object	Metric
MATLAB Function "fcn"	Decision

6. SubSystem block "BCM_AB_FrontAirBagActivation"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM

Child Systems: BCM_AB_Compare_Driver, BCM_AB_Compare_Front, BCM_AB_Compare_Left_Rear, BCM_AB_Compare_Right_Rear, BCM_AB_SIDE_WeightSensor

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	82
Condition	NA	65% (99/152) condition outcomes
Decision	100% (2/2) decision outcomes	83% (34/41) decision outcomes
MCDC	NA	53% (32/60) conditions reversed the outcome
Execution	NA	100% (36/36) objective outcomes

Decisions analyzed

enable logical value	100%
false	1764/1796 T1
true	32/1796 T1

RelationalOperator block "BCM_AB_Decision_DS_AB"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	32 T1	0

RelationalOperator block "BCM_AB_Decision_SFL"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	32 T1	0

RelationalOperator block "BCM_AB_Decision_SUL"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	32 T1	0

Full Coverage

Model Object	Metric
Switch block "Switch_Driver"	Decision, Execution
Switch block "Switch_Front"	Decision, Execution
Switch block "Switch_LRear"	Decision, Execution
Switch block "Switch_RRear"	Decision, Execution
RelationalOperator block "BCM_AB_Decision_LF"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LF_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LF_UL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR_UL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR_UL"	Condition, Execution
Constant block "BCM_AB_Compare_DS_AB"	Execution
Constant block "BCM_AB_Compare_LF"	Execution
Constant block "BCM_AB_Compare_LF_FL"	Execution
Constant block "BCM_AB_Compare_LF_UL"	Execution
Constant block "BCM_AB_Compare_LR"	Execution
Constant block "BCM_AB_Compare_LR_UL"	Execution
Constant block "BCM_AB_Compare_SFL"	Execution
Constant block "BCM_AB_Compare_SUL"	Execution
Constant block "BCM_LR_Decision_LR_FL"	Execution
Constant block "Constant"	Execution
Constant block "Constant1"	Execution
Constant block "Constant10"	Execution
Constant block "Constant11"	Execution
Constant block "Constant2"	Execution
Constant block "Constant3"	Execution
Constant block "Constant9"	Execution

7. SubSystem block "BCM_AB_Compare_Driver"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation/BCM_AB_Compare_Driver

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	32 T1	0

8. SubSystem block "BCM_AB_Compare_Front"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

9. SubSystem block "BCM_AB_Compare_Left_Rear"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

10. SubSystem block "BCM_AB_Compare_Right_Rear"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

11. MATLAB Function "BCM_AB_SIDE_WeightSensor"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	76
Condition	NA	59% (71/120) condition outcomes
Decision	NA	77% (24/31) decision outcomes
MCDC	NA	53% (32/60) conditions reversed the outcome

MATLAB Function "fcn"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_FrontAirBagActivation/BCM_AB_SIDE_WeightSensor

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	75
Condition	59% (71/120) condition outcomes
Decision	77% (24/31) decision outcomes
MCDC	53% (32/60) conditions reversed the outcome

```
1  function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)
2      if (driver==0 && l_front==0 && l_rear==0 && r_rear==1)      %0001
3          DRIVER=0;
4          L_FRONT=0;
5          L_REAR=0;
6          R_REAR=1;
7
8      elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)
9          DRIVER=0;
10         L_FRONT=0;
11         L_REAR=1;
12         R_REAR=0;
13
14     elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)
15         DRIVER=0;
16         L_FRONT=0;
17         L_REAR=1;
18         R_REAR=1;
19
20     elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)
21         DRIVER=0;
22         L_FRONT=1;
23         L_REAR=0;
24         R_REAR=0;
25
26     elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)
27         DRIVER=0;
28         L_FRONT=1;
29         L_REAR=0;
30         R_REAR=1;
31
32     elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)
```

```
33          DRIVER=0;
34          L_FRONT=1;
35          L_REAR=1;
36          R_REAR=0;
37
38      elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)
39          DRIVER=0;
40          L_FRONT=1;
41          L_REAR=1;
42          R_REAR=1;
43
44      elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)
45          DRIVER=1;
46          L_FRONT=0;
47          L_REAR=0;
48          R_REAR=0;
49
50      elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)
51          DRIVER=1;
52          L_FRONT=0;
53          L_REAR=0;
54          R_REAR=1;
55
56      elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)
57          DRIVER=1;
58          L_FRONT=0;
59          L_REAR=1;
60          R_REAR=0;
61
62      elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)
63          DRIVER=1;
64          L_FRONT=0;
65          L_REAR=1;
66          R_REAR=1;
67
68      elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)
69          DRIVER=1;
70          L_FRONT=1;
71          L_REAR=0;
72          R_REAR=0;
73
74      elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==1)
75          DRIVER=1;
76          L_FRONT=1;
77          L_REAR=0;
78          R_REAR=1;
79
80      elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)
81          DRIVER=1;
82          L_FRONT=1;
83          L_REAR=1;
84          R_REAR=0;
85
86      elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)
87          DRIVER=1;
88          L_FRONT=1;
89          L_REAR=1;
90          R_REAR=1;
91
92      else
93          DRIVER=0;
94          L_FRONT=0;
95          L_REAR=0;
96          R_REAR=0;
97      end
```

#1: function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)

Decisions analyzed

function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)	100%
executed	160/160 T1

#2: if (driver==0 && l_front==0 && l_rear==0 && r_rear==1) %0001

Decisions analyzed

if (driver==0 && l_front==0 && l_rear==0 && r_rear==1)	50%
false	160/160 T1

true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==0	0	0
l_rear==0	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==0 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#8: elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==0	0	0
l_rear==1	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==1 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#14: elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==0	0	0
l_rear==1	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==1 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#20: elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==1	0	0
l_rear==0	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==0 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#26: elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==1	0	0
l_rear==0	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==0 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#32: elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1

l_front==1	0	0
l_rear==1	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==1 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#38: elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)	50%
false	160/160 T1
true	0/160

Conditions analyzed

Description	True	False
driver==0	0	160 T1
l_front==1	0	0
l_rear==1	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==1 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#44: elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)	100%
false	152/160

	T1
true	8/160 T1

Conditions analyzed

Description	True	False
driver==1	148 T1	12 T1
l_front==0	68 T1	80 T1
l_rear==0	28 T1	40 T1
r_rear==0	8 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==0 && r_rear==0		
driver==1	**T**TTT T1	**F**xxx T1
l_front==0	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==0	TTT**T** T1	TTT**F** T1

#50: elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)	100%
false	140/152 T1
true	12/152 T1

Conditions analyzed

Description	True	False
driver==1	140 T1	12 T1
l_front==0	60 T1	80 T1
l_rear==0	20 T1	40 T1
r_rear==1	12 T1	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==0 && r_rear==1		
driver==1	**T**TTT T1	**F**xxx T1
l_front==0	T**T**TT T1	T**F**xx T1

l_rear==0	TTTT T1	TTFx T1
r_rear==1	TTTT T1	TTTF T1

#56: elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)	100%
false	128/140 T1
true	12/140 T1

Conditions analyzed

Description	True	False
driver==1	128 T1	12 T1
l_front==0	48 T1	80 T1
l_rear==1	32 T1	16 T1
r_rear==0	12 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==1 && r_rear==0		
driver==1	TTTT T1	Fxxx T1
l_front==0	TTTT T1	TFxx T1
l_rear==1	TTTT T1	TTFx T1
r_rear==0	TTTT T1	TTTF T1

#62: elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)	100%
false	116/128 T1
true	12/128 T1

Conditions analyzed

Description	True	False
driver==1	116 T1	12 T1
l_front==0	36 T1	80 T1

l_rear==1	20 T1	16 T1
r_rear==1	12 T1	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==1 && r_rear==1		
driver==1	**T**TTT T1	**F**xxx T1
l_front==0	T**T**TT T1	T**F**xx T1
l_rear==1	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	TTT**F** T1

#68: elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)	100%
false	104/116 T1
true	12/116 T1

Conditions analyzed

Description	True	False
driver==1	104 T1	12 T1
l_front==1	72 T1	32 T1
l_rear==0	32 T1	40 T1
r_rear==0	12 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==0 && r_rear==0		
driver==1	**T**TTT T1	**F**xxx T1
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==0	TTT**T** T1	TTT**F** T1

#74: elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==0 &&	100%

r_rear==1)	
false	92/104 T1
true	12/104 T1

Conditions analyzed

Description	True	False
driver==1	92 T1	12 T1
l_front==1	60 T1	32 T1
l_rear==0	20 T1	40 T1
r_rear==1	12 T1	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==0 && r_rear==1		
driver==1	**T**TTT T1	**F**xxx T1
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	TTT**F** T1

#80: elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)	100%
false	80/92 T1
true	12/92 T1

Conditions analyzed

Description	True	False
driver==1	80 T1	12 T1
l_front==1	48 T1	32 T1
l_rear==1	32 T1	16 T1
r_rear==0	12 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==1 && r_rear==0		
driver==1	**T**TTT T1	**F**xxx T1

l_front==1	TTTT T1	TFxx T1
l_rear==1	TTTT T1	TTFx T1
r_rear==0	TTTT T1	TTTF T1

#86: elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)	100%
false	68/80 T1
true	12/80 T1

Conditions analyzed

Description	True	False
driver==1	68 T1	12 T1
l_front==1	36 T1	32 T1
l_rear==1	20 T1	16 T1
r_rear==1	12 T1	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==1 && r_rear==1		
driver==1	TTTT T1	Fxxx T1
l_front==1	TTTT T1	TFxx T1
l_rear==1	TTTT T1	TTFx T1
r_rear==1	TTTT T1	TTTF T1

12. SubSystem block "BCM_AB_Side_AB_Activation"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM

Child Systems: BCM_AB_Decision_Driver, BCM_AB_Decision_LFront, BCM_AB_Decision_LRear, BCM_AB_Decision_RRear, BCM_AB_WeightSensor

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	82
Condition	NA	59% (89/152) condition outcomes
Decision	100% (2/2) decision outcomes	80% (33/41) decision outcomes
MCDC	NA	33% (20/60) conditions reversed the outcome
Execution	NA	100% (36/36) objective outcomes

Decisions analyzed

enable logical value	100%
false	1768/1796 T1
true	28/1796 T1

RelationalOperator block "BCM_AB_Decision_DS_AB"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Uncovered Links:

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input1 == input2	28 T1	0

RelationalOperator block "BCM_AB_Decision_DS_FL"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Uncovered Links:

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input1 == input2	28 T1	0

RelationalOperator block "BCM_AB_Decision_DS_UL"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Uncovered Links:

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input1 == input2	28 T1	0

Full Coverage

Model Object	Metric
Switch block "BCM_AB_Switch_Driver"	Decision, Execution
Switch block "BCM_AB_Switch_FrontLeft"	Decision, Execution
Switch block "BCM_AB_Switch_LeftRear"	Decision, Execution
Switch block "BCM_AB_Switch_RightRear"	Decision, Execution
RelationalOperator block "BCM_AB_Decision_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_LR_UL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR_AB"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR_FL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_RR_UL"	Condition, Execution
RelationalOperator block "BCM_AB_Decision_UL"	Condition, Execution
RelationalOperator block "BCM_AB_Desicion_AB"	Condition, Execution
Constant block "BCM_AB_Compare_DS_AB"	Execution
Constant block "BCM_AB_Compare_DS_FL"	Execution
Constant block "BCM_AB_Compare_DS_UL"	Execution
Constant block "BCM_AB_Compare_FL"	Execution
Constant block "BCM_AB_Compare_LF"	Execution
Constant block "BCM_AB_Compare_LF_AB"	Execution
Constant block "BCM_AB_Compare_LR"	Execution
Constant block "BCM_AB_Compare_LR_FL"	Execution
Constant block "BCM_AB_Compare_RR_AB"	Execution
Constant block "BCM_AB_Compare_RR_FL"	Execution
Constant block "BCM_AB_Compare_RR_UL"	Execution
Constant block "BCM_AB_Compare_UL"	Execution
Constant block "Constant"	Execution
Constant block "Constant1"	Execution
Constant block "Constant2"	Execution
Constant block "Constant3"	Execution

13. SubSystem block "BCM_AB_Decision_Driver"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation/BCM_AB_Decision_Driver

Uncovered Links:

Metric	Coverage

Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	28 T1	0

14. SubSystem block "BCM_AB_Decision_LFront"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

15. SubSystem block "BCM_AB_Decision_LRear"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

16. SubSystem block "BCM_AB_Decision_RRear"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

17. MATLAB Function "BCM_AB_WeightSensor"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	76
Condition	NA	51% (61/120) condition outcomes
Decision	NA	74% (23/31) decision outcomes
MCDC	NA	33% (20/60) conditions reversed the outcome

MATLAB Function "fcn"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_AIRBAGSYSTEM/BCM_AB_Side_AB_Activation/BCM_AB_WeightSensor

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	75
Condition	51% (61/120) condition outcomes
Decision	74% (23/31) decision outcomes
MCDC	33% (20/60) conditions reversed the outcome

```
 1  function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)
 2      if (driver==0 && l_front==0 && l_rear==0 && r_rear==1)      %0001
 3          DRIVER=0;
 4          L_FRONT=0;
 5          L_REAR=0;
 6          R_REAR=1;
 7
 8      elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)
 9          DRIVER=0;
10          L_FRONT=0;
11          L_REAR=1;
12          R_REAR=0;
13
14      elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)
15          DRIVER=0;
16          L_FRONT=0;
17          L_REAR=1;
18          R_REAR=1;
19
20      elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)
21          DRIVER=0;
22          L_FRONT=1;
23          L_REAR=0;
24          R_REAR=0;
25
26      elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)
27          DRIVER=0;
28          L_FRONT=1;
29          L_REAR=0;
30          R_REAR=1;
31
32      elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)
33          DRIVER=0;
34          L_FRONT=1;
35          L_REAR=1;
36          R_REAR=0;
37
38      elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)
39          DRIVER=0;
40          L_FRONT=1;
41          L_REAR=1;
42          R_REAR=1;
43
44      elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)
45          DRIVER=1;
46          L_FRONT=0;
47          L_REAR=0;
48          R_REAR=0;
49
50      elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)
51          DRIVER=1;
52          L_FRONT=0;
53          L_REAR=0;
54          R_REAR=1;
55
```

```
56      elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)
57          DRIVER=1;
58          L_FRONT=0;
59          L_REAR=1;
60          R_REAR=0;
61
62      elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)
63          DRIVER=1;
64          L_FRONT=0;
65          L_REAR=1;
66          R_REAR=1;
67
68      elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)
69          DRIVER=1;
70          L_FRONT=1;
71          L_REAR=0;
72          R_REAR=0;
73
74      elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==1)
75          DRIVER=1;
76          L_FRONT=1;
77          L_REAR=0;
78          R_REAR=1;
79
80      elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)
81          DRIVER=1;
82          L_FRONT=1;
83          L_REAR=1;
84          R_REAR=0;
85
86      elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)
87          DRIVER=1;
88          L_FRONT=1;
89          L_REAR=1;
90          R_REAR=1;
91
92      else
93          DRIVER=0;
94          L_FRONT=0;
95          L_REAR=0;
96          R_REAR=0;
97      end
```

#1: function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)

Decisions analyzed

function [DRIVER,L_FRONT,L_REAR,R_REAR] = fcn(driver, l_front,l_rear,r_rear)	100%
executed	140/140 T1

#2: if (driver==0 && l_front==0 && l_rear==0 && r_rear==1) %0001

Decisions analyzed

if (driver==0 && l_front==0 && l_rear==0 && r_rear==1)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==0	0	0
l_rear==0	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==0 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#8: elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==0)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==0	0	0
l_rear==1	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==1 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#14: elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==0 && l_rear==1 && r_rear==1)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1

l_front==0	0	0
l_rear==1	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==0 && l_rear==1 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==0	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#20: elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==0)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==1	0	0
l_rear==0	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==0 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#26: elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==0 && r_rear==1)	50%
false	140/140

	T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==1	0	0
l_rear==0	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==0 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==0	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#32: elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==0)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==1	0	0
l_rear==1	0	0
r_rear==0	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==1 && r_rear==0		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==0	(TTT**T**)	(TTT**F**)

#38: elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==0 && l_front==1 && l_rear==1 && r_rear==1)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==0	0	140 T1
l_front==1	0	0
l_rear==1	0	0
r_rear==1	0	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==0 && l_front==1 && l_rear==1 && r_rear==1		
driver==0	(**T**TTT)	**F**xxx T1
l_front==1	(T**T**TT)	(T**F**xx)
l_rear==1	(TT**T**T)	(TT**F**x)
r_rear==1	(TTT**T**)	(TTT**F**)

#44: elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==0)	50%
false	140/140 T1
true	0/140

Conditions analyzed

Description	True	False
driver==1	140 T1	0
l_front==0	48 T1	92 T1
l_rear==0	8 T1	40 T1
r_rear==0	0	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==0 && r_rear==0		
driver==1	(**T**TTT)	(**F**xxx)
l_front==0	(T**T**TT)	T**F**xx T1
l_rear==0	(TT**T**T)	TT**F**x T1
r_rear==0	(TTT**T**)	TTT**F** T1

#50: elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==0 && r_rear==1)	100%
false	132/140 T1
true	8/140 T1

Conditions analyzed

Description	True	False
driver==1	140 T1	0
l_front==0	48 T1	92 T1
l_rear==0	8 T1	40 T1
r_rear==1	8 T1	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==0 && r_rear==1		
driver==1	**T**TTT T1	(**F**xxx)
l_front==0	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	(TTT**F**)

#56: elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==0)	100%
false	124/132 T1
true	8/132 T1

Conditions analyzed

Description	True	False
driver==1	132 T1	0
l_front==0	40 T1	92 T1
l_rear==1	28 T1	12 T1
r_rear==0	8 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==1 && r_rear==0		
driver==1	**T**TTT T1	(**F**xxx)
l_front==0	T**T**TT T1	T**F**xx T1
l_rear==1	TT**T**T T1	TT**F**x T1
r_rear==0	TTT**T** T1	TTT**F** T1

#62: elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==0 && l_rear==1 && r_rear==1)	100%
false	116/124 T1
true	8/124 T1

Conditions analyzed

Description	True	False
driver==1	124 T1	0
l_front==0	32 T1	92 T1
l_rear==1	20 T1	12 T1
r_rear==1	8 T1	12 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==0 && l_rear==1 && r_rear==1		
driver==1	**T**TTT T1	(**F**xxx)
l_front==0	T**T**TT T1	T**F**xx T1
l_rear==1	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	TTT**F** T1

#68: elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==0)	100%
false	108/116 T1
true	8/116 T1

Conditions analyzed

Description	True	False
driver==1	116 T1	0
l_front==1	68 T1	48 T1
l_rear==0	28 T1	40 T1
r_rear==0	8 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==0 && r_rear==0		
driver==1	**T**TTT T1	(**F**xxx)
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==0	TTT**T** T1	TTT**F** T1

#74: elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==0 && r_rear==1)	100%
false	100/108 T1
true	8/108 T1

Conditions analyzed

Description	True	False
driver==1	108 T1	0
l_front==1	60 T1	48 T1
l_rear==0	20 T1	40 T1
r_rear==1	8 T1	12 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==0 && r_rear==1		
driver==1	**T**TTT T1	(**F**xxx)
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==0	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	TTT**F** T1

#80: elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==0)	100%
false	92/100 T1
true	8/100 T1

Conditions analyzed

Description	True	False
driver==1	100 T1	0
l_front==1	52 T1	48 T1
l_rear==1	28 T1	24 T1
r_rear==0	8 T1	20 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==1 && r_rear==0		
driver==1	**T**TTT T1	(**F**xxx)
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==1	TT**T**T T1	TT**F**x T1
r_rear==0	TTT**T** T1	TTT**F** T1

#86: elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)

Decisions analyzed

elseif (driver==1 && l_front==1 && l_rear==1 && r_rear==1)	100%
false	84/92 T1
true	8/92

	T1

Conditions analyzed

Description	True	False
driver==1	92 T1	0
l_front==1	44 T1	48 T1
l_rear==1	20 T1	24 T1
r_rear==1	8 T1	12 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
driver==1 && l_front==1 && l_rear==1 && r_rear==1		
driver==1	**T**TTT T1	(**F**xxx)
l_front==1	T**T**TT T1	T**F**xx T1
l_rear==1	TT**T**T T1	TT**F**x T1
r_rear==1	TTT**T** T1	TTT**F** T1

18. SubSystem block "BCM_Powerwindow_and_Door_operation"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE

Child Systems: BCM_Door_system, Front_left_subsystem, Rear_right_Subsystem, driver_control_Subsystem, rear_left_Subsystem

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	267
Condition	NA	100% ((104+130)/234) condition outcomes
Decision	NA	100% ((194+134)/328) decision outcomes
MCDC	NA	100% ((28+86)/114) conditions reversed the outcome
Execution	NA	100% (26/26) objective outcomes

19. SubSystem block "BCM_Door_system"

Justified J1.

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation

Child Systems: BCM_Automatic_Door_lock, BCM_Delay_System_door lock and BCM_interior light subsystem, Compare To Constant

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	4
Condition	NA	100% ((29+5)/34) condition outcomes
Decision	NA	100% ((5+3)/8) decision outcomes
MCDC	NA	100% ((5+9)/14) conditions reversed the outcome
Execution	NA	100% (20/20) objective outcomes

Logic block "OR1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system

Metric	Coverage
Cyclomatic Complexity	0
Condition	100% ((3+1)/4) condition outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input port 1	-	1796 T1
input port 2	1692 T1	104 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	-	
input port 2	F**T** T1	F**F** T1

20. SubSystem block "BCM_Automatic_Door_lock"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	2
Condition	NA	100% ((4+4)/8) condition outcomes
Decision	NA	100% ((2+2)/4) decision outcomes
MCDC	NA	100% ((0+3)/3) conditions reversed the outcome
Execution	NA	100% (5/5) objective outcomes

If block "If"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Automatic_Door_lock

Metric	Coverage
Cyclomatic Complexity	2
Decision	100% ((2+2)/4) decision outcomes
Execution	100% (1/1) objective outcomes

Decisions analyzed

input 1 "if" condition	100%
false	1796/1796 T1
true	-
input 2 "elseif" condition	100%
false	-
true	1796/1796 T1

Logic block "AND"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Automatic_Door_lock

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	100% ((2+2)/4) condition outcomes
MCDC	100% ((0+3)/3) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input port 1	1796 T1	-
input port 2	-	1796 T1

MC/DC analysis (combinations in parentheses did not occur)

Includes 2 blocks

Decision/Condition	**True Out**	**False Out**
C1 && (C2 || C3)		
C1 (AND In1)	-	
C2 (OR In1)	-	
C3 (OR In2)	-	

Logic block "OR"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Automatic_Door_lock

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	100% ((2+2)/4) condition outcomes
MCDC	see AND
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input port 1	-	1796 T1
input port 2	-	1796 T1

Full Coverage

Model Object	**Metric**
Constant block "Constant"	Execution
Constant block "reset"	Execution

21. SubSystem block "BCM_Delay_System_door lock and BCM_inter..."

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system

Child Systems: Compare To Constant, Repeating Sequence Stair

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	2
Condition	NA	100% (20/20) condition outcomes
Decision	NA	100% ((3+1)/4) decision outcomes
MCDC	NA	100% ((4+5)/9) conditions reversed the outcome
Execution	NA	100% (13/13) objective outcomes

Logic block "AND"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem

Metric	Coverage
Cyclomatic Complexity	0
Condition	100% (10/10) condition outcomes
MCDC	100% ((1+4)/5) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	-	
input port 2	T**T**TTT T1	T**F**TTT T1
input port 3	-	
input port 4	-	
input port 5	-	

Logic block "AND1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem

Metric	Coverage
Cyclomatic Complexity	0
Condition	100% (8/8) condition outcomes
MCDC	100% ((3+1)/4) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	**T**TTT T1	**F**TTT T1
input port 2	T**T**TT T1	T**F**TT T1
input port 3	-	
input port 4	TTT**T** T1	TTT**F** T1

Switch block "Switch1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem

Metric	Coverage
Cyclomatic Complexity	1
Decision	100% ((1+1)/2) decision outcomes
Execution	100% (1/1) objective outcomes

Decisions analyzed

trigger >= threshold	100%
false (output is from 3rd input port)	-
true (output is from 1st input port)	8576/8576 T1

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution
DataTypeConversion block "Data Type Conversion"	Execution
Sum block "Add"	Execution
Constant block "Constant4"	Execution
Constant block "Constant5"	Execution
Delay block "Delay1"	Execution

22. SubSystem block "Compare To Constant"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

23. SubSystem block "Repeating Sequence Stair"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem

Child Systems: LimitedCounter

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (3/3) objective outcomes

Full Coverage

Model Object	Metric
SignalConversion block "Out"	Execution

24. SubSystem block "LimitedCounter"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system/BCM_Delay_System_door lock and BCM_interior light subsystem/Repeating Sequence Stair

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
S-Function block "Data Type Propagation"	Execution
UnitDelay block "Output"	Execution

25. SubSystem block "Compare To Constant"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/BCM_Door_system

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

26. SubSystem block "Front_left_subsystem"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation

Child Systems: decision_Box

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	43
Condition	NA	100% ((7+13)/20) condition outcomes
Decision	100% (2/2) decision outcomes	100% ((43+17)/60) decision outcomes
MCDC	NA	100% ((2+8)/10) conditions reversed the outcome
Execution	NA	100% (2/2) objective outcomes

Decisions analyzed

enable logical value	100%
false	628/1796 T1
true	1168/1796 T1

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution

27. SubSystem block "decision_Box"

Justified J2.

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem
Child Systems: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	39
Condition	NA	100% ((7+13)/20) condition outcomes
Decision	NA	100% ((37+17)/54) decision outcomes
MCDC	NA	100% ((2+8)/10) conditions reversed the outcome

28. Chart "BCM_SAFETY_MODULE/BCM_Powerwindow_and_Do..."

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box
Child Systems: Normal, Normal2

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	3	38
Condition	NA	100% ((7+13)/20) condition outcomes
Decision	100% (4/4) decision outcomes	100% ((37+17)/54) decision outcomes
MCDC	NA	100% ((2+8)/10) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Fault"	748/1164 T1
State "Normal"	260/1164 T1
State "Normal2"	120/1164 T1
State "block_start"	36/1164 T1

Transition "[data ==1]" from "Normal2" to "block_start"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box

Metric	Coverage
Cyclomatic Complexity	1
Decision	100% ((1+1)/2) decision outcomes

```
1  [data ==1 ]
```

#1: [data ==1]

Decisions analyzed

data ==1	100%
false	120/120 T1

true	-

Full Coverage

Model Object	Metric
Transition "[data1 == 1]" from "Normal" to "Fault"	Decision
Transition "[data1 == 0 && data==1]{warning=0;}" from "Fault" to "Normal"	Condition, Decision, MCDC
Transition "[data ==1]" from "block_start" to "Normal"	Decision
Transition "[data == -1]" from "Normal" to "block_start"	Decision
Transition "[data ==-1]" from "block_start" to "Normal2"	Decision
Transition "[data1==1]" from "Normal2" to "Fault"	Decision
Transition "[data==-1]{warning=0;}" from "Fault" to "Normal2"	Decision
Transition "[data1 ==1]" from "block_start" to "Fault"	Decision

29. State "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	6	9
Decision	100% ((7+1)/8) decision outcomes	100% ((13+1)/14) decision outcomes

Decisions analyzed

Substate executed	100%
State "Go"	48/248 T1
State "PrepareToStop"	60/248 T1
State "Start"	80/248 T1
State "second"	60/248 T1
Substate exited when parent exits	100%
State "Go"	-
State "PrepareToStop"	4/12 T1
State "Start"	4/12 T1
State "second"	4/12 T1

Full Coverage

Model Object	Metric
Transition "after(1,sec)" from Junction #1 to Junction #0	Decision
Transition "after(1,sec)" from "PrepareToStop" to "second"	Decision
Transition "after(1,sec)" from "Go" to "PrepareToStop"	Decision

30. State "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	8	16
Condition	NA	100% ((3+13)/16) condition outcomes
Decision	100% ((2+8)/10) decision outcomes	100% ((3+15)/18) decision outcomes
MCDC	NA	100% ((0+8)/8) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Start1"	112/112 T1
State "five"	-
State "four"	-
State "three"	-
State "two"	-
Substate exited when parent exits	100%
State "Start1"	8/8 T1
State "five"	-
State "four"	-
State "three"	-
State "two"	-

Transition "after(1,sec)[op1==50]" from "five" to "three"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	

op1==50	-

Transition "after(1,sec)[op1==25]" from "three" to "two"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==75]" from "four" to "five"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==100]" from "Start1" to "four"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Front_left_subsystem/decision_Box.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==100]
```

#1: after(1,sec)[op1==100]

Decisions analyzed

after(1,sec)[op1==100]	100%
false	112/112 T1
true	-

Conditions analyzed

Description	True	False
after(1,sec)	72 T1	40 T1
op1==100	-	72 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==100]		
after(1,sec)	-	
op1==100	-	

31. SubSystem block "Rear_right_Subsystem"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation

Child Systems: Chart1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	74
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	100% (2/2) decision outcomes	100% ((46+42)/88) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome
Execution	NA	100% (2/2) objective outcomes

Decisions analyzed

enable logical value	100%
false	628/1796 T1
true	1168/1796 T1

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution

32. SubSystem block "Chart1"

Justified J3.

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem

Child Systems: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	70
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	NA	100% ((40+42)/82) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome

33. Chart "BCM_SAFETY_MODULE/BCM_Powerwindow_and_Do..."

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Child Systems: Normal, Normal2

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	3	69
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	100% (4/4) decision outcomes	100% ((40+42)/82) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Fault"	748/1164 T1
State "Normal"	276/1164 T1
State "Normal2"	104/1164 T1
State "block_start"	36/1164 T1

Transition "[data ==1 && data1 ==0]" from "block_start" to "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% (2/2) decision outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome

```
1  [data ==1 && data1 ==0 ]
```

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	32/36 T1
true	4/36 T1

Conditions analyzed

Description	**True**	**False**
data ==1	4 T1	32 T1
data1 ==0	4 T1	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
data ==1 && data1 ==0		
data ==1	**T**T T1	**F**x T1
data1 ==0	-	

Transition "[data ==-1 && data1==0]" from "block_start" to "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  [data ==-1 && data1==0]
```

#1: [data ==-1 && data1==0]

Decisions analyzed

data ==-1 && data1==0	100%
false	28/28 T1
true	-

Conditions analyzed

Description	**True**	**False**

data ==-1	16 T1	12 T1
data1==0	-	16 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==-1 && data1==0		
data ==-1	-	
data1==0	-	

Transition "[data ==1 && data1 ==0]" from "Normal2" to "block_start"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  [data ==1 && data1 ==0]
```

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	104/104 T1
true	-

Conditions analyzed

Description	True	False
data ==1	-	104 T1
data1 ==0	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==1 && data1 ==0		
data ==1	-	
data1 ==0	-	

Full Coverage

Model Object	Metric
Transition "[data1 == 1]" from "Normal" to "Fault"	Decision
Transition "[data1 == 0 && data==1]{warning=0;}." from "Fault" to "Normal"	Condition, Decision, MCDC
Transition "[data ==-1]" from "Normal" to "block_start"	Decision

Transition "[data1==1]" from "Normal2" to "Fault"	Decision
Transition "[data1 ==0 && data==-1]{warning=0;}" from "Fault" to "Normal2"	Condition, Decision, MCDC
Transition "[data1 ==1]" from "block_start" to "Fault"	Decision

34. State "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	6	9
Decision	100% ((7+1)/8) decision outcomes	100% ((13+1)/14) decision outcomes

Decisions analyzed

Substate executed	100%
State "Go"	60/264 T1
State "PrepareToStop"	60/264 T1
State "Start"	84/264 T1
State "second"	60/264 T1
Substate exited when parent exits	100%
State "Go"	4/12 T1
State "PrepareToStop"	4/12 T1
State "Start"	-
State "second"	4/12 T1

Full Coverage

Model Object	Metric
Transition "after(1,sec)" from Junction #1 to Junction #0	Decision
Transition "after(1,sec)" from "PrepareToStop" to "second"	Decision
Transition "after(1,sec)" from "Go" to "PrepareToStop"	Decision

35. State "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	22	43
Condition	NA	100% ((3+37)/40) condition outcomes
Decision	100% ((3+21)/24) decision outcomes	100% ((7+39)/46) decision outcomes
MCDC	NA	100% ((0+20)/20) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Start1"	4/100 T1
State "five"	-

State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	-
State "four3"	-
State "state5"	96/100 T1
State "three"	-
State "three1"	-
State "two"	-
Substate exited when parent exits	100%
State "Start1"	-
State "five"	-
State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	-
State "four3"	-
State "state5"	4/4 T1
State "three"	-
State "three1"	-
State "two"	-

Transition "after(1,sec)[op1==100]" from Junction #2 to Junction #3

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==100]
```

#1: after(1,sec)[op1==100]

Decisions analyzed

after(1,sec)[op1==100]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	4 T1

op1==100	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==100]		
after(1,sec)	-	
op1==100	-	

Transition "after(1,sec)[op1==50]" from "five" to "three"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==25]" from "three" to "two"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==75]" from "four" to "five"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==75]" from Junction #4 to Junction #5

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	4 T1
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==25]" from "five1" to "three1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	-

op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==50]" from "four1" to "five1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==50]" from Junction #6 to Junction #7

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==25]" from "four2" to "five2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "[op1==0]" from "Start1" to "state5"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	**Coverage**
Cyclomatic Complexity	1
Decision	100% ((1+1)/2) decision outcomes

```
1  [op1==0]
```

#1: [op1==0]

Decisions analyzed

op1==0	100%
false	-
true	4/4 T1

Transition "after(1,sec)[op1==25]" from "Start1" to Junction #8

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/Rear_right_Subsystem/Chart1.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	4 T1
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

36. SubSystem block "driver_control_Subsystem"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation
Child Systems: Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	72
Condition	NA	100% ((32+28)/60) condition outcomes
Decision	100% (2/2) decision outcomes	100% ((54+30)/84) decision outcomes
MCDC	NA	100% ((11+19)/30) conditions reversed the outcome

Decisions analyzed

enable logical value	100%
false	628/1796 T1
true	1168/1796 T1

37. SubSystem block "Chart"

Justified J4.
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem
Child Systems: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	70
Condition	NA	100% ((32+28)/60) condition outcomes
Decision	NA	100% ((52+30)/82) decision outcomes
MCDC	NA	100% ((11+19)/30) conditions reversed the outcome

38. Chart "BCM_SAFETY_MODULE/BCM_Powerwindow_and_Do..."

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart
Child Systems: Normal, Normal2

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	3	69
Condition	NA	100% ((32+28)/60) condition outcomes
Decision	100% (4/4) decision outcomes	100% ((52+30)/82) decision outcomes
MCDC	NA	100% ((11+19)/30) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Fault"	552/1164 T1
State "Normal"	372/1164 T1
State "Normal2"	184/1164 T1
State "block_start"	56/1164 T1

Transition "[data ==1 && data1 ==0]" from "block_start" to "Normal"

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes

Decision	100% (2/2) decision outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome

```
1  [data ==1 && data1 ==0 ]
```

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	40/56 T1
true	16/56 T1

Conditions analyzed

Description	True	False
data ==1	16 T1	40 T1
data1 ==0	16 T1	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==1 && data1 ==0		
data ==1	**T**T T1	**F**x T1
data1 ==0	-	

Transition "[data ==1 && data1 ==0]" from "Normal2" to "block_start"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% (2/2) decision outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome

```
1  [data ==1 && data1 ==0]
```

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	172/184 T1
true	12/184 T1

Conditions analyzed

Description	True	False
data ==1	12 T1	172 T1
data1 ==0	12 T1	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==1 && data1 ==0		
data ==1	**T**T T1	**F**x T1
data1 ==0	-	

Full Coverage

Model Object	Metric
Transition "[data1 == 1]" from "Normal" to "Fault"	Decision
Transition "[data1 == 0 && data==1]{warning=0;}" from "Fault" to "Normal"	Condition, Decision, MCDC
Transition "[data ==-1]" from "Normal" to "block_start"	Decision
Transition "[data ==-1 && data1==0]" from "block_start" to "Normal2"	Condition, Decision, MCDC
Transition "[data1==1]" from "Normal2" to "Fault"	Decision
Transition "[data1 ==0 && data==-1]{warning=0;}" from "Fault" to "Normal2"	Condition, Decision, MCDC
Transition "[data1 ==1]" from "block_start" to "Fault"	Decision

39. State "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	6	9
Decision	100% (8/8) decision outcomes	100% (14/14) decision outcomes

Decisions analyzed

Substate executed	100%
State "Go"	72/344 T1
State "PrepareToStop"	60/344 T1
State "Start"	152/344 T1
State "second"	60/344 T1
Substate exited when parent exits	100%
State "Go"	16/28 T1
State "PrepareToStop"	4/28 T1
State "Start"	4/28 T1
State "second"	4/28 T1

Full Coverage

Model Object	Metric

Transition "after(1,sec)" from Junction #1 to Junction #0	Decision
Transition "after(1,sec)" from "PrepareToStop" to "second"	Decision
Transition "after(1,sec)" from "Go" to "PrepareToStop"	Decision

40. State "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	22	43
Condition	NA	100% ((14+26)/40) condition outcomes
Decision	100% ((7+17)/24) decision outcomes	100% ((16+30)/46) decision outcomes
MCDC	NA	100% ((3+17)/20) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Start1"	52/168 T1
State "five"	-
State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	8/168 T1
State "four3"	64/168 T1
State "state5"	44/168 T1
State "three"	-
State "three1"	-
State "two"	-
Substate exited when parent exits	100%
State "Start1"	-
State "five"	-
State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	4/16 T1
State "four3"	4/16 T1
State "state5"	8/16 T1
State "three"	-
State "three1"	-
State "two"	-

Transition "after(1,sec)[op1==100]" from Junction #2 to Junction #3

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==100]
```

#1: after(1,sec)[op1==100]

Decisions analyzed

after(1,sec)[op1==100]	100%
false	52/52 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	8 T1	44 T1
op1==100	-	8 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==100]		
after(1,sec)	-	
op1==100	-	

Transition "after(1,sec)[op1==50]" from "five" to "three"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**

after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==25]" from "three" to "two"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==75]" from "four" to "five"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==75]" from Junction #4 to Junction #5

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	48/48 T1
true	-

Conditions analyzed

Description	True	False
after(1,sec)	4 T1	44 T1
op1==75	-	4 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==25]" from "five1" to "three1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==50]" from "four1" to "five1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**

after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==50]" from Junction #6 to Junction #7

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% (2/2) decision outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	36/40 T1
true	4/40 T1

Conditions analyzed

Description	True	False
after(1,sec)	4 T1	36 T1
op1==50	4 T1	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	**T**T T1	**F**x T1
op1==50	-	

Transition "after(1,sec)[op1==25]" from "four2" to "five2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/driver_control_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2

Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	8/8 T1
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	8 T1
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Full Coverage

Model Object	Metric
Transition "[op1==0]" from "Start1" to "state5"	Decision
Transition "after(1,sec)[op1==25]" from "Start1" to Junction #8	Condition, Decision, MCDC

41. SubSystem block "rear_left_Subsystem"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation

Child Systems: Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	74
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	100% (2/2) decision outcomes	100% ((46+42)/88) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome
Execution	NA	100% (2/2) objective outcomes

Decisions analyzed

enable logical value	100%
false	628/1796 T1
true	1168/1796 T1

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution

42. SubSystem block "Chart"

Justified J5.

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem

Child Systems: BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	1	70
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	NA	100% ((40+42)/82) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome

43. Chart "BCM_SAFETY_MODULE/BCM_Powerwindow_and_Do..."

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Child Systems: Normal, Normal2

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	3	69
Condition	NA	100% ((18+42)/60) condition outcomes
Decision	100% (4/4) decision outcomes	100% ((40+42)/82) decision outcomes
MCDC	NA	100% ((5+25)/30) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Fault"	748/1164 T1
State "Normal"	276/1164 T1
State "Normal2"	104/1164 T1
State "block_start"	36/1164 T1

Transition "[data ==1 && data1 ==0]" from "block_start" to "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% (2/2) decision outcomes
MCDC	100% ((1+1)/2) conditions reversed the outcome

```
1  [data ==1 && data1 ==0 ]
```

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	32/36 T1

true	4/36 T1

Conditions analyzed

Description	True	False
data ==1	4 T1	32 T1
data1 ==0	4 T1	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==1 && data1 ==0		
data ==1	**T**T T1	**F**x T1
data1 ==0	-	

Transition "[data ==-1 && data1==0]" from "block_start" to "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((3+1)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  [data ==-1 && data1==0]
```

#1: [data ==-1 && data1==0]

Decisions analyzed

data ==-1 && data1==0	100%
false	28/28 T1
true	-

Conditions analyzed

Description	True	False
data ==-1	16 T1	12 T1
data1==0	-	16 T1

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==-1 && data1==0		
data ==-1	-	
data1==0	-	

Transition "[data ==1 && data1 ==0]" from "Normal2" to "block_start"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

1 `[data ==1 && data1 ==0]`

#1: [data ==1 && data1 ==0]

Decisions analyzed

data ==1 && data1 ==0	100%
false	104/104 T1
true	-

Conditions analyzed

Description	True	False
data ==1	-	104 T1
data1 ==0	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
data ==1 && data1 ==0		
data ==1	-	
data1 ==0	-	

Full Coverage

Model Object	Metric
Transition "[data1 == 1]" from "Normal" to "Fault"	Decision
Transition "[data1 == 0 && data==1]{warning=0;}" from "Fault" to "Normal"	Condition, Decision, MCDC
Transition "[data ==-1]" from "Normal" to "block_start"	Decision
Transition "[data1==1]" from "Normal2" to "Fault"	Decision
Transition "[data1 ==0 && data==-1]{warning=0;}" from "Fault" to "Normal2"	Condition, Decision, MCDC
Transition "[data1 ==1]" from "block_start" to "Fault"	Decision

44. State "Normal"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	6	9
Decision	100% ((7+1)/8) decision outcomes	100% ((13+1)/14) decision outcomes

Decisions analyzed

Substate executed	100%
State "Go"	60/264

	T1
State "PrepareToStop"	60/264 T1
State "Start"	84/264 T1
State "second"	60/264 T1
Substate exited when parent exits	100%
State "Go"	4/12 T1
State "PrepareToStop"	4/12 T1
State "Start"	-
State "second"	4/12 T1

Full Coverage

Model Object	Metric
Transition "after(1,sec)" from Junction #1 to Junction #0	Decision
Transition "after(1,sec)" from "PrepareToStop" to "second"	Decision
Transition "after(1,sec)" from "Go" to "PrepareToStop"	Decision

45. State "Normal2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	22	43
Condition	NA	100% ((3+37)/40) condition outcomes
Decision	100% ((3+21)/24) decision outcomes	100% ((7+39)/46) decision outcomes
MCDC	NA	100% ((0+20)/20) conditions reversed the outcome

Decisions analyzed

Substate executed	100%
State "Start1"	4/100 T1
State "five"	-
State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	-
State "four3"	-
State "state5"	96/100 T1
State "three"	-
State "three1"	-
State "two"	-
Substate exited when parent exits	100%

State "Start1"	-
State "five"	-
State "five1"	-
State "five2"	-
State "four"	-
State "four1"	-
State "four2"	-
State "four3"	-
State "state5"	4/4 T1
State "three"	-
State "three1"	-
State "two"	-

Transition "after(1,sec)[op1==100]" from Junction #2 to Junction #3

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==100]
```

#1: after(1,sec)[op1==100]

Decisions analyzed

after(1,sec)[op1==100]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	4 T1
op1==100	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==100]		
after(1,sec)	-	
op1==100	-	

Transition "after(1,sec)[op1==50]" from "five" to "three"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==25]" from "three" to "two"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==75]" from "four" to "five"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==75]" from Junction #4 to Junction #5

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==75]
```

#1: after(1,sec)[op1==75]

Decisions analyzed

after(1,sec)[op1==75]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	4 T1
op1==75	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==75]		
after(1,sec)	-	
op1==75	-	

Transition "after(1,sec)[op1==25]" from "five1" to "three1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "after(1,sec)[op1==50]" from "four1" to "five1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==50]" from Junction #6 to Junction #7

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==50]
```

#1: after(1,sec)[op1==50]

Decisions analyzed

after(1,sec)[op1==50]	100%
false	-
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	-
op1==50	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==50]		
after(1,sec)	-	
op1==50	-	

Transition "after(1,sec)[op1==25]" from "four2" to "five2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	2
Condition	100% ((0+4)/4) condition outcomes
Decision	100% ((0+2)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	-
true	-

Conditions analyzed

Description	True	False
after(1,sec)	-	-
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

Transition "[op1==0]" from "Start1" to "state5"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	Coverage
Cyclomatic Complexity	1
Decision	100% ((1+1)/2) decision outcomes

```
1  [op1==0]
```

#1: [op1==0]

Decisions analyzed

op1==0	100%
false	-

true	4/4 T1

Transition "after(1,sec)[op1==25]" from "Start1" to Junction #8

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_Powerwindow_and_Door_operation/rear_left_Subsystem/Chart.Normal2

Metric	**Coverage**
Cyclomatic Complexity	2
Condition	100% ((1+3)/4) condition outcomes
Decision	100% ((1+1)/2) decision outcomes
MCDC	100% ((0+2)/2) conditions reversed the outcome

```
1  after(1,sec)[op1==25]
```

#1: after(1,sec)[op1==25]

Decisions analyzed

after(1,sec)[op1==25]	100%
false	4/4 T1
true	-

Conditions analyzed

Description	**True**	**False**
after(1,sec)	-	4 T1
op1==25	-	-

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	**True Out**	**False Out**
after(1,sec)[op1==25]		
after(1,sec)	-	
op1==25	-	

46. SubSystem block "BCM_WC_CONTROL"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE

Child Systems: BCM_WC_FR_Automodes, BCM_WC_FR_manual, BCM_WC_Rear_windshield

Metric	**Coverage (this object)**	**Coverage (inc. descendants)**
Cyclomatic Complexity	0	2
Condition	NA	68% (27/40) condition outcomes
Decision	NA	100% (4/4) decision outcomes
MCDC	NA	50% (4/8) conditions reversed the outcome
Execution	NA	100% (20/20) objective outcomes

Full Coverage

Model Object	**Metric**
Logic block "BCM_WC_FW_Statuscheck"	Condition, MCDC, Execution

Switch block "BCM_WC_FW_ENABLE"	Decision, Execution
Constant block "Constant"	Execution

47. SubSystem block "BCM_WC_FR_Automodes"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL

Child Systems: BCM_FR_AUTOMODE0, BCM_FR_AUTOMODE1, BCM_FR_AUTOMODE2, BCM_FR_AUTOMODE3, BCM_FR_AUTOMODE4, BCM_FR_AUTOMODE5

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	67% (8/12) condition outcomes
Execution	NA	100% (6/6) objective outcomes

48. SubSystem block "BCM_FR_AUTOMODE0"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

49. SubSystem block "BCM_FR_AUTOMODE1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

50. SubSystem block "BCM_FR_AUTOMODE2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes/BCM_FR_AUTOMODE2

Uncovered Links:

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input1 == input2	0	1796 T1

51. SubSystem block "BCM_FR_AUTOMODE3"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	**Coverage (this object)**	**Coverage (inc. descendants)**
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes/BCM_FR_AUTOMODE3

Uncovered Links:

Metric	**Coverage**
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	**True**	**False**
input1 == input2	0	1796 T1

52. SubSystem block "BCM_FR_AUTOMODE4"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	**Coverage (this object)**	**Coverage (inc. descendants)**
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes/BCM_FR_AUTOMODE4
Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	0	1796 T1

53. SubSystem block "BCM_FR_AUTOMODE5"

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_Automodes/BCM_FR_AUTOMODE5
Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 == input2	0	1796 T1

54. SubSystem block "BCM_WC_FR_manual"

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL
Child Systems: BCM_FRM_COND, BCM_FRM_COND1, BCM_FRM_COND2, BCM_FRM_COND3, LessThan30

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	1
Condition	NA	50% (9/18) condition outcomes
Decision	NA	100% (2/2) decision outcomes
MCDC	NA	0% (0/4) conditions reversed the outcome
Execution	NA	100% (9/9) objective outcomes

Logic block "Bw30_60"

Justify or Exclude
Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual
Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (2/4) condition outcomes
MCDC	0% (0/2) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input port 1	0	1796 T1
input port 2	1796 T1	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	(**T**T)	**F**T T1
input port 2	(T**T**)	(T**F**)

Logic block "Bw60_100"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (2/4) condition outcomes
MCDC	0% (0/2) conditions reversed the outcome
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input port 1	0	1796 T1
input port 2	1796 T1	0

MC/DC analysis (combinations in parentheses did not occur)

Decision/Condition	True Out	False Out
expression for output		
input port 1	(**T**T)	**F**T T1
input port 2	(T**T**)	(T**F**)

Full Coverage

Model Object	Metric
Switch block "BCM_FRM_Selection"	Decision, Execution
Constant block "Constant"	Execution

55. SubSystem block "BCM_FRM_COND"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual/BCM_FRM_COND

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 > input2	0	1796 T1

56. SubSystem block "BCM_FRM_COND1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual/BCM_FRM_COND1

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 <= input2	1796 T1	0

57. SubSystem block "BCM_FRM_COND2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual/BCM_FRM_COND2

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 > input2	0	1796 T1

58. SubSystem block "BCM_FRM_COND3"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual/BCM_FRM_COND3

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 <= input2	1796 T1	0

59. SubSystem block "LessThan30"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	50% (1/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

RelationalOperator block "Compare"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_FR_manual/LessThan30

Uncovered Links:

Metric	Coverage
Cyclomatic Complexity	0
Condition	50% (1/2) condition outcomes
Execution	100% (1/1) objective outcomes

Conditions analyzed

Description	True	False
input1 <= input2	1796 T1	0

60. SubSystem block "BCM_WC_Rear_windshield"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL

Child Systems: BCM_WC_COMP

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (6/6) condition outcomes
MCDC	NA	100% (2/2) conditions reversed the outcome
Execution	NA	100% (2/2) objective outcomes

Full Coverage

Model Object	Metric
Logic block "BCM_WC_Conditioncheck"	Condition, MCDC, Execution

61. SubSystem block "BCM_WC_COMP"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/BCM_WC_CONTROL/BCM_WC_Rear_windshield

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	0	0
Condition	NA	100% (2/2) condition outcomes
Execution	NA	100% (1/1) objective outcomes

Full Coverage

Model Object	Metric
RelationalOperator block "Compare"	Condition, Execution

62. SubSystem block "TP_Monitoring"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE

Child Systems: IndicateHigh1, IndicateHigh2, IndicateHigh3, IndicateHigh4, IndicateLow1, IndicateLow2, IndicateLow3, IndicateLow4

Metric	Coverage (this object)	Coverage (inc. descendants)

Cyclomatic Complexity	0	24
Decision	NA	100% (32/32) decision outcomes
Execution	NA	100% (32/32) objective outcomes

Full Coverage

Model Object	Metric
Switch block "Switch"	Decision, Execution
Switch block "Switch1"	Decision, Execution
Switch block "Switch2"	Decision, Execution
Switch block "Switch3"	Decision, Execution
Switch block "Switch4"	Decision, Execution
Switch block "Switch5"	Decision, Execution
Switch block "Switch6"	Decision, Execution
Switch block "Switch7"	Decision, Execution
Constant block "Disable5"	Execution
Constant block "Disable6"	Execution
Constant block "Disable7"	Execution
Constant block "Disable8"	Execution
Constant block "Enable5"	Execution
Constant block "Enable6"	Execution
Constant block "Enable7"	Execution
Constant block "Enable8"	Execution
Constant block "disable1"	Execution
Constant block "disable2"	Execution
Constant block "disable3"	Execution
Constant block "disable4"	Execution
Constant block "enable1"	Execution
Constant block "enable2"	Execution
Constant block "enable3"	Execution
Constant block "enable4"	Execution

63. SubSystem block "IndicateHigh1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	1248/1796 T1
true	548/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

64. SubSystem block "IndicateHigh2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	1252/1796 T1
true	544/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

65. SubSystem block "IndicateHigh3"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	1456/1796 T1
true	340/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

66. SubSystem block "IndicateHigh4"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes

Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	1444/1796 T1
true	352/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

67. SubSystem block "IndicateLow1"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	548/1796 T1
true	1248/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

68. SubSystem block "IndicateLow2"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	544/1796 T1
true	1252/1796 T1

Full Coverage

Model Object	Metric

Constant block "Constant"	Execution

69. SubSystem block "IndicateLow3"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	340/1796 T1
true	1456/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution

70. SubSystem block "IndicateLow4"

Justify or Exclude

Parent: Automotive_model2/BCM_SAFETY_MODULE/TP_Monitoring

Metric	Coverage (this object)	Coverage (inc. descendants)
Cyclomatic Complexity	2	2
Decision	100% (2/2) decision outcomes	100% (2/2) decision outcomes
Execution	NA	100% (1/1) objective outcomes

Decisions analyzed

enable logical value	100%
false	352/1796 T1
true	1444/1796 T1

Full Coverage

Model Object	Metric
Constant block "Constant"	Execution