



Document History

Ver. Rel. No.	Release Date	Prepared. By	Reviewed By	Approved By	Remarks/Revision Details
	23/02/2021	Srushti Wilson Rangari		Dr.Vivek Kaundal, Bhargav N	First Draft
	25/02/2021	Srushti Wilson Rangari		Dr.Vivek Kaundal, Bhargav N	Second Draft
	27/02/2021	Srushti Wilson Rangari		Dr.Vivek Kaundal, Bhargav N	Final Draft



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Activity 1 – COMPILATION APROACH

This is the complete compilation process of the sample program for ARM Cortex Mx processor based boards. Following are the compilation stages of a C program:

- 1. Preprocessor stage
- 2. Compilation stage
- 3. Assembly stage
- 4. Linking stage

1.1- MAKE FILE

Below is the make file for the sample program:

Fig 1.1.1 make file



The command to run this make file in the command prompt is:

```
C\\Windows\System32\cmd.ee
:\Users\Training\Desktop\Activity>make -f Makefile.mak
rm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -Wal1 -09 -o main.o main.c
:\Users\Training\Desktop\Activity>arm-none-eabi-gcc -nostdlib -T stm32_ls.ld *.o -o final.elf
:\Users\Training\Desktop\Activity>
```

Fig 1.1.2 Make command

- -mcpu=cortex-m4 is used to select our cortex-m4 processor which is used
- -mthumb is used to generate the code that executes in ARM state
- main.o is the target file
- main.c is the dependency

1.2- STARTUP CODE

- The startup file is responsible for setting up the right environments to run the code in main.c file.
- Some part of the startup code is target (processor) dependent.
- Role of startup file:
 - 1. Create a MCU specific vector table for microcontroller.
 - 2. To write a startup code which initializes .data and .bss section in SRAM.
 - 3. Call main()



```
#define STACK_START SRAM_END
          extern uint32_t _etext;
extern uint32_t _sdata;
extern uint32_t _edata;
extern uint32_t _la_data;
          //prototype of main
          int main(void);
        void __libc_init_array(void);
/* function prototypes of STM32F407x system exception an void Reset_Handler (void);

void MR. Handler (void) attribute void Martinate Handler (void) attribute void Martinate Handler (void) attribute void Martinate Handler (void) attribute void Barfalt Handler (void) attribute void Stw. Handler (void) attribute void FVD. HROHandler (void) attribute void FVD. HROHandler (void) attribute void FVD. HROHandler (void) attribute void EXTIO. HROHandler (void) attribute void EALI Streams. HROHandler (void) attribute (void CANI, RNO IROHandler (void) attribute (void) EALI Streams. HROHandler, (void) attribute (void CANI, RNO IROHandler (void) attribute (void) EALI Streams. HROHandler, (void) a
        /* function prototypes of STM32F407x system exception and IRQ handlers */
                                                                                                                                                                                                                                    (void) attribute ((voek, alias("Default_Handler")));
  | while(1);
                                    //copy .data section to SRAM
uint32_t size = (uint32_t)&_edata - (uint32_t)&_sdata;
                       uint8 t *pDst = (uint8 t*) & sdata; //sram
uint8 t *pSrc = (uint8 t*) & la_data; //flash
                           for(uint32_t i =0 ; i < size ; i++)
{</pre>
                                      *pDst++ = *pSrc++;
                                    //Init. the .bsg section to zero in SRAM
size = (uint32_t)&_ebss - (uint32_t)&_sbss;
pDst = (uint8_t*)&_sbss;
for(uint32_t i =0 ; i < size ; i++)
{</pre>
                                    *pDst++ = 0;
                                    //__libc_init_array();
                                      main();
```

Fig 1.2.1 Startup code



In startup code we use variable attributes to store some variables in the user defined function. Function attributes:

- Weak: Lets programmer override already defined weak function (dummy function) with the same function name.
- Alias: Lets programmer give any alias name for same function.

The startup.o file generated is of elf executable format, various sections of which are shown below:

```
C:\Windows\System32\cmd.exe
                                                                                                              X
:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>make -f Makefile.mak
rm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -Wall -00 -o stm32_startup.o stm32_startup.c
:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>arm-none-eabi-objdump.exe -h stm32_startup.o
tm32_startup.o:
                  file format elf32-littlearm
Sections:
                         VMA
                                   LMA
                                            File off
dx Name
                                                     Algn
 0 .text
                00000090 00000000
                                  00000000
                                            00000034
                                                     2**2
                CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
                                            000000c4 2**0
 1 .data
                00000000 00000000 00000000
                2 .bss
                00000188 00000000 00000000 000000c4
 3 .isr_vector
                CONTENTS, ALLOC, LOAD, RELOC, DATA
                0000004e 00000000 00000000 0000024c
 4 .comment
                CONTENTS, READONLY
 5 .ARM.attributes 0000002e 00000000 00000000 0000029a 2**0
                CONTENTS, READONLY
:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>
```

Fig 1.2.2: Startup command



1.3- LINKER SCRIPT

- Linkers take one or more object files or libraries as input and combines them to create a single executable file as output.
- Linker scripts decide how different sections of object file should be merged to create an output file.
- Reset handler is the entry point to the application
- Entry command is used to set the "Entry point address" information in the header of final elf file generated.

Syntax: Entry(symbol_name) Entry(Reset_Handler)



Fig 1.3.1 command to generate final.elf file

1.4- DEBUGGING TECHNIQUES

- The STM32F407VG is embedded with on chip debugger for debugging the code.
- The OCD ON-Chip Debugger aims to provide debugging, in system programming and boundary scan testing for embedded target devices.
- OCD is a free and opensource host application allows you to program, debug, and analyze your applications using GDB.
- It supports various target boards based on different processor architecture.



Activity 2 – IMPLEMENTATION OF PROTOCOLS USING STM IDE

Implementation of protocols for STM32F407VG microcontroller featuring ARM32 bit ARM-cortex - M4 with FPU core using HAL library.

2.1 GPIO:

Toggling LED at pin PD12 at GREEN_LED_GPIO_PORT. Serial wire is enabled at pin PA13.

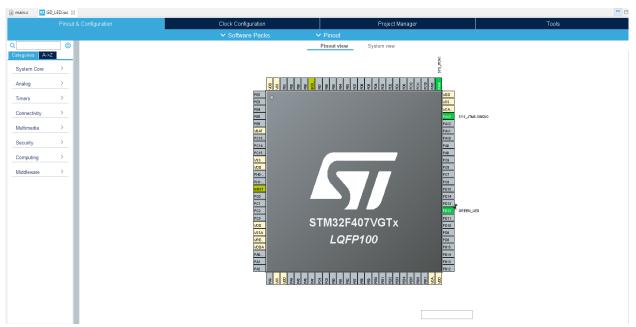


Fig: 2.1.1 GPIO pin configuration

```
/* Initialize all configured peripherals */
MX_GPIO_Init();
/* USER CODE BEGIN 2 */

/* USER CODE END 2 */

/* USER CODE BEGIN WHILE */
while (1)
{
    HAL_GPIO_TogglePin(GREEN_LED_GPIO_Port, GREEN_LED_Pin);
    HAL_Delay(500);
    /* USER CODE END WHILE */

    /* USER CODE END WHILE */
}

/* USER CODE END 3 */
}
```

Fig: 2.1.2 GPIO configuration code

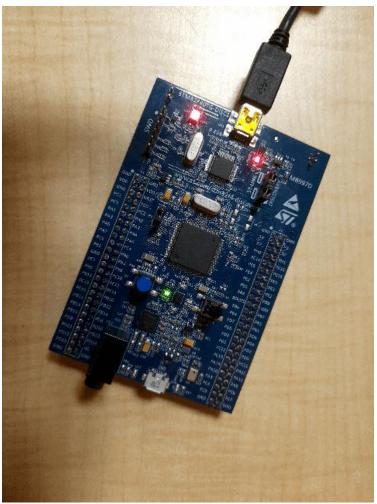


Fig: 2.1.2 LED toggling



2.2 EXTI:

Blue button at PAO works as an external interrupt. When the blue button is pressed the Green LED at pin PD12 toggles.



Fig: 2.2.1 EXTI pin configuration

In the main.c file a flag is initialized and if the flag == 1, the condition under the if loop executed to toggle the LED at PD12.



Fig: 2.2.1 EXTI configuration code

2.3 ADC

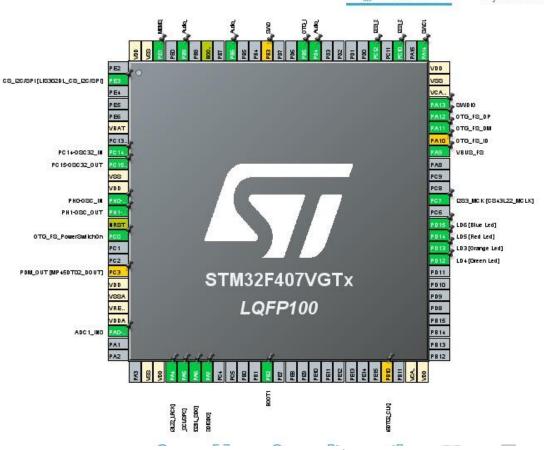


Fig: 2.3.1 ADC pin configuration



```
€ *main.c 🖾
  19⊖/* USER CODE END Header */
20 /* Includes ------
21 #include "main.h"
22 #include "usb_host.h"
 24
25 ADC_HandleTypeDef hadc1;
  26
27 I2C_HandleTypeDef hi2c1;
  29 I2S_HandleTypeDef hi2s3;
30
   31 SPI_HandleTypeDef hspi1;
  5 void SystemClock Config(void);
56 static void MX_GPIO_Init(void);
57 static void MX_IZC1_Init(void);
58 static void MX_IZS3_Init(void);
59 static void MX_SPII_Init(void);
  40 static void MX_ADC1_Init(void);
41 void MX_USB_HOST_Process(void);
 42
 uint32_t adc_value;
  46⊖ int main(void)
        {
  49
50
           HAL_Init();
  51
52
           SystemClock_Config();
           MX_GPIO_Init();
   54
           MX_I2C1_Init();
MX_I2S3_Init();
  55
€ *main.c 🛭
            MX_GPIO_Init();
            MX_I2C1_Init();
MX_I2S3_Init();
    55
            MX_SPI1_Init();
           MX_USB_HOST_Init();
MX_ADC1_Init();
   60
            while (1)
    61
   62
              MX_USB_HOST_Process();
    63
64
65
66
               HAL_ADC_Start(&hadc1);
    67
68
    69
               if(HAL_ADC_PollForConversion(&hadc1, 5)== HAL_OK){
   70
71
72
73
74
75
76
77
78
79
                      adc_value= HAL_ADC_GetValue(&hadc1);
               }
               HAL_ADC_Stop(&hadc1);
HAL_Delay(100);
            }
    81
82
          void SystemClock_Config(void)
   85
        {
            RCC_OscInitTypeDef RCC_OscInitStruct = {0};
RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
RCC_PeriphCLKInitTypeDef PeriphClkInitStruct = {0};
   86
87
```

Fig: 2.3.2 ADC configuration code



2.4 SPI

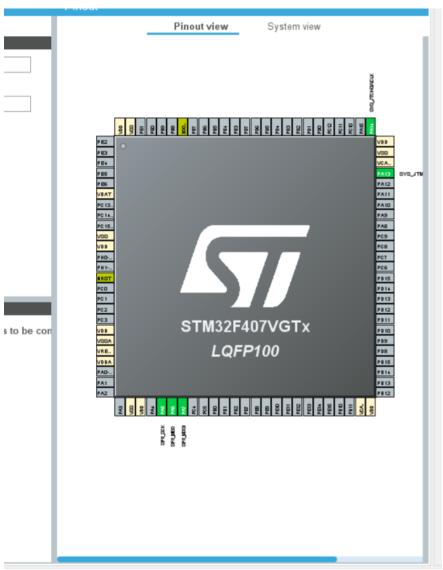


Fig: 2.4.1 SPI Pin configuration



```
🖺 Project Explorer 🛭 🕒 😘 🍸 🖇 🗀 📔 main.c 🗵
                                                                                                         /**

* @file : main.c

* @brief : Main program body

* @attention
      Project explorer ()

IDE ADC program

IDE ExtHal

IDE LedHal

IDE PWM

IDE PWM1

IDE SPI
                                                                                                 1 /* USER CODE BEGIN Header */ 2^{\ominus} /**
                                                                                                                                                                                                                                                                                                                                                                                                CDT Build Console [SPI]
                                                                                                          * <h2><center>&copy; Copyright (c) 2021 STMicroelectronics.
* All rights reserved.</center></h2>
                                                                                                         * All rights reserved.</renters/nz/

* 
* 
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* the "License"; You may not use this file except in compliance with the 
* License. You may obtain a copy of the License at: 
* 
opensource.org/licenses/BSD-3-Clause
                                                                                              25 26 /* USER CODE END Includes */
                                                                                              37<sup>®</sup>/* Private macro -----
38 /* USER CODE BEGIN PM */
                                                                                                39
40 /* USER CODE END PM */
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         □ ¾ i → 1 □
   🔐 Build Analyzer 🚊 Static Stack Analyzer 🎋 Debug 🛭
                                                                                                        J 🛱 🔚 🔓 📵 🔟
  as man.c ≈ 43 SPI_HandleTypeDef hspi1;
44
45 /* USER CODE BEGIN PV */
46 uint8,t bufffer_tx[10] = {1,2,3,4,5,6,7,8,9,10}; // Transfer Buffer
47 uint8.t buffer_rx[10]; // Receiver Buffer
48 /* USER CODE END PV */
49
49
49
                                                                                                                                                                                                                                                                                                                                                                                   CDT Build Console [SPI]
                                                                                                      void SystemClock_Config(void);
static void MX_GPIO_Init(void);
static void MX_SPII_Init(void);
/* USER CODE BEGIN PFP */
                                                                                              34 /* USER CODE END PFP */
55 /* USER CODE END PFP */
57 /* S8% Private user code --
59 /* USER CODE BEGIN 0 */
60 /* USER CODE END 0 */
62 /* C82 /*
                                                                                                        * @brief The application entry point.
* @retval int
*/
             STM32F407VGTX_RAM.lc
                                                                                                       {
    /* USER CODE BEGIN 1 */
                                                                                                          /* USER CODE END 1 */
                                                                                                          /* MCU Configuration---
                                                                                                          /* Reset of all peripherals, Initializes the Flash interface and the \underline{\sf Systick}. */ \mathtt{HAL\_Init()};
                                                                                                          /* USER CODE BEGIN Init */
                                                                                                          /* USER CODE END Init */
                                                                                                            /* Configure the syste
SystemClock_Config();
   📓 Build Analyzer 🚊 Static Stack Analyzer 🎄 Debug 🛭
```



```
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```

Fig: 2.4.2 SPI configuration code



2.5 UART

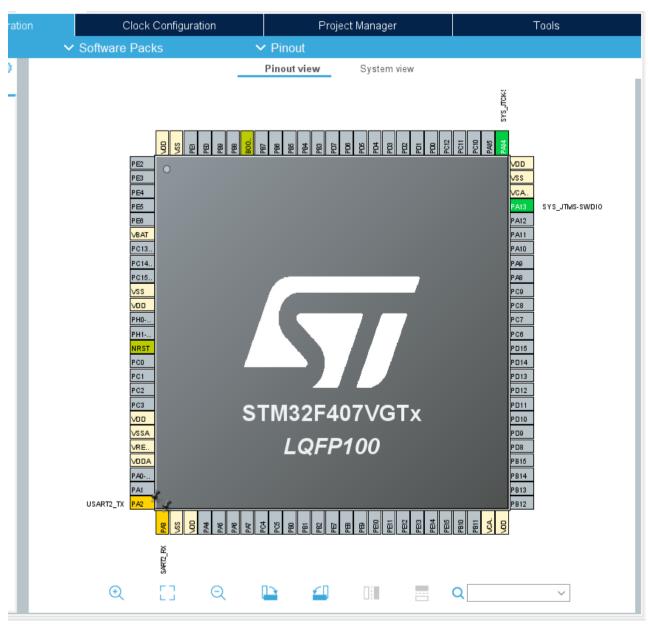


Fig: 2.5.1 UART Pin configuration



```
Q : pr | Tr to
  Project Explorer ⋈ 😑 🤩 🎖 🖁 🗆 🗈 📝 🔞 main.c ⋈
                                                                                                                                                                                                                                                                                                                                                                           🗆 🚼 Outline 🔞 Build Targets 🚼 Problems 🔊 Tasks 😩 Console 🛭 🥅 Properties
                                                                                                                                                                                                                                                                                                                                                                                                                                                                      100 /* USER CODE END 3 */
101 }
102
                                                                                                                                                                                                                                                                                                                                                                                  CDT Build Console [SPI]
                                                                                        * @brief System Clock Configuration
105 * @retval None
                                                                                                    void SystemClock_Config(void)
                                                                                         111 1120 /** Configure the main internal regulator output voltage 113 */
                                                                                                      */
HAL RCC_PWR_CLK_ENABLE();
HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
/** Initializes the RCC_oscillators according to the specified parameters
* in the RCC_oscInitypeDef structure.
                                                                                                        */
RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
RCC_OscInitStruct.HSIState = RCC_HSI_ON;
RCC_OscInitStruct.HSICalibrationValue = RCC_HSICALIBRATION_DEFAULT;
RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
                                                                                                        if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
                                                                                                       {
Error_Handler();
                                                                                                       } /** Initializes the CPU, AHB and APB buses clocks
                                                                                                      */
RCC_CLKInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
RCC_CLKINitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_HS1;
RCC_CLKInitStruct.AMBCLKDivider = RCC_SYSCLK_DIV1;
RCC_CLKInitStruct.AMBCLKDivider = RCC_SYSCLK_DIV1;
RCC_CLKINItStruct.AMBCLKDivider = RCC_HCLK_DIV1;
RCC_CLKINITSTRUCT.AMBCLKDivider = RCC_HCLK_DIV1;
                                                                                                        if (HAL RCC ClockConfig(&RCC ClkInitStruct, FLASH LATENCY 0) != HAL OK)
  🔐 Build Analyzer 🚊 Static Stack Analyzer 💠 Debug 🛭
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        □ 🔆 | i→ 🚦 🗆 🗆
                                                                                                  😃 🛱 📴 🔓 🚾 💽 🔤
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Build Targets  Problems  Tasks  □ Console  □ Properties □ □

Outline  

Build Targets  Tasks  □ Console  □ □ Properties □ □

Outline  

Build Targets  Tasks  □ Tasks  □ Console  □ Properties □ □ □

Outline  Ou
                                                                                          49 void SystemClock_Config(void);
50 static void MX_GPIO_Init(void);
51 /* USER CODE BEGIN PFP */
52
53 /* USER CODE END PFP */
54
                                                                                                                                                                                                                                                                                                                                                                                   CDT Build Console [SPI]
                                                                                          * @brief The application entry point.
* @retval int
                                                                                                   {
   /* USER CODE BEGIN 1 */
                                                                                           68 /* USER CODE END 1 */
                                                                                                    /* MCU Configuration-----
                                                                                                    /* Reset of all peripherals, Initializes the Flash interface and the \underline{\sf Systick.} */ \underline{\sf HAL\_Init()};
                                                                                                      /* USER CODE BEGIN Init */
                                                                                                     /* USER CODE END Init */
                                                                                                    /* Configure the system clock */
SystemClock_Config();
                                                                                                    /* USER CODE BEGIN SysInit */
                                                                                                        /* Initialize all configured peripherals */
                                                                                                      MX_GPIO_Init();

/* USER CODE BEGIN 2 */
                                                                                                      /* USER CODE END 2 */
  📓 Build Analyzer 🛓 Static Stack Analyzer 🎋 Debug 🖂
                                                                                                   Q 🛱 🔚 🔒 🙀
```



Fig: 2.5.2 UART configuration code

Activity 3 – PROJECT ON BCM MODULE

BCM module was implemented using STM32f407VG microcontroller featuring 32 bit ARM-cortex - M4 with FPU core.

This BCM module have following features:

- 1. Alarm system
- 2. Seat control
- 3. Power mirror
- 4. Automatic wiper system

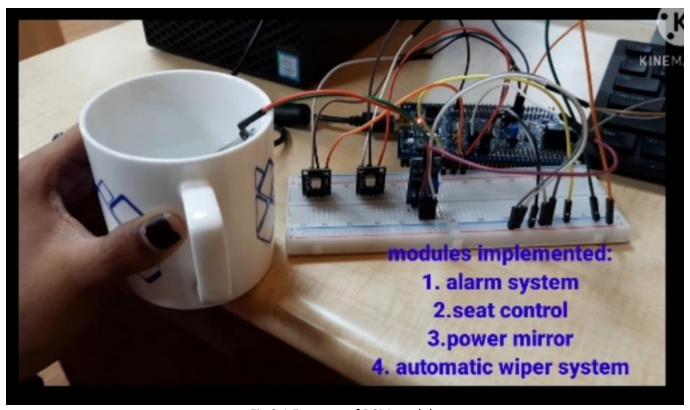


Fig 3.1 Features of BCM module



Below is the pinout and configuration of STM microcontroller used:

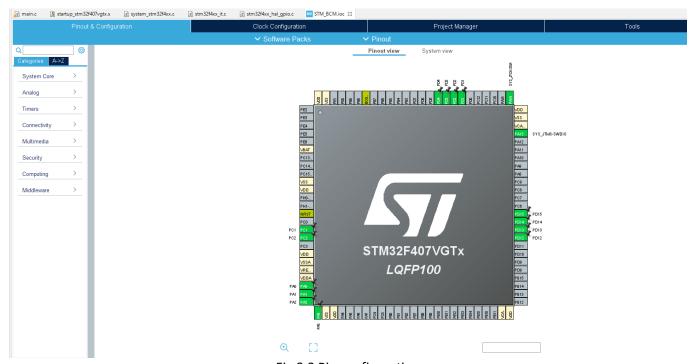


Fig 3.2 Pin configuration

1. Alarm system:

PA1: Denotes power status of car (0: OFF, 1: ON)

PA2: Denotes the door status (0: OFF, 1: ON)

PA3: Denotes the door status (0: door closed, 1: door open)

Alarm system uses two variables PA! and PA2 and Dashboard light as output.

Door safety system checks whether door is locked or unlocked.



```
if(HAL_GPIO_ReadPin(PA1_GPIO_Port, PA1_Pin) == 1)
{

if(HAL_GPIO_ReadPin(PA2_GPIO_Port, PA2_Pin)==0 && (HAL_GPIO_ReadPin(PA3_GPIO_Port, PA3_Pin)==0)){

HAL_GPIO_TogglePin(PD1_GPIO_Port, PD1_Pin);

HAL_Delay(900);

}else if((HAL_GPIO_ReadPin(PA2_GPIO_Port, PA2_Pin)==1) && (HAL_GPIO_ReadPin(PA3_GPIO_Port, PA3_Pin)==0)){

HAL_GPIO_TogglePin(PD2_GPIO_Port, PD2_Pin);

HAL_GPIO_TogglePin(PD1_GPIO_Port, PD1_Pin);

HAL_Delay(100);

}else if(HAL_GPIO_ReadPin(PA2_GPIO_Port, PA2_Pin)==1 && HAL_GPIO_ReadPin(PA3_GPIO_Port, PA3_Pin)==1) {

HAL_GPIO_TogglePin(PD2_GPIO_Port, PD2_Pin);

HAL_Delay(100);

}else{

//pass comment
}
```

Fig 3.3 Alarm system-code

Implementation:

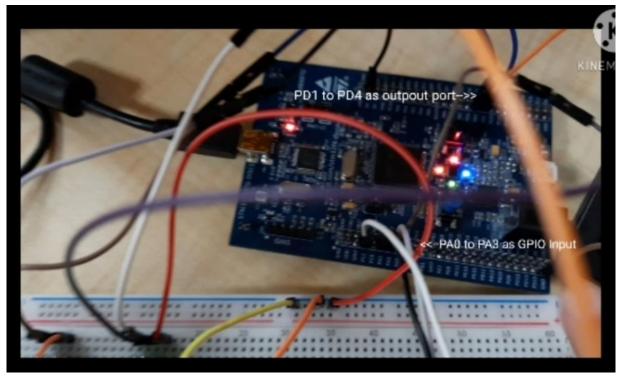


Fig 3.4 Alarm system - Implementation



2. Seat control:

Adjustment of seat with the help of buttons in two direction

Forward direction: when the input is 1 Reverse direction: when the input is 0

When the input is 1 LED glows in clockwise direction When the input is 0 LED glows in anti-clockwise direction

```
if(HAL_GPIO_ReadPin(PA0_GPIO_Port, PA0_Pin)==1){

    HAL_GPIO_TogglePin(PD12_GPIO_Port, PD12_Pin);
    HAL_Delay(100);
    HAL_GPIO_TogglePin(PD13_GPIO_Port, PD13_Pin);
    HAL_Delay(100);
    HAL_GPIO_TogglePin(PD14_GPIO_Port, PD14_Pin);
    HAL_Delay(100);
    HAL_GPIO_TogglePin(PD15_GPIO_Port, PD15_Pin);
    HAL_Delay(100);
}
}else{

    //pass comment
}
```

Fig 3.5 Seat control system-code

Implementation:





Fig 3.6 Seat control system- Implementation

3. Power mirror:

Two of the LEDs glow as a sign of mirrors unfolding when provided with power supply PD12 and PD13 are the output pins PA0 and PA3 are the input pins.

Fig 4.6 Power mirror-code

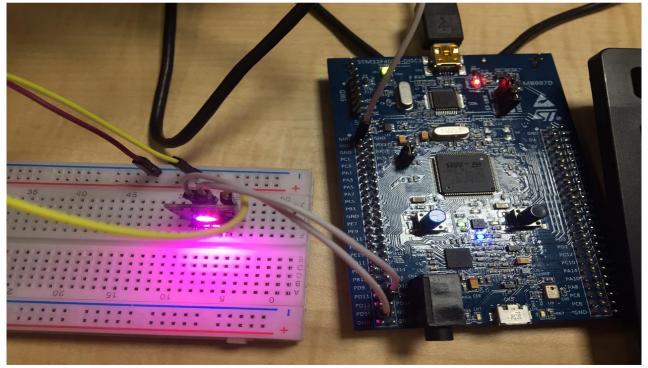


Fig 3.7 Power mirror- Implementation



4. Automatic wiper system:

Whenever the humidity sensor senses the presence of moisture over the windshield it sends the signal to turn ON the wipers.

The input was taken from humidity sensor at PC1
The output was given to the external led at pin PD3

Fig 3.8 Automatic wiper system-code

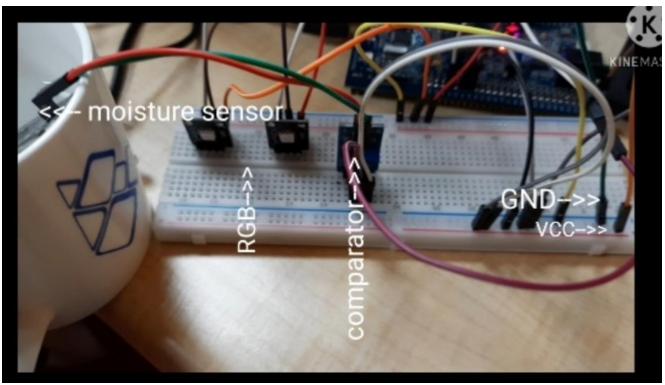


Fig 3.9 Automatic wiper system- Implementation



Final integrated project:

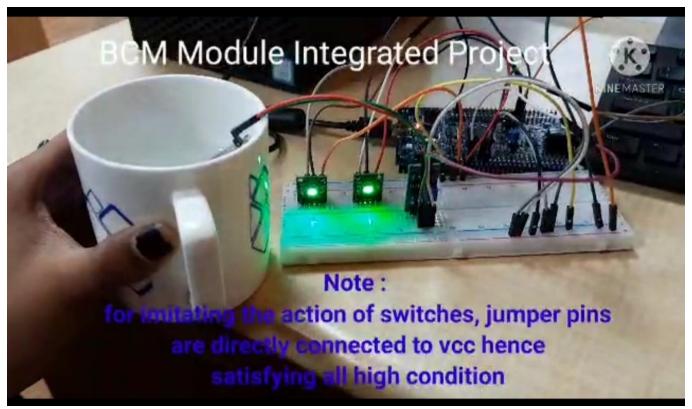


Fig 3.10: Final Integrated BCM