



Learning Report – Embedded C



L&T Technology Services



Document History

| Ver. Rel. No. | Release Date | Prepared. By | Reviewed By | Approved By | Remarks/Revision Details |
|---------------|--------------|--------------|-------------|-------------|--------------------------|
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Contents

| | |
|---|-------------------------|
| ACTIVITY 1 – COMPILATION APPROACH..... | 4 |
| 1.1- MAKE FILE | 4ERROR! BOOKMARK |
| NOT DEFINED. | |
| 1.2- STARTUP CODE..... | 5 |
| 1.3- LINKER FILE..... | 8 |
| 1.4- DEBUGGING TECHNIQUES..... | 9 |
| ACTIVITY 2 – IMPLEMENTATION OF PROTOCOLS USING STM IDE | 10 |
| 2.1 GPIO..... | 10 |
| 2.2 EXTI..... | 12 |
| 2.3 ADC..... | 14 |
| 2.4 SPI..... | 16 |
| 2.5 UART..... | 19 |
| ACTIVITY 3 – PROJECT ON BCM MODULE | 21 |

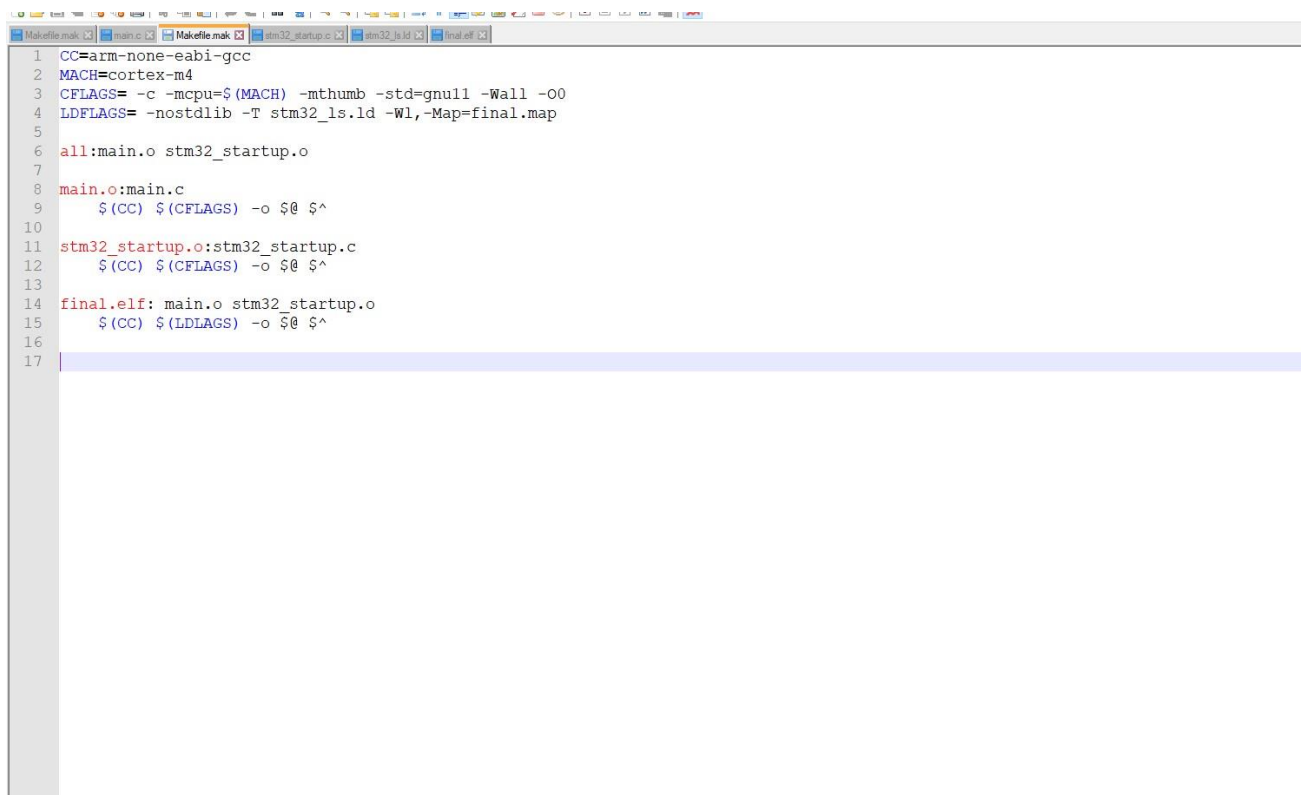
Activity 1 – COMPILATION APPROACH

This is the complete compilation process of the sample program for ARM Cortex Mx processor based boards. Following are the compilation stages of a C program:

1. Preprocessor stage
2. Compilation stage
3. Assembly stage
4. Linking stage

1.1- MAKE FILE

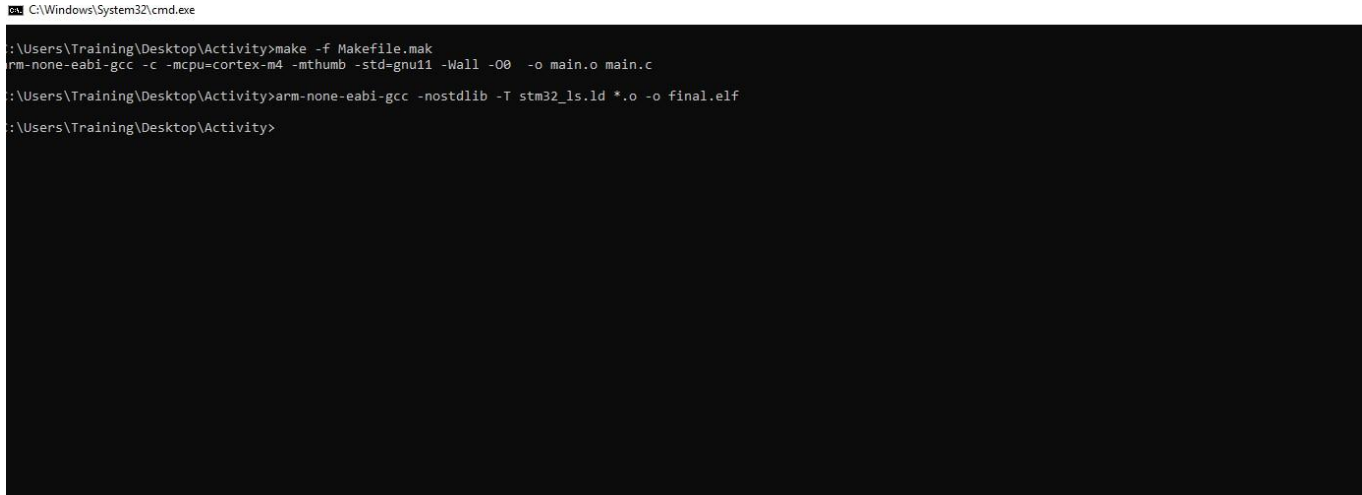
Below is the make file for the sample program:



```
1 CC=arm-none-eabi-gcc
2 MACH=cortex-m4
3 CFLAGS= -c -mcpu=$(MACH) -mthumb -std=gnu11 -Wall -O0
4 LDFLAGS= -nostdlib -T stm32_ls.ld -Wl,-Map=final.map
5
6 all:main.o stm32_startup.o
7
8 main.o:main.c
9     $(CC) $(CFLAGS) -o $@ $^
10
11 stm32_startup.o:stm32_startup.c
12     $(CC) $(CFLAGS) -o $@ $^
13
14 final.elf: main.o stm32_startup.o
15     $(CC) $(LDFLAGS) -o $@ $^
16
17
```

Fig 1.1.1 make file

The command to run this make file in the command prompt is:



```
C:\Windows\System32\cmd.exe
C:\Users\Training\Desktop\Activity>make -f Makefile.mak
arm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -Wall -O0 -o main.o main.c
C:\Users\Training\Desktop\Activity>arm-none-eabi-gcc -nostdlib -T stm32_ls.ld *.o -o final.elf
C:\Users\Training\Desktop\Activity>
```

Fig 1.1.2 Make command

- **-mcpu=cortex-m4** is used to select our cortex-m4 processor which is used
- **-mthumb** is used to generate the code that executes in ARM state
- **main.o** is the target file
- **main.c** is the dependency

1.2- STARTUP CODE

- The startup file is responsible for setting up the right environments to run the code in main.c file.
- Some part of the startup code is target (processor) dependent.
- Role of startup file:
 1. Create a MCU specific vector table for microcontroller.
 2. To write a startup code which initializes .data and .bss section in SRAM.
 3. Call main()

```

1 #include<stdint.h>
2
3 #define SRAM_START 0x20000000U
4 #define SRAM_SIZE (128U * 1024U) //128KB
5 #define SRAM_END ((SRAM_START) + (SRAM_SIZE))
6
7 #define STACK_START SRAM_END
8
9 extern uint32_t _etext;
10 extern uint32_t _edata;
11 extern uint32_t _la_data;
12 extern uint32_t _la_data;
13
14 extern uint32_t _ebss;
15 extern uint32_t _ebss;
16
17 //prototype of main
18
19 int main(void);
20
21 void __libc_init_array(void);
22
23
24 /* function prototypes of STM32F407x system exception and IRQ handlers */
25 void Reset_Handler(void);
26
27 void NMI_Handler (void) __attribute__((weak, alias("Default_Handler")));
28 void HardFault_Handler (void) __attribute__((weak, alias("Default_Handler")));
29 void MemManage_Handler (void) __attribute__((weak, alias("Default_Handler")));
30 void BusFault_Handler (void) __attribute__((weak, alias("Default_Handler")));
31 void UsageFault_Handler (void) __attribute__((weak, alias("Default_Handler")));
32 void SVC_Handler (void) __attribute__((weak, alias("Default_Handler")));
33 void DebugMon_Handler (void) __attribute__((weak, alias("Default_Handler")));
34 void PendSV_Handler (void) __attribute__((weak, alias("Default_Handler")));
35 void SysTick_Handler (void) __attribute__((weak, alias("Default_Handler")));
36 void WWDG_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
37 void PVD_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
38 void TAMP_STAMP_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
39 void RTC_WKUP_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
40 void RCC_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
41 void EXTI0_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
42 void EXTI1_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
43 void EXTI2_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
44 void EXTI3_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
45 void EXTI4_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
46 void DMA1_Stream0_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
47 void DMA1_Stream1_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
48 void DMA1_Stream2_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
49 void DMA1_Stream3_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
50 void DMA1_Stream4_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
51 void DMA1_Stream5_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
52 void DMA1_Stream6_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
53 void DMA1_Stream7_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
54 void ADC_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
55 void CAN1_TX_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
56 void CAN1_RX0_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
57 void CAN1_RX1_IRQHandler (void) __attribute__((weak, alias("Default_Handler")));
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```

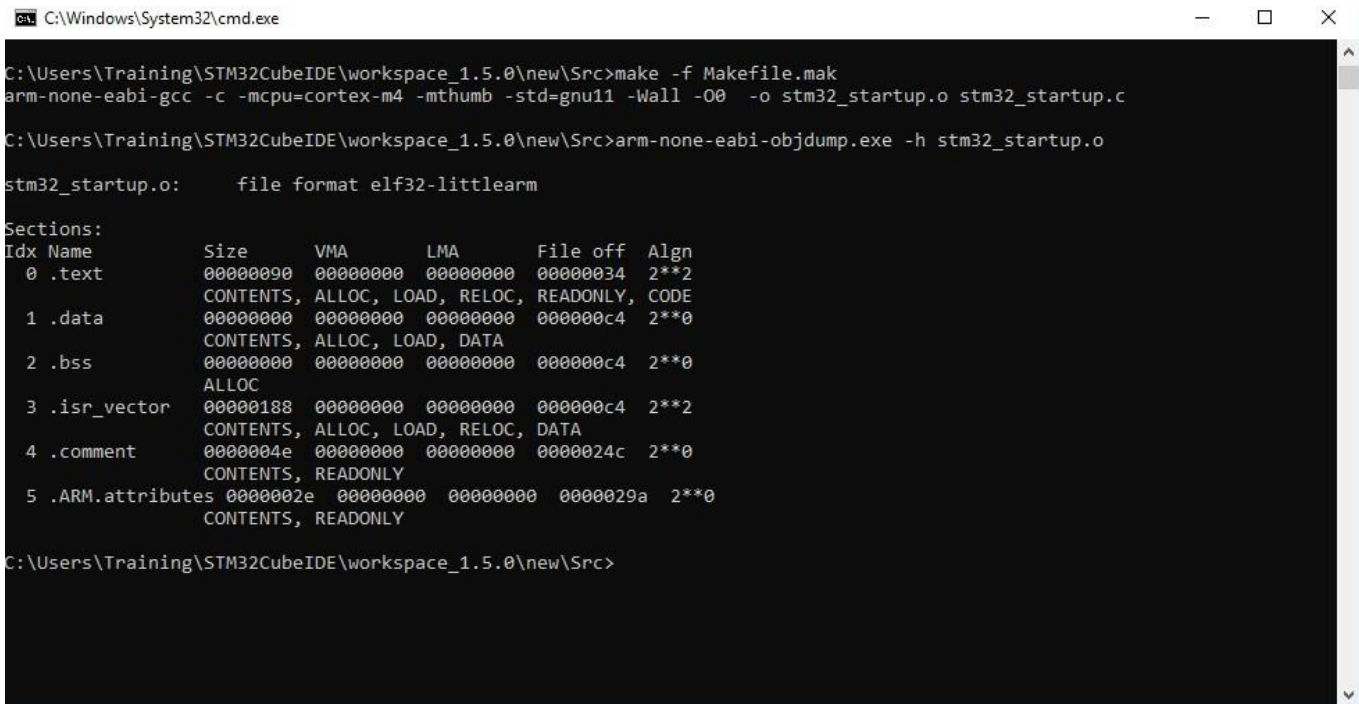
Fig 1.2.1 Startup code

In startup code we use variable attributes to store some variables in the user defined function.

Function attributes:

- Weak: Lets programmer override already defined weak function (dummy function) with the same function name.
- Alias: Lets programmer give any alias name for same function.

The startup.o file generated is of elf executable format, various sections of which are shown below:



```

C:\Windows\System32\cmd.exe

C:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>make -f Makefile.mak
arm-none-eabi-gcc -c -mcpu=cortex-m4 -mthumb -std=gnu11 -Wall -O0 -o stm32_startup.o stm32_startup.c

C:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>arm-none-eabi-objdump.exe -h stm32_startup.o

stm32_startup.o:      file format elf32-littlearm

Sections:
Idx Name              Size      VMA       LMA       File off  Algn
 0 .text              00000090  00000000  00000000  00000034  2**2
    CONTENTS, ALLOC, LOAD, RELOC, READONLY, CODE
 1 .data              00000000  00000000  00000000  000000c4  2**0
    CONTENTS, ALLOC, LOAD, DATA
 2 .bss              00000000  00000000  00000000  000000c4  2**0
    ALLOC
 3 .isr_vector        00000188  00000000  00000000  000000c4  2**2
    CONTENTS, ALLOC, LOAD, RELOC, DATA
 4 .comment            0000004e  00000000  00000000  0000024c  2**0
    CONTENTS, READONLY
 5 .ARM.attributes    0000002e  00000000  00000000  0000029a  2**0
    CONTENTS, READONLY

C:\Users\Training\STM32CubeIDE\workspace_1.5.0\new\Src>

```

Fig 1.2.2: Startup command

1.3- LINKER SCRIPT

- Linkers take one or more object files or libraries as input and combines them to create a single executable file as output.
- Linker scripts decide how different sections of object file should be merged to create an output file.
- Reset handler is the entry point to the application
- Entry command is used to set the “Entry point address” information in the header of final elf file generated.

Syntax: Entry(symbol_name)

Entry(Reset_Handler)

```
1  {
2  FLASH(ux):ORIGIN =0x08000000,LENGTH =1024K
3  SRAM(ux):ORIGIN =0x20000000,LENGTH =128K
4  }
5
6  SECTIONS
7  {
8  .text :
9  {
10  *(.isr_vector)
11  *(.text)
12  *(.text.*)
13  *(.init)
14  *(.fini)
15  *(.rodata)
16  *(.rodata.*)
17  . = ALIGN(4);
18  _etext = .;
19  }> FLASH
20
21  _la_data = LOADADDR(.data);
22
23  .data :
24  {
25  _edata = .;
26  *(.data)
27  *(.data.*)
28  . = ALIGN(4);
29  _edata = .;
30  }> SRAM AT> FLASH
31
32  .bss :
33  {
34  _sbss = .;
35  __bss_start__ = _sbss;
36  *(.bss)
37  *(.bss.*)
38  *(COMMON)
39  . = ALIGN(4);
40  _sbss = .;
41  __bss_end__ = _sbss;
42  . = ALIGN(4);
43  end = .;
44  _end = .;
45  }> SRAM
46
47 }
```



```
C:\Windows\System32\cmd.exe
C:\Users\Training\Documents\Embedded C\STM project\Activity1>arm-none-eabi-gcc -nostdlib -T stm32_ls.ld *.o -o final.elf
c:/program files (x86)/gnu arm embedded toolchain/10 2020-q4-major/bin/../lib/gcc/arm-none-eabi/10.2.1/../../../../arm-none-eabi/bin/ld.exe: stm3
2_startup.o: in function 'Reset_Handler':
stm32_startup.c:(.text+0x80): undefined reference to `_la_data'
collect2.exe: error: ld returned 1 exit status

C:\Users\Training\Documents\Embedded C\STM project\Activity1>arm-none-eabi-gcc -nostdlib -T stm32_ls.ld *.o -o final.elf

C:\Users\Training\Documents\Embedded C\STM project\Activity1>dir
Volume in drive C has no label.
Volume Serial Number is F8FC-05CA

Directory of C:\Users\Training\Documents\Embedded C\STM project\Activity1

23-02-2021  14:29    <DIR>          .
23-02-2021  14:29    <DIR>          ..
23-02-2021  14:29                135,752 final.elf
23-02-2021  14:24                2,034 main.c
23-02-2021  14:24                1,940 main.o
23-02-2021  11:35                 220 Makefile.mak
23-02-2021  14:28                 662 stm32_ls.ld
23-02-2021  14:27            12,847 stm32_startup.c
23-02-2021  14:24                 5,688 stm32_startup.o
                7 File(s)              159,143 bytes
                2 Dir(s) 125,771,227,136 bytes free

C:\Users\Training\Documents\Embedded C\STM project\Activity1>
```

Fig 1.3.1 command to generate final.elf file

1.4- DEBUGGING TECHNIQUES

- The STM32F407VG is embedded with on chip debugger for debugging the code.
- The OCD ON-Chip Debugger aims to provide debugging, in system programming and boundary scan testing for embedded target devices.
- OCD is a free and opensource host application allows you to program, debug, and analyze your applications using GDB.
- It supports various target boards based on different processor architecture.

Activity 2 – IMPLEMENTATION OF PROTOCOLS USING STM IDE

Implementation of protocols for STM32F407VG microcontroller featuring ARM32 bit ARM-cortex M4 with FPU core using HAL library.

2.1 GPIO:

Toggling LED at pin PD12 at GREEN_LED_GPIO_PORT. Serial wire is enabled at pin PA13.

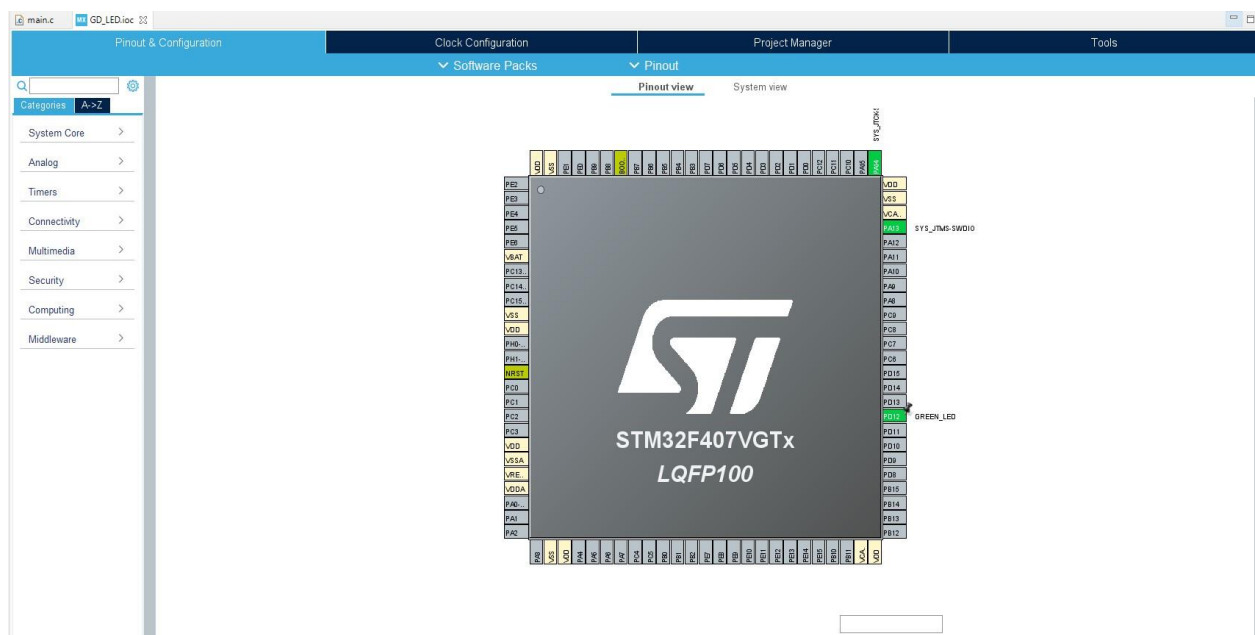


Fig: 2.1.1 GPIO pin configuration

```
/* Initialize all configured peripherals */
MX_GPIO_Init();
/* USER CODE BEGIN 2 */

/* USER CODE END 2 */

/* Infinite loop */
/* USER CODE BEGIN WHILE */
while (1)
{
    HAL_GPIO_TogglePin(GREEN_LED_GPIO_Port, GREEN_LED_Pin);
    HAL_Delay(500);
    /* USER CODE END WHILE */

    /* USER CODE BEGIN 3 */
}
/* USER CODE END 3 */
}
```

Fig: 2.1.2 GPIO configuration code

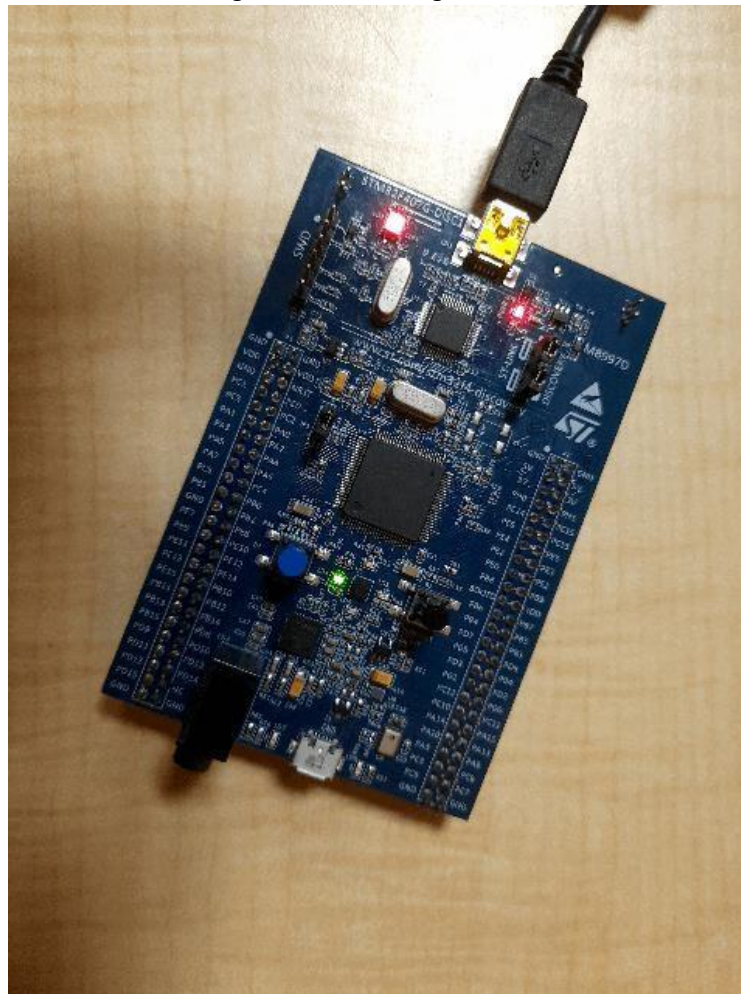


Fig: 2.1.2 LED toggling

2.2 EXTI:

Blue button at PA0 works as an external interrupt.

When the blue button is pressed the Green LED at pin PD12 toggles.

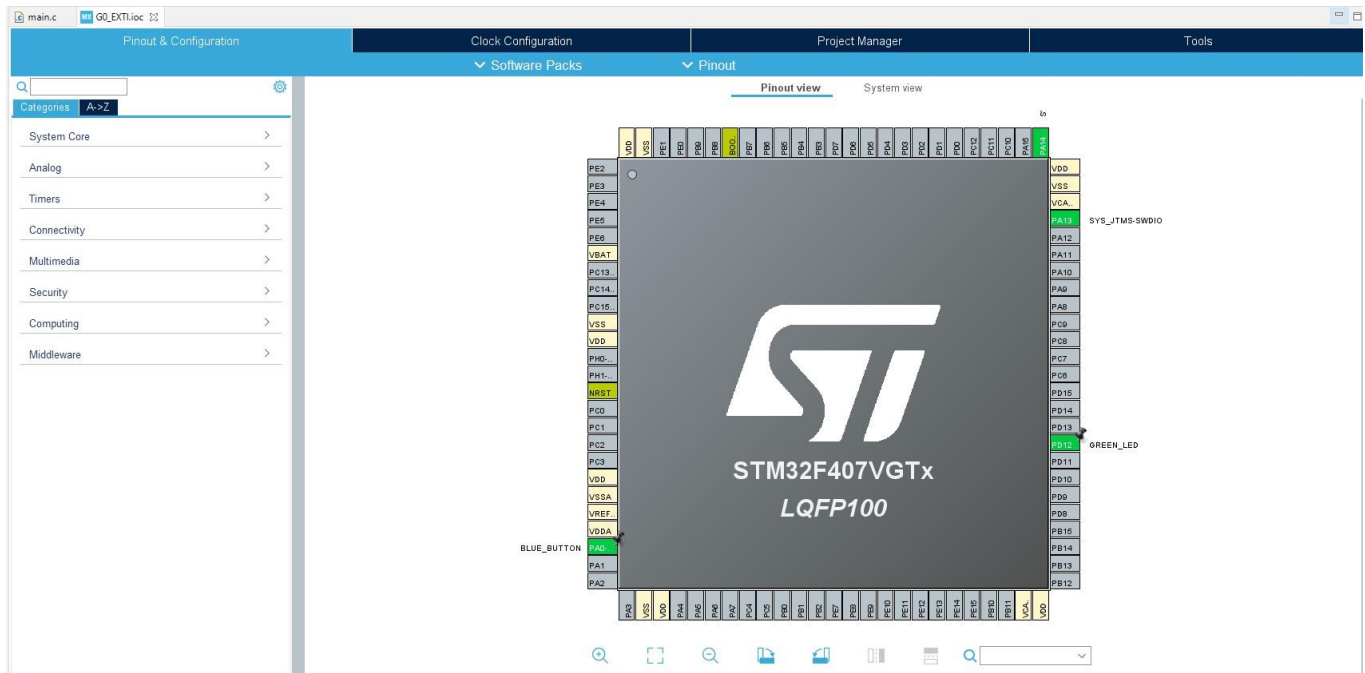


Fig: 2.2.1 EXTI pin configuration

In the main.c file a flag is initialized and if the flag == 1, the condition under the if loop executed to toggle the LED at PD12.

```
main.c  G0_EXTLib
43
44 /* USER CODE BEGIN PV */
45 uint8_t flag = 0;
46 /* USER CODE END PV */
47
48 /* Private function prototypes -----*/
49 void SystemClock_Config(void);
50 static void MX_GPIO_Init(void);
51 /* USER CODE BEGIN PFP */
52
53 /* USER CODE END PFP */
54
55 /* Private user code -----*/
56 /* USER CODE BEGIN 0 */
57
58 /* USER CODE END 0 */
59
60 /**
61  * @brief The application entry point.
62  * @retval int
63  */
64 int main(void)
65 {
66     /* USER CODE BEGIN 1 */
67
68     /* USER CODE END 1 */
69
70     /* MCU Configuration-----*/
71
72     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
73     HAL_Init();
74
75     /* USER CODE BEGIN Init */
76
77     /* USER CODE END Init */
78
79     /* Configure the system clock */
80     SystemClock_Config();
81
82     /* USER CODE BEGIN SysInit */
83
84     /* USER CODE END SysInit */
85
86     /* Initialize all configured peripherals */
87     MX_GPIO_Init();
88     /* USER CODE BEGIN 2 */
89
90     /* USER CODE END 2 */
91
92     /* Infinite loop */
93     /* USER CODE BEGIN WHILE */
94     while (1)
95     {
96         /* USER CODE END WHILE */
97
98         if(!flag){
99             HAL_GPIO_TogglePin(GREEN_LED_GPIO_Port, GREEN_LED_Pin);
100             HAL_Delay(500);
101             flag = 0;
102         }
103     }
104     /* USER CODE BEGIN 3 */
105
106     /* USER CODE END 3 */
107 }
```

Fig: 2.2.1 EXTI configuration code

2.3 ADC

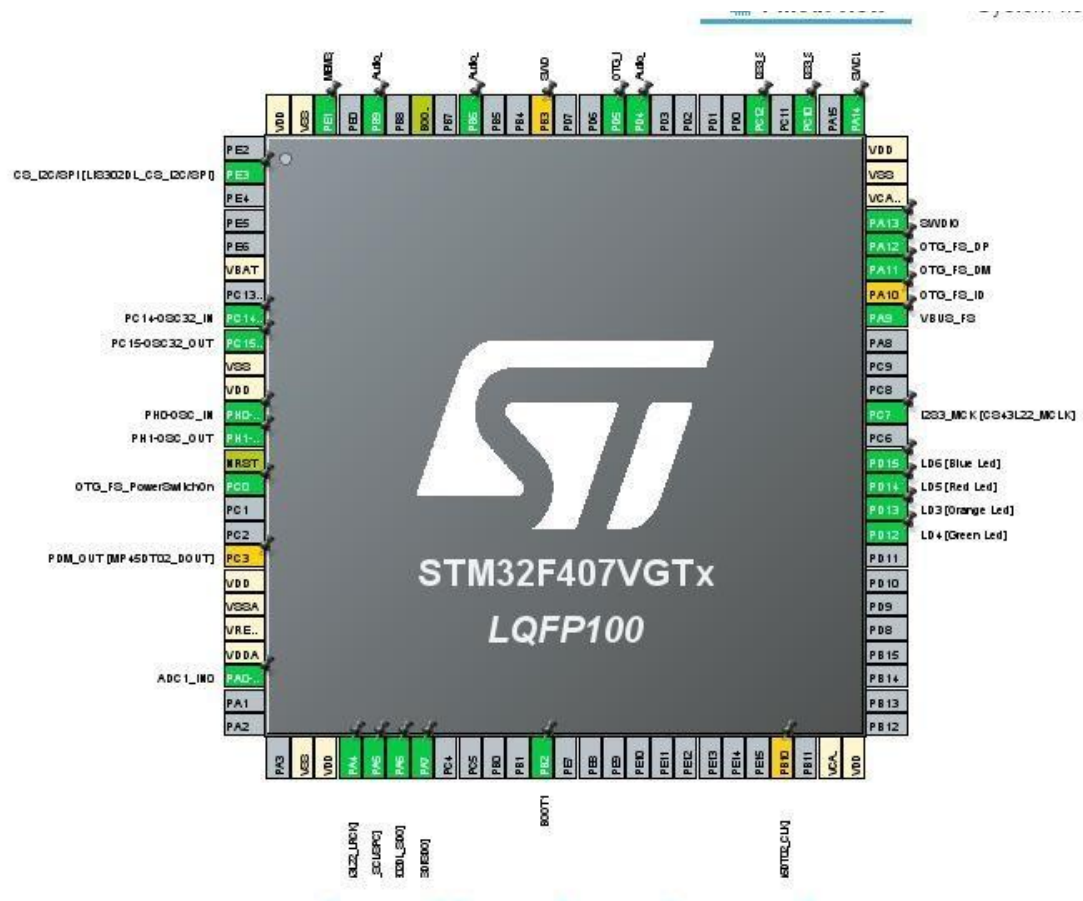


Fig:

2.3.1 ADC pin configuration

```
*main.c
19 /* USER CODE END Header */
20 /* Includes -----*/
21 #include "main.h"
22 #include "usb_host.h"
23
24
25 ADC_HandleTypeDef hadc1;
26
27 I2C_HandleTypeDef hi2c1;
28
29 I2S_HandleTypeDef hi2s3;
30
31 SPI_HandleTypeDef hspi1;
32
33
34 /* Private function prototypes -----*/
35 void SystemClock_Config(void);
36 static void MX_GPIO_Init(void);
37 static void MX_I2C1_Init(void);
38 static void MX_I2S3_Init(void);
39 static void MX_SPI1_Init(void);
40 static void MX_ADC1_Init(void);
41 void MX_USB_HOST_Process(void);
42
43
44 uint32_t adc_value;
45
46 int main(void)
47 {
48     HAL_Init();
49     SystemClock_Config();
50     MX_GPIO_Init();
51     MX_I2C1_Init();
52     MX_I2S3_Init();
53     MX_SPI1_Init();
54     MX_USB_HOST_Init();
55     MX_ADC1_Init();
56
57     while (1)
58     {
59         MX_USB_HOST_Process();
60
61         HAL_ADC_Start(&hadc1);
62
63         if(HAL_ADC_PollForConversion(&hadc1, 5) == HAL_OK){
64             adc_value = HAL_ADC_GetValue(&hadc1);
65         }
66
67         HAL_ADC_Stop(&hadc1);
68         HAL_Delay(100);
69     }
70 }
71
72 void SystemClock_Config(void)
73 {
74     RCC_OscInitTypeDef RCC_OscInitStruct = {0};
75     RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
76     RCC_PeriphCLKInitTypeDef PeriphClkInitStruct = {0};
77 }
```

Fig : 2.3.2 ADC configuration code

2.4 SPI

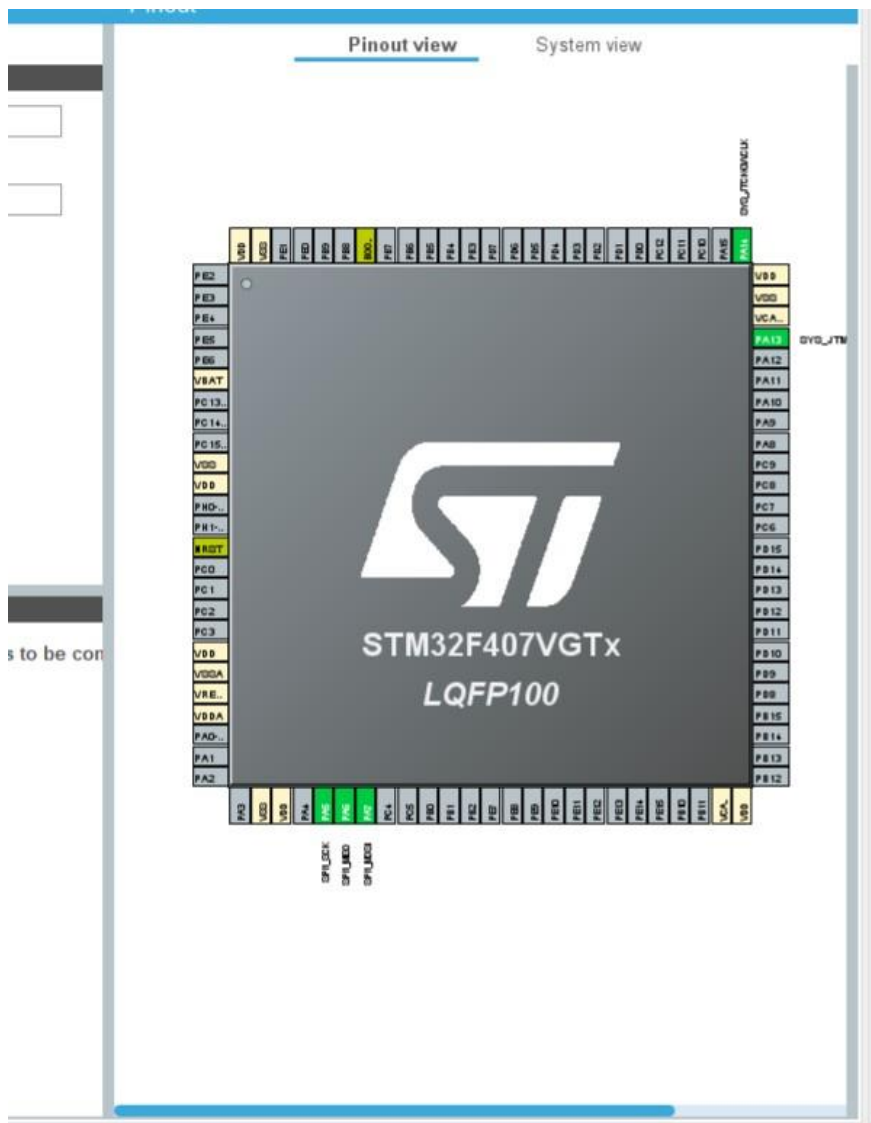
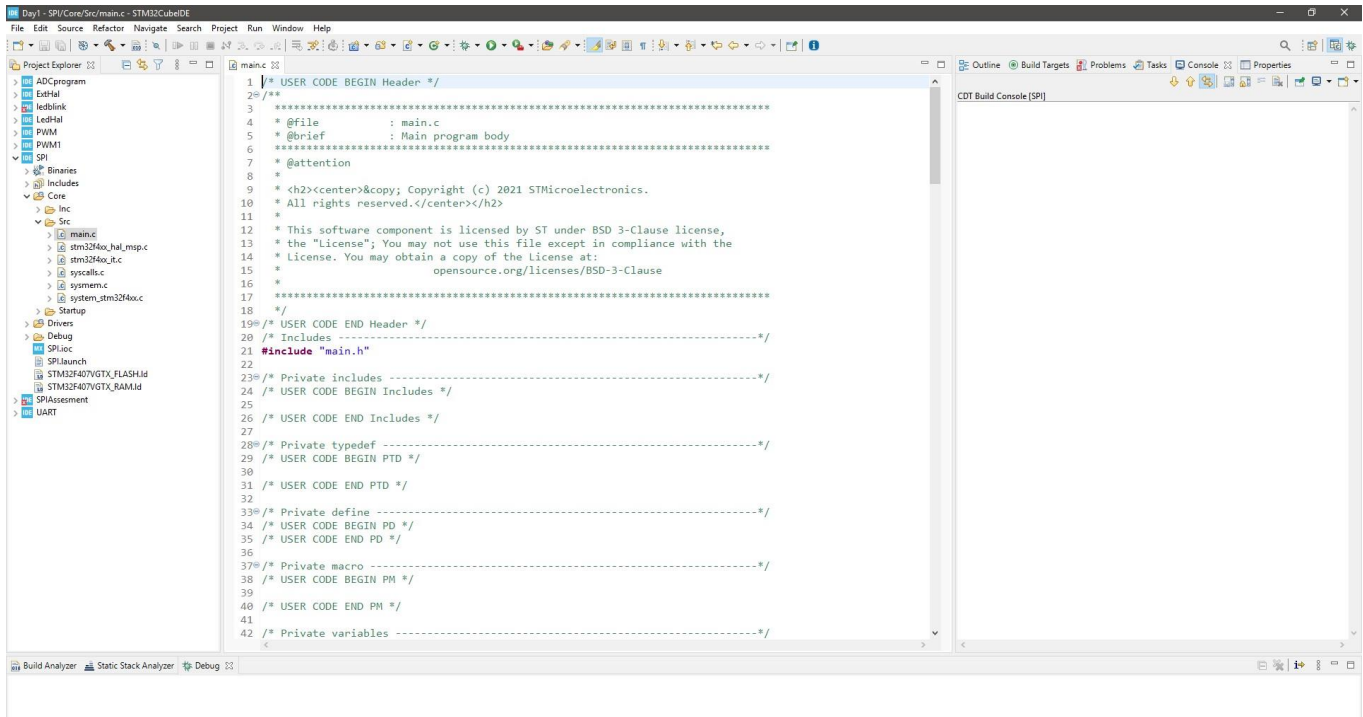
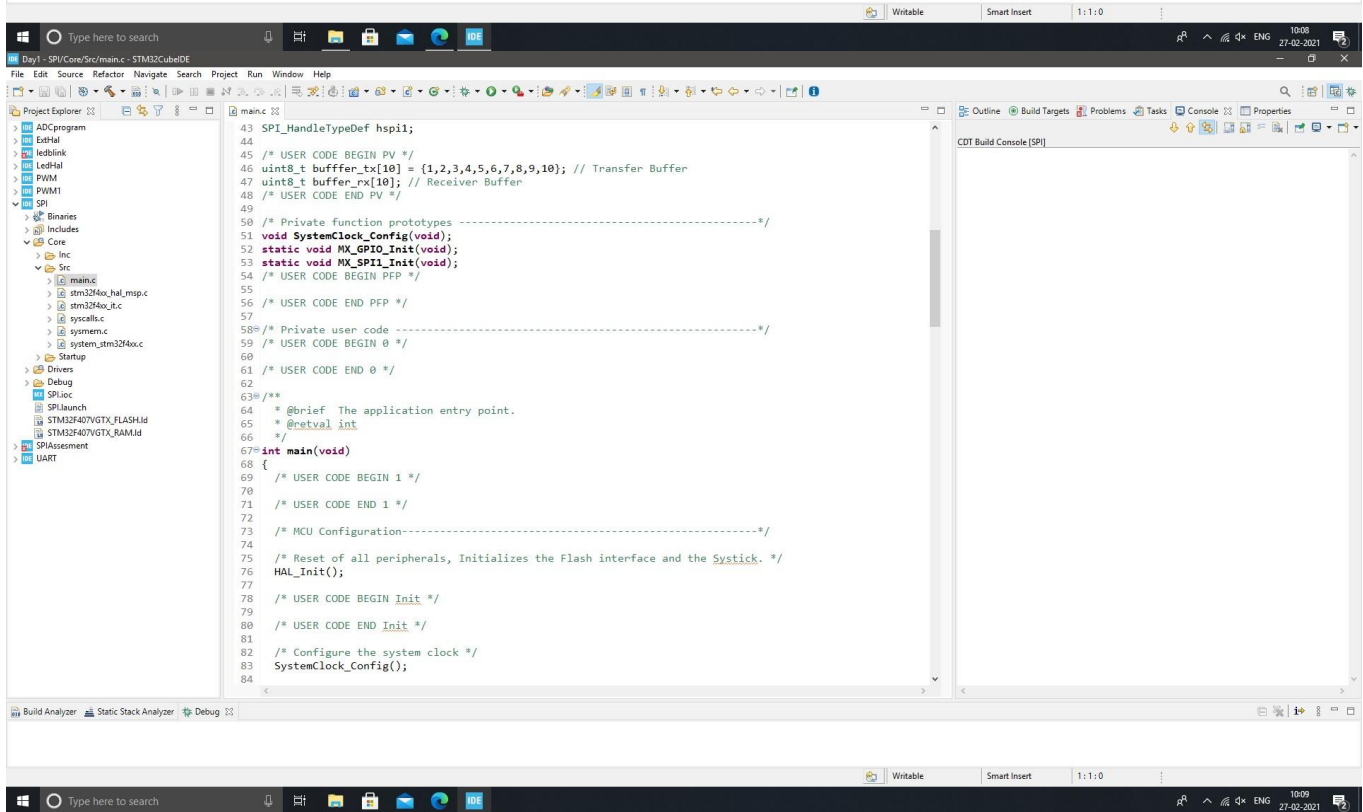


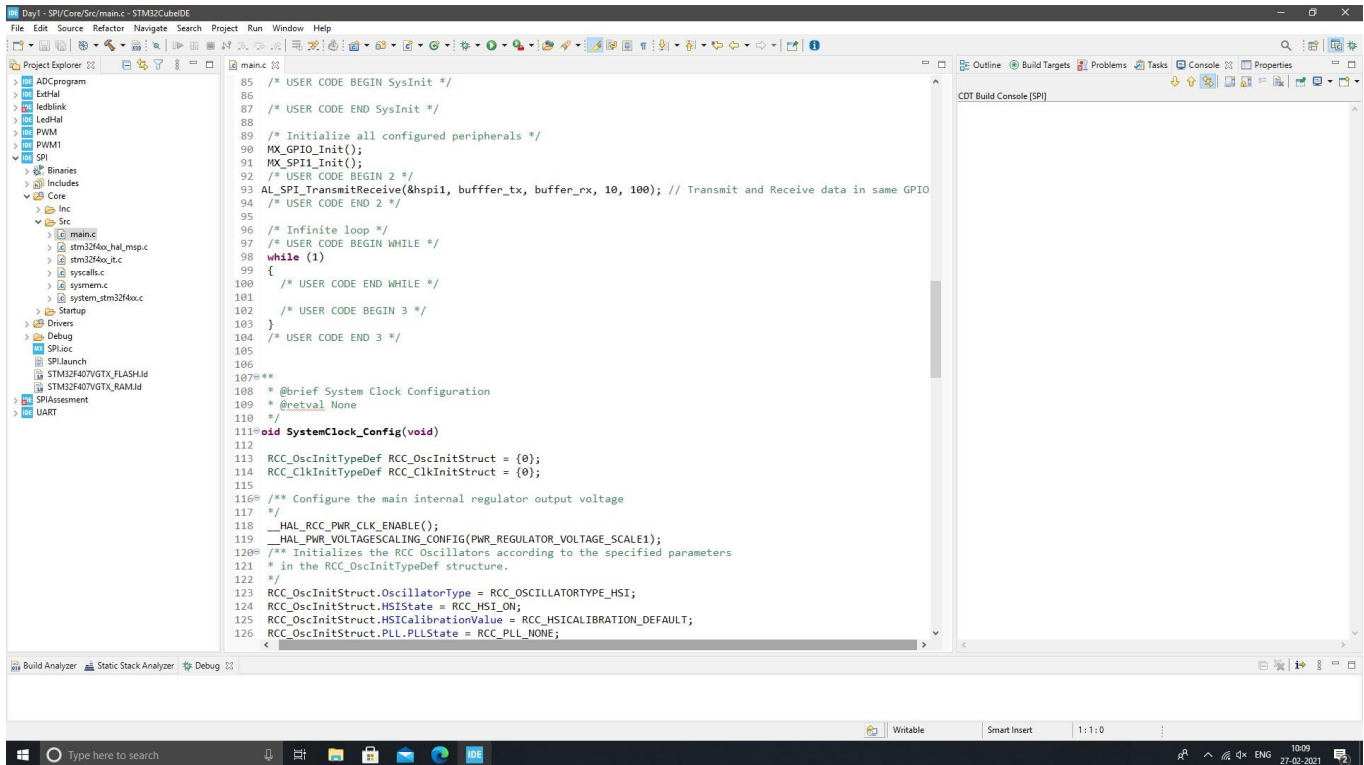
Fig: 2.4.1 SPI Pin configuration



```
1  /* USER CODE BEGIN Header */
2  /**
3   * @file          : main.c
4   * @brief         : Main program body
5   *
6   * @attention
7   *
8   * <h2><center>&copy; Copyright (c) 2021 STMicroelectronics.
9   * All rights reserved.</center></h2>
10  *
11  * This software component is licensed by ST under BSD 3-Clause license,
12  * the "License"; You may not use this file except in compliance with the
13  * License. You may obtain a copy of the license at:
14  *      opensource.org/licenses/BSD-3-Clause
15  *
16  */
17  /* USER CODE END Header */
18  #include "main.h"
19  /* Private includes */
20  /* USER CODE BEGIN Includes */
21  /* USER CODE END Includes */
22  /* Private typedef */
23  /* USER CODE BEGIN PTD */
24  /* USER CODE END PTD */
25  /* Private define */
26  /* USER CODE BEGIN PD */
27  /* USER CODE END PD */
28  /* Private macro */
29  /* USER CODE BEGIN PM */
30  /* USER CODE END PM */
31  /* Private variables */
32  /* USER CODE BEGIN PM */
33  /* USER CODE END PM */
34  /* USER CODE BEGIN PD */
35  /* USER CODE END PD */
36  /* USER CODE BEGIN PTD */
37  /* USER CODE END PTD */
38  /* USER CODE BEGIN Includes */
39  /* USER CODE END Includes */
40  /* Private function prototypes */
41  void SystemClock_Config(void);
42  static void MX_GPIO_Init(void);
43  static void MX_SPI1_Init(void);
44  /* USER CODE BEGIN PFP */
45  /* USER CODE END PFP */
46  /* Private user code */
47  /* USER CODE BEGIN 0 */
48  /* USER CODE END 0 */
49  /**
50  * @brief The application entry point.
51  * @retval int
52  */
53  int main(void)
54  {
55  /* USER CODE BEGIN 1 */
56  /* USER CODE END 1 */
57  /* MCU Configuration */
58  /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
59  HAL_Init();
60  /* USER CODE BEGIN Init */
61  /* USER CODE END Init */
62  /* Configure the system clock */
63  SystemClock_Config();
64  /* USER CODE BEGIN PM */
65  /* USER CODE END PM */
66  }
```



```
43 SPI_HandleTypeDef hspi1;
44
45 /* USER CODE BEGIN PV */
46 uint8_t buffer_tx[10] = {1,2,3,4,5,6,7,8,9,10}; // Transfer Buffer
47 uint8_t buffer_rx[10]; // Receiver Buffer
48 /* USER CODE END PV */
49
50 /* Private function prototypes */
51 void SystemClock_Config(void);
52 static void MX_GPIO_Init(void);
53 static void MX_SPI1_Init(void);
54 /* USER CODE BEGIN PFP */
55 /* USER CODE END PFP */
56
57 /* Private user code */
58 /* USER CODE BEGIN 0 */
59 /* USER CODE END 0 */
60
61 /* USER CODE BEGIN 1 */
62 /* USER CODE END 1 */
63 /**
64  * @brief The application entry point.
65  * @retval int
66  */
67 int main(void)
68 {
69 /* USER CODE BEGIN 1 */
70 /* USER CODE END 1 */
71
72 /* MCU Configuration */
73 /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
74 HAL_Init();
75 /* USER CODE BEGIN Init */
76 /* USER CODE END Init */
77
78 /* Configure the system clock */
79 SystemClock_Config();
80 /* USER CODE BEGIN PM */
81 /* USER CODE END PM */
82 }
```



```
85 /* USER CODE BEGIN SysInit */
86
87 /* USER CODE END SysInit */
88
89 /* Initialize all configured peripherals */
90 MX_GPIO_Init();
91 MX_SPI1_Init();
92 /* USER CODE BEGIN 2 */
93 AL_SPI_TransmitReceive(&hspi1, buffer_tx, buffer_rx, 10, 100); // Transmit and Receive data in same GPIO
94 /* USER CODE END 2 */
95
96 /* Infinite loop */
97 /* USER CODE BEGIN WHILE */
98 while (1)
99 {
100 /* USER CODE END WHILE */
101
102 /* USER CODE BEGIN 3 */
103 }
104 /* USER CODE END 3 */
105
106
107 /**
108  * @brief System Clock Configuration
109  * @retval None
110  */
111 void SystemClock_Config(void)
112 {
113 RCC_OscInitTypeDef RCC_OscInitStruct = {0};
114 RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
115
116 /** Configure the main internal regulator output voltage
117  */
118 __HAL_RCC_PWR_CLK_ENABLE();
119 __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
120 /** Initializes the RCC Oscillators according to the specified parameters
121  * in the RCC_OscInitTypeDef structure.
122  */
123 RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
124 RCC_OscInitStruct.HSIState = RCC_HSI_ON;
125 RCC_OscInitStruct.HSICalibrationValue = RCC_HSICALIBRATION_DEFAULT;
126 RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
```

Fig: 2.4.2 SPI configuration code

2.5 UART

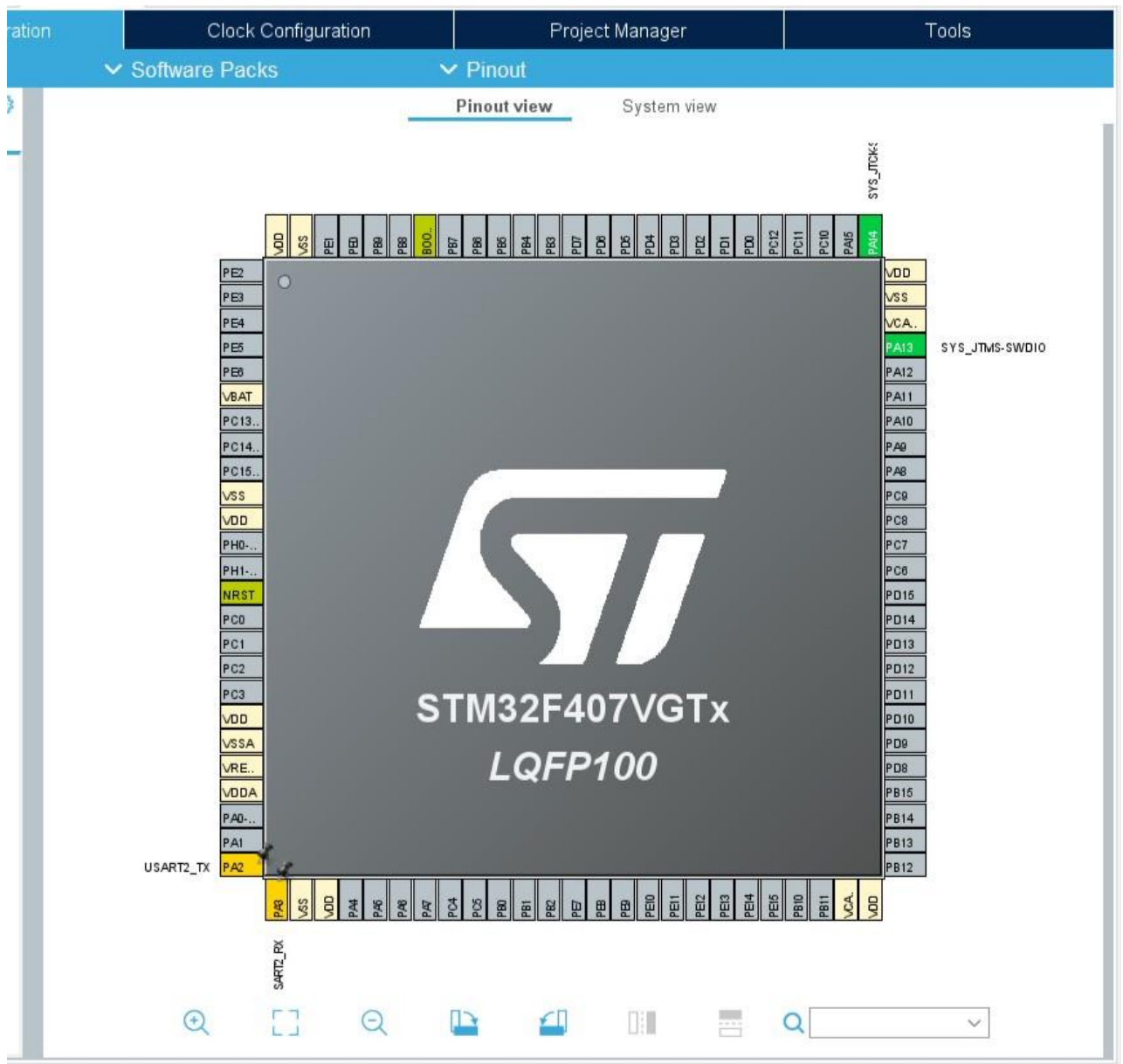
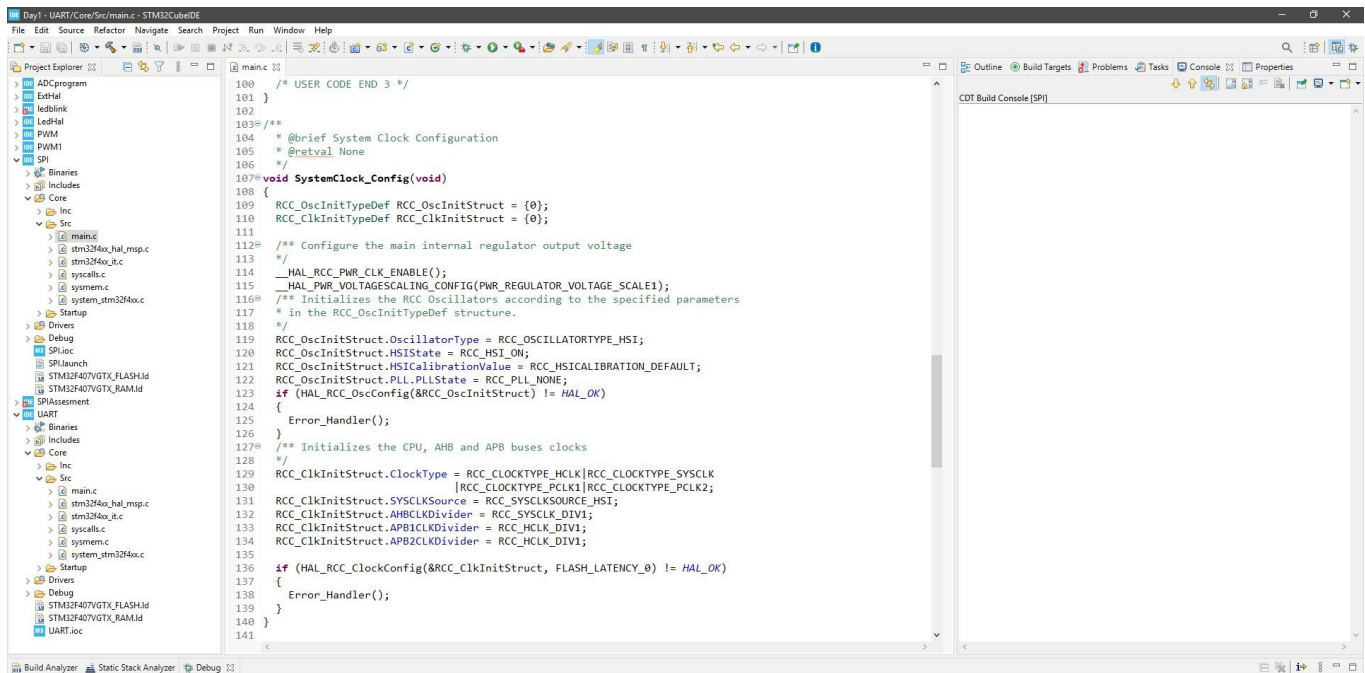
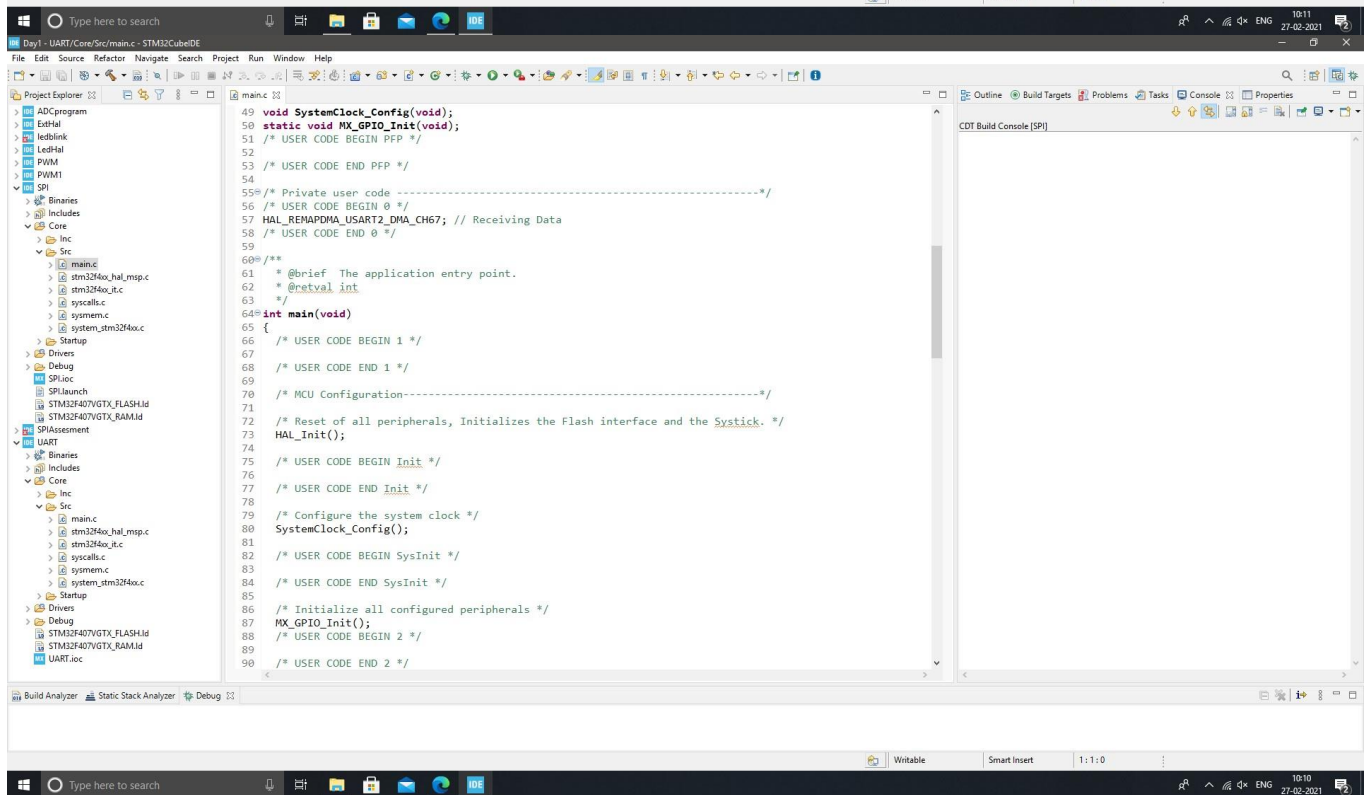


Fig: 2.5.1 UART Pin configuration



```
100 /* USER CODE END 3 */
101 }
102
103 /**
104  * @brief System Clock Configuration
105  * @retval None
106  */
107 void SystemClock_Config(void)
108 {
109     RCC_OscInitTypeDef RCC_OscInitStruct = {0};
110     RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
111
112     /** Configure the main internal regulator output voltage
113     */
114     __HAL_RCC_PWR_CLK_ENABLE();
115     __HAL_PWR_VOLTAGESCALING_CONFIG(PWR_REGULATOR_VOLTAGE_SCALE1);
116     /** Initializes the RCC Oscillators according to the specified parameters
117     * in the RCC_OscInitTypeDef structure.
118     */
119     RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
120     RCC_OscInitStruct.HSIState = RCC_HSI_ON;
121     RCC_OscInitStruct.HSICalibrationValue = RCC_HSICALIBRATION_DEFAULT;
122     RCC_OscInitStruct.PLL.PLLState = RCC_PLL_NONE;
123     if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
124     {
125         Error_Handler();
126     }
127     /** Initializes the CPU, AHB and APB buses clocks
128     */
129     RCC_ClkInitStruct.ClockType = RCC_CLOCKTYPE_HCLK|RCC_CLOCKTYPE_SYSCLK
130         |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
131     RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_HSI;
132     RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV1;
133     RCC_ClkInitStruct.APB1CLKDivider = RCC_HCLK_DIV1;
134     RCC_ClkInitStruct.APB2CLKDivider = RCC_HCLK_DIV1;
135
136     if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_0) != HAL_OK)
137     {
138         Error_Handler();
139     }
140 }
141
```



```
49 void SystemClock_Config(void);
50 static void MX_GPIO_Init(void);
51 /* USER CODE BEGIN PFP */
52
53 /* USER CODE END PFP */
54
55 /* Private user code -----*/
56 /* USER CODE BEGIN 0 */
57 HAL_REMAPDMA_USART2_DMA_CH67; // Receiving Data
58 /* USER CODE END 0 */
59
60 /**
61  * @brief The application entry point.
62  * @retval int
63  */
64 int main(void)
65 {
66     /* USER CODE BEGIN 1 */
67
68     /* USER CODE END 1 */
69
70     /* MCU Configuration -----*/
71
72     /* Reset of all peripherals, Initializes the Flash interface and the Systick. */
73     HAL_Init();
74
75     /* USER CODE BEGIN Init */
76
77     /* USER CODE END Init */
78
79     /* Configure the system clock */
80     SystemClock_Config();
81
82     /* USER CODE BEGIN SysInit */
83
84     /* USER CODE END SysInit */
85
86     /* Initialize all configured peripherals */
87     MX_GPIO_Init();
88     /* USER CODE BEGIN 2 */
89
90     /* USER CODE END 2 */

```

Fig: 2.5.2 UART configuration code

Activity 3 – PROJECT ON BCM MODULE

BCM module was implemented using STM32f407VG microcontroller featuring 32 bit ARM-cortex M4 with FPU core.

This BCM module have following features:

1. Sunroof Control
2. Power Window

Fig 3.1 Features of BCM module

Below is the pinout and configuration of STM microcontroller used:

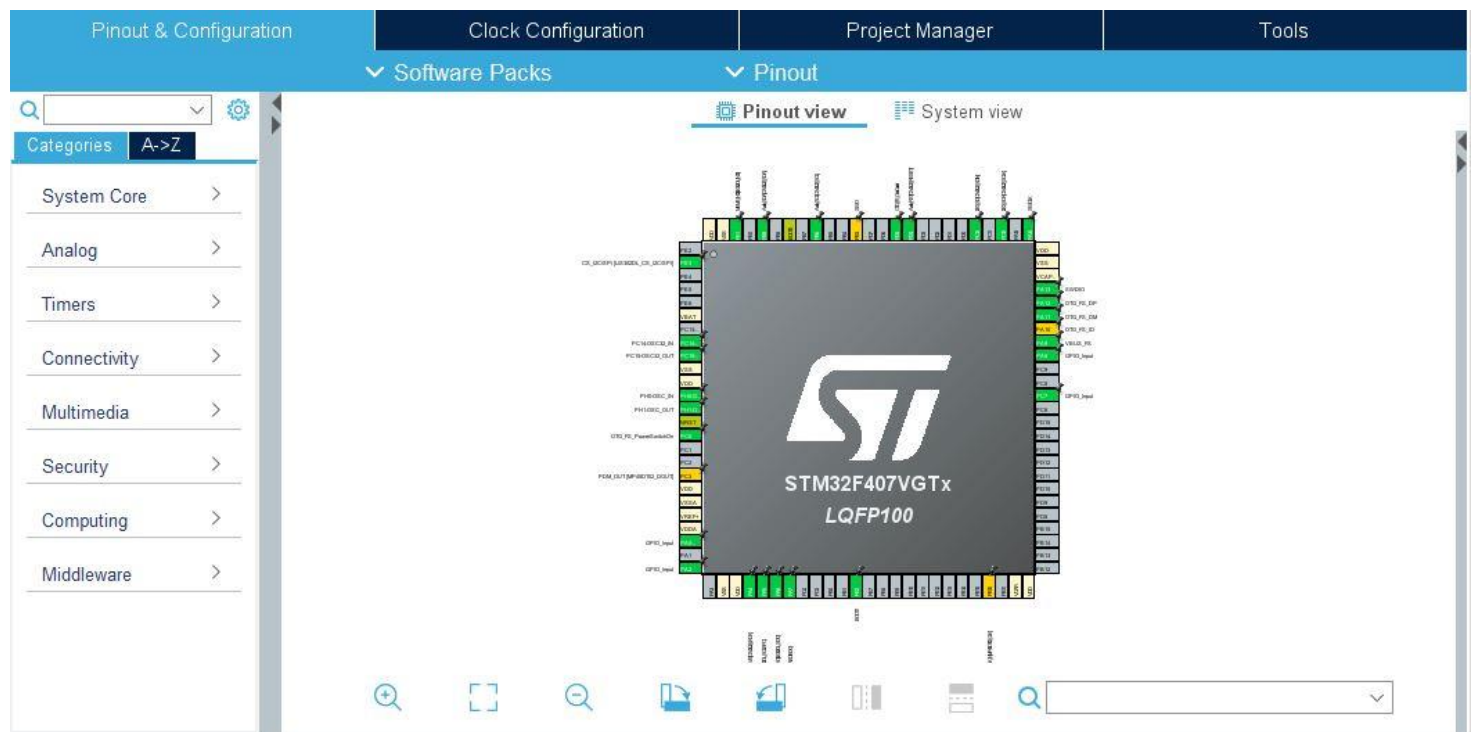


Fig 3.2 Pin configuration

1. Sunroof Control:

When we're pressing the switch in the STM32 Board, the LED glows which implicates that the Sunroof is opened and when we're releasing the button, the LED stops glowing which implicates that the Sunroof is closed.

```
//Sunroof control feature
if((HAL_GPIO_ReadPin(GPIOC, GPIO_PIN_7))==GPIO_PIN_SET)
{
    HAL_GPIO_WritePin(GPIOD, GPIO_PIN_12, GPIO_PIN_RESET); // sunroof open
}
else if((HAL_GPIO_ReadPin(GPIOC, GPIO_PIN_7))==GPIO_PIN_RESET)
{
    HAL_GPIO_WritePin(GPIOD, GPIO_PIN_12, GPIO_PIN_SET); // sunroof closed
}
```

Fig 3.3 Sunroof Control-code

Implementation:

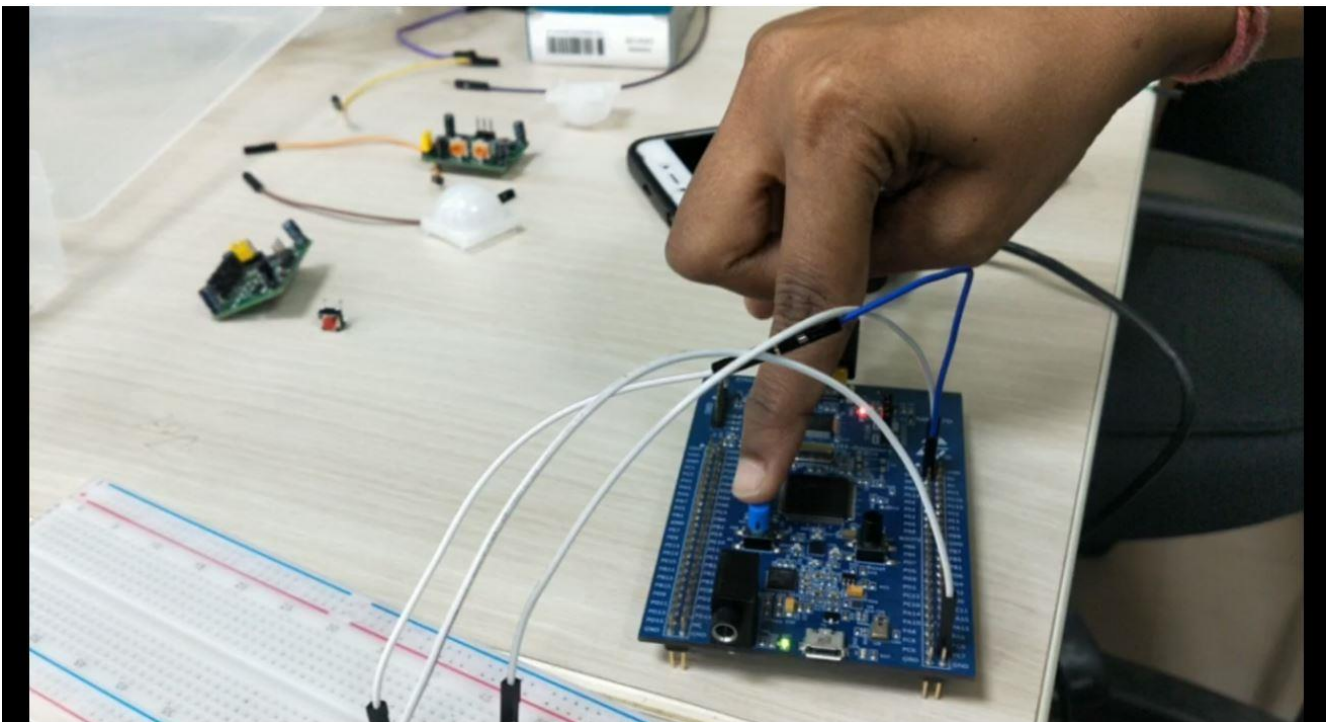


Fig 3.4 Sunroof Control - Implementation

2. Power Window:

When we're pressing the Push button , the LED glows which implicates that the Power Window is opened and when we're releasing the button , the LED stops glowing which implicates that the Power Window is closed.

```
//Power window feature
if((HAL_GPIO_ReadPin(GPIOA, GPIO_PIN_0))==GPIO_PIN_SET)
{
    HAL_GPIO_WritePin(GPIOD, GPIO_PIN_13, GPIO_PIN_SET); // Power window open
}
else if((HAL_GPIO_ReadPin(GPIOA, GPIO_PIN_0))==GPIO_PIN_RESET)
{
    HAL_GPIO_WritePin(GPIOD, GPIO_PIN_13, GPIO_PIN_RESET); // power window closed
}
```

Fig 3.5 Power Window- code

Implementation:

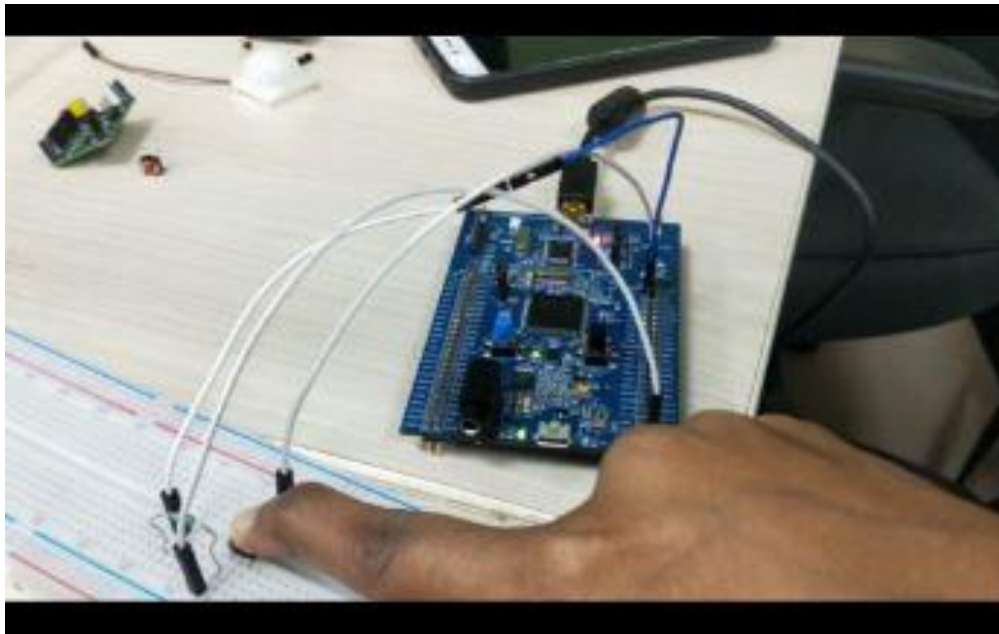


Fig 3.6 Power Window- Implementation