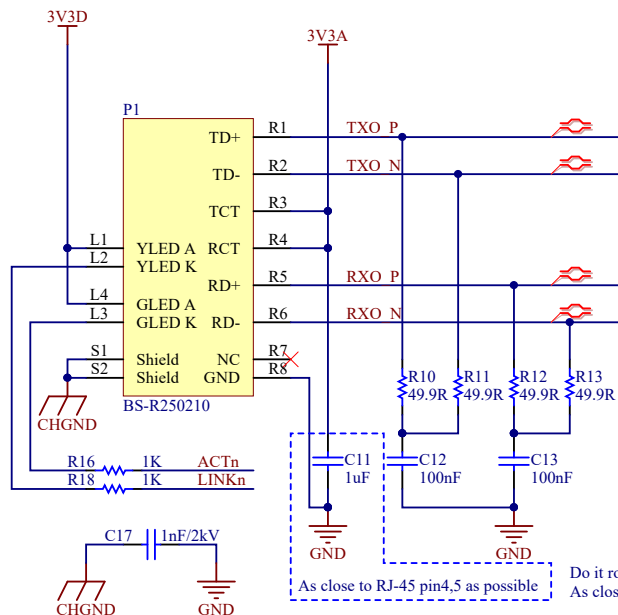
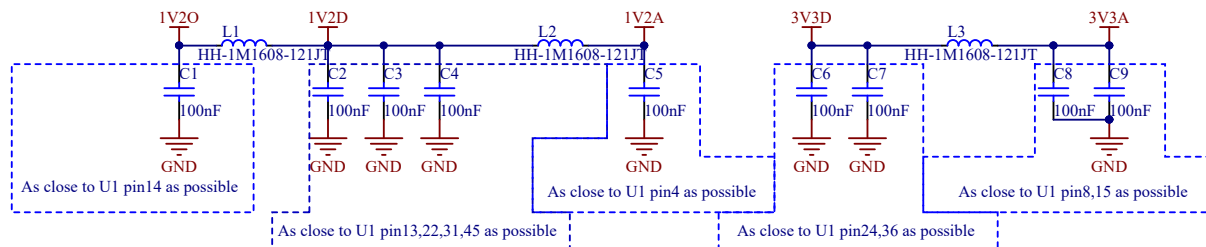
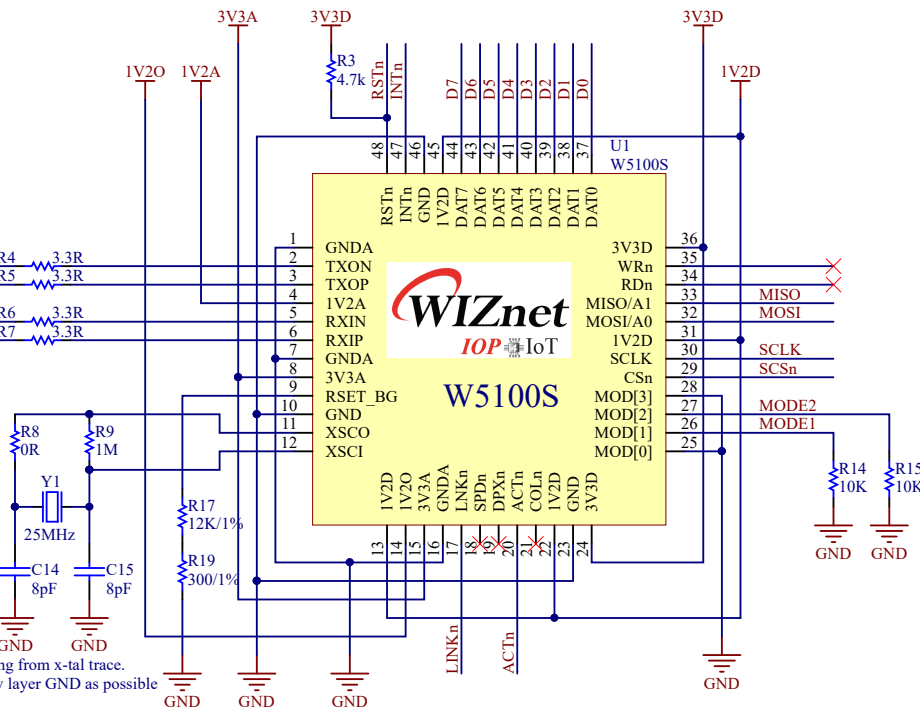


2.54mm 1x6 Pin Header DIP 2.54mm 1x6 Pin Header DIP



Do it round routing from x-tal trace.  
As close to below layer GND as possible



Title		
Size	Number	Revision
A4	WIZ810S_io	
Date:	2019/1/2	Sheet of
File:	F:\PCB制图\WIZ810S IO\B\WIZ810S IO.SchDoc	Drawn By:

V1.0

Comment	Description	Designator	Footprint	LibRef	Quantity
100nF	Ceramic CAP 0603(1608)	C1, C2, C3, C4, C5, C6, C7, C8, C9, C12, C13	0603-C	CAP-0603	11
1uF	Ceramic CAP 0603(1608)	C11	0603-C	CAP-0603	1
8pF	Ceramic CAP 0603(1608)	C14, C15	0603-C	CAP-0603	2
1nF/2kV	Ceramic CAP 1808(4520)	C17	CAP-1206	CAP-1808	1
2.54mm 1x6 Pin Header DIP	2.54mm 1x6 Pin Header DIP	J1, J2	IDC-1X6	2.54mm 1x6 Pin Header DIP	2
HH-1M1608-121JT	Inductor 0603(1608)	L1, L2, L3	0603-L	IND-0603	3
BS-R250210	BS-R250210	P1	BS-R250210	BS-R250210	1
4.7K	RES0603	R1, R2, R3	0603-R	RES-0603	3
3.3R	RES0603	R4, R5, R6, R7	0603-R	RES-0603	4
0R	RES0603	R8	0603-R	RES-0603	1
1M	RES0603	R9	0603-R	RES-0603	1
49.9R	RES0603	R10, R11, R12, R13	0603-R	RES-0603	4
10K	RES0603	R14, R15	0603-R	RES-0603	2
1K	RES0603	R16, R18	0603-R	RES-0603	2
12K/1%	RES0603	R17	RES-0603	RES-0603	1
300/1%	RES0603	R19	RES-0603	RES-0603	1
W5100S	WIZnet Hardwired TCP/IP Chip	U1	WIZNET W5100S QFN	W5100S	1
25MHz	X-tal 25MHz,3.2x2.5 SMD	Y1	OSC-82J0626	OSC-82J0626-25MHz	1