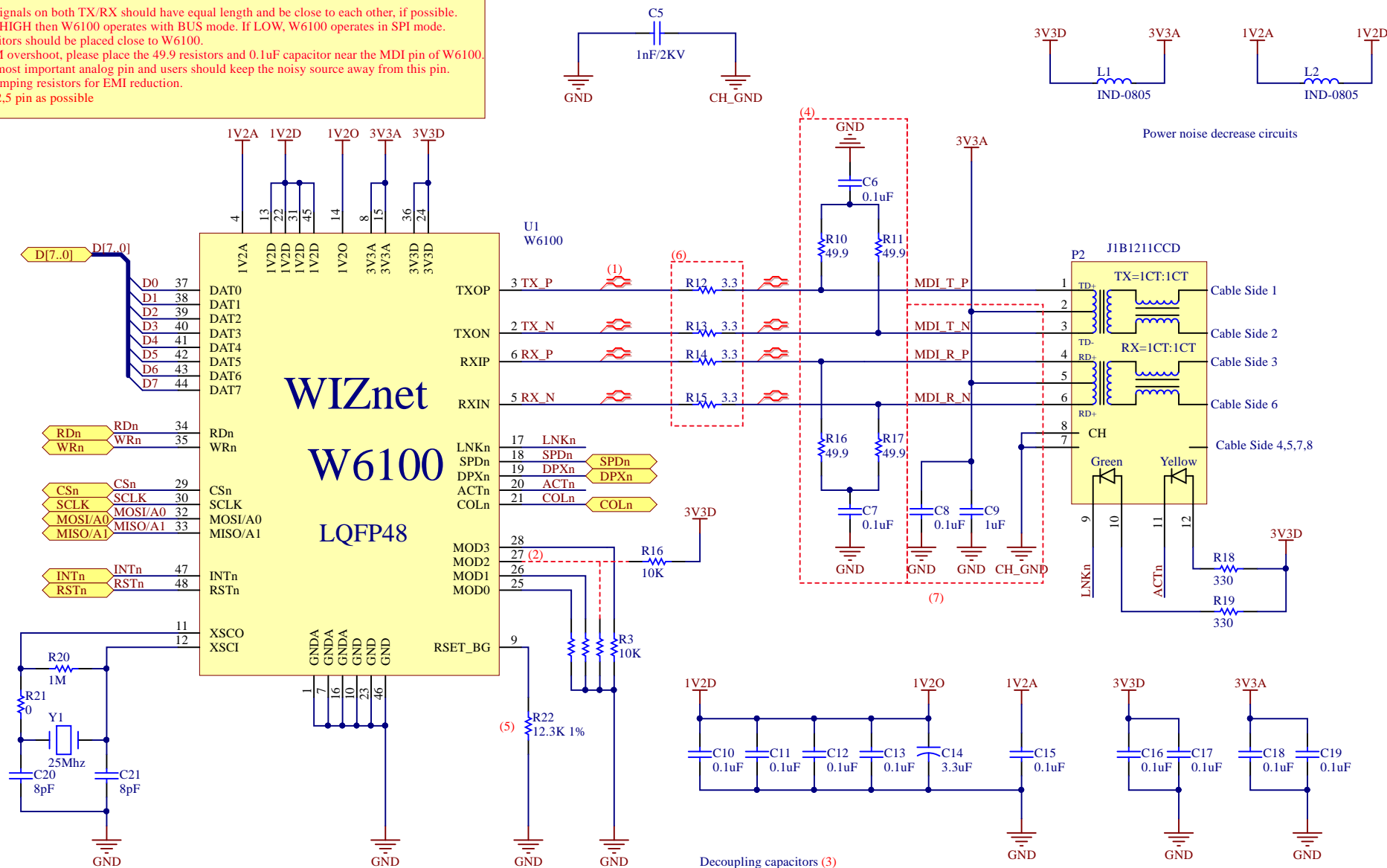


# !!NOTE!!

- (1) The differential signals on both TX/RX should have equal length and be close to each other, if possible.
- (2) When MOD[2] is HIGH then W6100 operates with BUS mode. If LOW, W6100 operates in SPI mode.
- (3) Decoupling capacitors should be placed close to W6100.
- (4) To avoid the 100M overshoot, please place the 49.9 resistors and 0.1uF capacitor near the MDI pin of W6100.
- (5) RSET\_BG is the most important analog pin and users should keep the noisy source away from this pin.
- (6) Option - Apply damping resistors for EMI reduction.
- (7) As close to RJ45 2,5 pin as possible



Title **W6100 Reference Schematic**

Size: **A4**

Number:

Revision: **R1**

Date: 2018-09-27

Sheet 1 of 1

Team: CS

Drawn By: \*

WIZnet Co., Ltd.  
5F, Humax Village  
Hwangseaul-ro, Bundang-gu  
Seongnam-si, Gyeonggi-do  
South Korea

