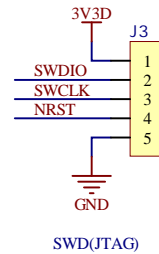


Do it round routing from x-tal trace.
As close to below layer GND as possible



Title V6100S-EVB

Name : 02 STM32F103VCT6 SchDoc

Date : 2019-02-19

Team TeamCS

Revisi on: V110

Sheet 2 of 3

Drawn By: Edvard

WZnet Co., Ltd.
5F, Hmax Village
Hwangseoul-ro, Bundang-gu
Seongnam-si, Gyeonggi-do
South Korea

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1

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A

B

C

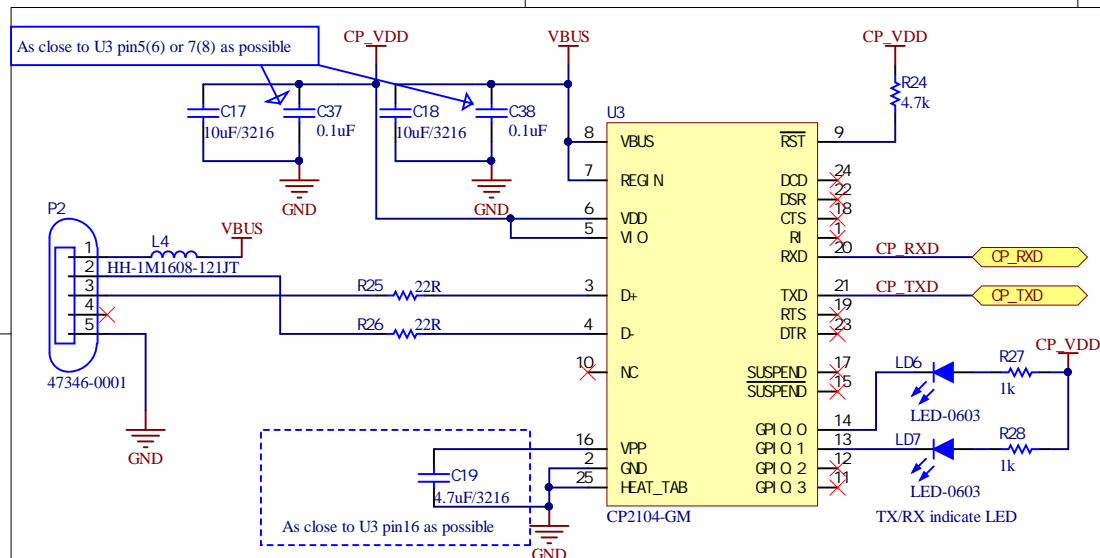
D

A

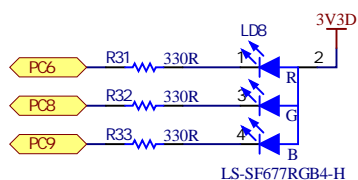
B

C

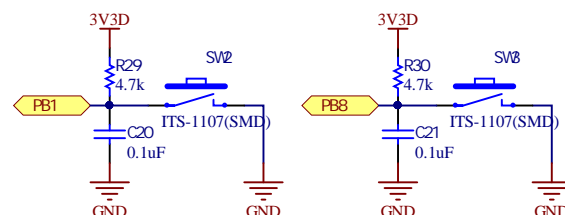
D



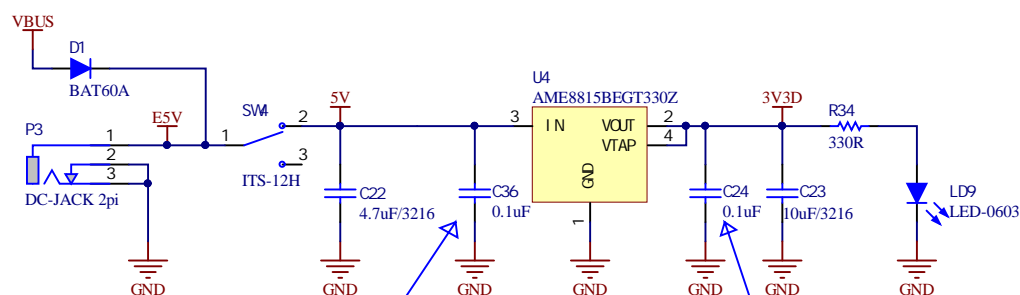
USB to UART



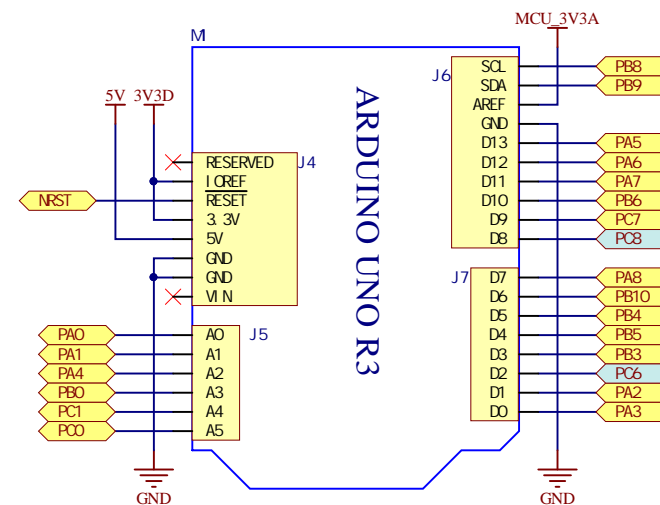
RGB LED



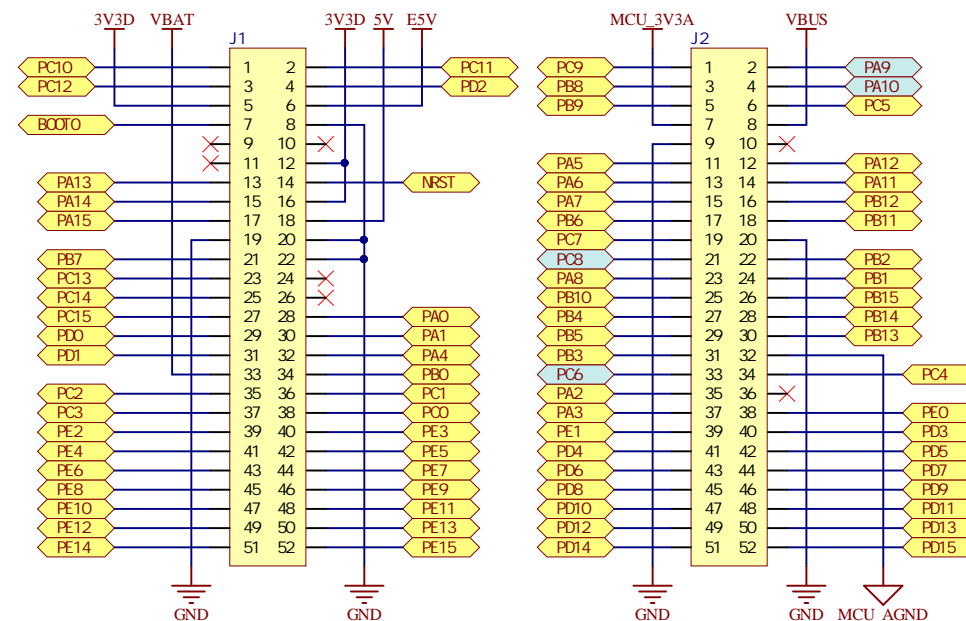
User Switch



DC Power



Ardiono pin



Pinout

Title V6100S-EVB

Name : 03 ETC. SchDoc

Date : 2019-02-19

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Sheet 3 of 3

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WZnet Co., Ltd.
5F, Hmax Village
Hwangseaul-ro, Bundang-gu
Seongnam-si, Gyeonggi-do
South Korea

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