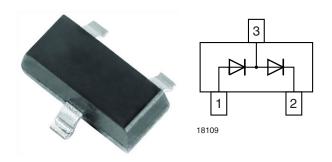


## Vishay Semiconductors

# **Small Signal Switching Diode, Dual in Series**



#### **MECHANICAL DATA**

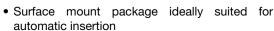
Case: SOT-23

Weight: approx. 8.8 mg
Packaging codes/options:

18/10K per 13" reel (8 mm tape), 10K/box 08/3K per 7" reel (8 mm tape), 15K/box

#### **FEATURES**

- Fast switching speed
- High conductance





Connected in series

- AEC-Q101 qualified
- 7.20 Q.0. qua.....
- Base P/N-E3 RoHS-compliant, commercial grade
- Base P/N-HE3 RoHS-compliant, AEC-Q101 qualified
- Material categorization: For definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

PARTS TABLE					
PART	ORDERING CODE	INTERNAL CONSTRUCTION	TYPE MARKING	REMARKS	
BAV99	BAV99-E3-08 or BAV99-E3-18	Dual diodes serial	JE	Tape and reel	
	BAV99-HE3-08 or BAV99-HE3-18	Duai diodes seriai	JE		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Non repetitive peak reverse voltage		$V_{RM}$	100		
Repetitive peak reverse voltage = working peak reverse voltage = DC blocking voltage		$V_{RRM} = V_{RWM} = V_{R}$	70	V	
Peak forward surge current	t <sub>p</sub> = 1 s		1	Α	
reak lorward surge current	t <sub>p</sub> = 1 μs	IFSM	4.5	7	
Average forward current	Half wave rectification with resistive load and f ≥ 50 MHz, on ceramic substrate 10 mm x 8 mm x 0.7 mm	I <sub>F(AV)</sub>	150	mA	
Forward current	On ceramic substrate 10 mm x 8 mm x 0.7 mm	I <sub>F</sub> 250			
Power dissipation	On ceramic substrate 10 mm x 8 mm x 0.7 mm	P <sub>tot</sub>	300	mW	

THERMAL CHARACTERISTICS (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Junction ambient	On ceramic substrate 10 mm x 8 mm x 0.7 mm	R <sub>thJA</sub>	430	K/W	
Junction and storage temperature range		$T_j = T_{stg}$	- 55 to + 150	°C	
Operating temperature range		T <sub>op</sub>	- 55 to + 150	°C	



# Vishay Semiconductors

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
	I <sub>F</sub> = 1 mA	V <sub>F</sub>			0.715	V
Forward valtage	$I_F = 10 \text{ mA}$				0.855	V
Forward voltage	$I_F = 50 \text{ mA}$				1	V
	I <sub>F</sub> = 150 mA				1.25	V
	V <sub>R</sub> = 70 V	I <sub>R</sub>			2500	nA
Reverse current	V <sub>R</sub> = 70 V, Tj = 150 °C				50	μA
	V <sub>R</sub> = 25 V, Tj = 150 °C				30	μA
Diode capacitance	$V_R = 0$ , $f = 1 MHz$	C <sub>D</sub>			1.5	pF
Reverse recovery time	$I_F$ = 10 mA to $i_R$ = 1 mA, $V_R$ = 6 V, $R_L$ = 100 $\Omega$	t <sub>rr</sub>			6	ns

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

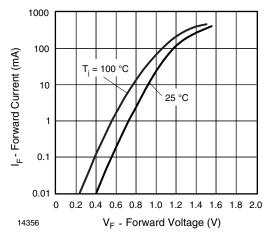


Fig. 1 - Forward Current vs. Forward Voltage

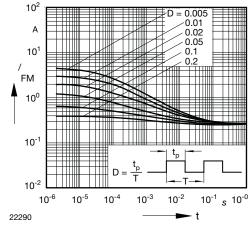
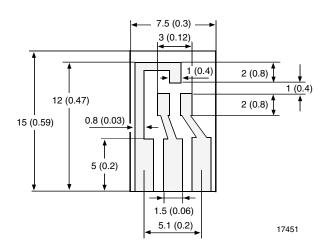


Fig. 2 - Peak forward current  $f_{FM} = f(t_p)$ 

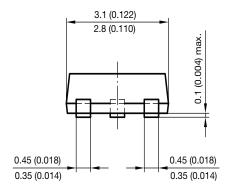
## LAYOUT FOR RthJA TEST

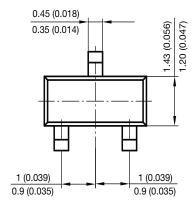
Thickness: Fiberglass 1.5 mm (0.059 inches) Copper leads 0.3 mm (0.012 inches)



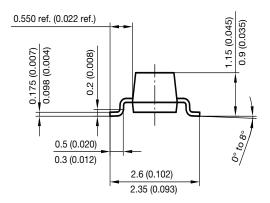


### PACKAGE DIMENSIONS in millimeters (inches): SOT-23

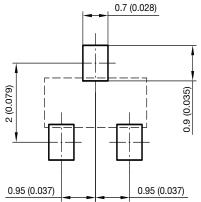




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#### Foot print recommendation:





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Vishay

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