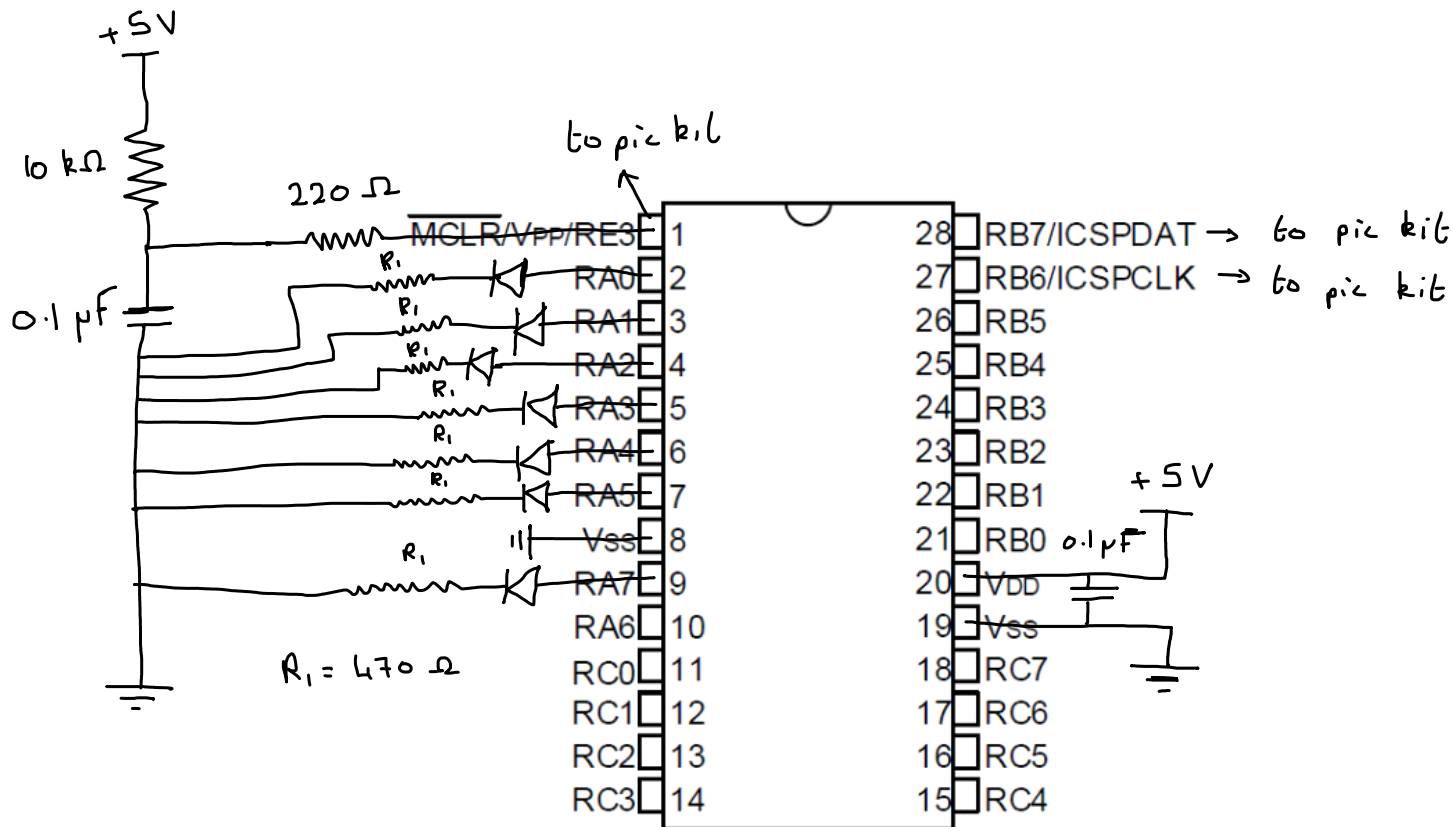


## Activity 3

Tuesday, February 2, 2021 2:32 PM



Initialization (runs only once at the start of the code)

- Configure TRISA so that RA0-RA5 and RA7 are outputs
- Configure ANSELA to Digital I/O
- Configure LATA so that RA0-RA5 = 0 and RA7 = 1

while(1)

```
{
    - While(LATA < 0xBF)
    {
        - Delay 250ms
        - Add one to LATA
    }
    - Delay 250ms
    - Set LATA back to start configuration (RA0-RA5=0) & RA7 = 1
}
```