For Virtual Memory

- **Q1: Where can a block be placed in the main memory?** *(placement)*
  - Fully Associative (Due to the exorbitant miss penalty)

- **Q2: How is a block found if it is in the upper level?** *(identification)*
  - Page Table
  - Translation look-aside buffer to reduce address translation time

- **Q3: Which block should be replaced on a miss?** *(replacement)*
  - LRU (provide use bit or reference bit)

- **Q4: What happens on a write?** *(Write strategy)*
  - Write Back

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