

Bluetooth Operation Classification to Detect the Anomaly Behavior by Monitoring the Power Amplifier Signal

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Motivation

Bluetooth is considered the most common choice for wireless communication in compact devices such as smartwatches, headsets, fitness trackers, and smartphones. In addition, through its extensive usage in Internet of Things (IoT) devices, Bluetooth has developed over the past 20 years to provide low energy consumption and high transfer rates which fit perfectly for area- and power-limited devices [1]. Due to the complexity of the integrated smart devices that use the Bluetooth communication protocol, most manufacturers are outsourcing the chips to third-party suppliers. The integration between many outsourced chips has resulted in the need to add a hardware security layer to ensure appropriate operation. For example, in Apple's smartphones, there is a dedicated co-processor, Secure Enclave, to handle all cryptographic operations and maintain the integrity of data protection [2].

Manufacturers normally buy their Bluetooth chips from third-party suppliers, which are then integrated into a complex hardware-software stack with a variety of potential vulnerabilities. Direct measurement of the output and input power signals can help security functions prevent unauthorized data transmission. Bluetooth, as any communication protocol, has vulnerabilities. On the other hand, researchers disclose those attacks in order to provide security updates to defend users' devices. For instance, in 2017, Armis identified a new Bluetooth attack vector called BlueBorne that can take control of the target device. BlueBorne attacks regular computers, smartphones, and IoT devices [3]. This security breach occurs without pairing to the targeted device nor even while the Bluetooth is in discovery mode. As the Bluetooth chip is responsible for establishing the connection and controlling the flow of data, BlueBorne and other security breaches could attack the Bluetooth communication chip without consent of the controller chip. Therefore, monitoring a Bluetooth chip at the hardware level is necessary to verify its correct operation.

This project is part of bigger project which proposes a compact supervisory circuit to classify the operation of a Bluetooth chip at low frequencies by monitoring the radio frequency (RF) output and the power input lines of the Bluetooth chip. The idea is that classification algorithm can be inexpensively fabricated on a low frequency integrated circuit in legacy technology and/or with minimal area. When the supervisory circuit detects abnormal behavior, it can be configured to shut down the Bluetooth chip. Using features extracted from the power lines, we are able to train several machine learning (ML) algorithms to classify different Bluetooth operation modes and parameters such as operation profile, distance between the paired devices, and number of connected devices. Previously, we already investigated the RF output signals and trained ML models that can separate Bluetooth advertising and transmit/receive modes with ~ 100% accuracy and classify the operation profile of the Bluetooth chip with ~ 100% accuracy.

Problem

One way to monitor the chip is to consider it as a black box which consumes and transmits power. Thus, abnormal behavior can be detected by learning the normal input/output (I/O) power signatures or by parameterizing aspects of the Bluetooth connection (e.g., profile type, distance between paired

devices, and number of connected devices) and comparing to the controller chip instructions. For this project, I am aiming to inspect the power input of the power amplifier which drives the chip's antenna. Also, I will investigate can the Power Amplifier signal replace the RF signal to get the same classification accuracy. In this project, I will investigate another power signal domain of the Bluetooth chip which is the IVDD (Current Voltage Drain-Drain) of the power amplifier that drives the antenna. This power domain will be better for the electronic circuit implementation as it is not a RF signal. The measurement of RF signals requires to double the RF power in order to compensate the effect of power splitting. On the other hand, the IVDD measurement overhead only 1% of the power amplifier signal.

Solution

Collecting the IVDD signal stream. Next, I will utilize MATLAB to extract features from the CSV files of the signals which produced by the oscilloscope. Then, train models to classify the profile and the mode of transmission.

Lab Setup

We prototype our supervisory circuit using evaluation boards and an oscilloscope in order to collect a data set sufficient for training and testing. An evaluation board for the CYW20706 Bluetooth chip is used to program and emulate different profiles and events. The CYW920706WCDEVAL evaluation board supports pinouts for measuring the power lines of the CYW20706 Bluetooth chip. Also, it allows a USB connection with a computer for programming the chip and controlling it while running the program. The Bluetooth board is programmed to act as 2 popular profiles: hands-free and headset, in addition to customized profiles using GATT services. While each profile is running, different events are occurring (dialing, hang-up, streaming music, etc.). The events are controlled using a graphical user interface on a laptop.

Project Tasks

1- Collecting the envelope of the RF signal and power amplifier IVDD signal at the same time using the oscilloscope at a sampling rate not more than 1MHz which is the sampling frequency of the ADC on the FPGA board (DE10-Lite Board). The project is limited with this frequency rate which maybe results an aliased sampled signal. Thus, one of the biggest challenges of this project is to relay on features of the aliased signal.

2- Using MATLAB, I will extract features from the power streams that can be denote to the operation of the Bluetooth chip. The project is focused on relatively big processing window of 640ms which can include huge number of transmission events. Consequently, the aim of the algorithm is to track the overall behavior of the chip.

3- Training different Machine learning algorithms such as, such as decision tree, K-Nearest Neighbor (KNN), support vector machine (SVM), and quadratic discriminant analysis, for the purpose of comparing their accuracy and prediction speed. For all classifiers, I will use 25% holdout validation for testing the models. Then, I will choose the best algorithm in accuracy, prediction speed, and the lowest computational implementation. The training will done using the classification learner tool of MATLAB.

4- Comparing the performance of the power amplifier classifiers and the enveloped RF ones from the points of accuracy, predication speed, and the computational complexity. In order to hardware implementation of the pre-trained model.

References

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