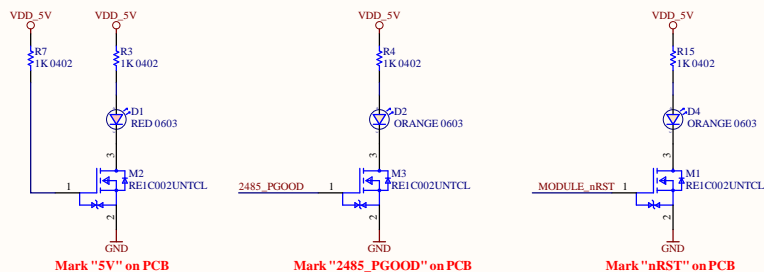


Project: *DM1098FFC*
Current Revision: *R1M0E1*

DM1098FFC Revision History:

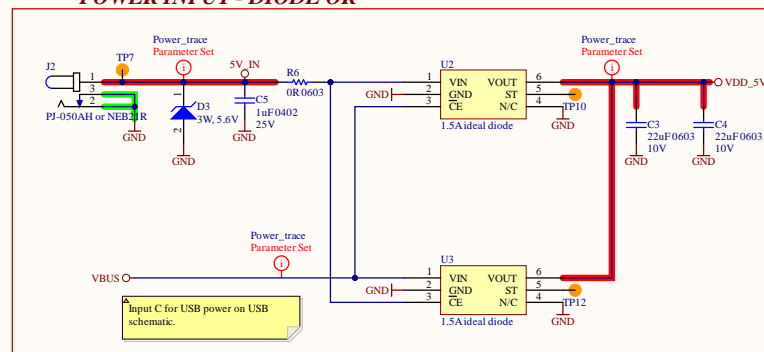
Date	Revision	Reason for Change	Changes Implemented
11/29/2019	R0M0E0 -> R1M1E1	1) Updating board to be aligned with Gen2 1099 boards, with SPI and uSD 2) Standardize FFC connector alignment with edge of board when connector is closed. 3) Existing design was not standardized on LuxonisMaster library format	1) Leveraged 1098OBC connector schematic. Updated J1 Hirose connector to be the Gen2 version with proper pin names, Added three 10-pin headers (not populated) to break out AUX io, SPI io, and uSD io, same as on 1098OBC. Updated schematic net names, harnesses and connections to accommodate the Gen2 changes. 2) Moved each of the FFC connectors toward the board edge so that they are flush with edge when closed. 3) Updated schematic and component information to align with LuxonisMaster library system
8/3/2020	R1M1E1 -> R1M1E2	1) Shutter sync updated to ext_strb	1) Removed R9, R14 (PN CRCW06030000Z0EA), added R10, R12 (PN CRCW06030000Z0EA)
12/28/2020	R1M1E2 -> R0M0E0	1) ESD protection 2) FFC connector stronger mechanics 3) Updating board with overvoltage protection and ideal diode to or USB VBUS and Barrel jack 5V 4) Outdated stack 5) Unused reset circuit 6) Unused strobe configuration resistors	1) Added protection diodes to MIPI lines 2) Changed FFC with Molex 505278 series 3) Added ideal diodes and zener diode for protection 4) Standardized 4L stackup rerouted all differential pairs and tuned lengths 5) Deleted reset circuit and its components and rerouted signals 6) Deleted strobe configuration resistors with corresponding silk and tracks
04/21/2021	R0M0E0 -> R1M0E1	1) Slow plug issue with USB type-C	1. Added 1uF capacitor to VBUS_DET updated PCB and fabrication files

Title DM1098FFC			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	Cannot open file C:\Users\BrianLuxonis\Documents\146
Size: Tabloid	Number: D0000190	Revision: R1M0E1		
Date: 21/04/2021	Time: 10:51:33	Sheet 1 of 5		
Drawn by: David Malovrh				

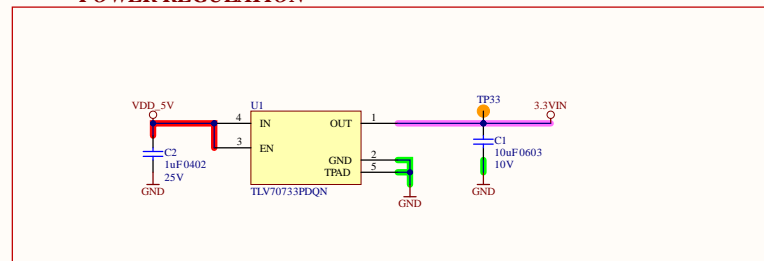


2485_PGOOD and MODULE_nRST both have pull ups to 1.8V on 1099 module. 2485_PGOOD is held low by open-drain output on 1099 PMIC until power is good. MODULE_nRST rises with 1.8V at POR, but can be held low by user button or 1099 ITAG.

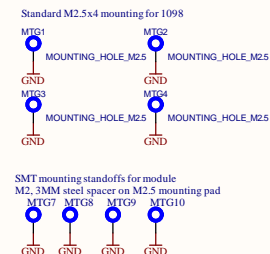
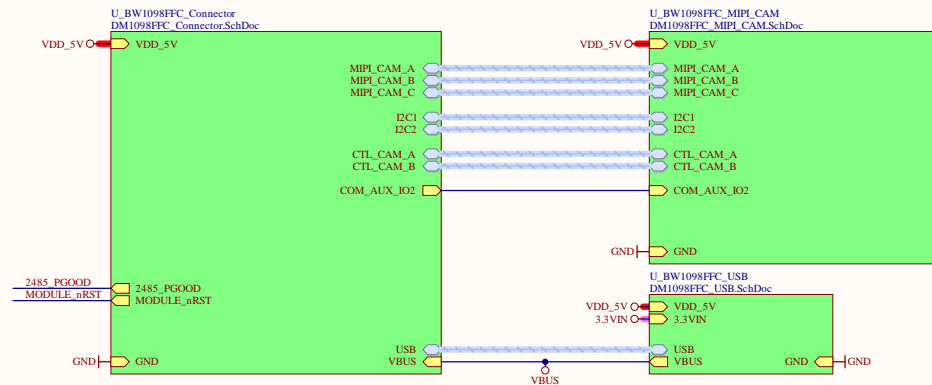
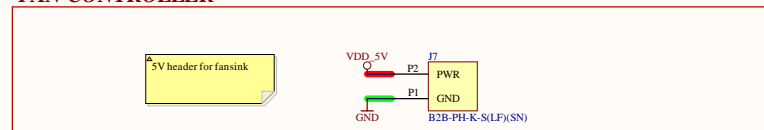
POWER INPUT - DIODE OR



POWER REGULATION



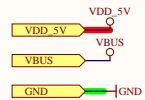
FAN CONTROLLER



U_Project_Information
Project_Information.SchDoc

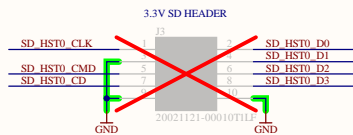
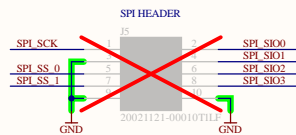
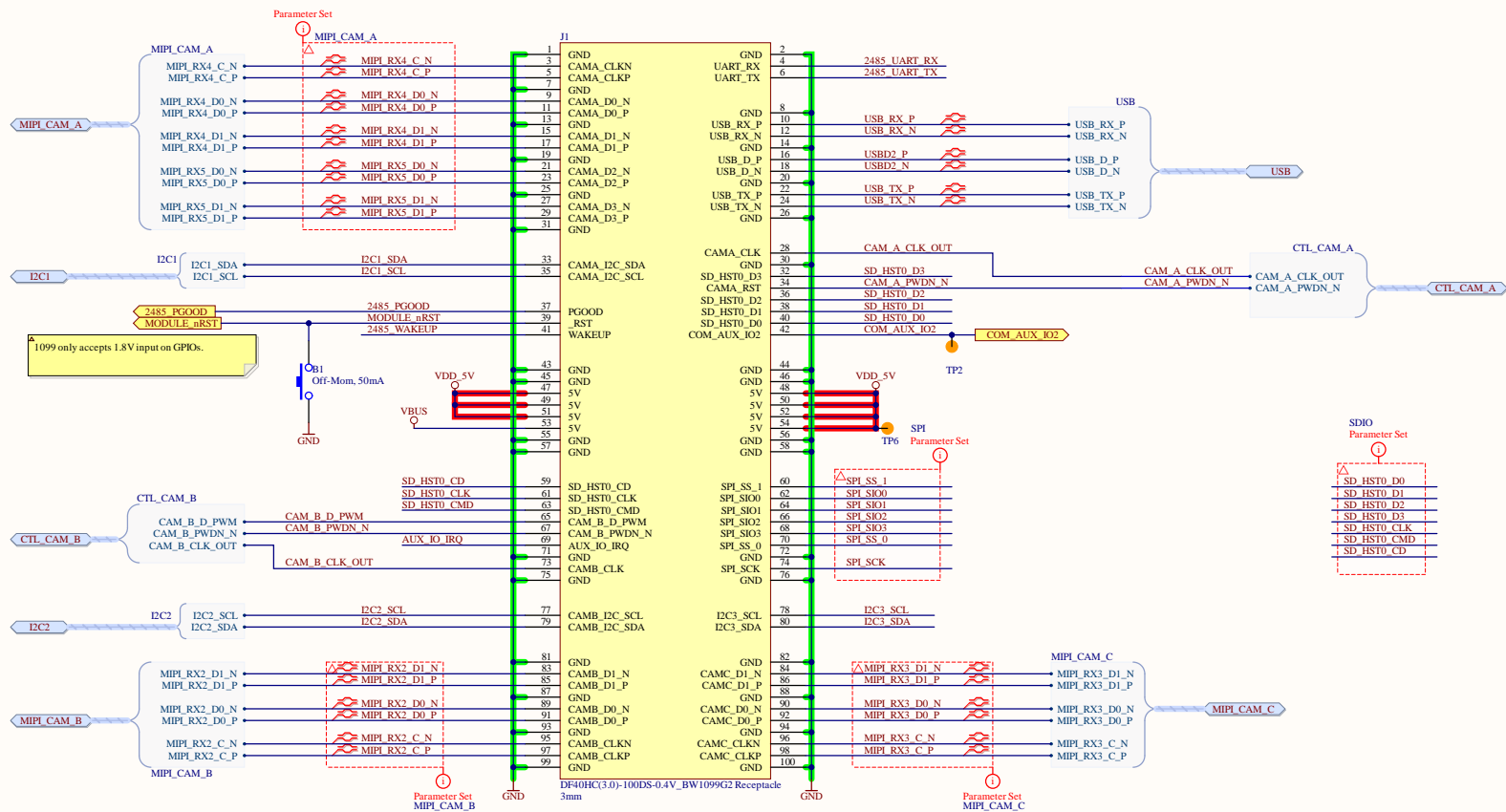


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Size: Tabloid	Number: D0000190	Revision:	Cannot open file C:\Users\BrianLaxson\Documents\...	
Date: 21/04/2021	Time: 10:51:33	Sheet 2 of 5		
Drawn by: David Malovrh				



VDD_5V drawn by BW1099 can be up to 1A
VBUS on BW1099 is only sense input

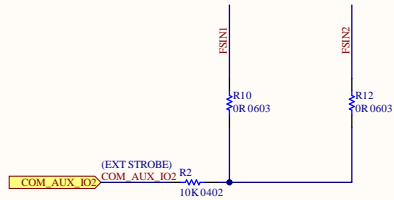
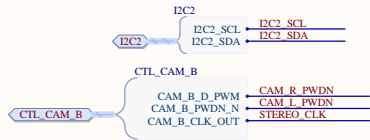
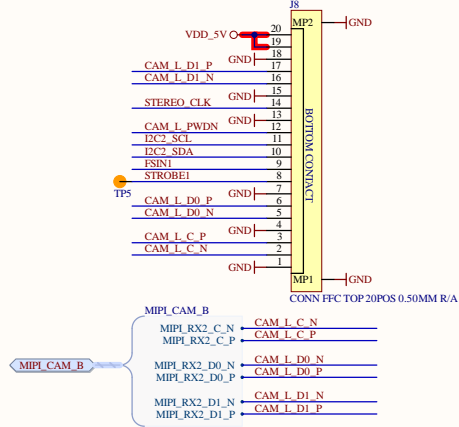
MIPI Lanes:
DPHY v1.2
Max 2.1 Gbps / lane



Title DMI098FFC			Laxsonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States		Cannot open file C:\Users\BrianLaxsonis\Documents\...
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Date: 21/04/2021	Time: 10:51:33	Sheet 3 of 5			
Drawn by: David Malovich					

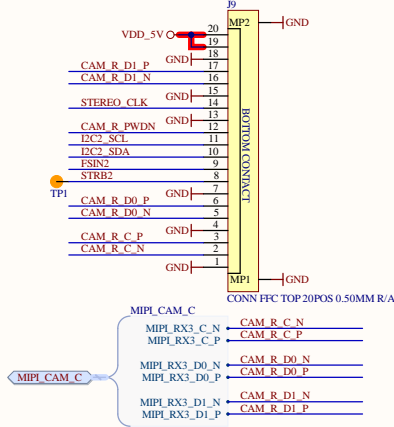
STEREO CAMERA PAIR

STEREO LEFT



Camera timing Sync Option

STEREO RIGHT



RGB CAMERA

RGB IMX378

