

SMT mounting standoffs for module
M2, 3MM steel spacer on M2.5 mounting pad
MTG7 MTG8

U_Project_Information
Project_Information.SchDoc

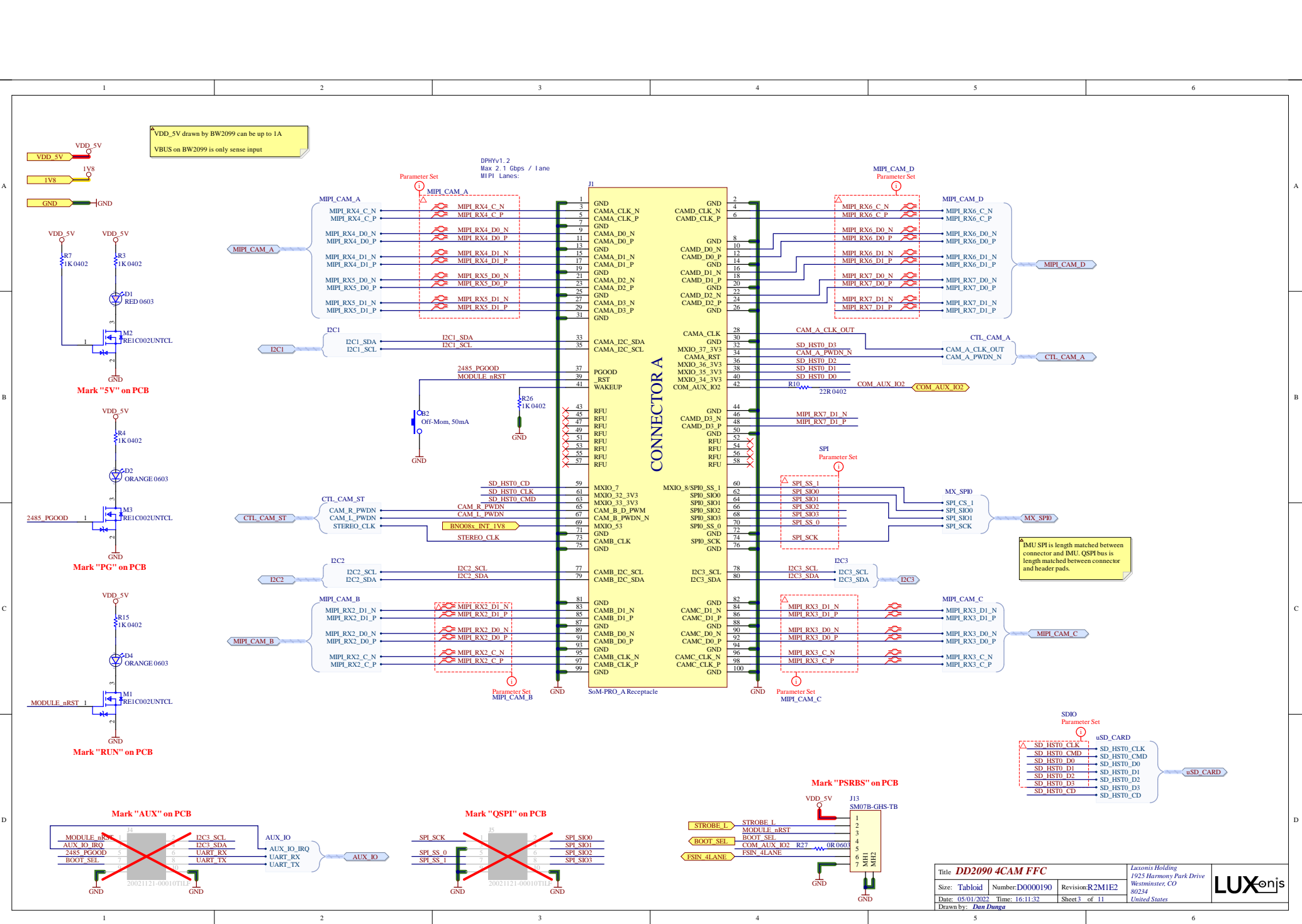


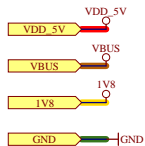
Title DD2090 4CAM FFC			Laxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	LUX onis
Size: Tabloid	Number: D0000190	Revision: R2M1E2		
Date: 05/01/2022	Time: 16:11:31	Sheet 1 of 11		
Drawn by: Dan Dunga				

Project: DD2090 4CAM FFC
Current Revision: R2M1E2
Assembly variant: Production

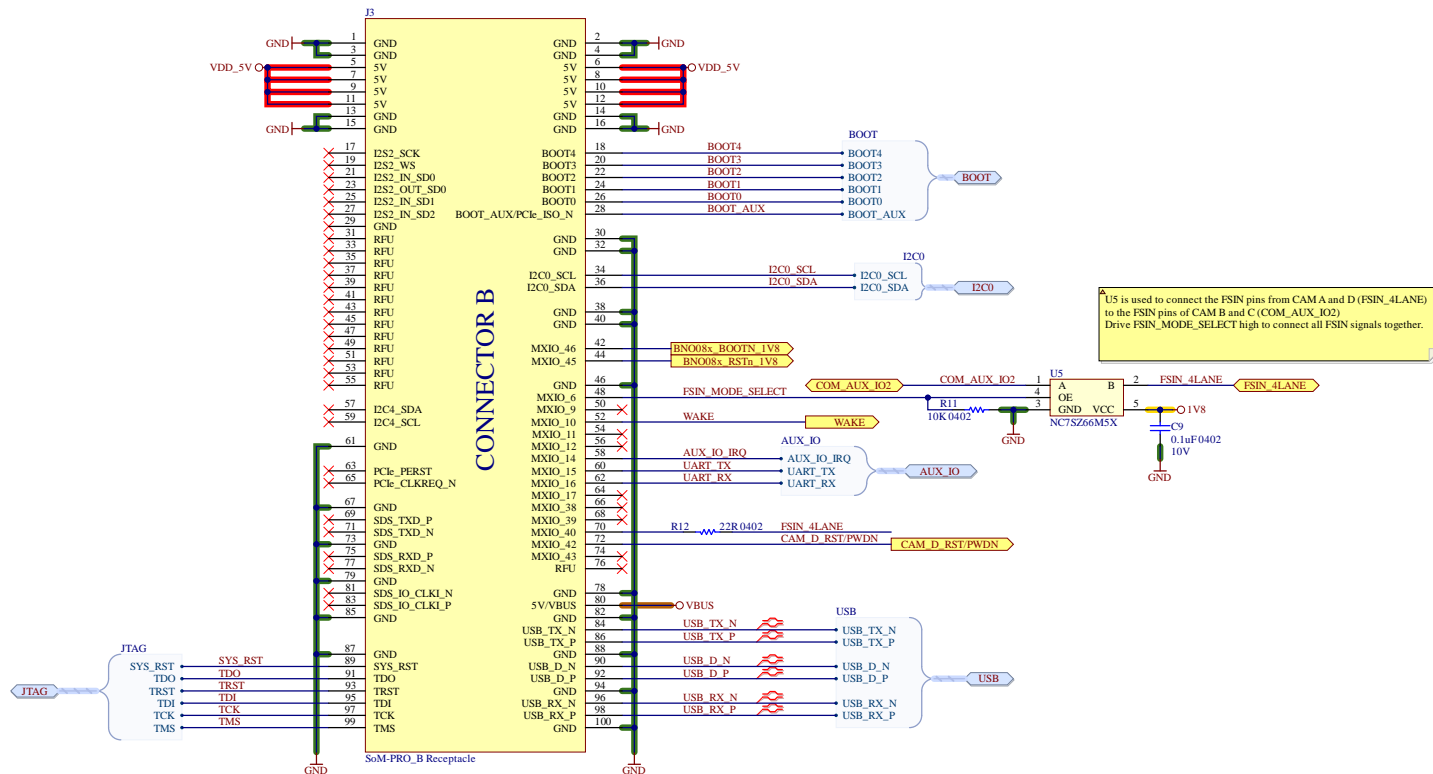
DD2090 4CAM FFC Revision History:

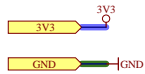
Date	Revision	Reason for Change	Changes Implemented
10 June 2021	DM1099FFC R1M0E1 -> DD2090 4CAM FFC R0M0E0	1) Changed SoM to BW2099 2) Added MIPI CAM D (4 lane) 3) Added uSD card 4) Added Software controll over FSIN signal between the 2 lane and 4 lane cameras (US) 5) Added ESD protection on USB signals	
	R0M0E0 -> R1M1E1	1) Corrected SoM footprint (mounting posts error) 2) BOOT should be connected to BOOT2 and BOOT4 for USB recovery 3) Updated assembly variant to default NOR boot mode	1) Increase the board from 62x38mm to 65x38mm 2) Added EEPROM
29 Dec 2021	R1M1E1 -> R2M1E2	1) Change the 3.3V power supply to 2Acapability 2) Increase silkscreen size and position for the connector markings to be more visible when assembled 3) Change USBC switch 4) Connecte BNO068 WAKE signal to MX 5) Update PRBS connector adding Strobe and 4-Lane FSYNC	1) Change the 3.3V power supply to 3Acapability (should be enough for any kind of CCM) 2) Increased silkscreen size and updated position for the connector markings 3) Changed USBC switch with NXP alternative 4) Connected WAKE signal to MXIO10 5) Updated PSRBS connector adding Strobe and 4-Lane FSYNC



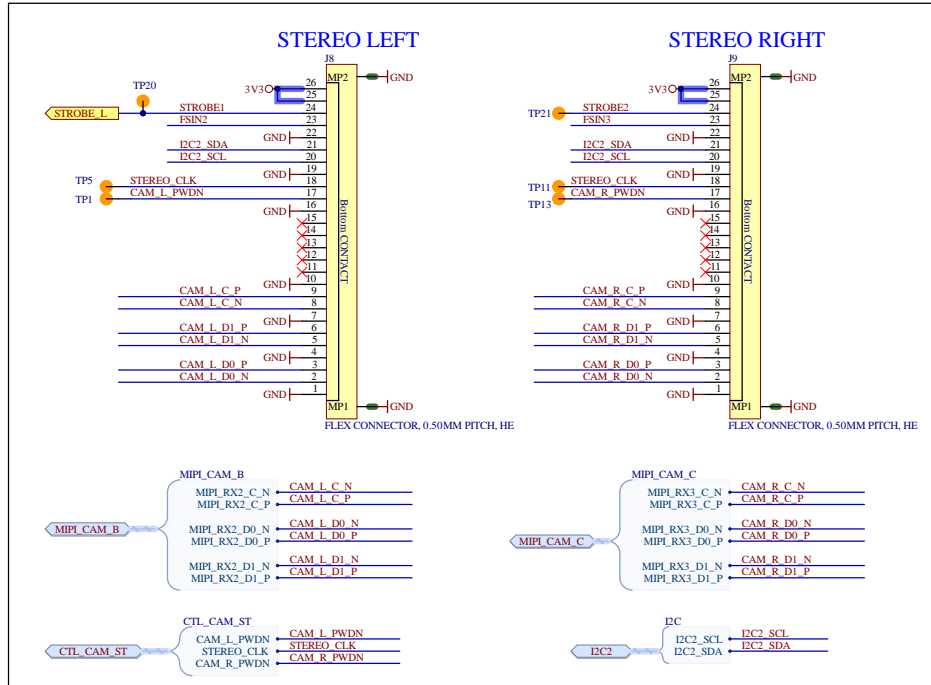


A
VDD_5V drawn by BW2099 can be up to 1A
VBUS on BW2099 is only sense input

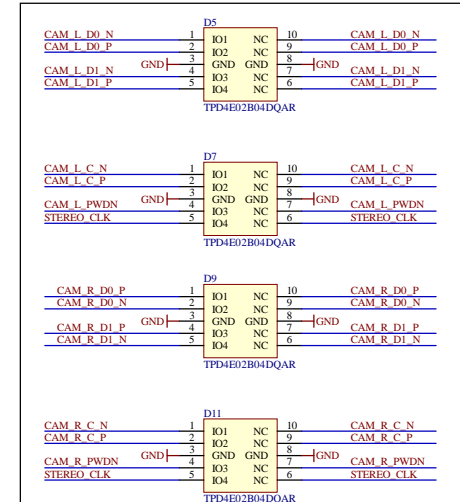




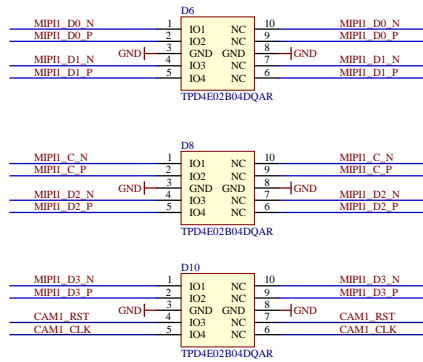
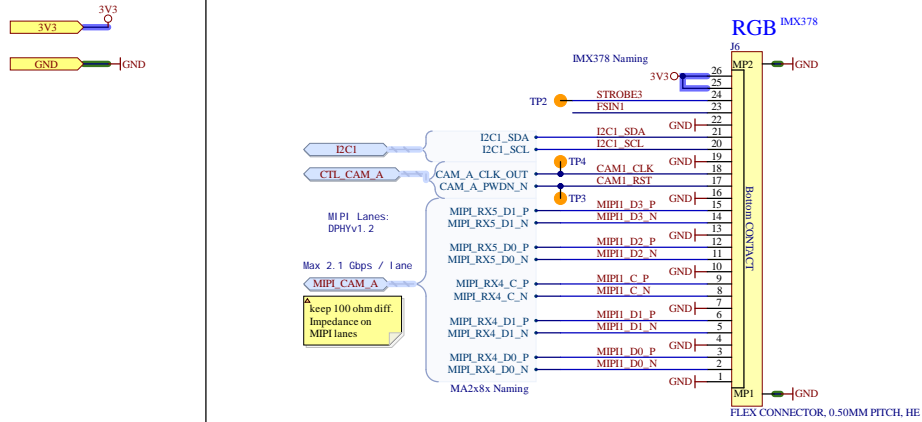
STEREO CAMERA PAIR



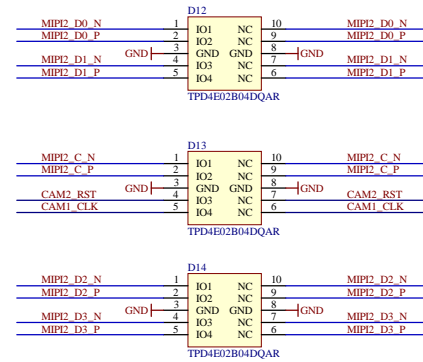
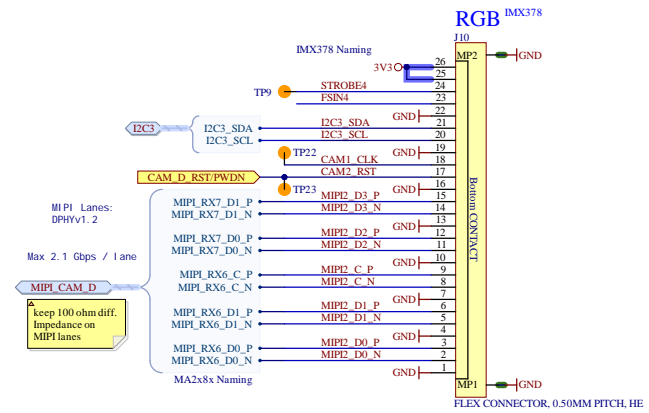
ESD PROTECTION

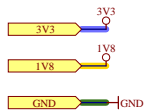


RGB CAMERA 1

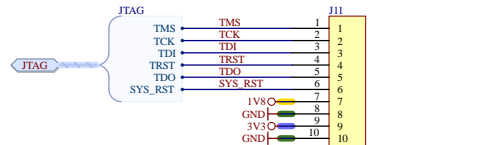


RGB CAMERA 2





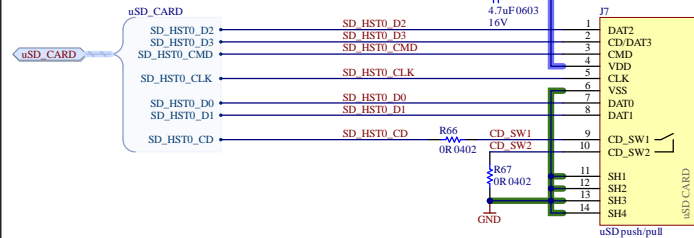
JTAG



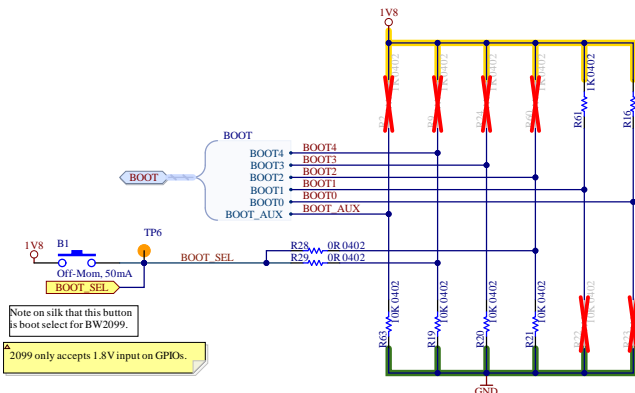
Mark "JTAG" on PCB

JTAG FFC configuration (10-pin, 0.5mm pitch).

3.3V GPIO (uSD)



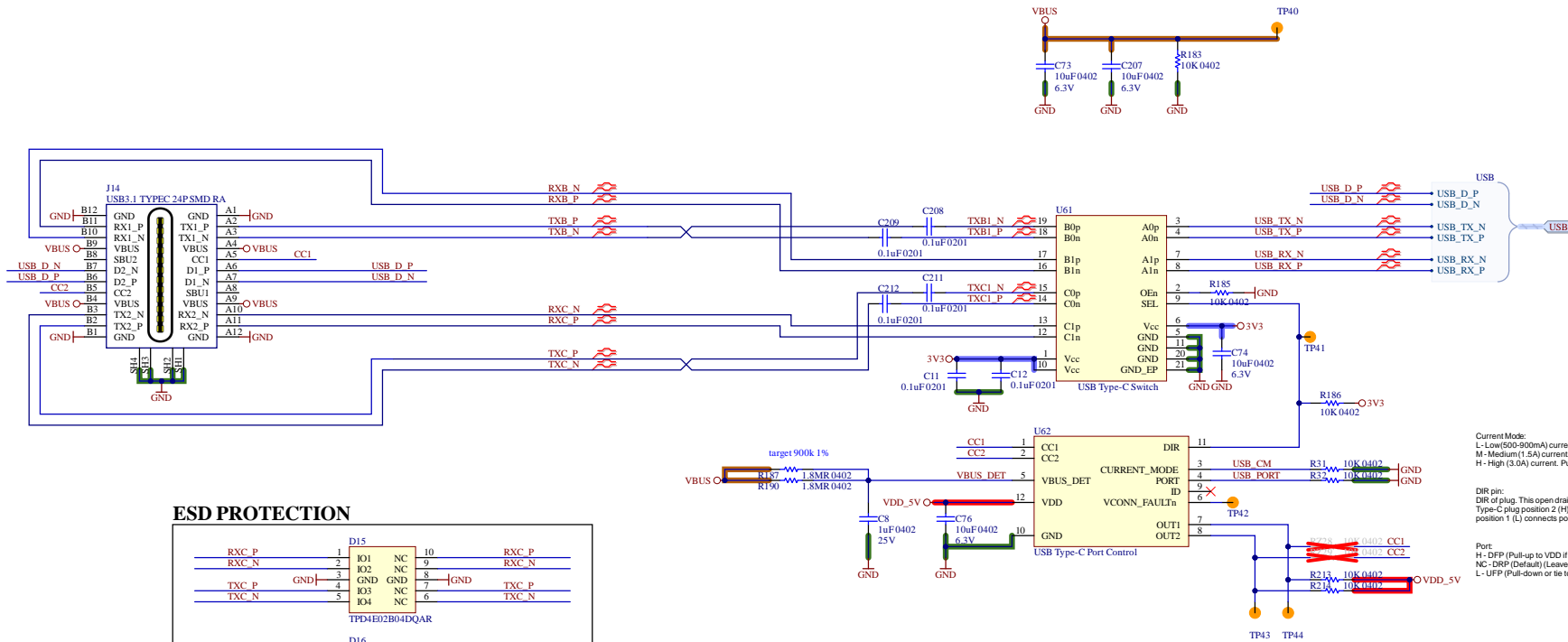
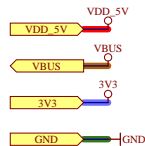
BOOT MODES



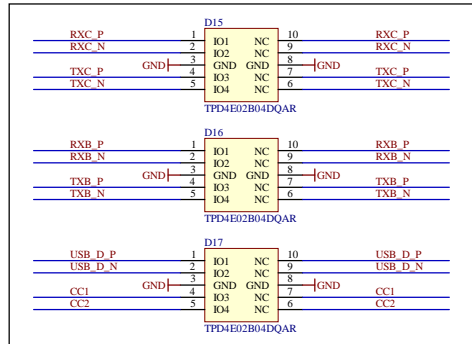
Note on silk that this button is boot select for BW2099.

2099 only accepts 1.8V input on GPIOs.

Title DD2090 4CAM FFC			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R2M1E2	LUX onis	
Date: 05/01/2022	Time: 16:11:32	Sheet 7 of 11		
Drawn by: <i>Dan Dunga</i>				



ESD PROTECTION

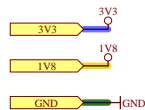


Current Mode:
 L- Low(500-900mA) current. (Default) Pull-down to GND or leave unconnected.
 M- Medium(1.5A) current. Pull-up to VDD with 500-kΩ resistor.
 H- High (3.0A) current. Pull-up to VDD with 10-kΩ resistor.

DIR pin:
 DIR of plug. This open drain output indicates the detected plug orientation:
 Type-C plug position 2 (H) connects Port A to Port C on U61; Type-C plug position 1 (L) connects port A to Port B on U61.

Port:
 H- DFP (Pull-up to VDD if DFP mode is desired)
 NC- DRP (Default) (Leave unconnected if DRP mode is desired)
 L- UFP (Pull-down or tie to GND if UFP mode is desired)

Title DD2090 4CAM FFC			<div>Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States</div> <div>LUXonis</div>
Size: Tabloid	Number: D0000190	Revision: R2M1E2	
Date: 05/01/2022	Time: 16:11:32	Sheet8 of 11	
Drawn by: David Malovich			



* For the switching sequence of power supply VDD and VDDIO it is mandatory that VDD is powered on and driven to the specified level before or at the same time as VDDIO is powered ON. Otherwise there are no limitations on the voltage levels of both pins relative to each other as long as they are used within the specified operating range.

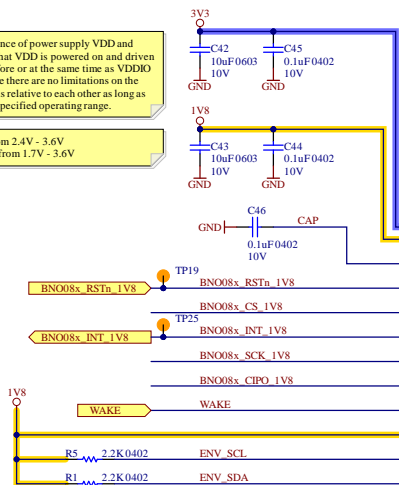
VDD can be powered from 2.4V - 3.6V
VDDIO can be powered from 1.7V - 3.6V

* NRST is the reset line for the BNO08X and can be either driven by the application processor or the board reset.

* The H_INTN pin is the application interrupt line that indicates the BNO08X requires attention. This should be tied to a GPIO with wake capability. The interrupt is active low. On the BNO085, if the host fails to respond to the assertion of H_INTN within approximately 10 ms, the BNO085 will timeout, deassert H_INTN and retry the operation.

* Pin 5 (PS1) and Pin 6 (PS0/WAKE) are the host interface protocol selection pins. For SPI selection, both pins must be high (from before reset until after the first assertion of H_INTN to select the SPI interface. Pin 5 may be tied to VDDIO. Pin 6 must be connected to a GPIO so that the WAKE functionality can be performed.

After reset the PS0/WAKE signal is used as a 'wake' signal taking the BNO08X out of sleep if the host wants to initiate communication with the BNO08X.



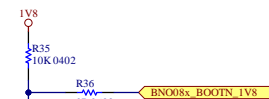
* The BNO08X supports environmental sensors (e.g. pressure sensors, ambient light sensors) on a secondary I2C interface. This interface should be pulled up via resistors regardless of the presence of the external sensor as the SW polls for sensors at reset.

* H_I2C ADDR:
SA0 = 0 = 0x4A (default)
SA0 = 1 = 0x4B

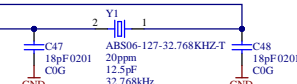
* BOOT MODE I2C:
SA0 = 0 = 0x28 (default)
SA0 = 1 = 0x49

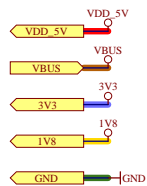
* BNO08x_BOOTN_1V8 is sampled at reset. If low, the BNO08x will enter bootloader mode.
* BNO08x_BOOTN_1V8 should be pulled high through a 10kR. To use the device firmware update (DFU) capability, it's recommended to connect Pin 4 to a GPIO pin on the external uC.

* CLKSEL0 = 0 selects external XTAL. Pin has internal pull down, but is connected to GND explicitly.

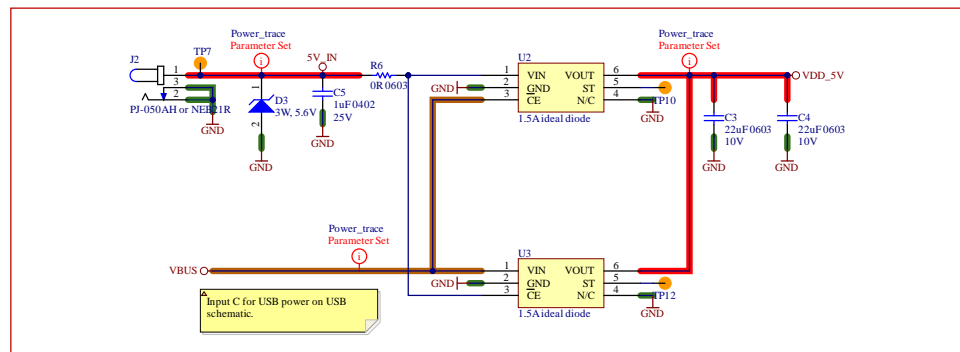


* The BNO08X can operate from an internal oscillator, an external 32.768 kHz clock or an external 32.768 kHz crystal. If an external clock is used it must be connected to pin 27. Hillcrest recommends a tolerance of 50ppm. If a crystal is used it must be connected across pins 26 and 27. Hillcrest recommends using a crystal with tolerance 50ppm with 12.5pF capacitor loading.

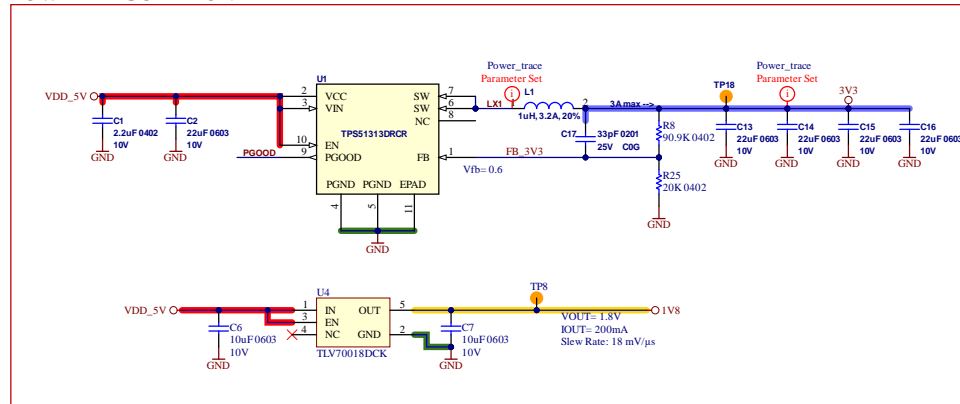


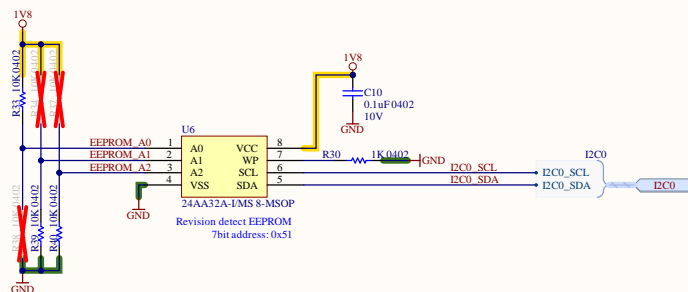


POWER INPUT - DIODE OR



POWER REGULATION





Title <i>DD2090 4CAM FFC</i>			<i>Luxonis Holding</i>	Cannot open file C:\Users\Brian.Luxonis\Documents\4CAM FFC
Size: <i>Tabloid</i>	Number: <i>D0000190</i>	Revision:	<i>1925 Harmony Park Drive</i>	
Date: <i>05/01/2022</i>	Time: <i>16:11:32</i>	Sheet <i>11</i> of <i>11</i>	<i>Westminster, CO</i>	
Drawn by: <i>Dan Dunga</i>			<i>80234</i>	
			<i>United States</i>	