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Project: BW1098FFC Current Revision: R2M1E2

BW1098FFC Revision History:

Date	Revision	Reason for Change	Changes Implemented
11/29/2019	R0M0E0 -> RIMIEI	Updating board to be aligned with Gen2 1099 boards, with SPI and uSD 2) Standardize FPC connector alignment with edge of board when connector is closed. 3) Existing design was not standardized on LuxonisMaster library format	1) Leveraged 10980BC connector schematic. Updated J1 Hirose connector to be the Gen2 version with proper pin names, Added three 10-pin headers (not populated) to break out AUX io, SPIio, and uSDio, same as on 10980BC. Updated schematic net names, harnesses and connections to accommodate the Gen2 changes. 2) Moved each of the FPC connectors toward the board edge so that they are flush with edge when closed. 3) Updated schematic and component information to align with LuxonisMaster library system
04/21/2021	RIMIE1 -> R2MIE2	Slow plug issue with USB type-C Update sync and reset circuitry	1. Added 1uF capacitor to VBUS, DET updated PCB and fabrication files 2) Updated sync and deleted reset circuitry

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