

A

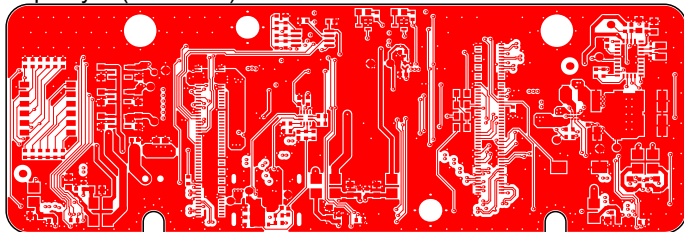
B

C

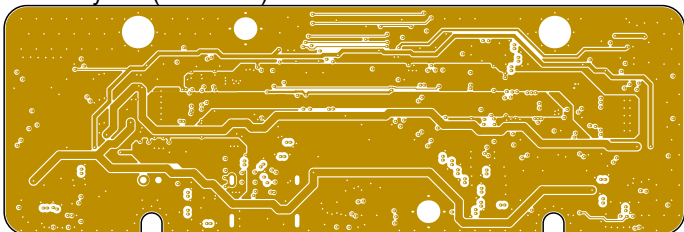
D

E

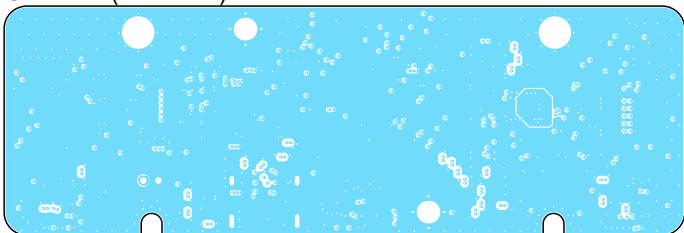
Top Layer (Scale 1:1)



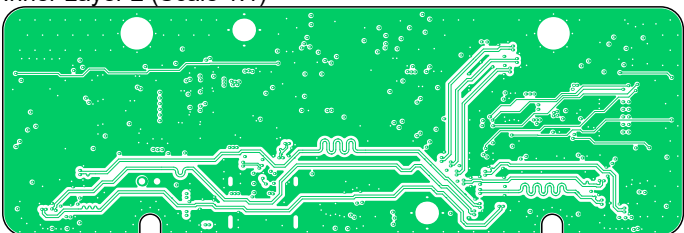
Inner Layer 1 (Scale 1:1)



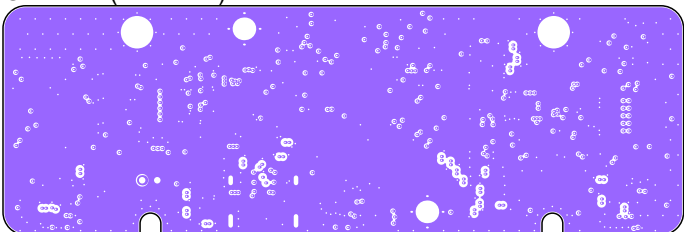
Ground 1 (Scale 1:1)



Inner Layer 2 (Scale 1:1)



Ground 2 (Scale 1:1)



FABRICATION NOTES:

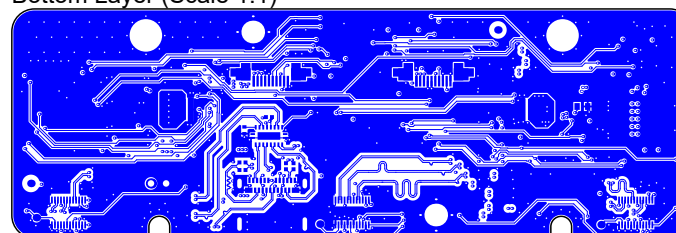
Fabricate per IPC-6011 & IPC-6012 CLASS 2
Inspect per IPC-A-600 CLASS 2
Test per IPC-TM-650

- * PCB has 6 copper layers
- * Copper thicknesses are finished and include base foil plus Cu plating on plated layers.
- * PCB thickness: See layer stack table
- * Min. trace width/clearance: 0.1mm/0.1mm
- * Min. hole drill/ring: 8mil/16mil
- * Soldermask gang relief is allowed for pads in same footprint, if footprint is NSMD.
- * Silkscreen, non-conductive epoxy ink, color: white
- * Solder mask color: black
- * Remove silkscreen as needed to prevent ink on any exposed copper
- * Surface finish: ENIG
- * Hole dimensions are finished size, +/-3mil
- * Linear board dimension tolerance: +/-10mil
- * Bow, twist, warp not to exceed 0.75% of greatest diagonal span
- * PCB shall be UL Recognized printed wiring board (ZPMV2), minimum flammability rating 94V-0
- * PCB shall be marked with fabricator company or trade name, UL mark, and date code using legend ink on secondary side
- * All PCBs shall be electrically tested for opens and shorts per gerber. Test marking shall be marked on secondary side.
- * GM1 must be used defining PCB outline border

Fabricator shall panelize the PCB using mouse bites and tab routing. V-scoring not allowed.

Controlled impedance differential pairs shall be within +/-10% for 100ohm targets, and +/-10% for 90ohm targets. See Sheet 3 for transmission line details and location of 90ohm differential pairs.

Bottom Layer (Scale 1:1)

Title: **NG2094**

Number: DXXXX

Revision: R0M0
E0

Date: 30/09/2021

Sheet: 1 of 3

Drawn by: Nejck Gostincar

LUXonjs

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Layer Stack Legend

Layer	Thickness	Type	Gerber	Df	Dk
Top Overlay		Legend	GTO		
Top Solder Mask	0.47mil(0.012mm)	Solder Mask	GTS		3,5
Top Layer	1.40mil(0.036mm)	Signal	GTL		
	3.03mil(0.077mm)	<i>Dielectric</i>		0,02	4
Inner Layer 1	0.98mil(0.025mm)	Signal	G1		
	2.95mil(0.075mm)	<i>Dielectric</i>		0,02	4
Ground 1	0.98mil(0.025mm)	Signal	G2		
	43.31mil(1.100mm)	<i>Dielectric</i>		0,02	4,3
Inner Layer 2	0.98mil(0.025mm)	Signal	G3		
	2.99mil(0.076mm)	<i>Dielectric</i>		0,02	4
Ground 2	0.98mil(0.025mm)	Signal	G4		
	3.03mil(0.077mm)	<i>Dielectric</i>		0,02	4
Bottom Layer	1.40mil(0.036mm)	Signal	GBL		
Bottom Solder Mask	0.47mil(0.012mm)	Solder Mask	GBS		3,5
Bottom Overlay		Legend	GBO		
Total thickness: 63.00mil(1.600mm)					

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
⊕	664	0.01in	Plated	
✕	8	0.01in	Plated	
□	4	0.02in	Plated	
◇	2	0.03in	Plated	
○	2	0.12in	Plated	
⊗	2	0.17in	Non-Plated	
682 Total				

Title: NG2094		
Number: DXXXX	Revision: R0M0 E0	
Date: 30/09/2021	Sheet: 2 of 3	PROPRIETARY AND CONFIDENTIAL
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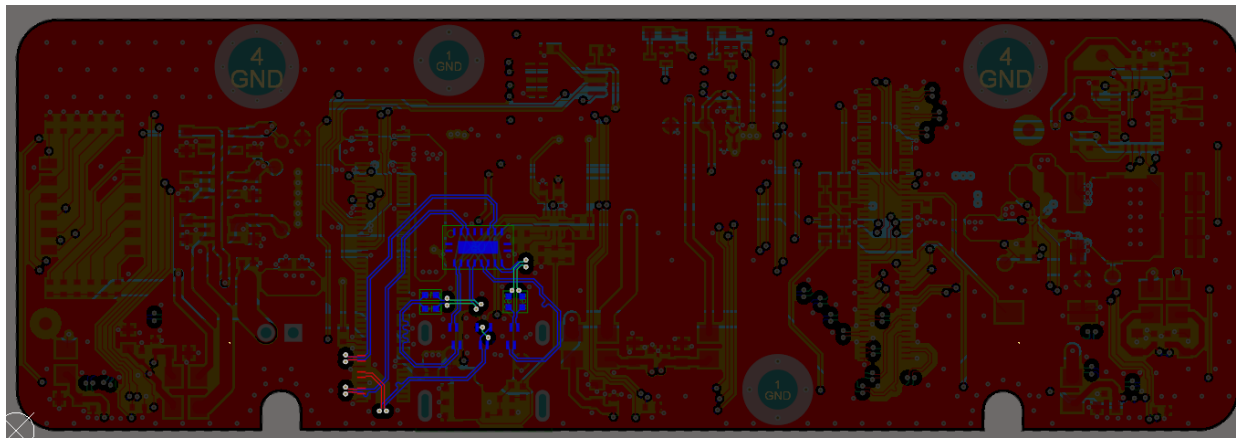
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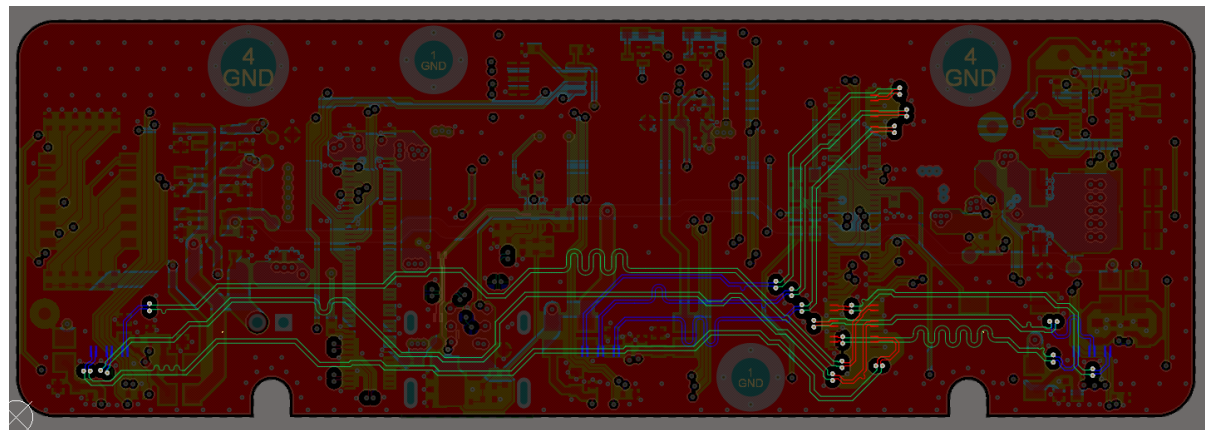
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90 OHM (+/-10%) DIFF PAIRS



100 OHM (+/-10%) DIFF PAIRS



Transmission Line Structure Table

Target Impedance	Trace layer	Lower Trace Width	Upper Trace Width	Gap	Reference layers
100	Top Layer	3.9mil(0.10mm)	3.9mil(0.10mm)	5.6mil(0.14mm)	Inner Layer 1
90	Top Layer	4.4mil(0.11mm)	4.4mil(0.11mm)	4.2mil(0.11mm)	Inner Layer 1
100	Inner Layer 2	3.3mil(0.08mm)	3.3mil(0.08mm)	9.7mil(0.25mm)	Ground 1,Ground 2
90	Inner Layer 2	3.6mil(0.09mm)	3.6mil(0.09mm)	5.8mil(0.15mm)	Ground 1,Ground 2
100	Bottom Layer	3.9mil(0.10mm)	3.9mil(0.10mm)	5.6mil(0.14mm)	Ground 2
90	Bottom Layer	4.4mil(0.11mm)	4.4mil(0.11mm)	4.1mil(0.11mm)	Ground 2

Title: **NG2094**

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