

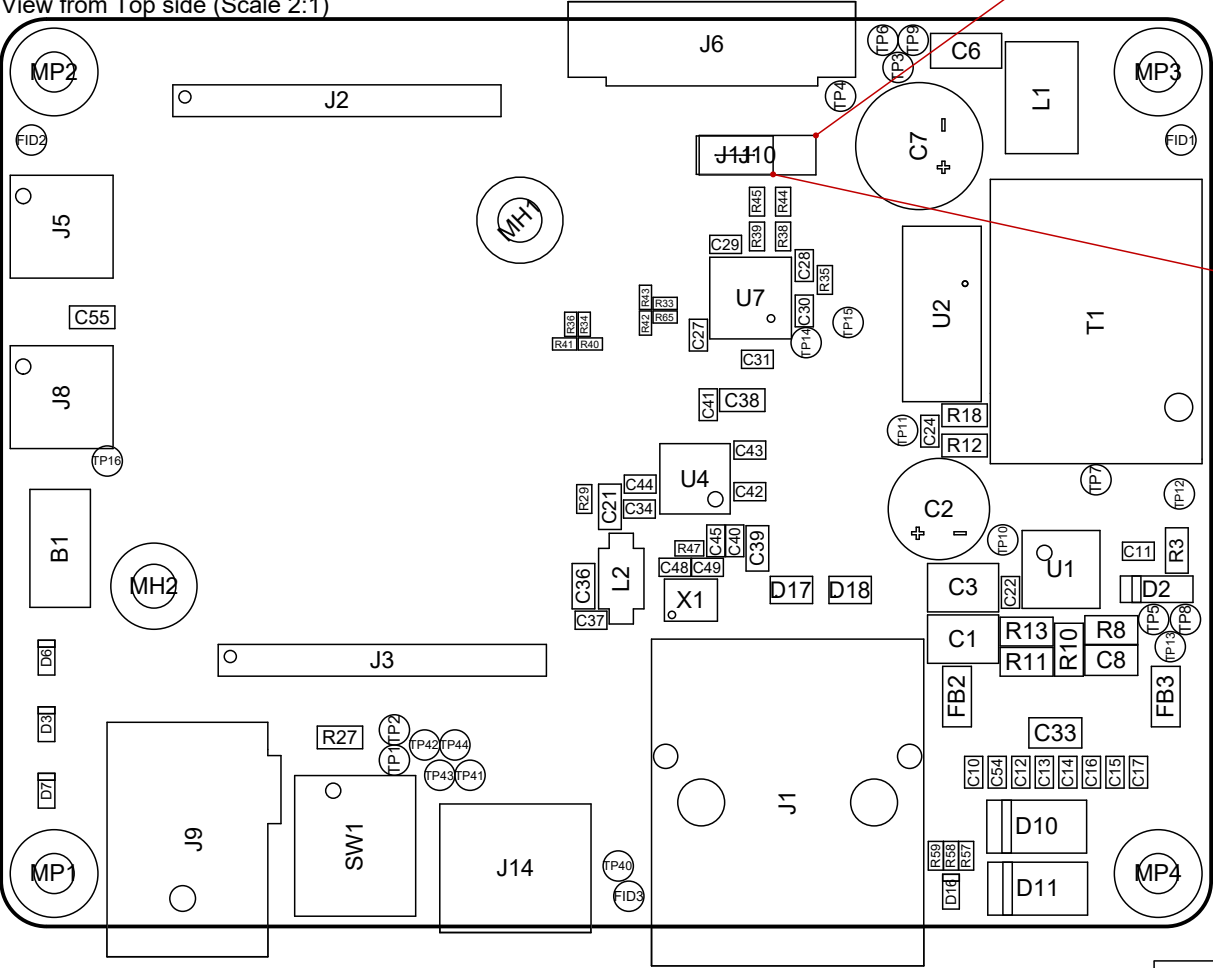
ASSEMBLY NOTES:

Assembly shall conform to IPC-A-610 Class 2.
Assembly shall conform with RoHS Directive 2011/65/EU.
Components shall be placed according to the associated CPL and BOM documents.
Lead-free SAC305 solder shall be used.
Reflow profile shall comply with J-STD-20C Lead-Free Reflow Profile.
BGA components shall be 100% checked with x-ray for solder bridges after reflow.
Finished assemblies shall be removed from panel prior to delivery.

Notes:

- 1 J10 is 3 pin header
- 2 J11 is header jumper, to be placed on J10, pins 2,3.

View from Top side (Scale 2:1)



A

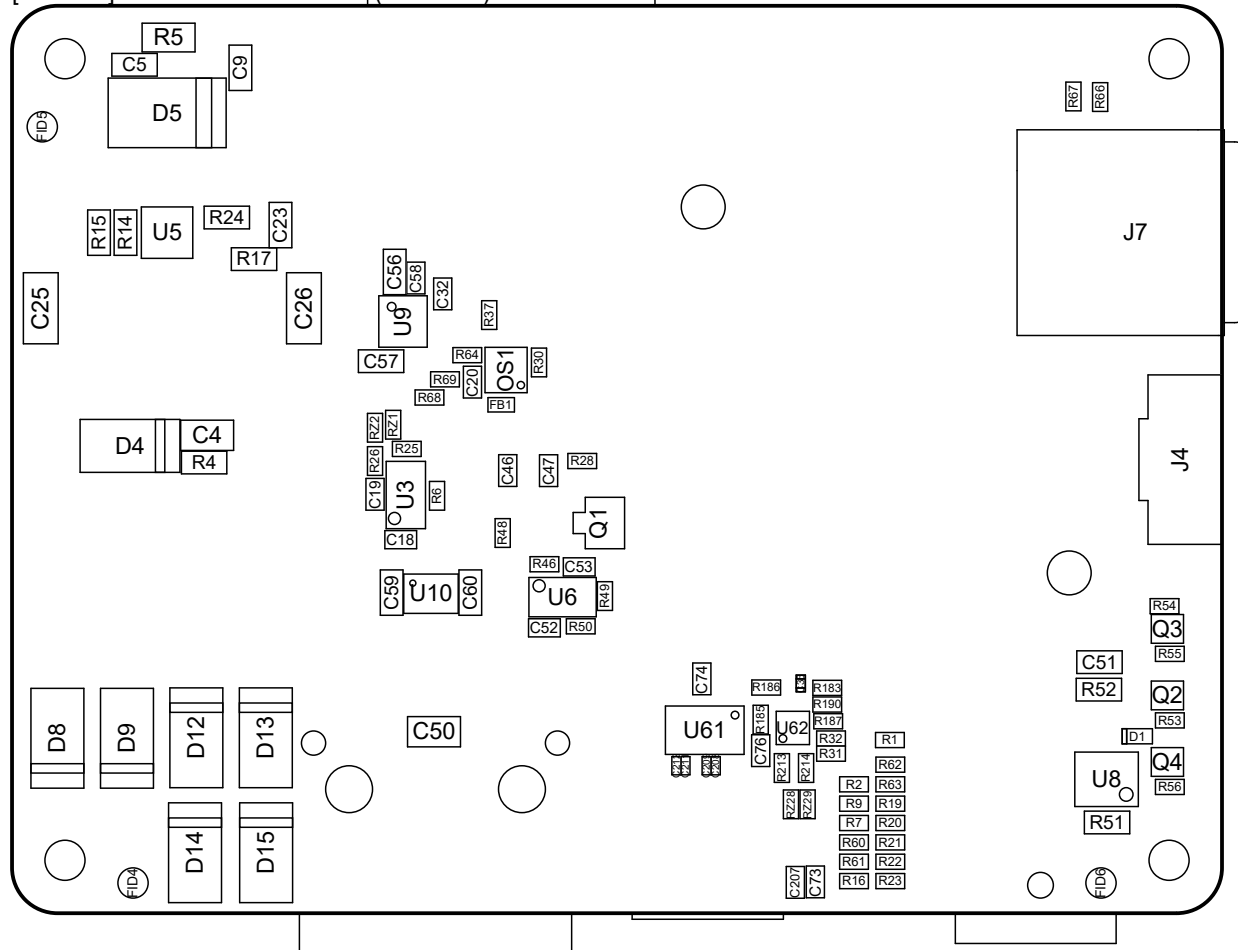
B

C

D

E

[Variant:]: View from Bottom side (Scale 2:1)

Title: **BW2098POE**

Number: D2098000

Revision: =revision

Date: 21/04/2021

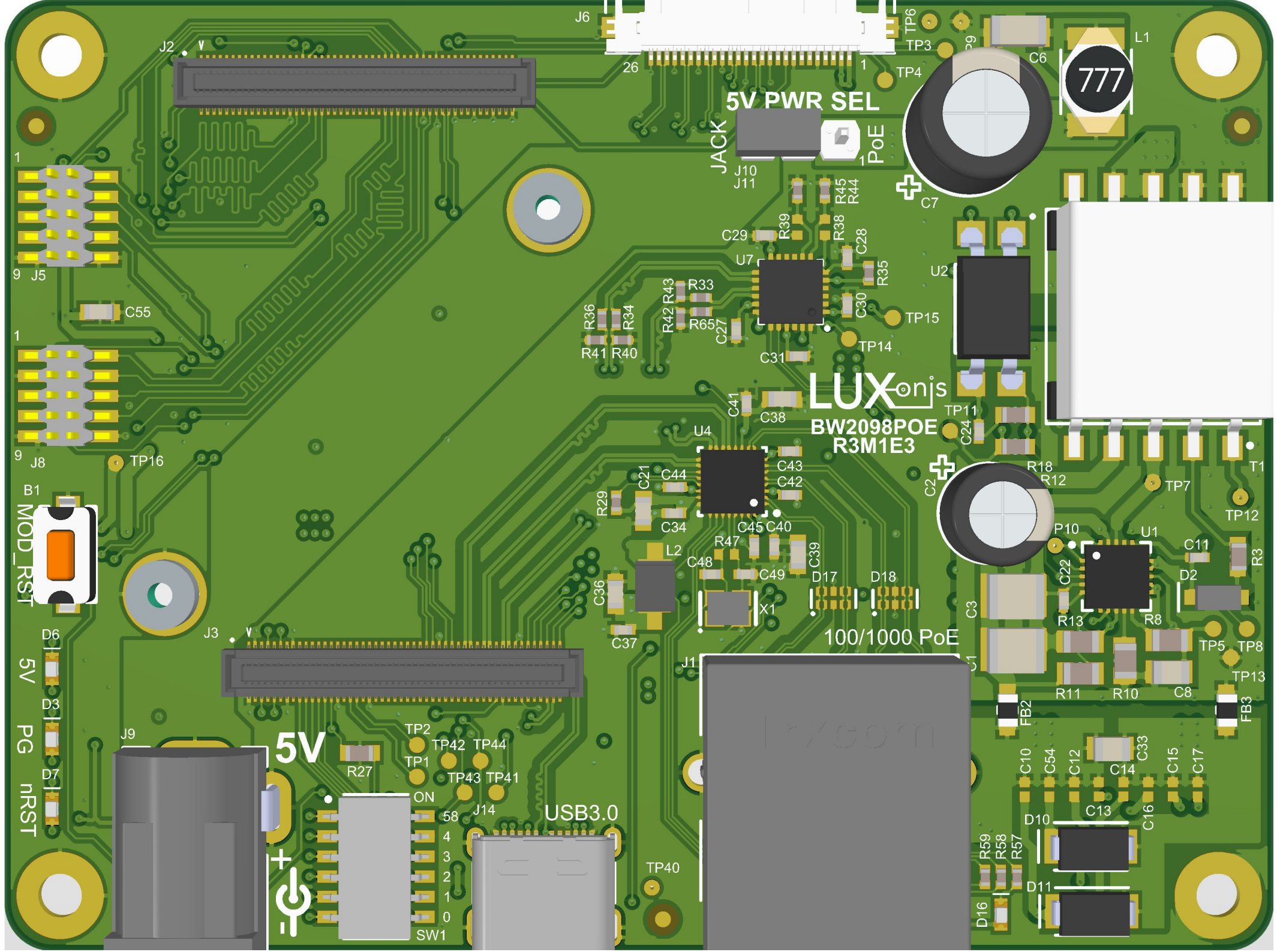
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LUXonjs

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BW2098POE
R3M1E3

J5 Header

MOD_nRST	I2C3_SCL
AUX_IO_IRQ	I2C3_SDA
2485_PGOOD	UART_RX
NC	UART_TX
GND	GND

J8 Header

SPI_SCK	SPI_SIO0
GND	SPI_SIO1
SPI_SS_0	SPI_SIO2
GPIO6	SPI_SIO3
GND	GND

SDIO

MODULE_JTAG

