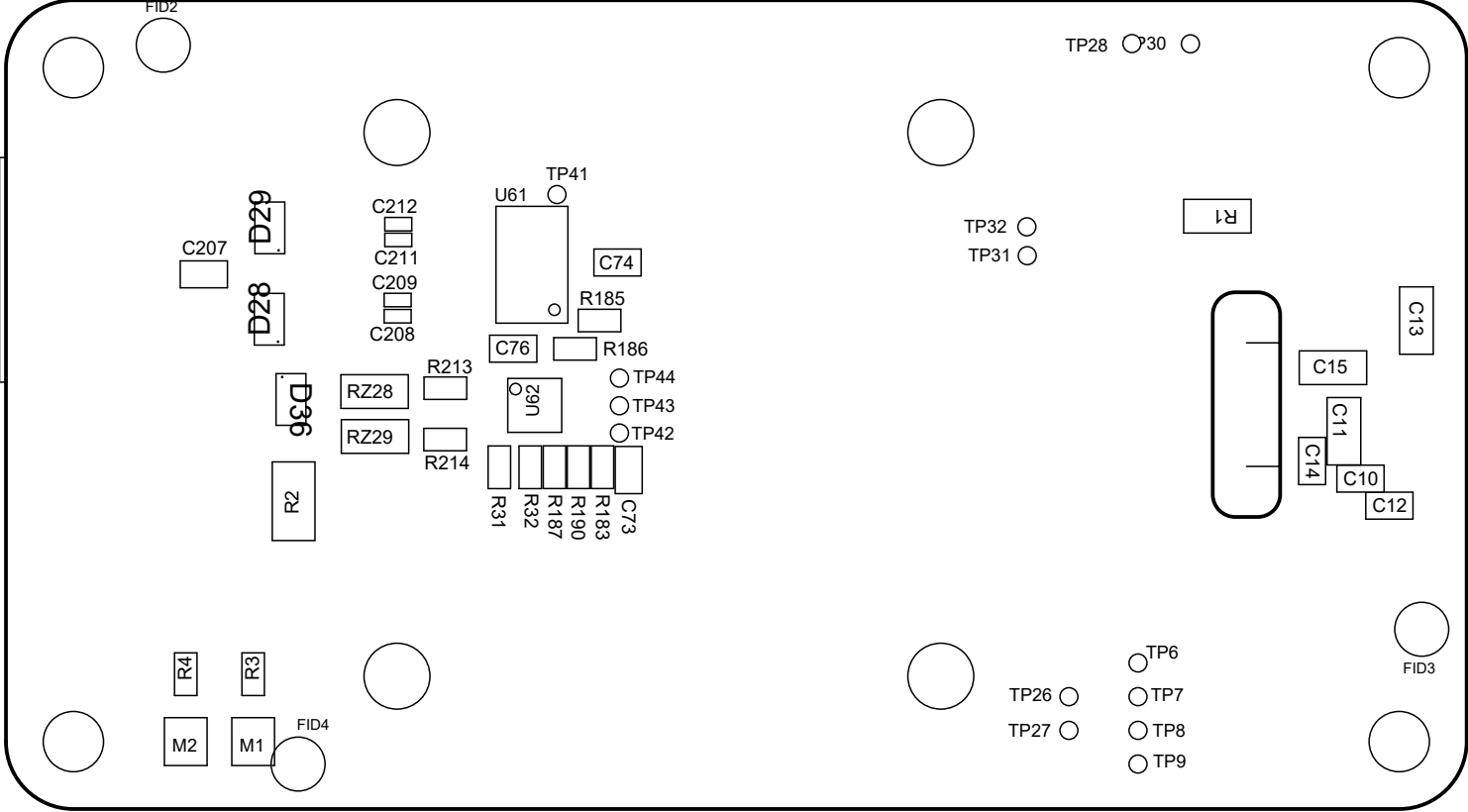


ASSEMBLY NOTES:

Assembly shall conform to IPC-A-610 Class 2.
Assembly shall conform with RoHS Directive 2011/65/EU.
Components shall be placed according to the associated CPL and BOM documents.
Lead-free SAC305 solder shall be used.
Reflow profile shall comply with J-STD-20C Lead-Free Reflow Profile.
BGA components shall be 100% checked with x-ray for solder bridges after reflow.
Finished assemblies shall be removed from panel prior to delivery.

[Variant: [All Variants]]: View from Top side (Scale 3)



Title: **BK1096**

Number: D000BK1096 Revision: R0M0
E0

Date: 10/3/2019 Sheet: 1 of 2

Drawn by: Blaž Kvas

LUXonjs

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A

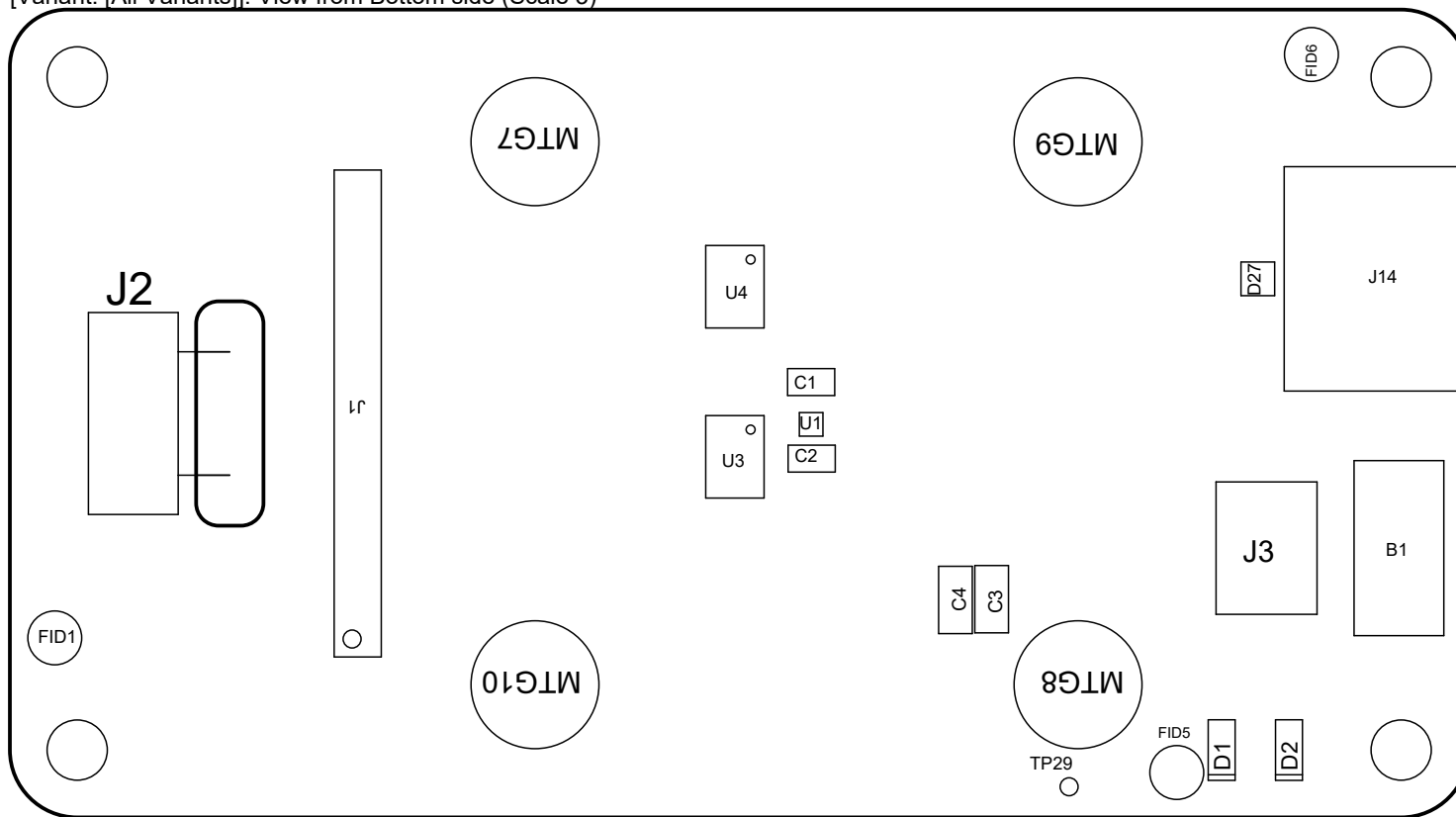
B

C

D

E

[Variant: [All Variants]]: View from Bottom side (Scale 3)



Title: **BK1096**

Number: D000BK1096 Revision: R0M0
E0

Date: 10/3/2019 Sheet: 2 of 2

Drawn by: Blaž Kvas

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A

B

C

D

E

LUXonjs

BK1096
ROM0E0

WAKEUP

PGOOD

GND

D29

D28

D36

VBUS

VDD_5V

GND

GND

I2C_SDA

I2C_SCL

1V8

1V05

FB3

FB1

FB2

R1

C15

C11

C14

C13

C10

C12

U61

SEL

C74

R185

R186

OUT1

OUT2

VCONN_FAULTn

C76

R213

RZ28

RZ29

R214

R31

R32

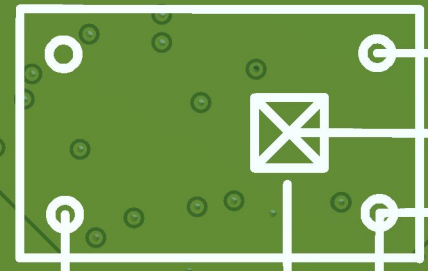
R187

R190

R183

C73

U62



15mm

15mm

44.9mm

14.1mm

UART_RX

A_C_PWM

A_AUX_IO1

A_AUX_IO2

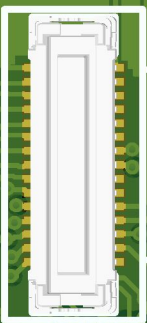
UART_TX

COM_AUX_IO2

GND

LUXonjs
BK1096
ROM0E0

J2



J1

C8

U4

C9

C1

C6

U1

C2

C7

C17

R5

L1

C16

U5

R6

C4

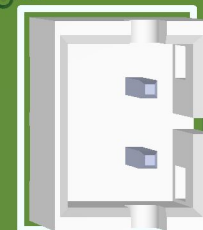
C3

J14

D27

5V UFP USB 3.1

J3



5V FAN

B1



RESET

OUT1

OUT2



RST