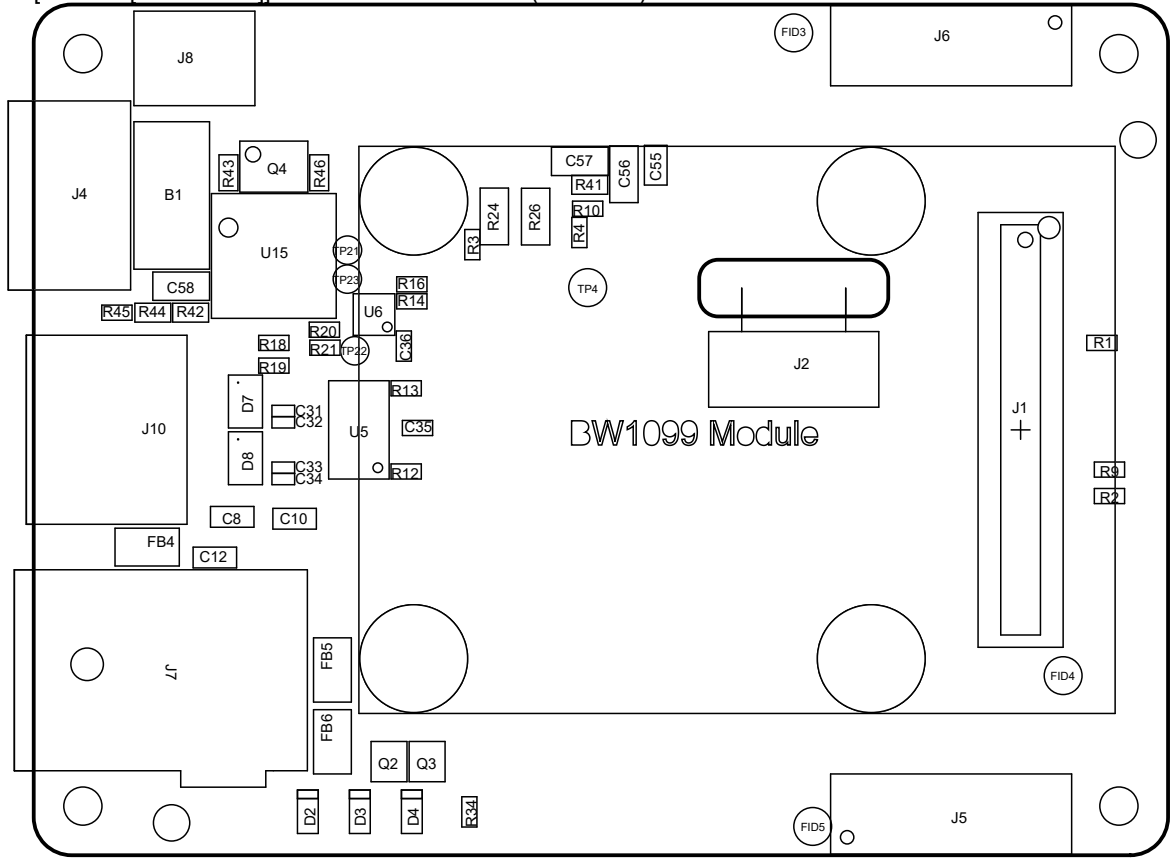



[Variant: [All Variants]]: View from Bottom side (Scale 2.5)



Title: DM1092		
Number: DXXXX	Revision: R0M0 E0	
Date: 19/01/2021	Sheet: 2 of 3	PROPRIETARY AND CONFIDENTIAL
Drawn by: David Malovrh		THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF LUXONIS HOLDING CORPORATION. ANY REPRODUCTION IN PART OR AS A WHOLE WITHOUT THE WRITTEN PERMISSION IS PROHIBITED.

A

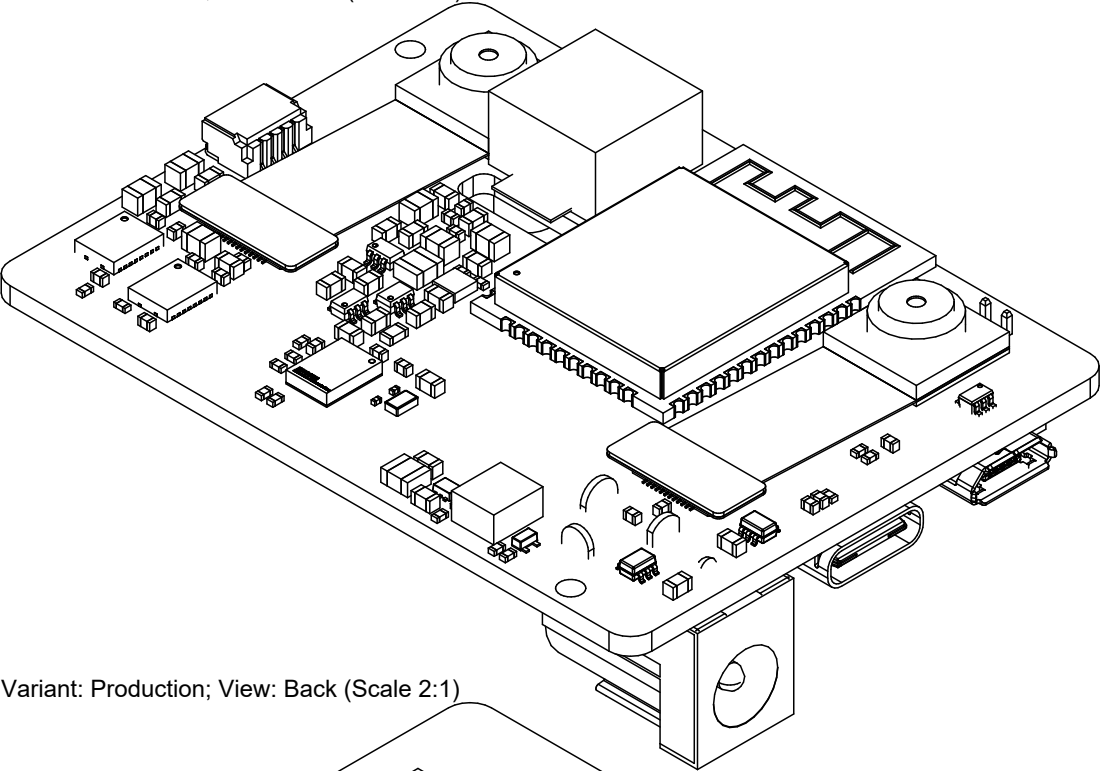
B

C

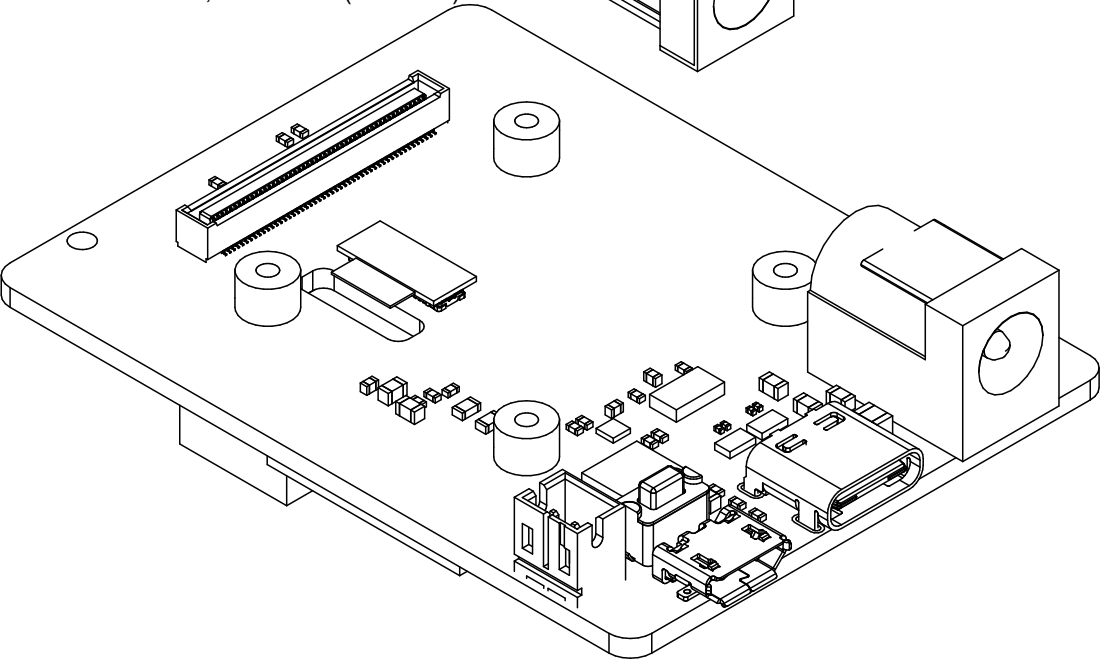
D

E

Variant: Production; View: Front (Scale 2:1)

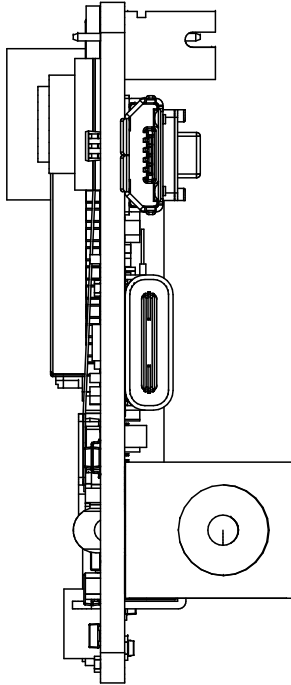
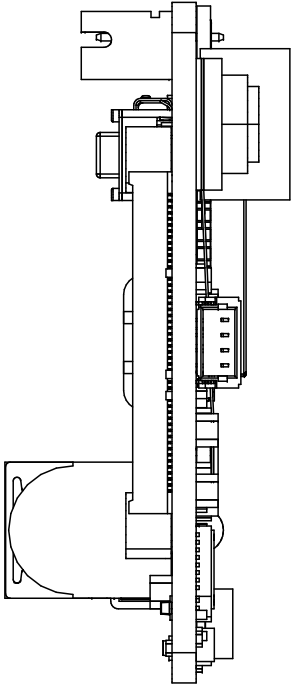


Variant: Production; View: Back (Scale 2:1)




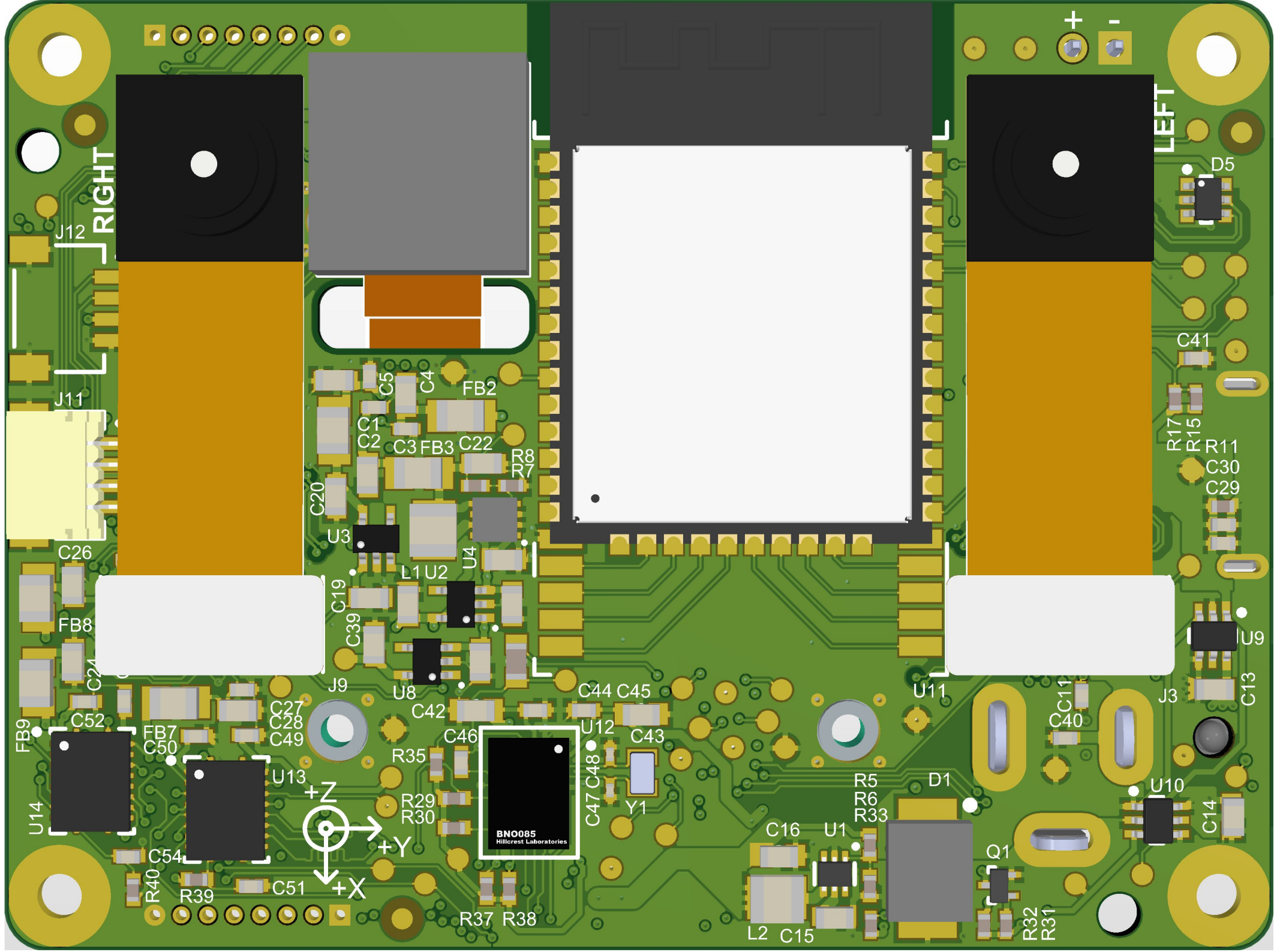
These drawings shown with "Production" variant.

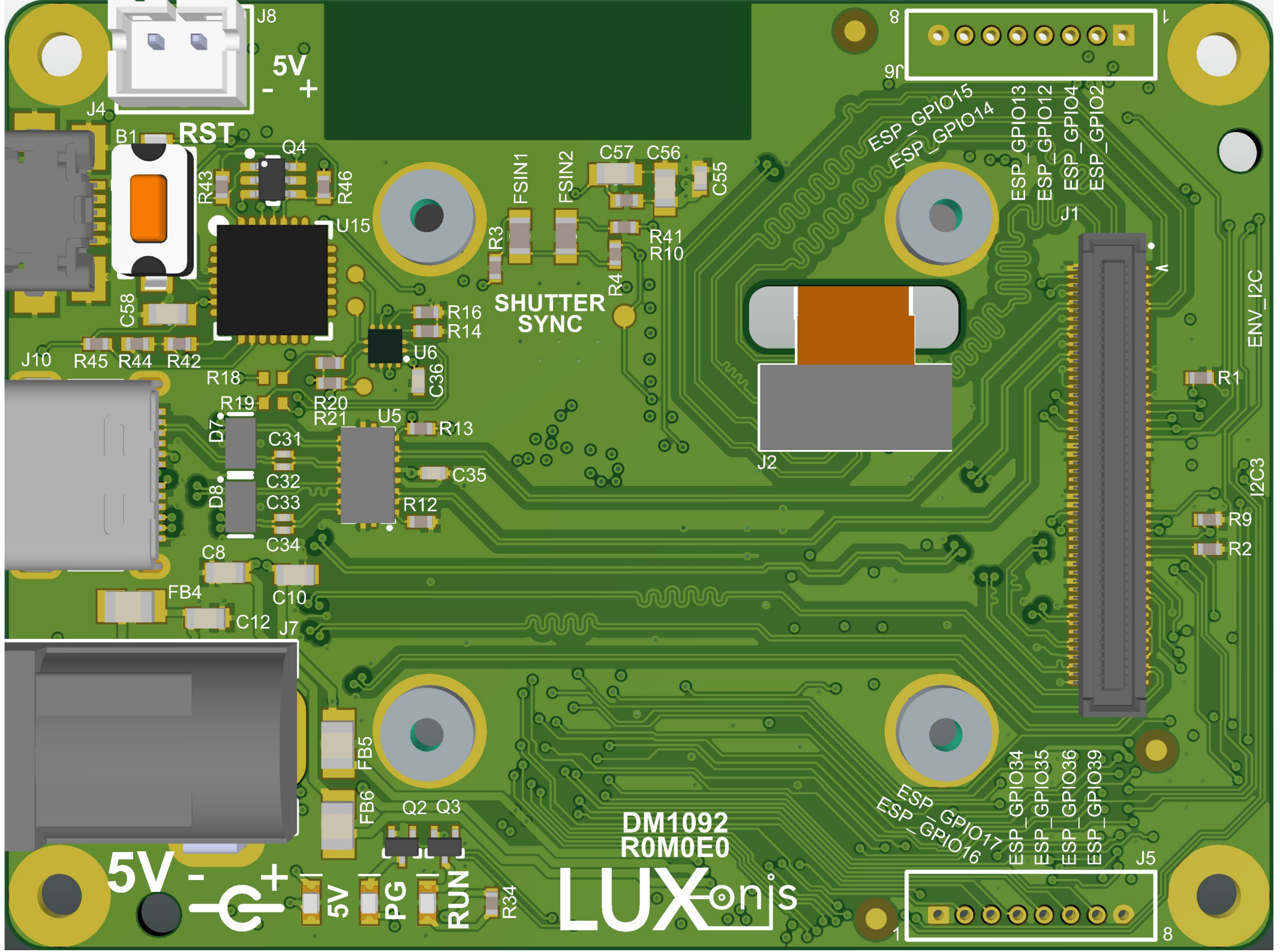
Variant: Production; View: Left (Scale 2:1)



Variant: Production; View: Right (Scale 2:1)

Title: DM1092		
Number: DXXXX	Revision: R0M0 E0	
Date: 19/01/2021	Sheet: 3 of 3	PROPRIETARY AND CONFIDENTIAL
Drawn by: David Malovrh		THE INFORMATION CONTAINED IN THIS DRAWING IS THE SOLE PROPERTY OF LUXONIS HOLDING CORPORATION. ANY REPRODUCTION IN PART OR AS A WHOLE WITHOUT THE WRITTEN PERMISSION IS PROHIBITED.





J8
5V
- +

B1 RST

SHUTTER
SYNC

DM1092
ROM0E0

LUXon's

ESP_GPIO15
ESP_GPIO14
ESP_GPIO13
ESP_GPIO12
ESP_GPIO4
ESP_GPIO2

ESP_GPIO34
ESP_GPIO17
ESP_GPIO16
ESP_GPIO35
ESP_GPIO36
ESP_GPIO39

ENV_I2C

I2C3