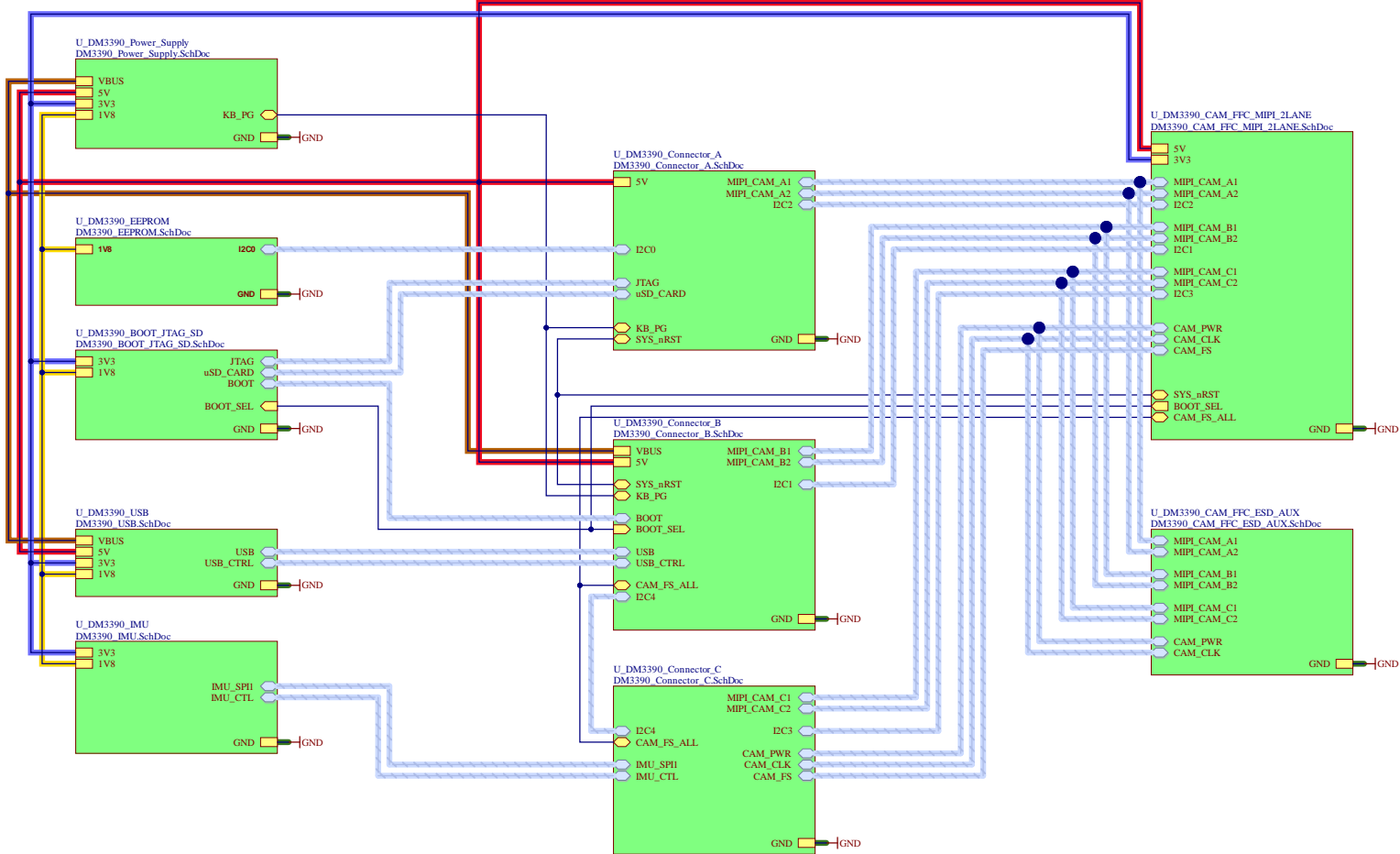



U_Project_Information
Project_Information.SchDoc



SMT mounting standoffs for module
M2, 3MM steel spacer on M2.5 mounting pad

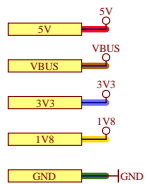


Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R0M0E0		
Date: 16/05/2022	Time: 14:45:28	Sheet 1 of 12		
Drawn by: David Malovrh				

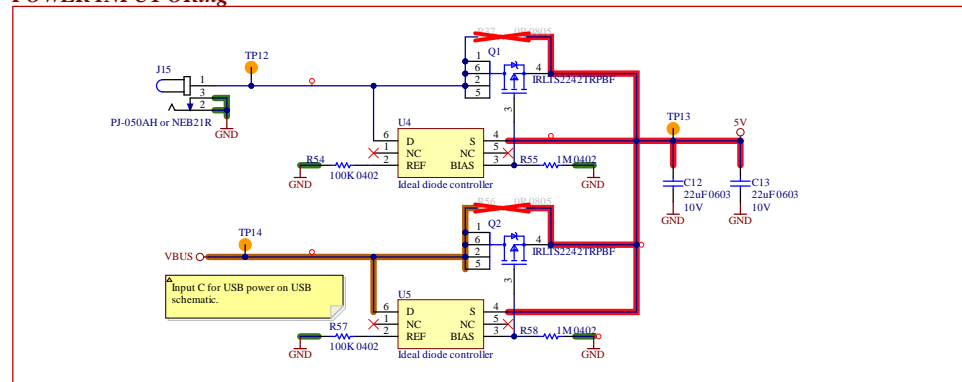
Project: DM3390
Current Revision: R0M0E0
Assembly variant: Production

DM3390 **Revision History:**

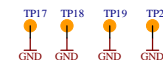
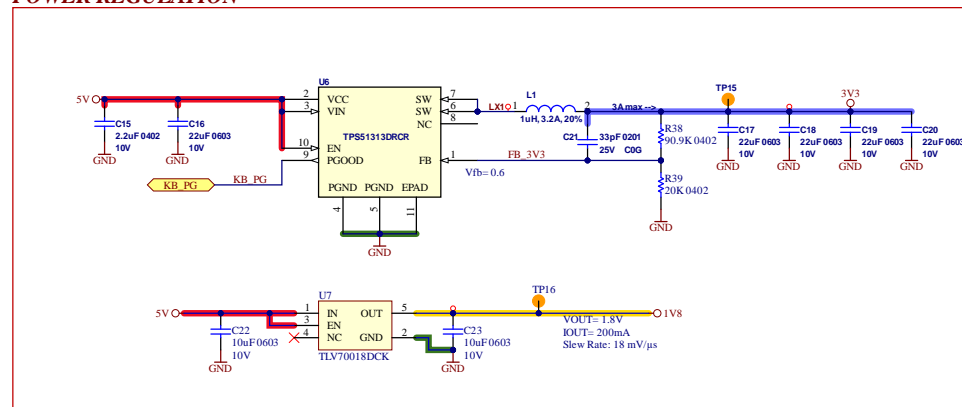
Date	Revision	Reason for Change	Changes Implemented
16 May 2022	R0M0E0		Initial release



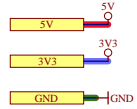
POWER INPUT ORing



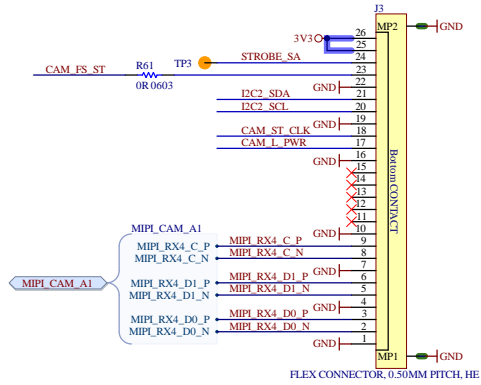
POWER REGULATION



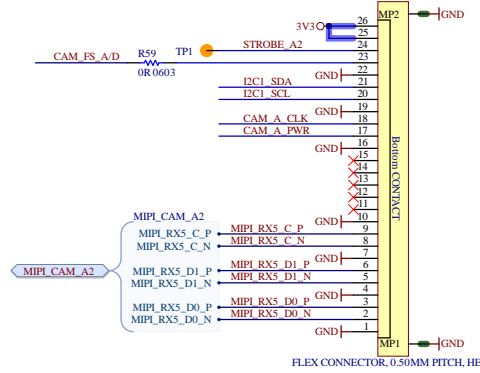
Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R0M0E0	<div>LUXonis</div>	
Date: 16/05/2022	Time: 14:45:28	Sheet 3 of 12		
Drawn by: David Malovrh				



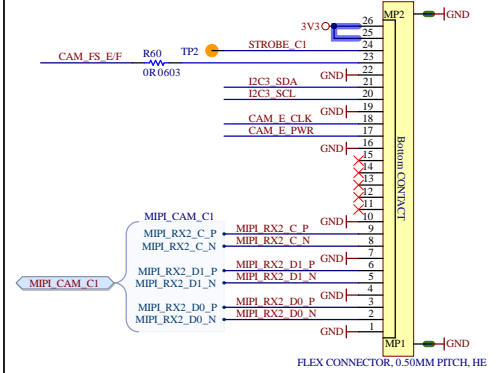
STEREO CAMERA PAIR A



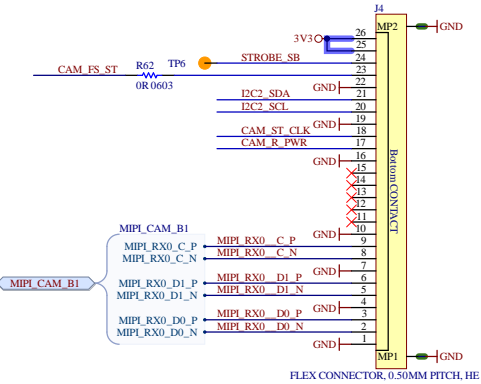
CAMERA A2



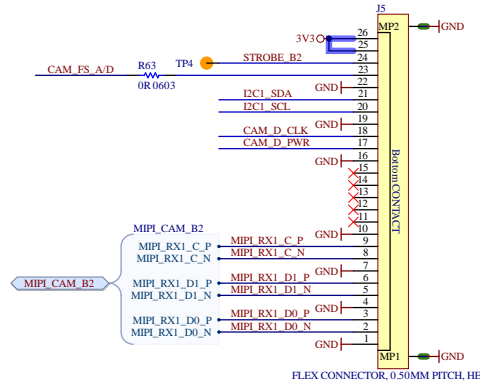
CAMERA C1



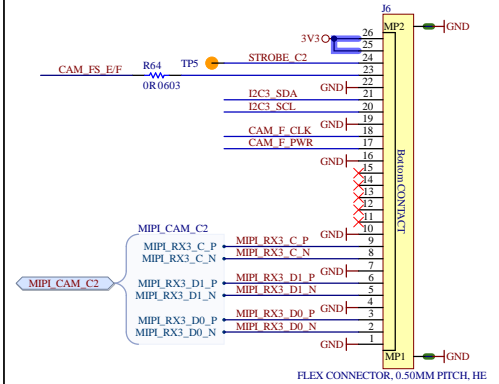
STEREO CAMERA PAIR B



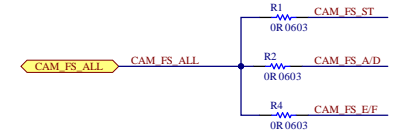
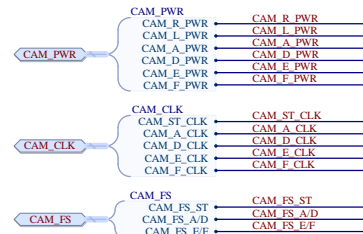
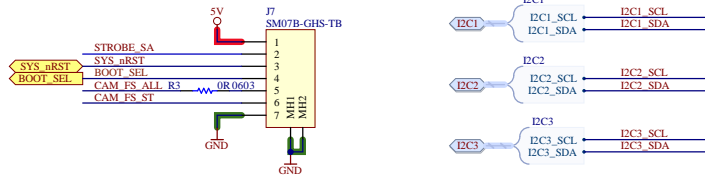
CAMERA B2




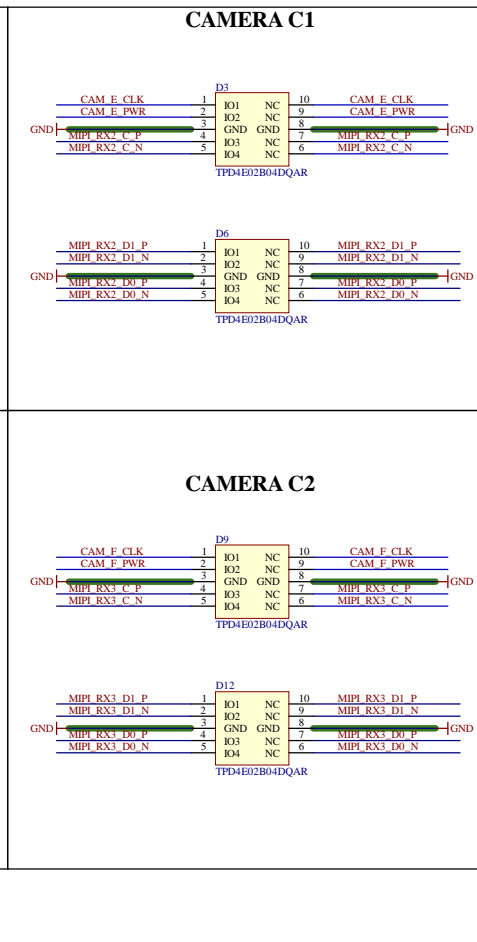
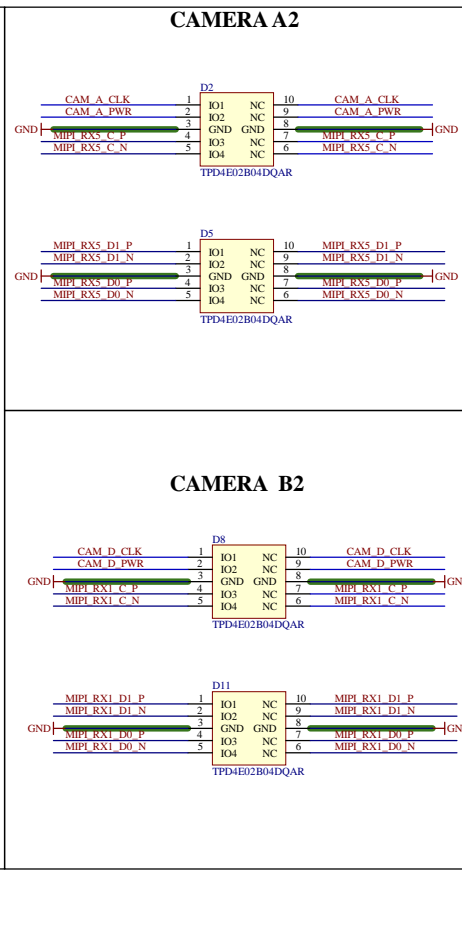
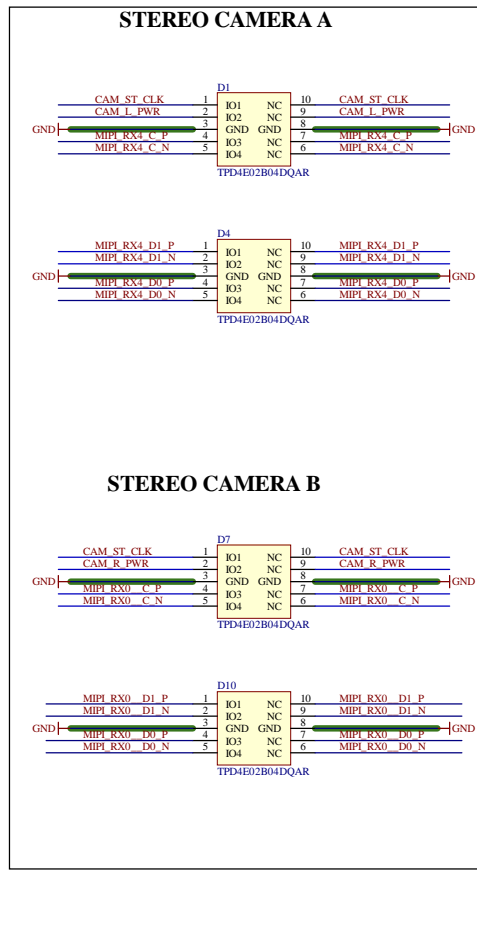
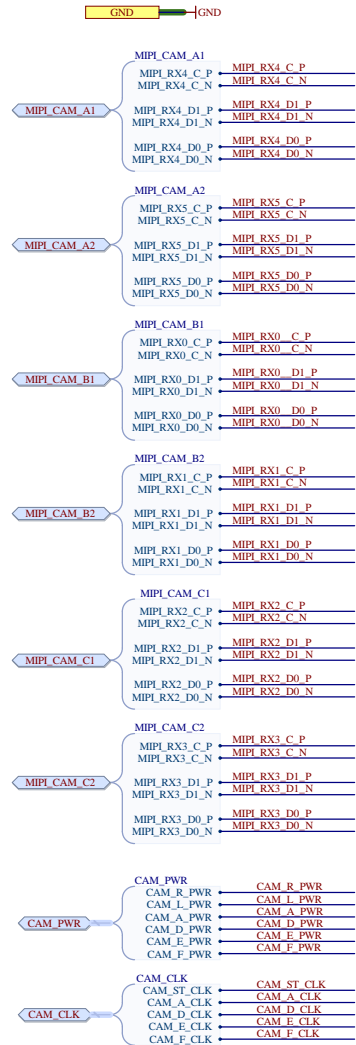
CAMERA C2

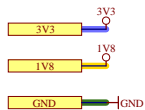


Mark "PSRBS" on PCB

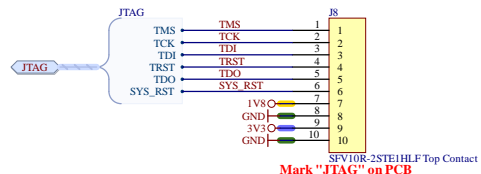


Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234	
Size: Tabloid	Number: D0000190	Revision: R0M0E0		
Date: 16/05/2022	Time: 14:45:28	Sheet 4 of 12	United States	
Drawn by: David Malovrh				





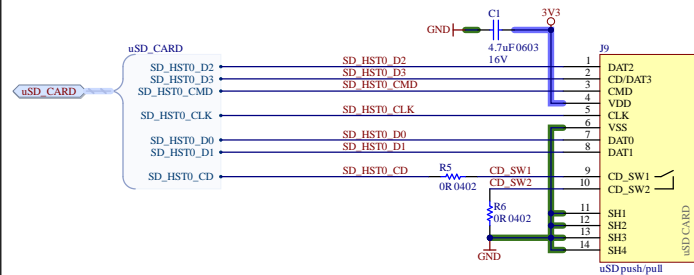
JTAG



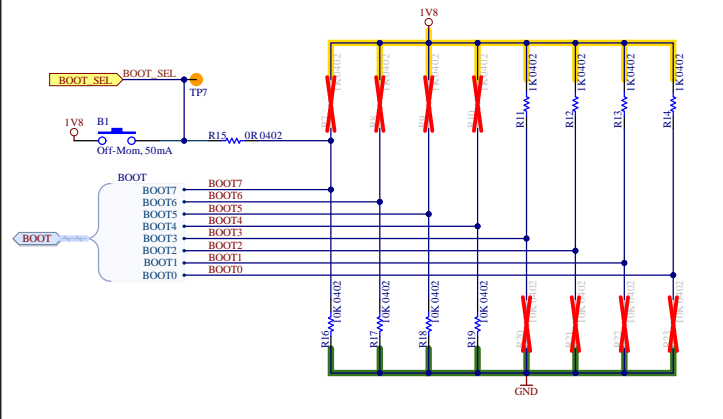
SFV10R-2STE1HLF Top Contact
Mark "JTAG" on PCB

JTAG FFC configuration (10-pin, 0.5mm pitch).

3.3V GPIO (uSD)



BOOT MODES



Note on silk that this button is boot select for BW2099.

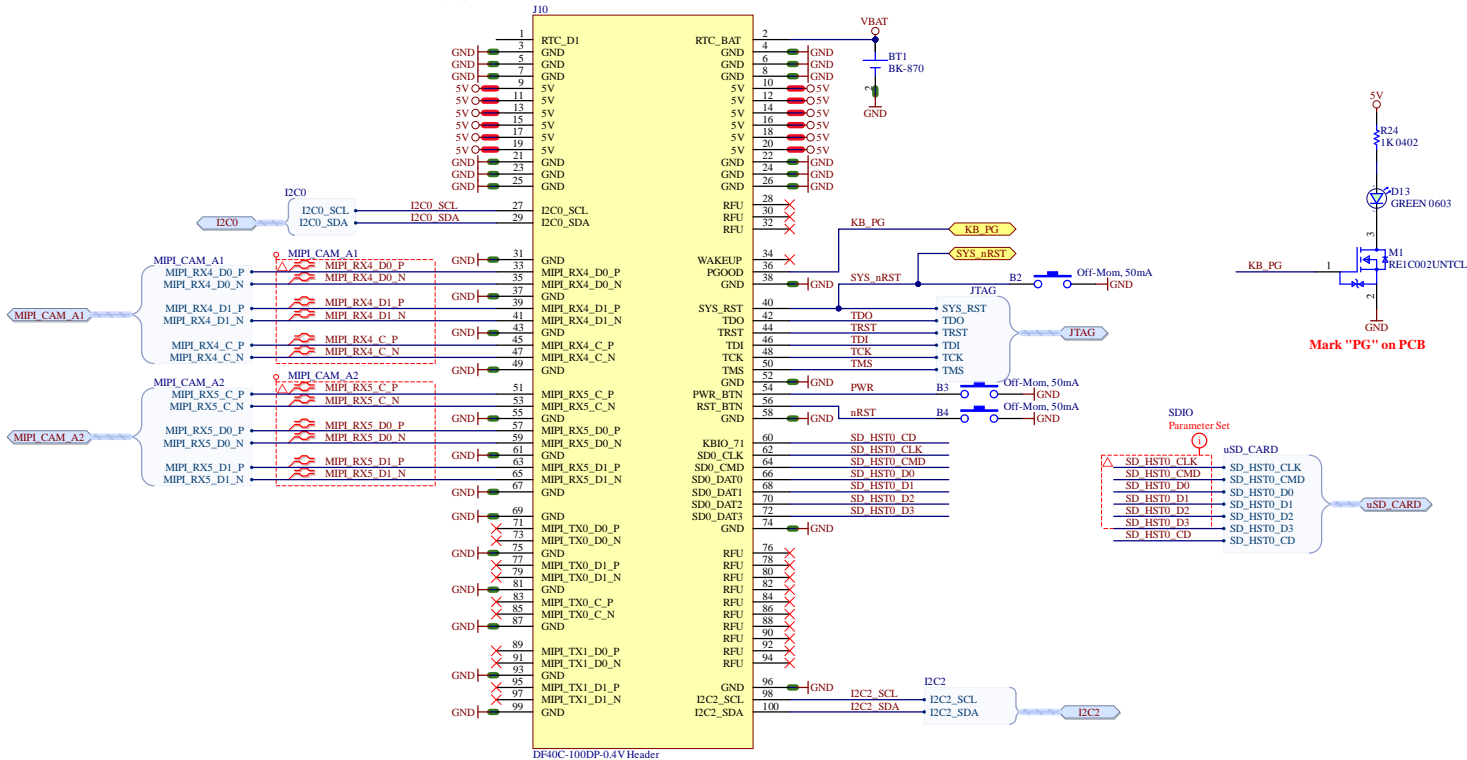
2099 only accepts 1.8V input on GPIOs.

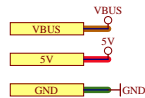
Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R0M0E0		
Date: 16/05/2022	Time: 14:45:29	Sheet 6 of 12		
Drawn by: David Malovrh				

5V drawn by BW2099 can be up to 1A
VBUS on BW2099 is only sense input

DM3399 CONNECTOR A

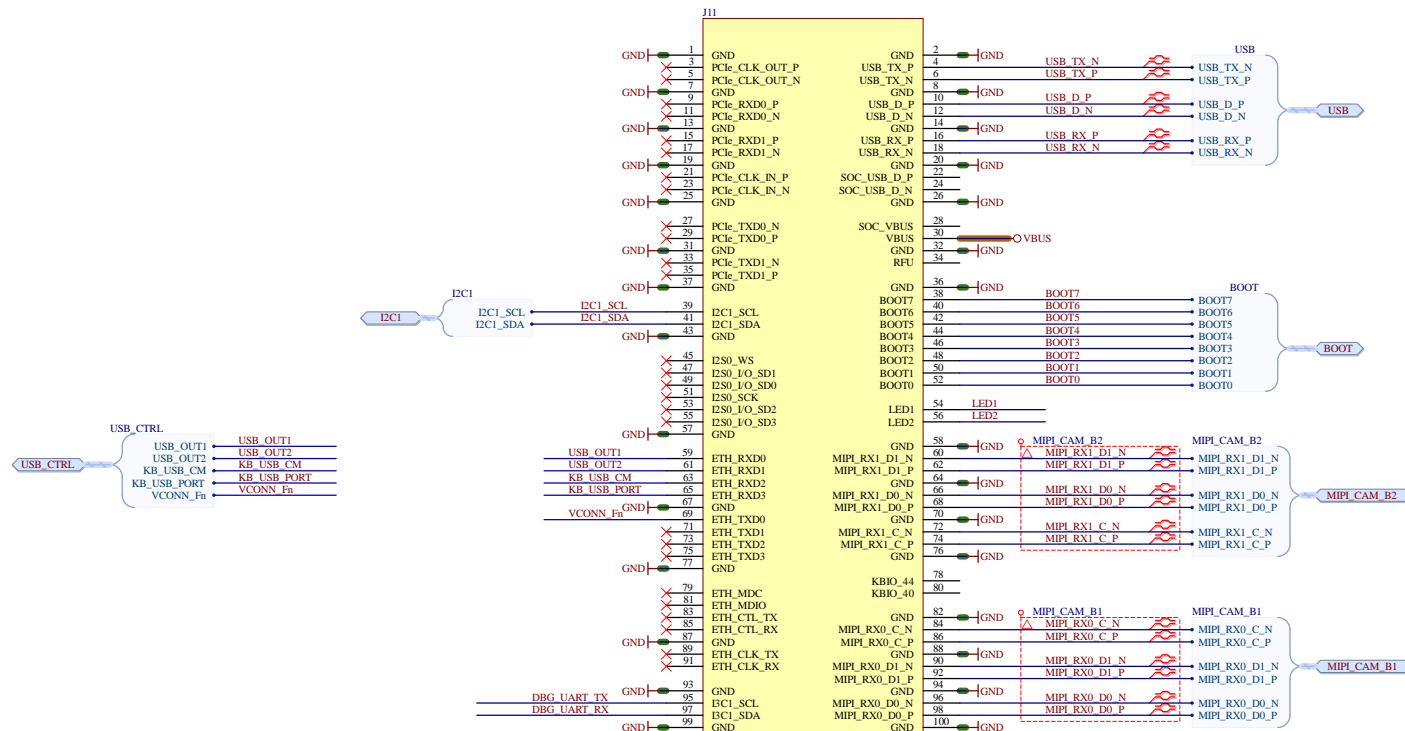
DPHYv1.2
Max 2.1 Gbps / lane
MIPI Lanes:





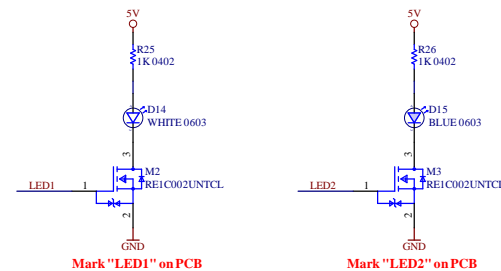
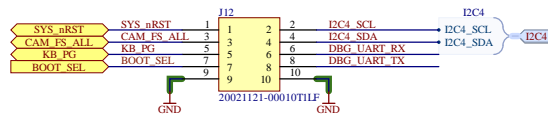
VDD_5V drawn by BW2099 can be up to 1A
VBUS on BW2099 is only sense input

DM3399 CONNECTOR B




DF40C-100DP-0.4V Header

Mark "AUX" on PCB



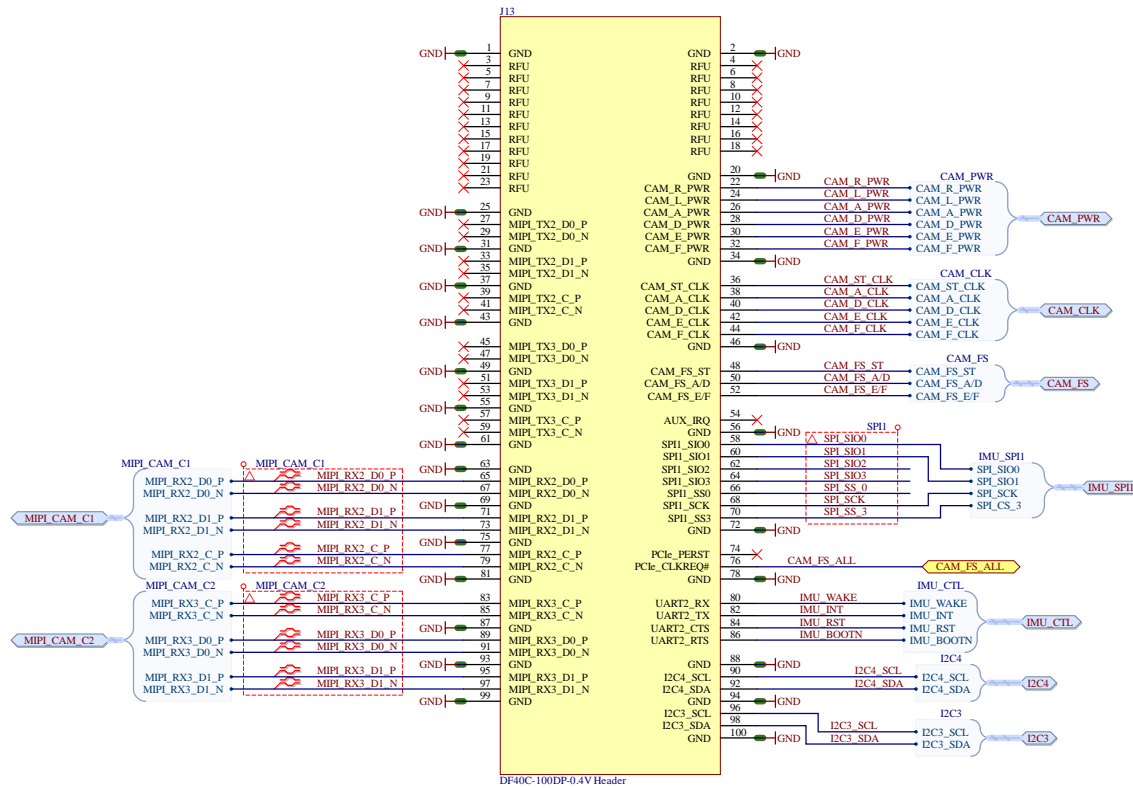
Mark "LED1" on PCB

Mark "LED2" on PCB

Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R0M0E0		
Date: 16/05/2022	Time: 14:45:29	Sheet 8 of 12		
Drawn by: David Malovrh				

^A 5V drawn by BW2099 can be up to 1A
VBUS on BW2099 is only sense input

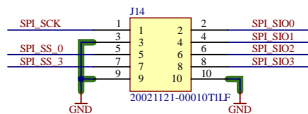
DM3399 CONNECTOR C

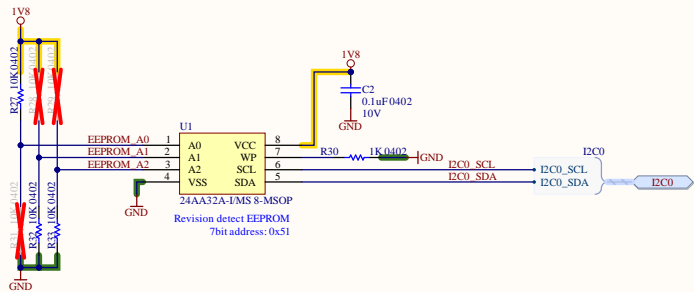
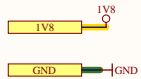


^A IMU SPI is length matched between connector and IMU. QSPI bus is length matched between connector and header pads.

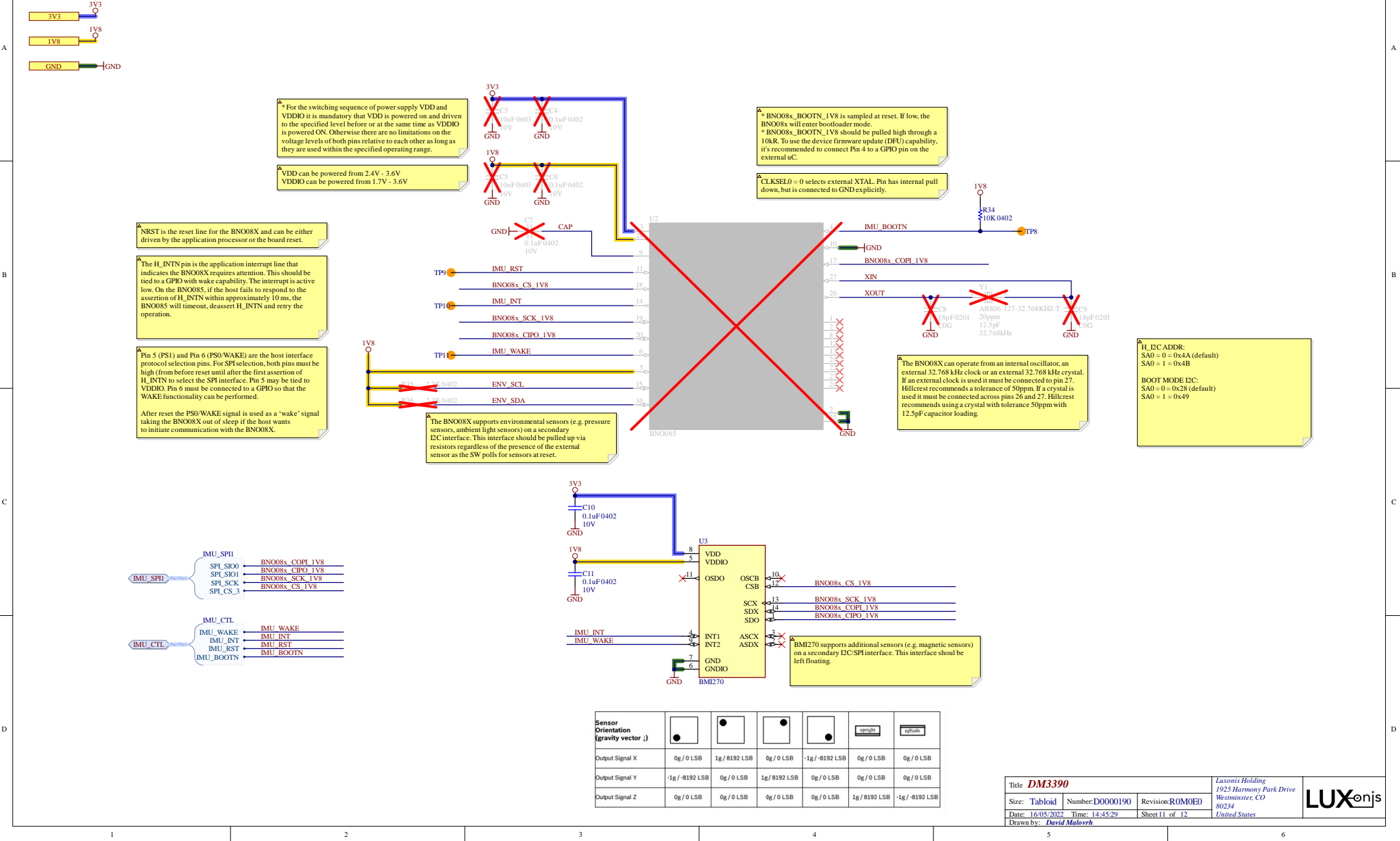
^A U5 is used to connect the FSIN pins from CAM A and D (FSIN_4LANE) to the FSIN pins of CAM B and C (COM_AUX_IO2). Drive FSIN_MODE_SELECT high to connect all FSIN signals together.

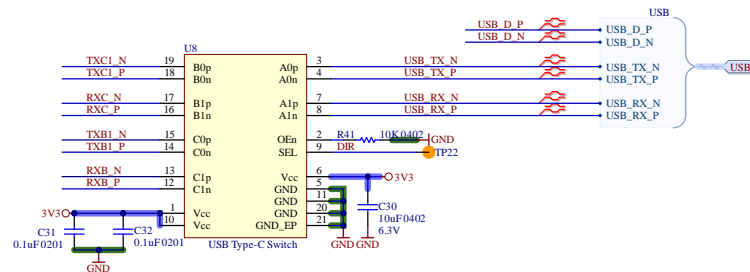
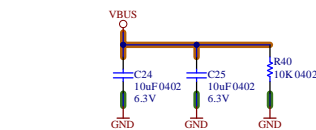
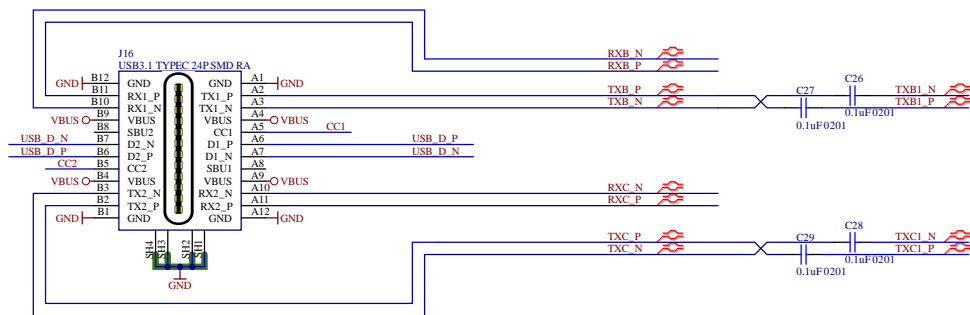
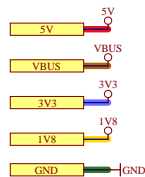
Mark "QSPT" on PCB



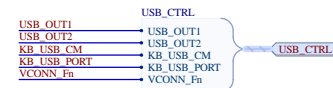
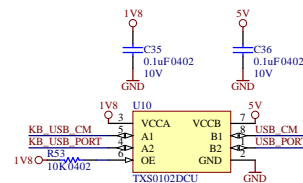
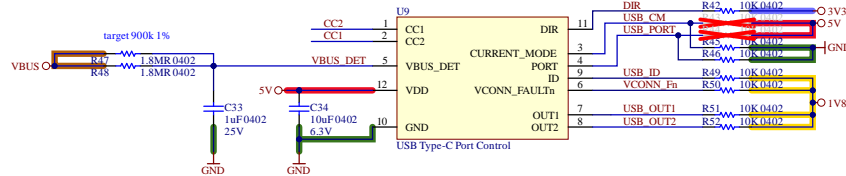
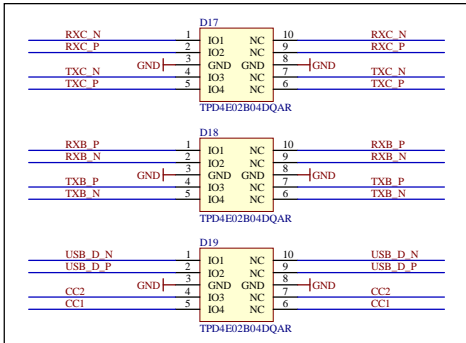



Title DM3390			<i>Luxonis Holding</i> <i>1925 Harmony Park Drive</i> <i>Westminster, CO</i> <i>80234</i>	Cannot open file C:\Users\BrianLuxonis\Documents\1446
Size: Tabloid	Number: D0000190	Revision:		
Date: 16/05/2022	Time: 14:45:29	Sheet 10 of 12	<i>United States</i>	
Drawn by: <i>David Malovrh</i>				





ESD PROTECTION



Title DM3390			Luxonis Holding 1925 Harmony Park Drive Westminster, CO 80234 United States	
Size: Tabloid	Number: D0000190	Revision: R0M0E0		
Date: 16/05/2022	Time: 14:45:29	Sheet 12 of 12		
Drawn by: David Malovrh				