

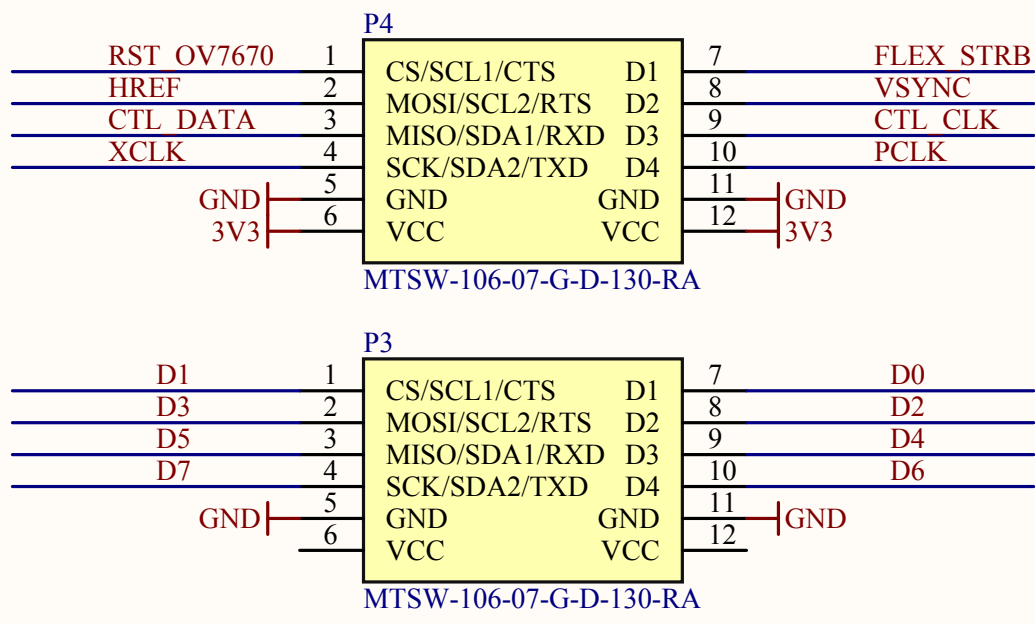
▲ xclk and pclk should run to gclk pins on fpga.

p4\_4 mark, p1\_4 bone both have gclk pin  
p3\_1 mark1 , p2\_1 on bone is only other

FINAL ROUTE HAS XCLK running to the GCLK pin. Future LOGi boards can accomodate both Gclk pins on each headr

▲ OV7670 flex connector camera option. Can adjust flex cable to allow camera to be positioned at multiple angles. Could do sweeps with stepper motor.

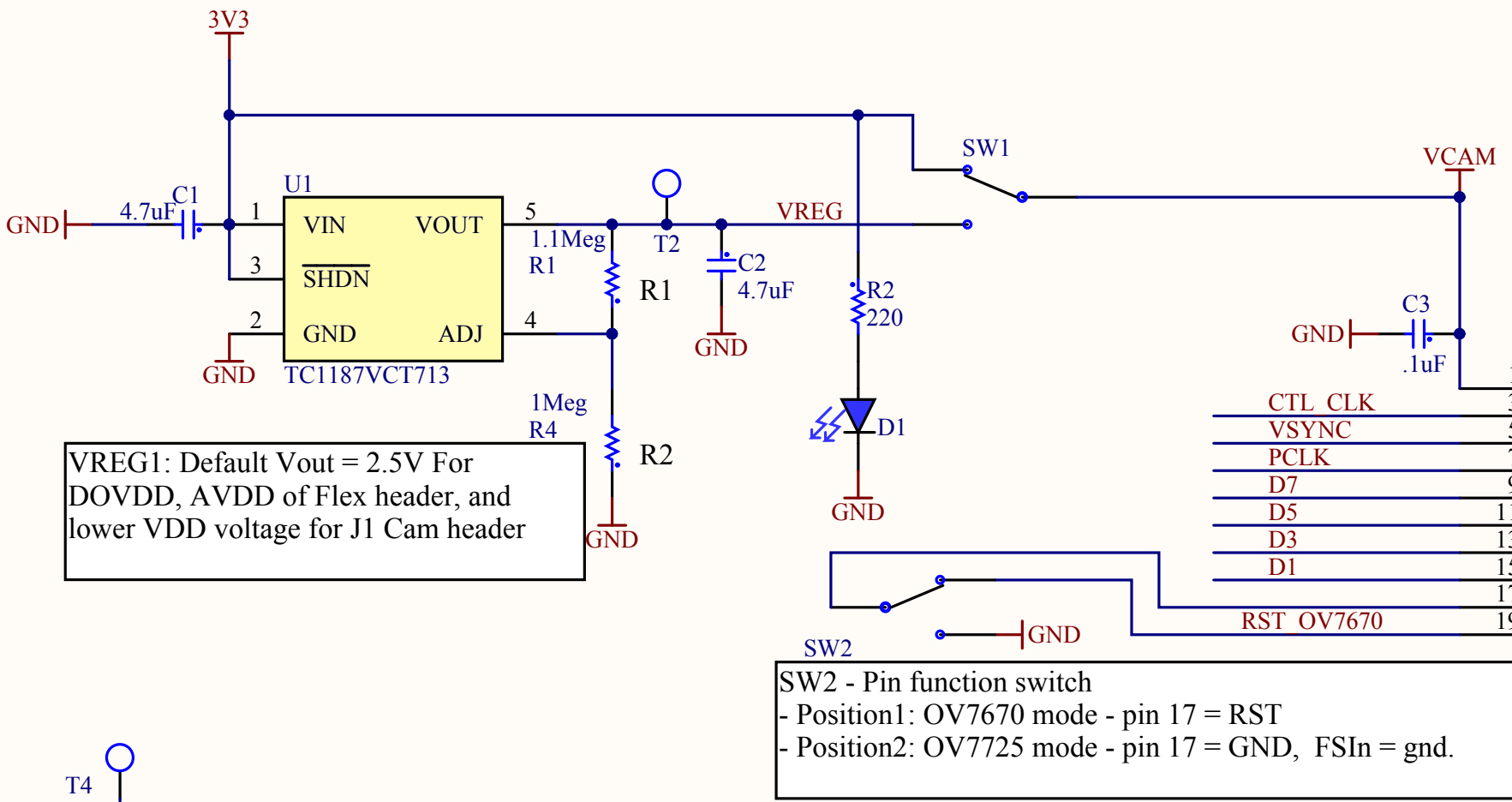
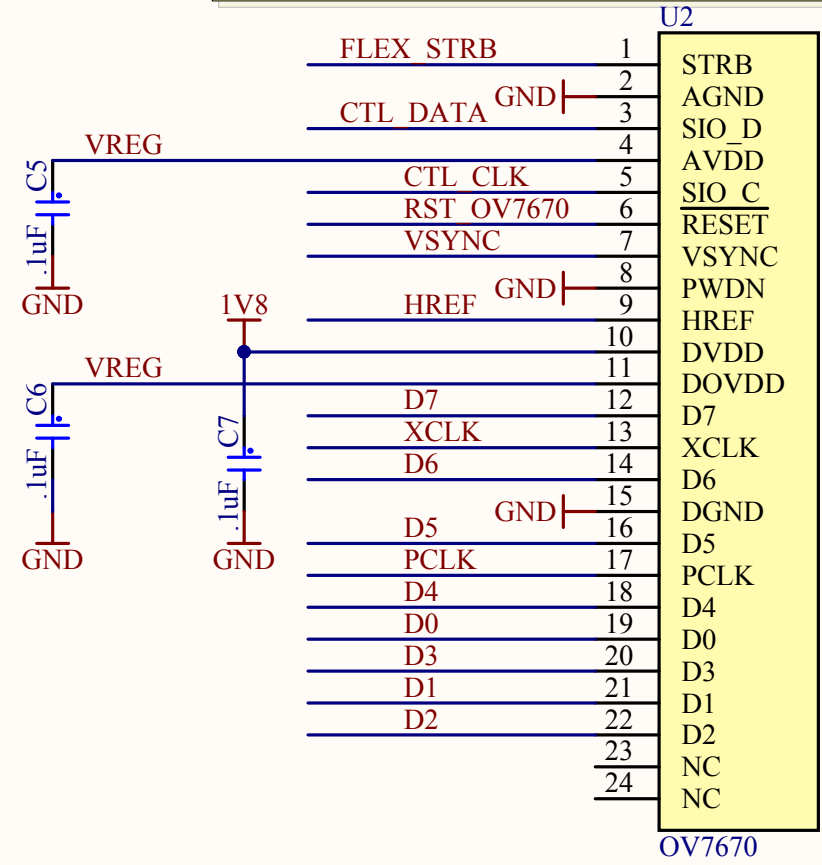
VOLTAGE REQUIREMENTS:  
DVDD = 1.8V  
Analog = 2.5V - 3V  
DOVDD = 1.7V - 3V



VREG VOUT Resistor Selection

$V_{out} = V_{ref}(R1/R2 + 1)$   
 $V_{ref} = 1.2V$   
R1 and R2 should range between 470K and 3M to limit bleed current

R2 = 1Meg  
Vout = 1.8V: R1=500k  
Vout = 2.5V: R1=1.1Meg  
Vout = 3V : R1 = 1.5Meg



Logo1  
Valent Logo 500  
Logo2  
Valent Logo 750

Title	PMOD Camera Adapter	
Revision:	RA.1	Sheet * of *
Date:	2/14/2013	Engineer: Michael Jones

