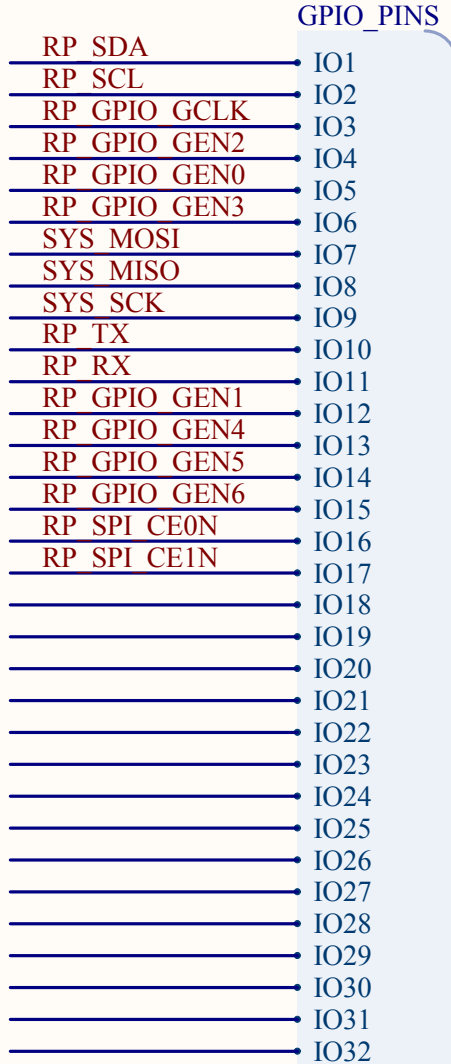
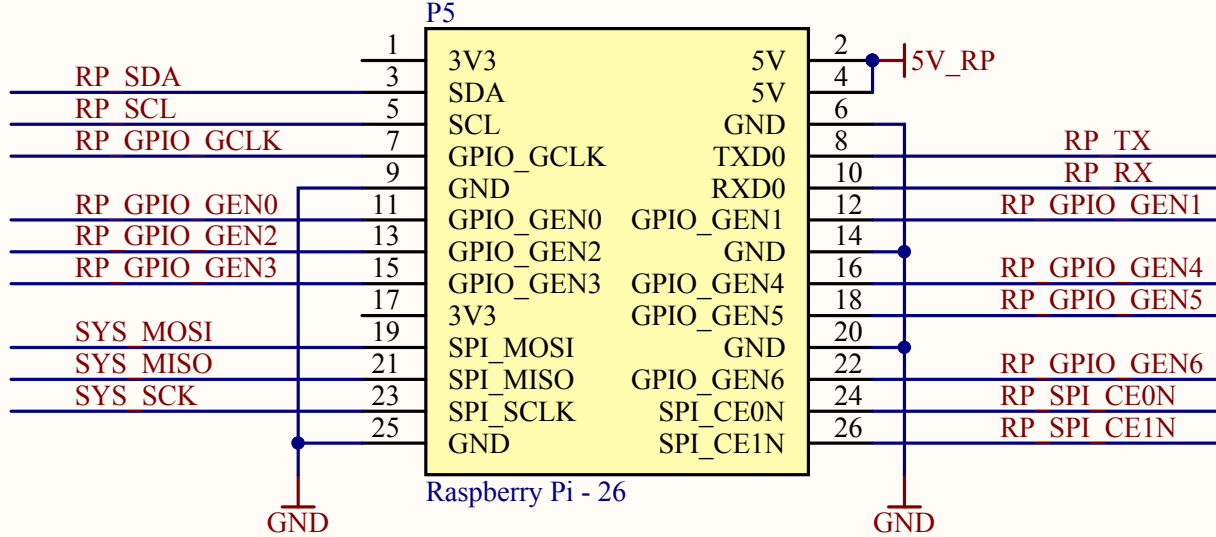
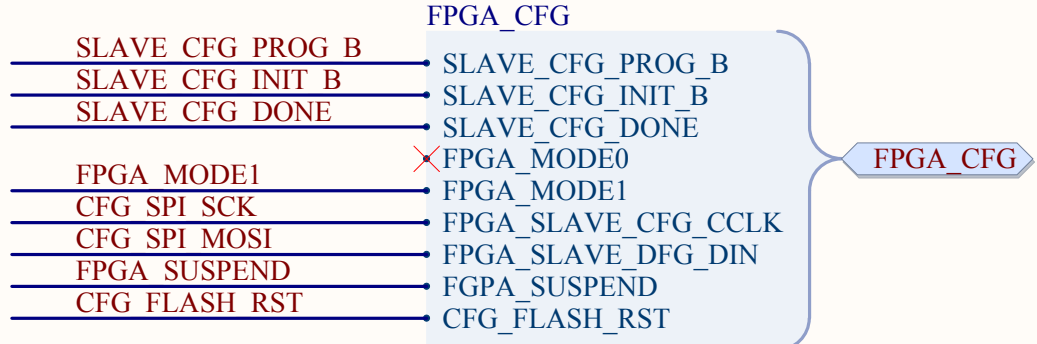
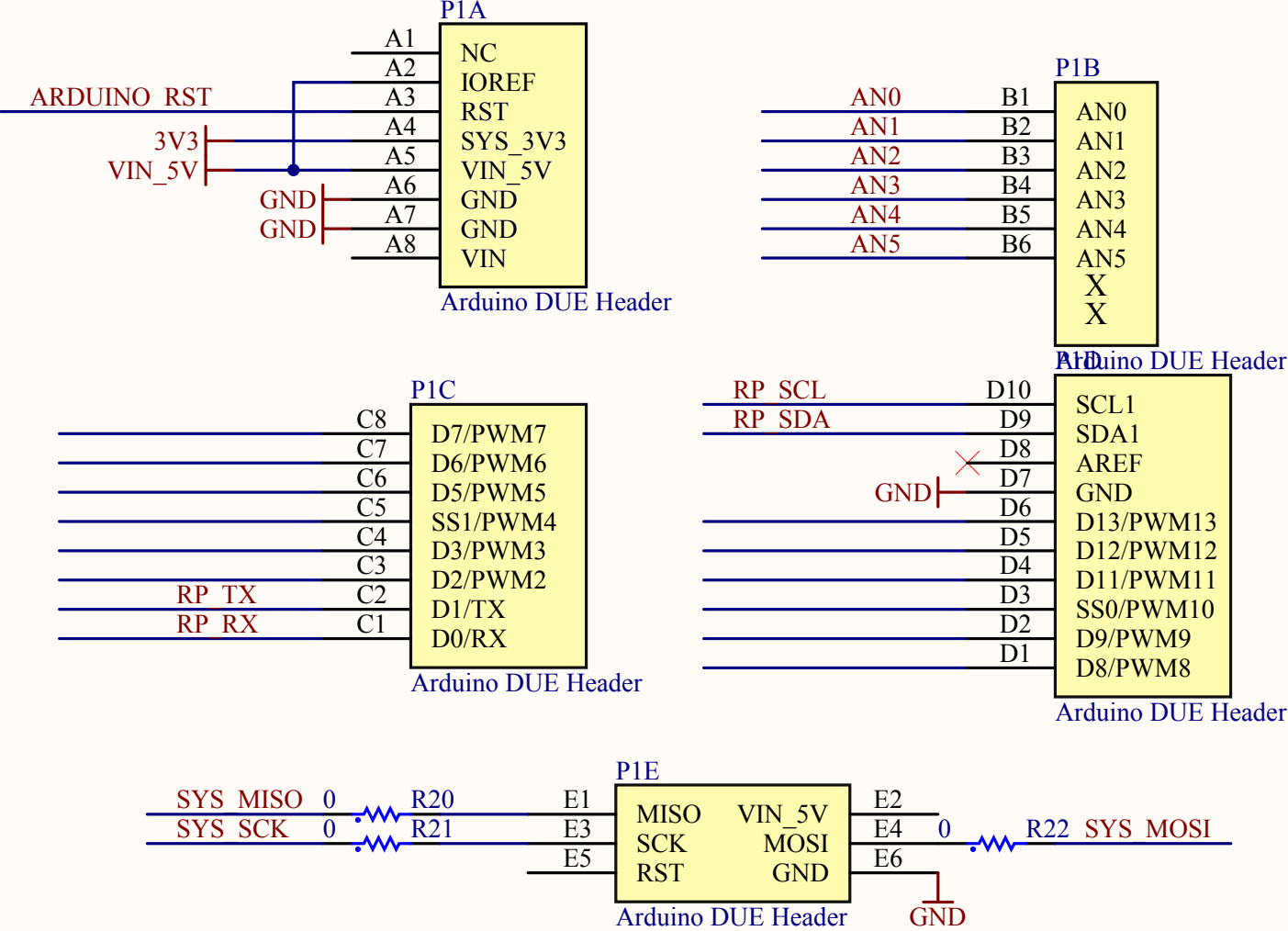
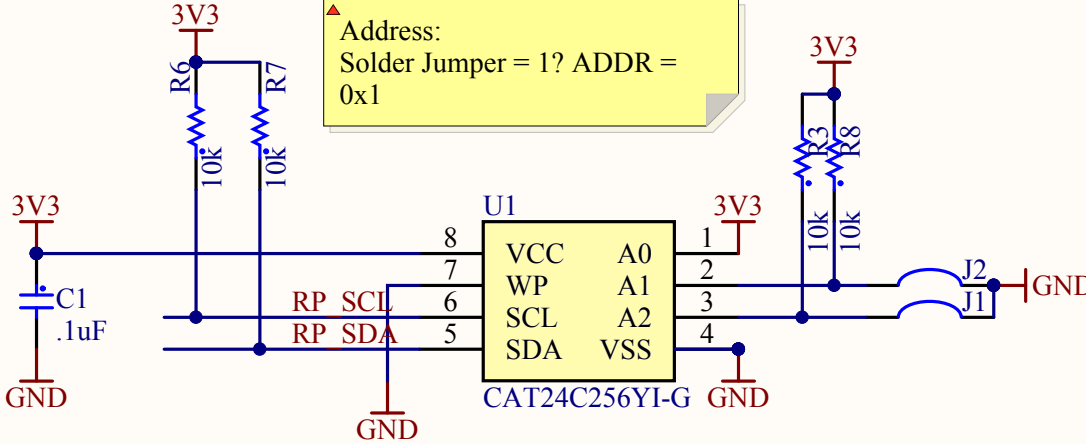
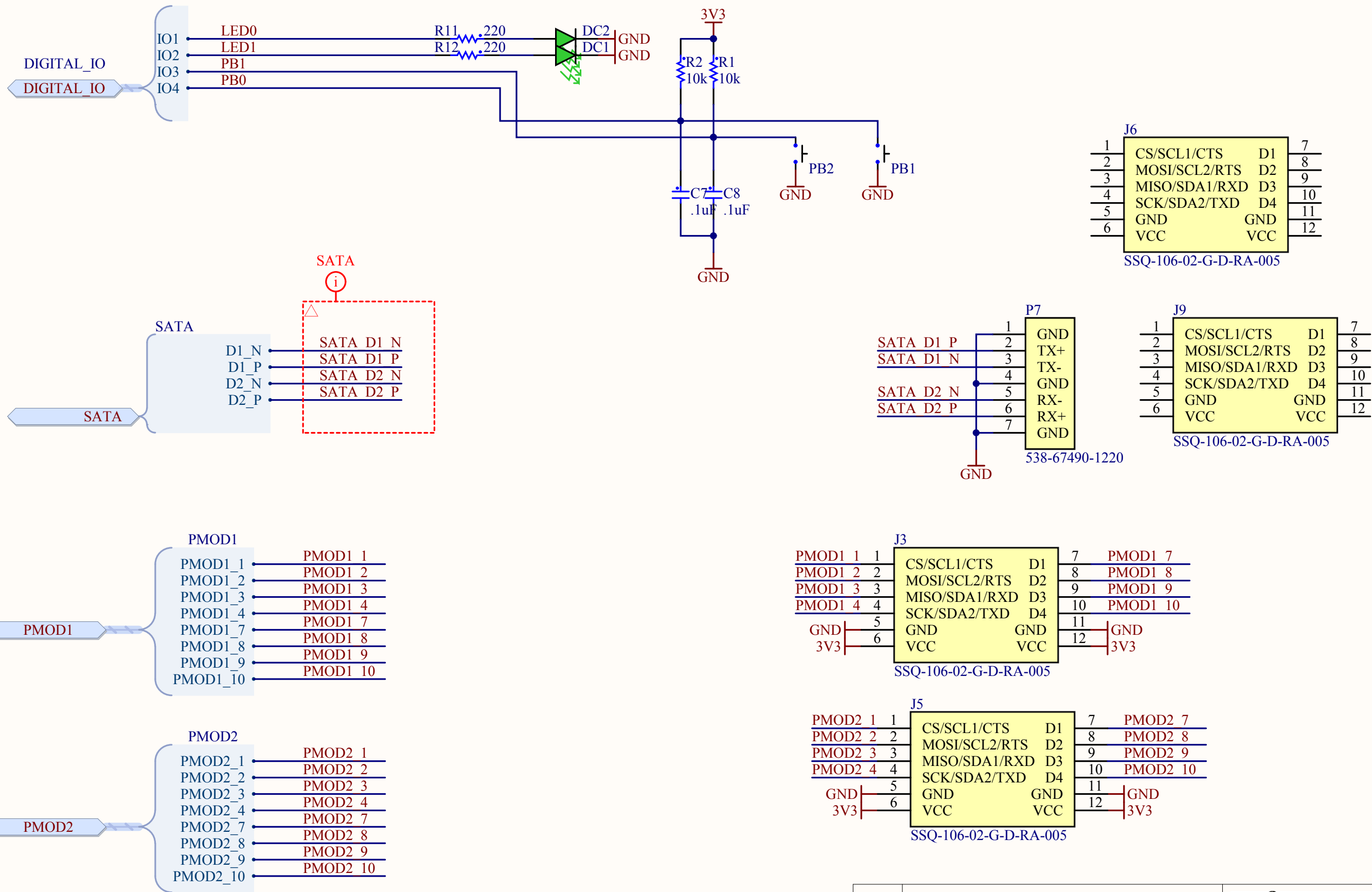
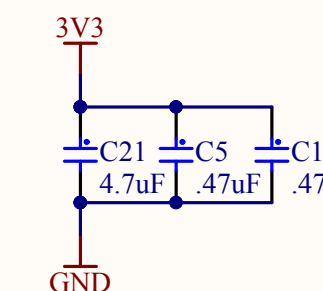
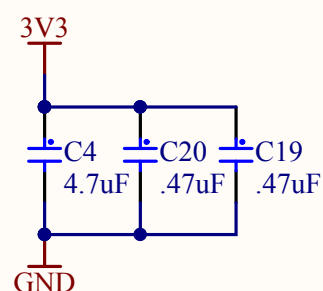
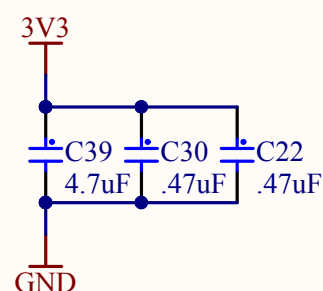
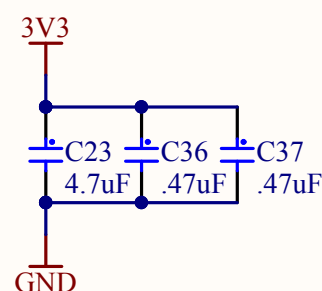
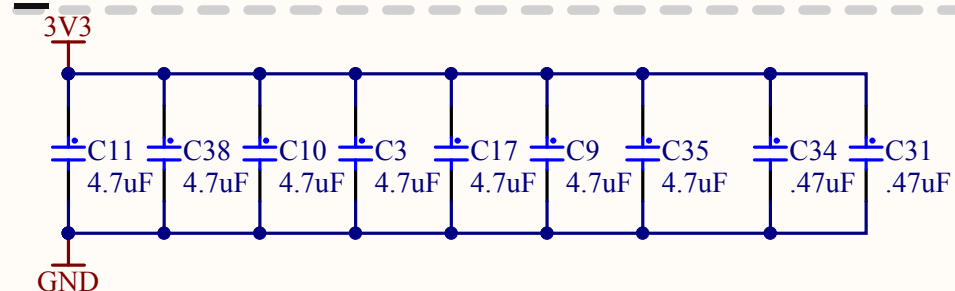
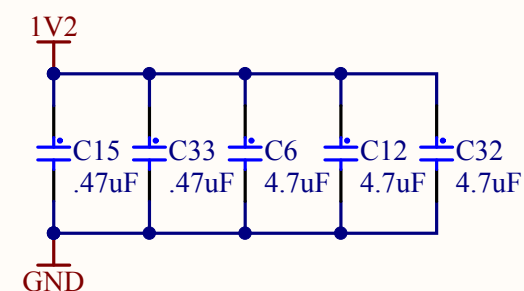
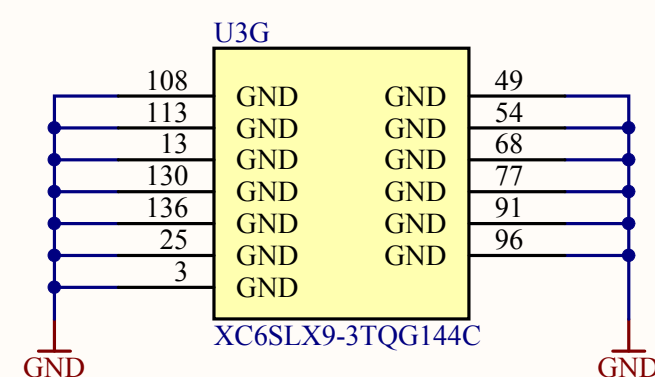
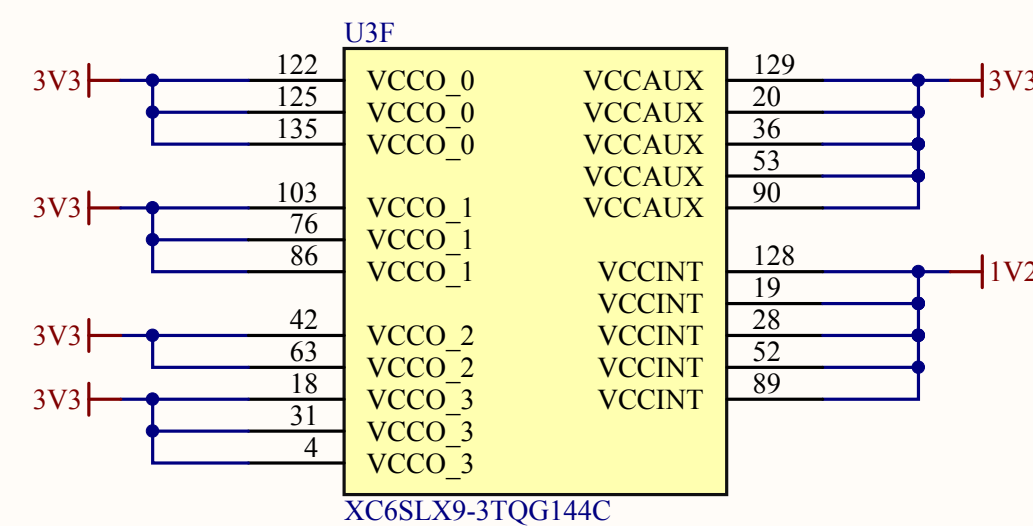


Raspberry Pi IO

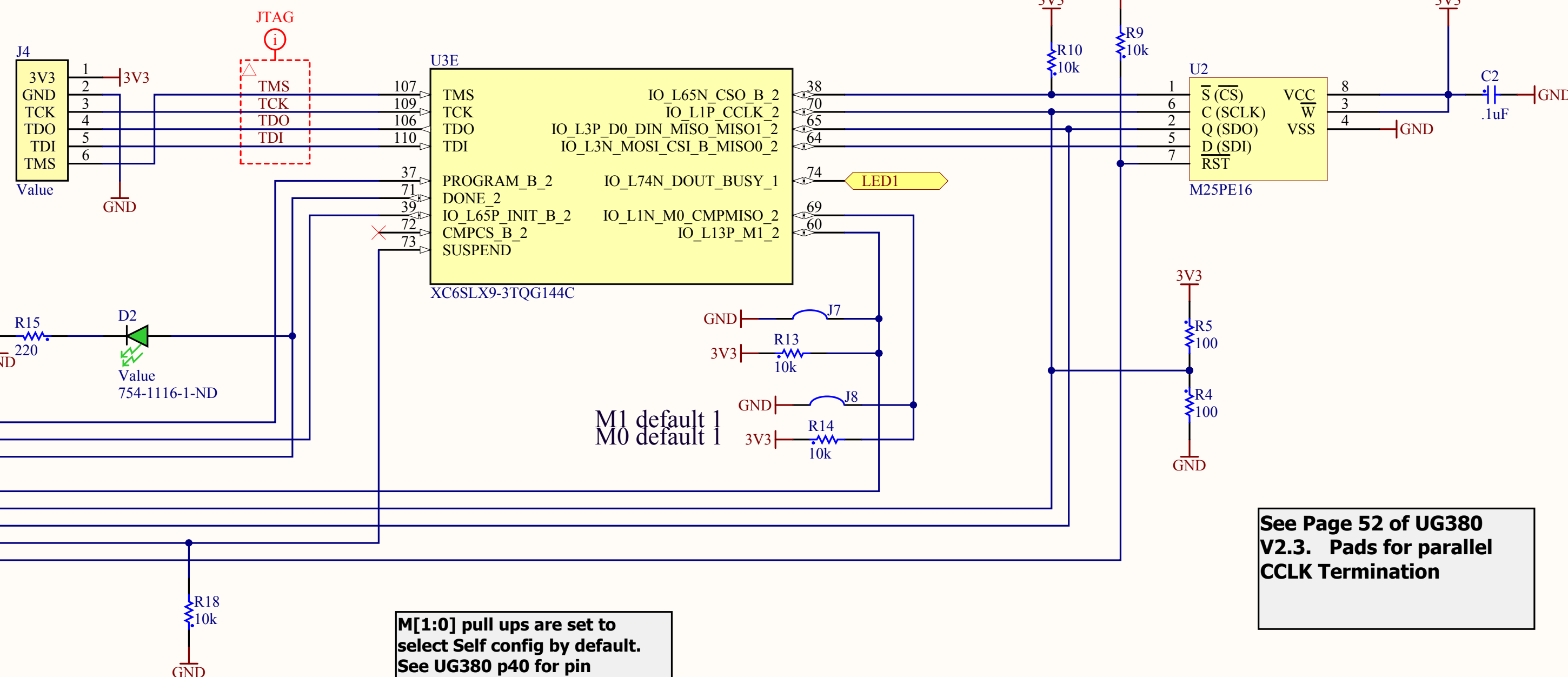


FPGA IO



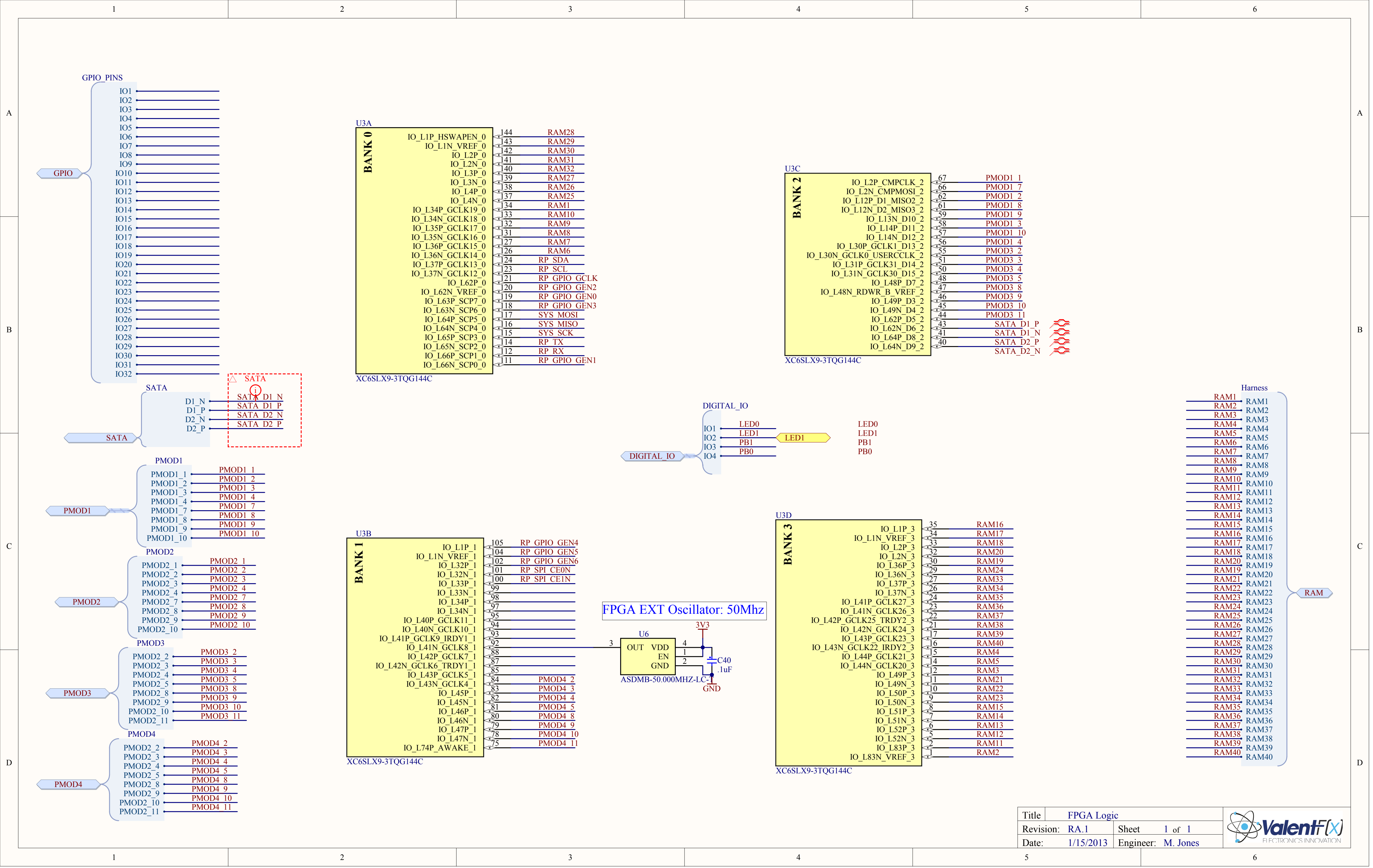


Decoupling cap configuration recommended by Xilinx UG393 (V1.2) for Spartan 6 power distribution



Title	FPGA Configuration		
Revision:	RA.1	Sheet	1 of 1
Date:	1/15/2013	Engineer:	M. Jones

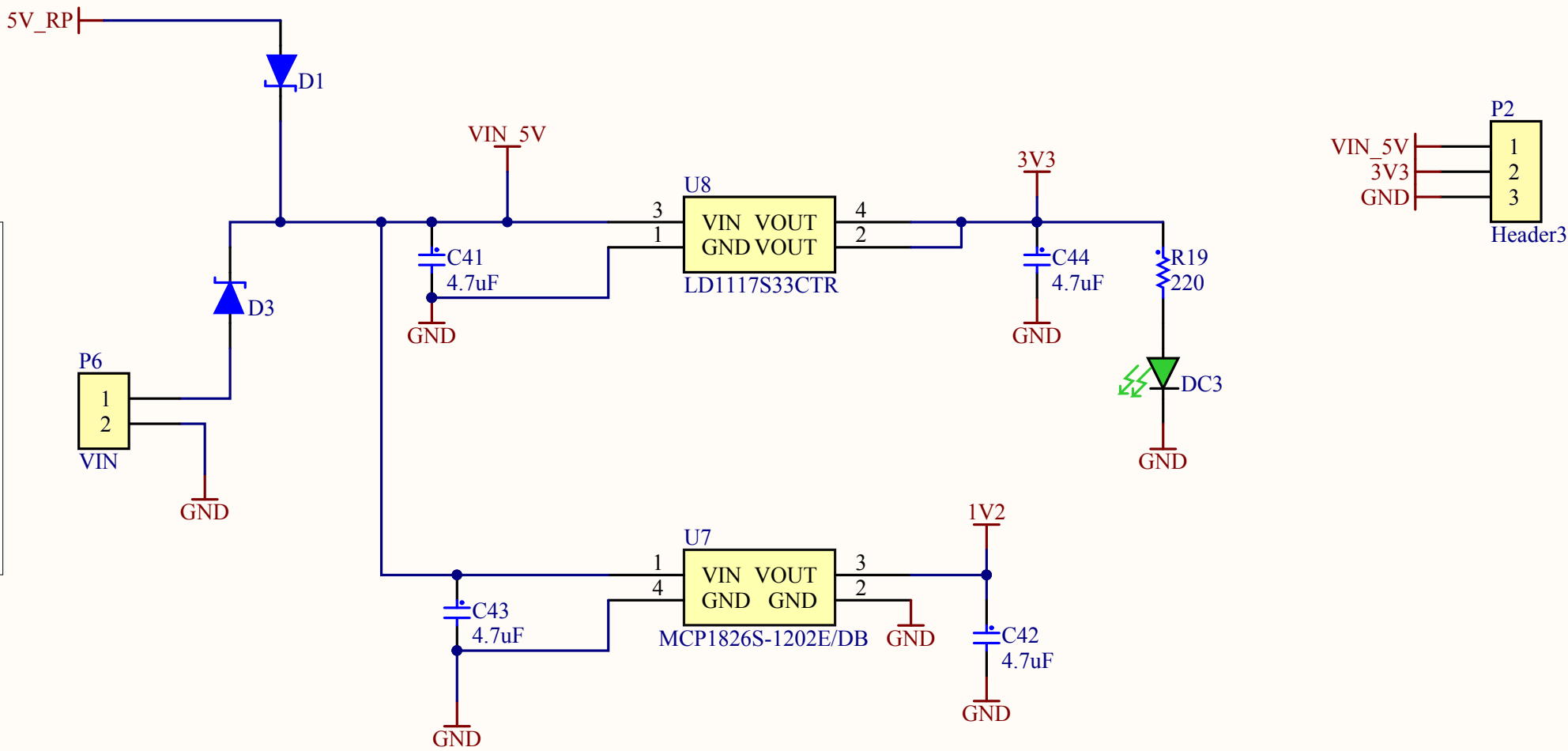




Power: By Default Power will be supplied by the Beaglebone.

Optionally power can be supplied through FPGA VIN header.

2 x Auxillary Power output Headers



Logo1
Valent Logo 750

Logo2
Valent Logo 1000

