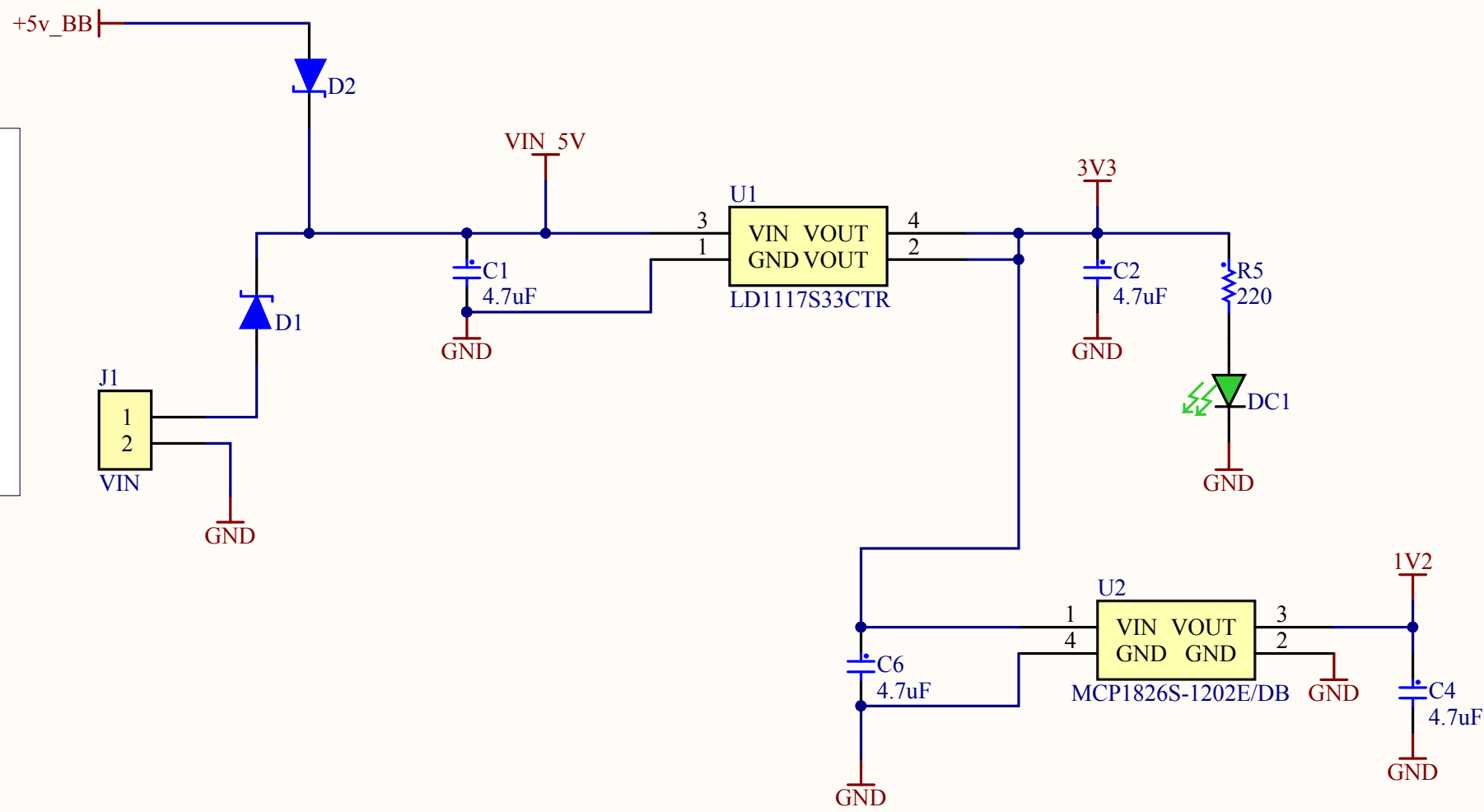
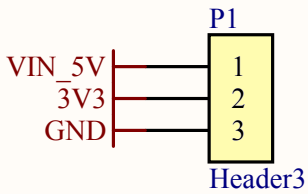


**Power: By Default Power will be supplied by the Beaglebone.**

**Optionally power can be supplied through FPGA VIN header J1.**

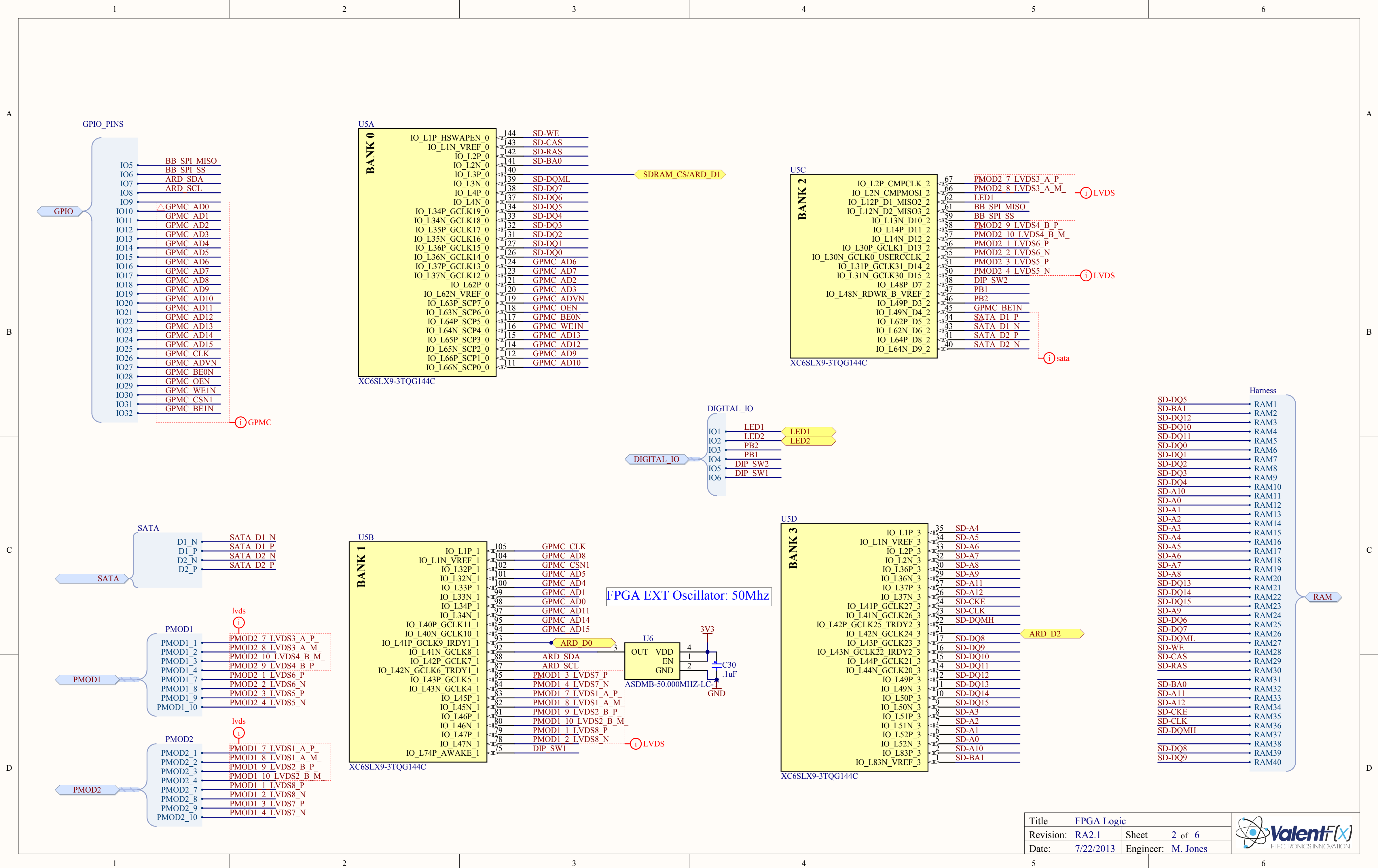


**1 x Auxillary Power output Headers**



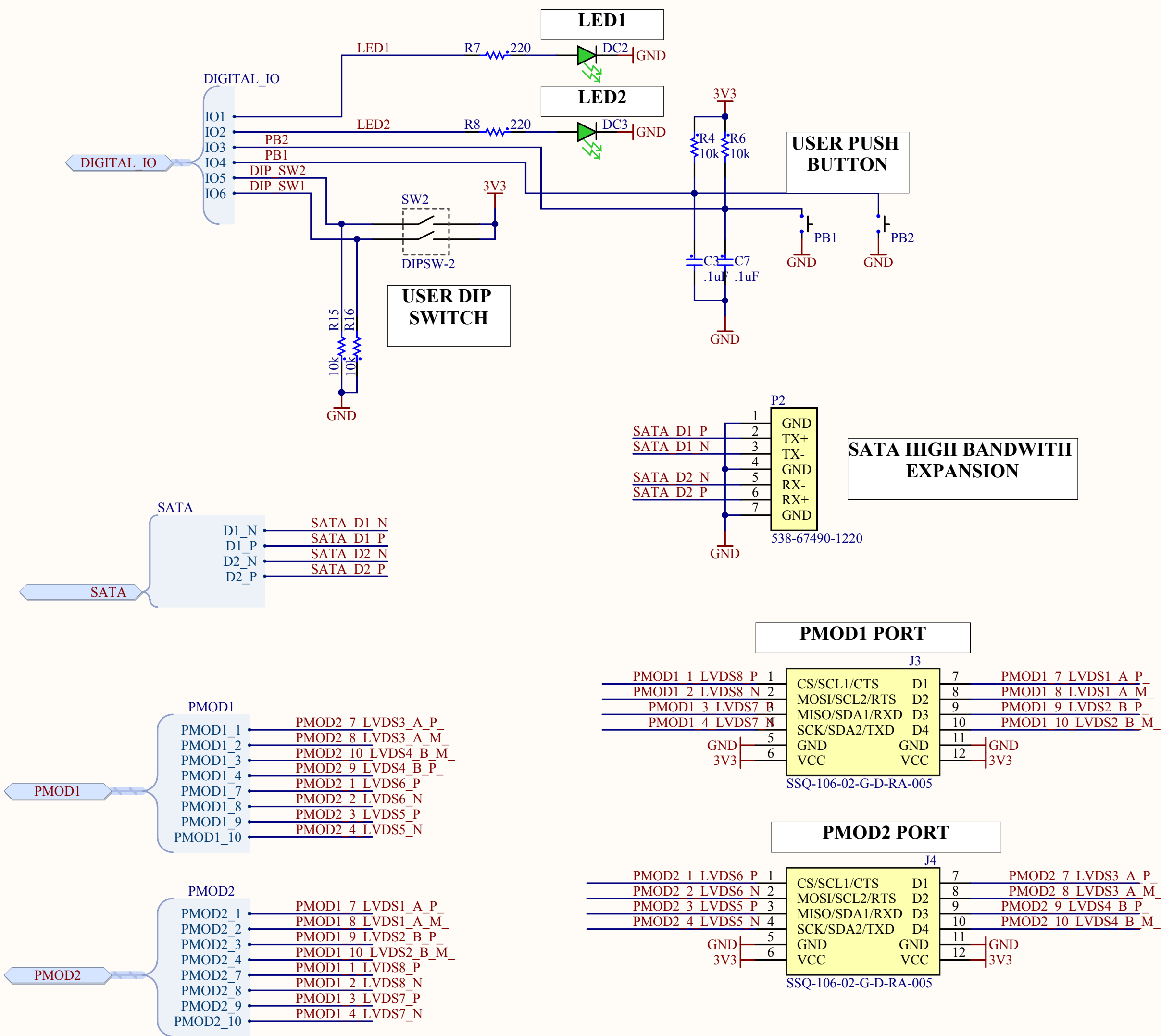
Logo1  
Valent Logo 750  
Logo2  
Valent Logo 1000







FPGA Digital IO



Title: Digital IO

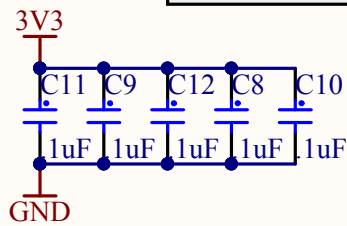
Revision: RA.1 Sheet: 4 of 6

Date: 7/22/2013 Engineer: M. Jones





SDRAM



Harness

|       |         |
|-------|---------|
| RAM1  | SD-DQ5  |
| RAM2  | SD-BA1  |
| RAM3  | SD-DQ12 |
| RAM4  | SD-DQ10 |
| RAM5  | SD-DQ11 |
| RAM6  | SD-DQ0  |
| RAM7  | SD-DQ1  |
| RAM8  | SD-DQ2  |
| RAM9  | SD-DQ3  |
| RAM10 | SD-DQ4  |
| RAM11 | SD-A10  |
| RAM12 | SD-A0   |
| RAM13 | SD-A1   |
| RAM14 | SD-A2   |
| RAM15 | SD-A3   |
| RAM16 | SD-A4   |
| RAM17 | SD-A5   |
| RAM18 | SD-A6   |
| RAM19 | SD-A7   |
| RAM20 | SD-A8   |
| RAM21 | SD-DQ13 |
| RAM22 | SD-DQ14 |
| RAM23 | SD-DQ15 |
| RAM24 | SD-A9   |
| RAM25 | SD-DQ6  |
| RAM26 | SD-DQ7  |
| RAM27 | SD-DQML |
| RAM28 | SD-WE   |
| RAM29 | SD-CAS  |
| RAM30 | SD-RAS  |
| RAM31 | SD-BA0  |
| RAM32 | SD-A11  |
| RAM33 | SD-A12  |
| RAM34 | SD-CKE  |
| RAM35 | SD-CLK  |
| RAM36 | SD-DQMH |
| RAM37 |         |
| RAM38 | SD-DQ8  |
| RAM39 | SD-DQ9  |
| RAM40 |         |

RAM

RAM

