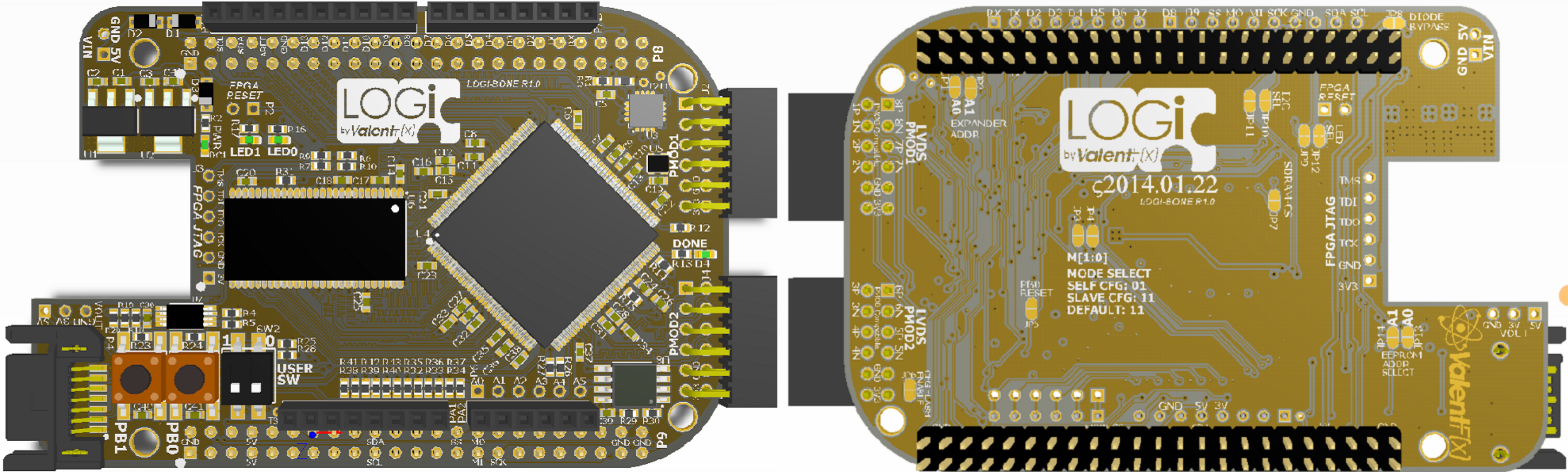


A

B

A

B

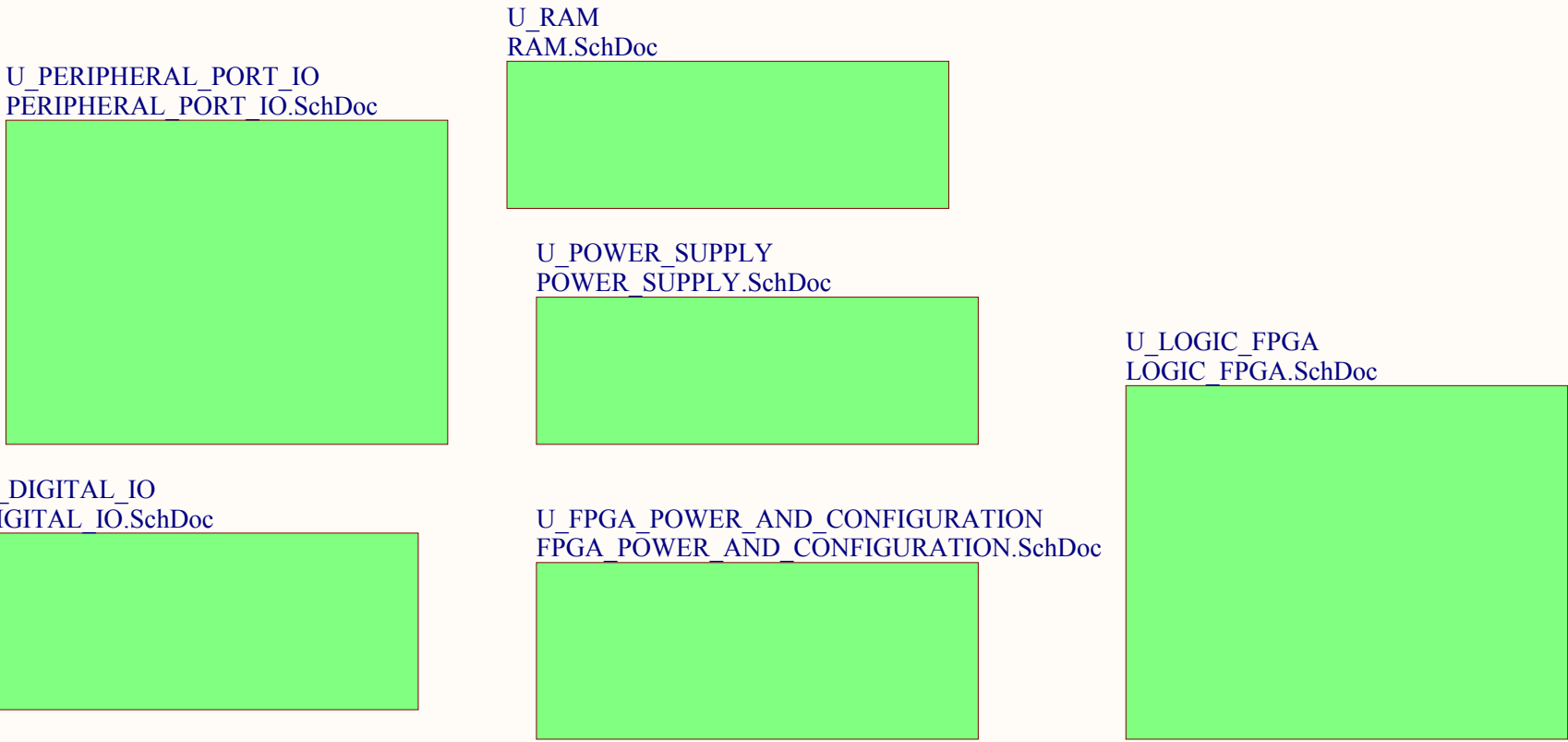


C

C

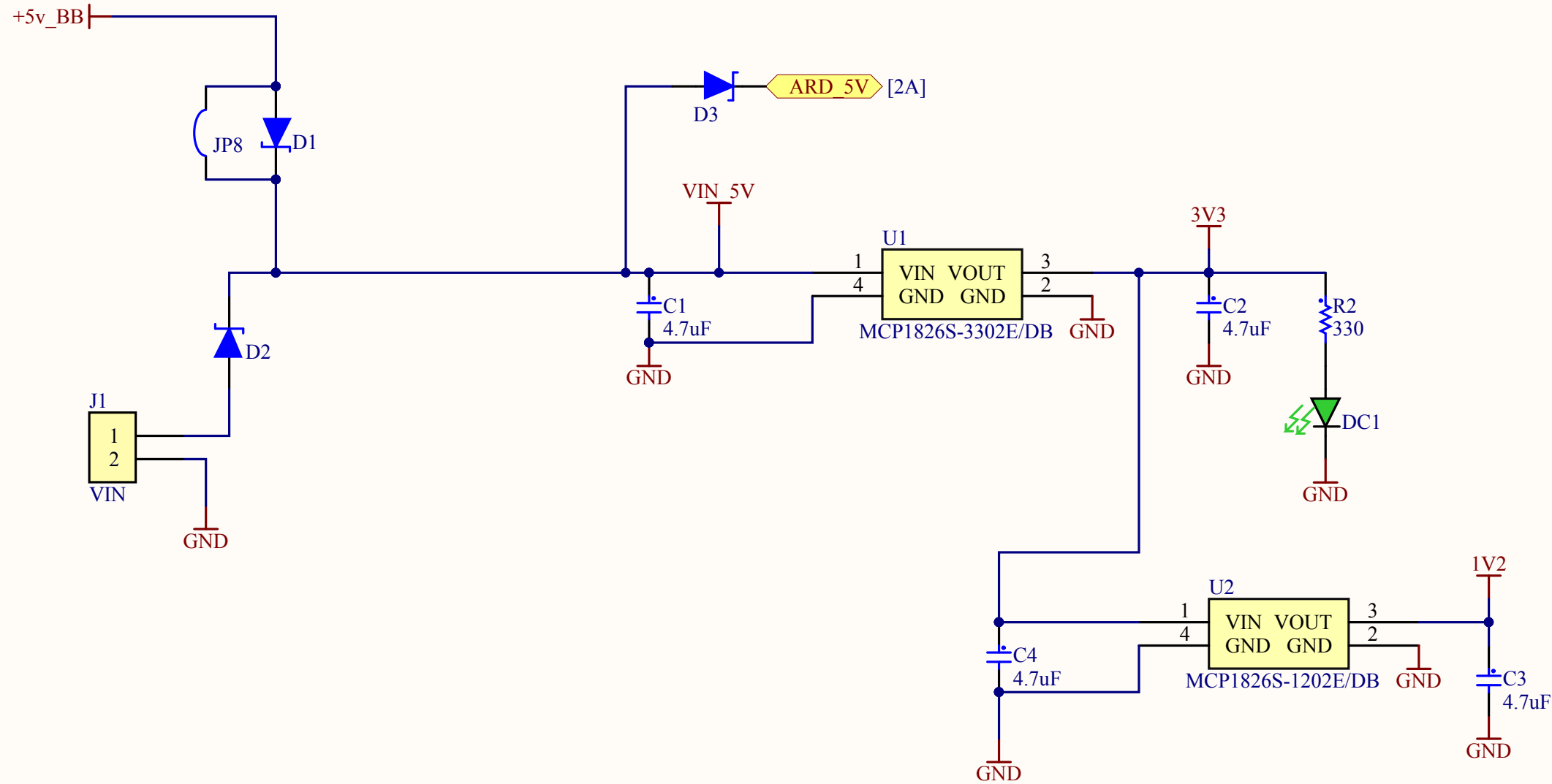
D

D

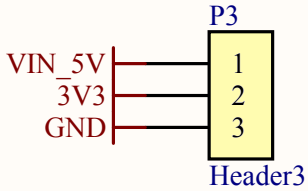


Power: By Default Power will be supplied by the Beaglebone.

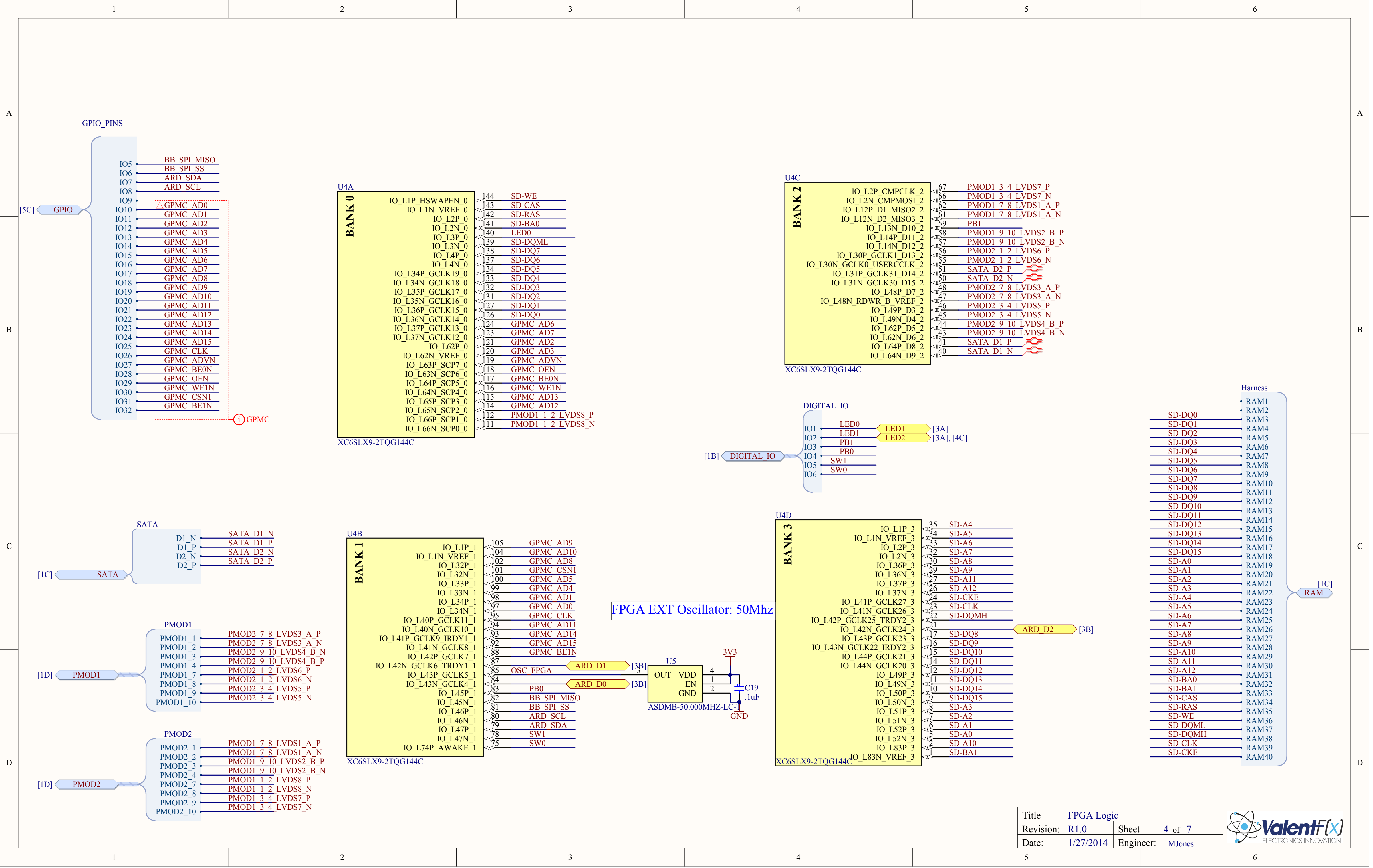
Optionally power can be supplied through FPGA VIN header J1.

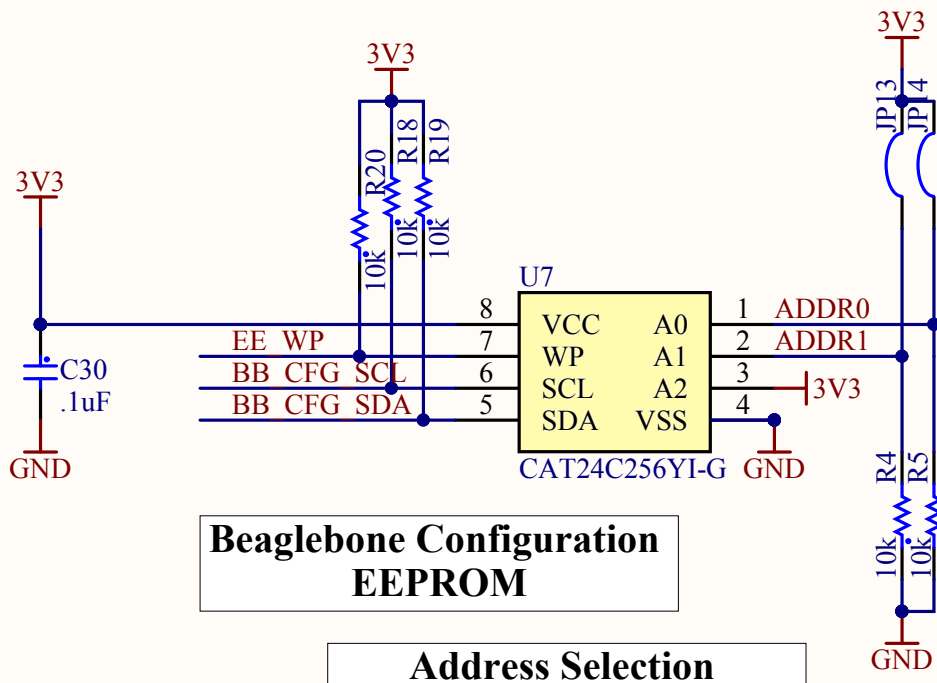


1 x Auxillary Power output Headers



- LOGI-LOGO-600
- LOGO3
- VALENTFX-LOGO-750
- Logo5
- LOGI-LOGO-750
- LOGO4

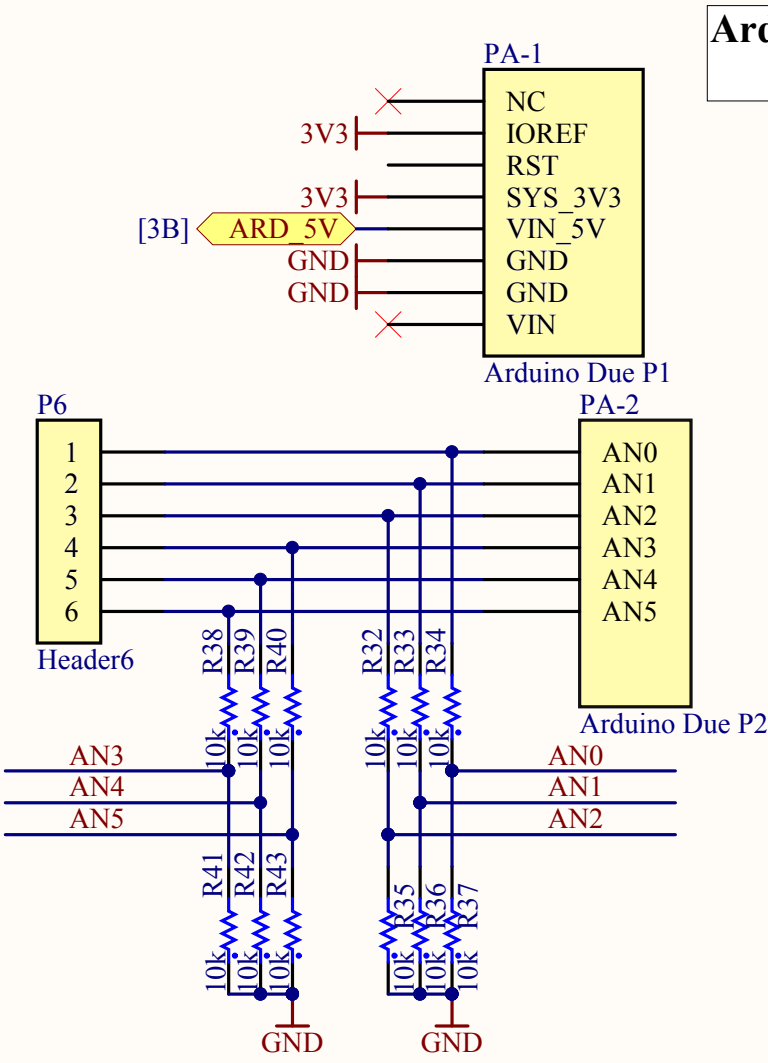




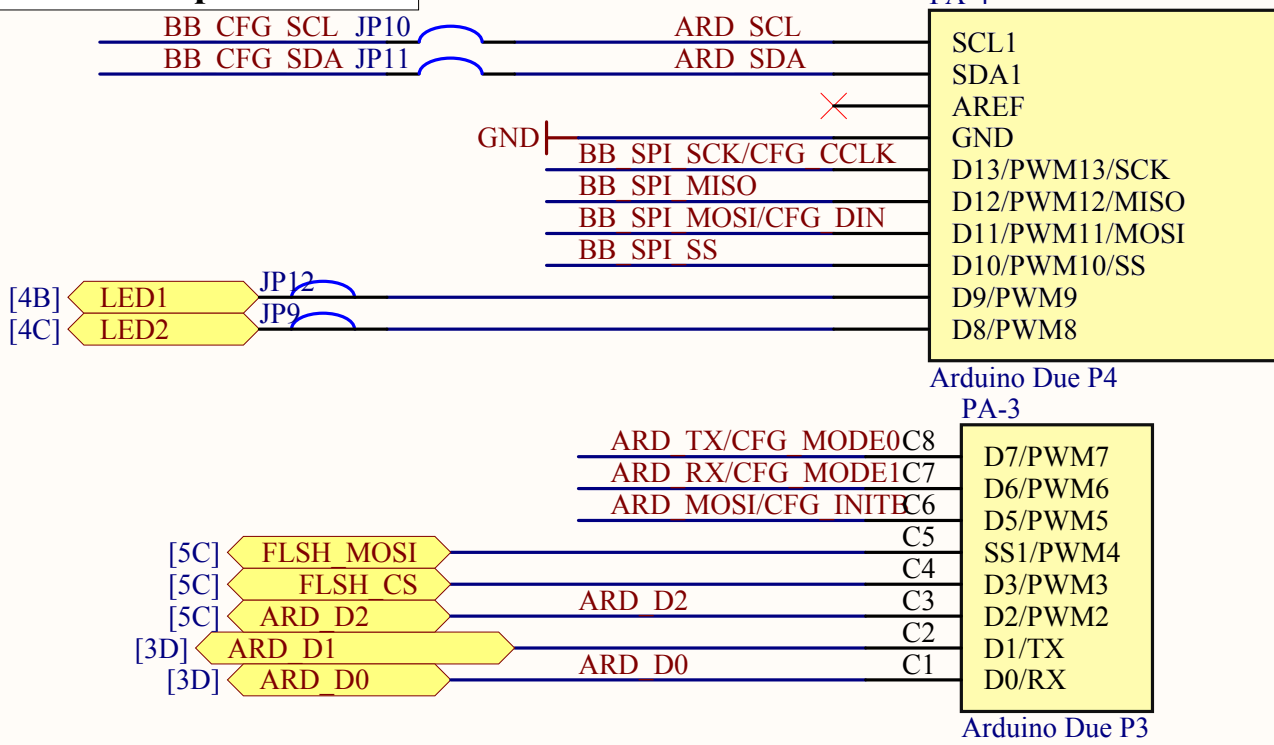
Beaglebone Configuration EEPROM

Address Selection DIP SW

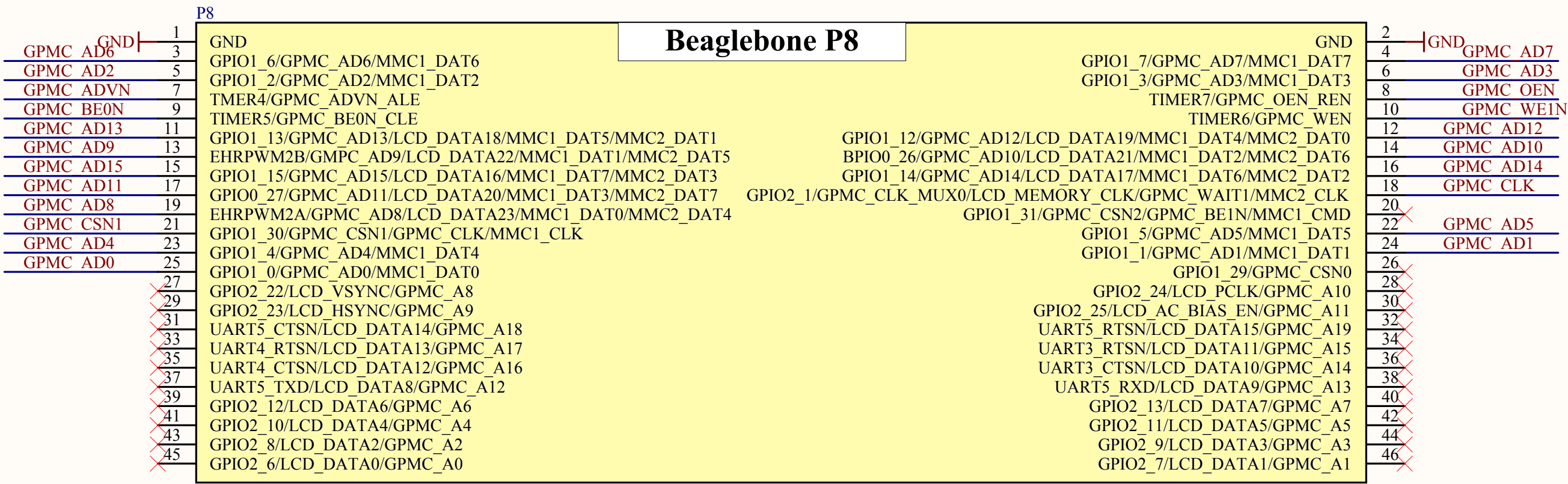
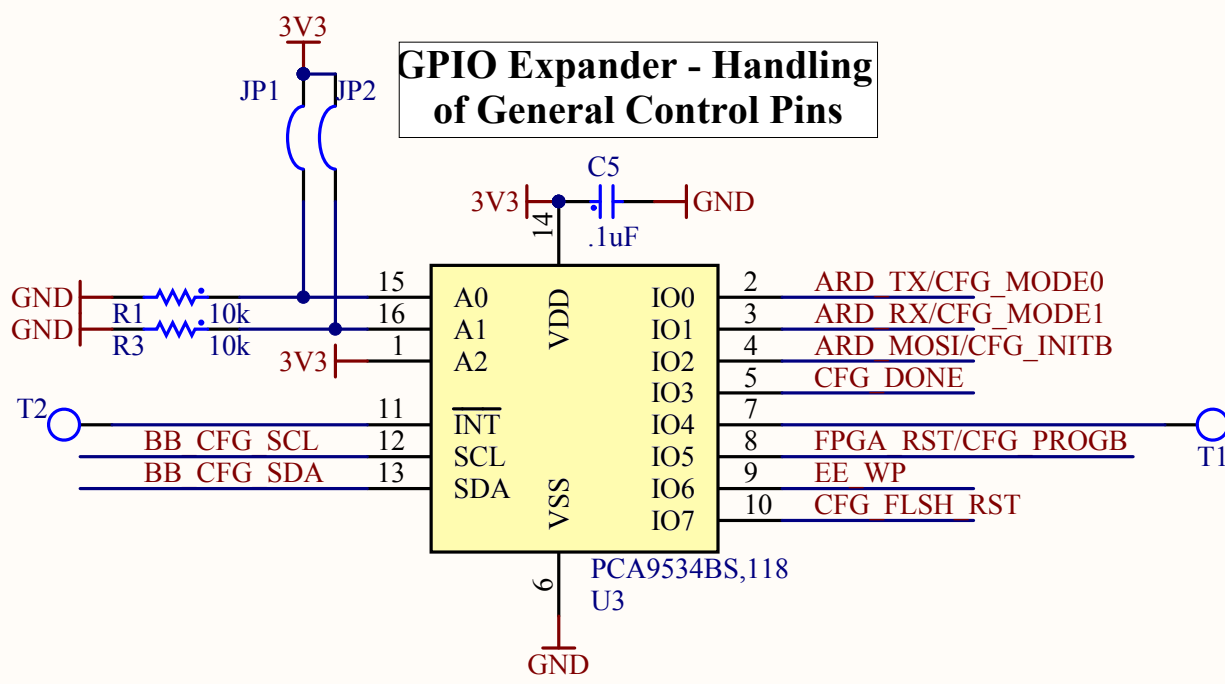
If the DIP SW is NOT installed the eerpom will hold the low address 0x54 (highest priority) for capes and be used for the bone pin configuration in the case multiple installed of capes



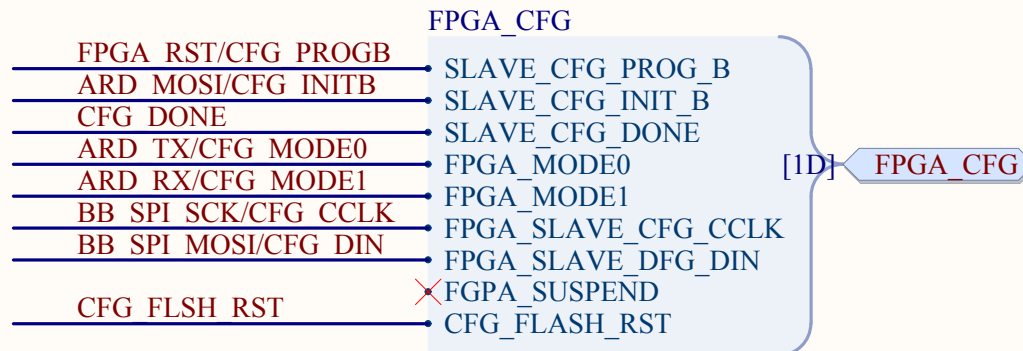
Arduino Header - Arduino Shield Expansion



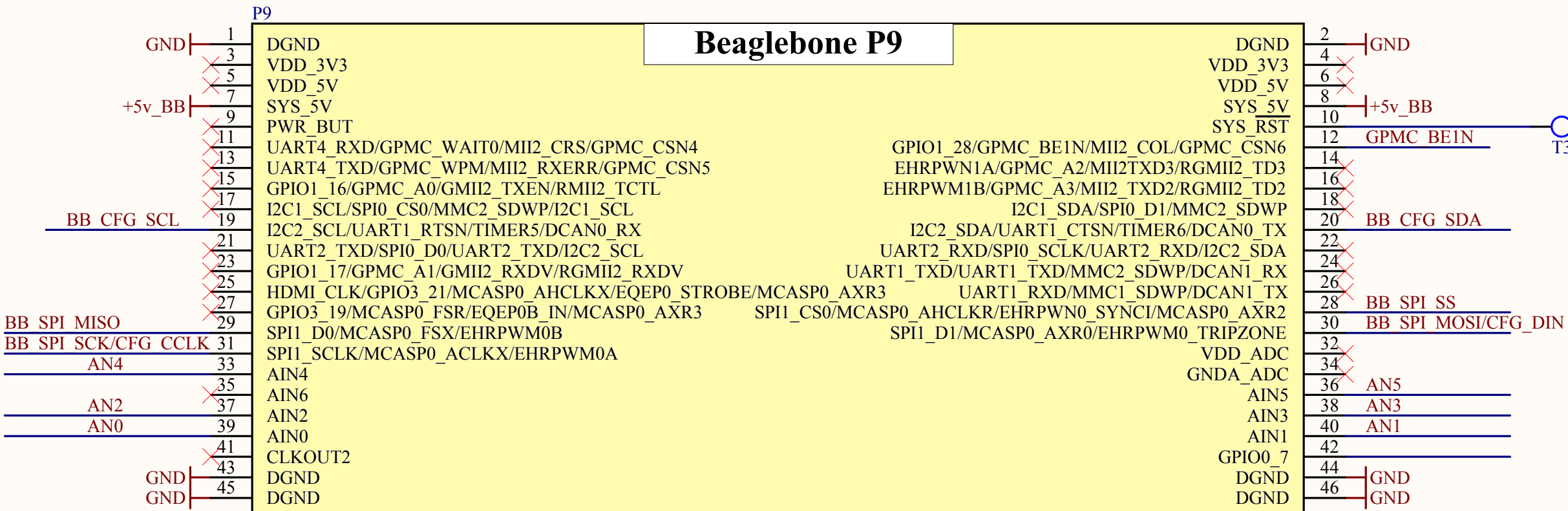
GPIO Expander - Handling of General Control Pins



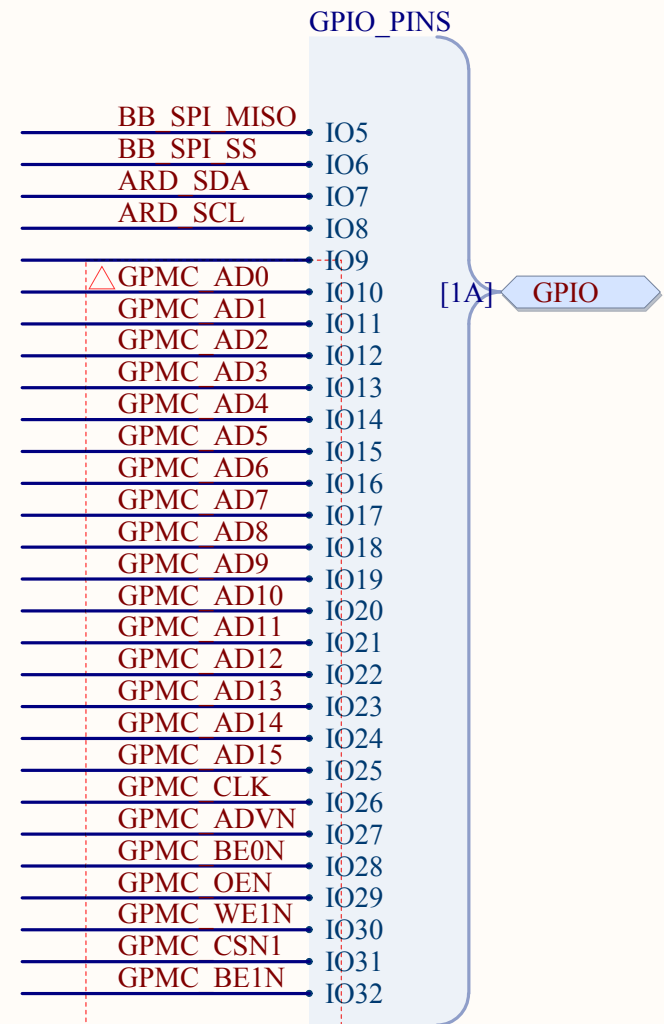
Beaglebone P8



FPGA_CFG



Beaglebone P9



GPIO_PINS

