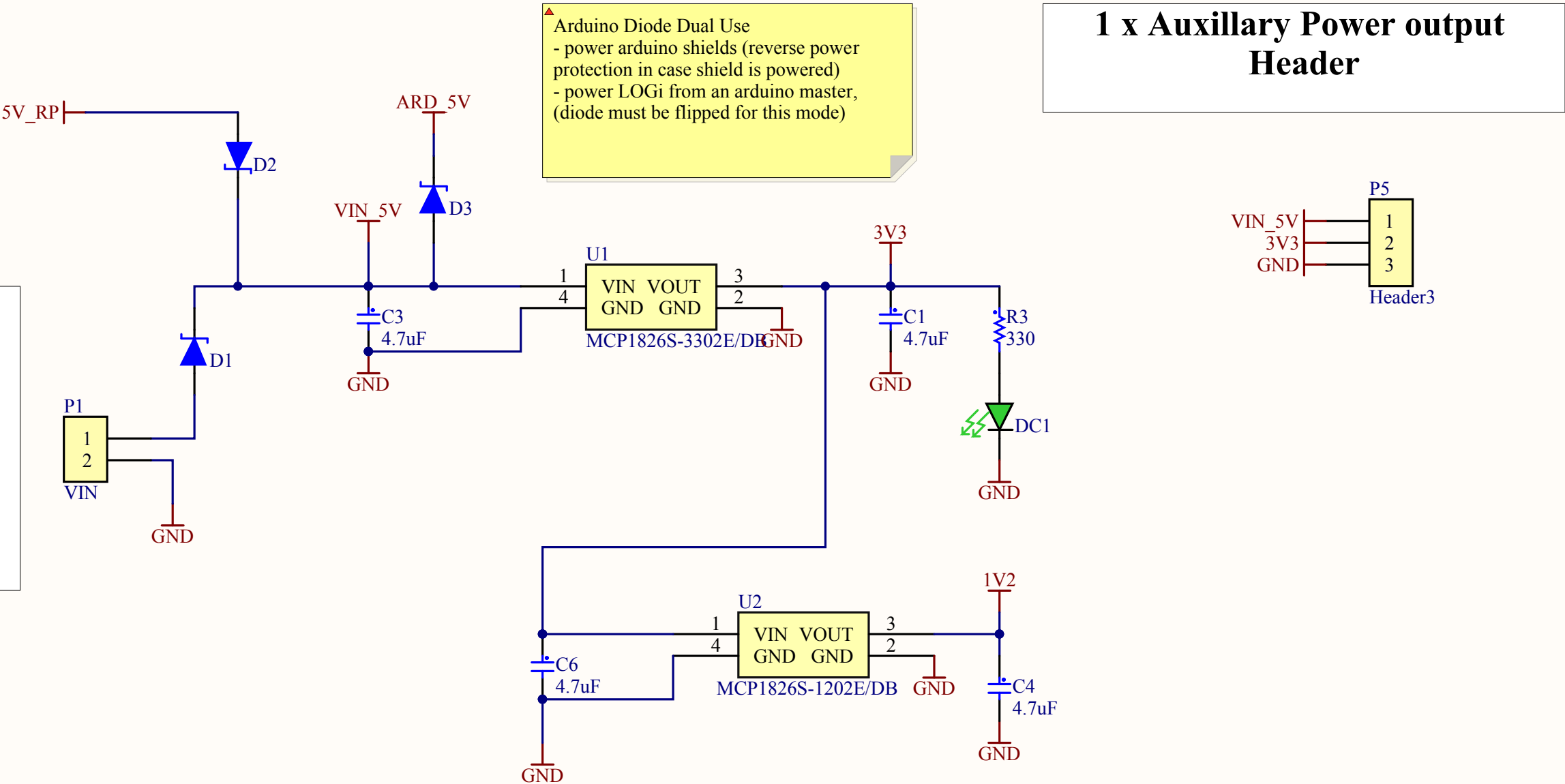


Power: By Default Power will be supplied by the .
Raspberry Pi
Optionally power can be supplied through
FPGA VIN header.

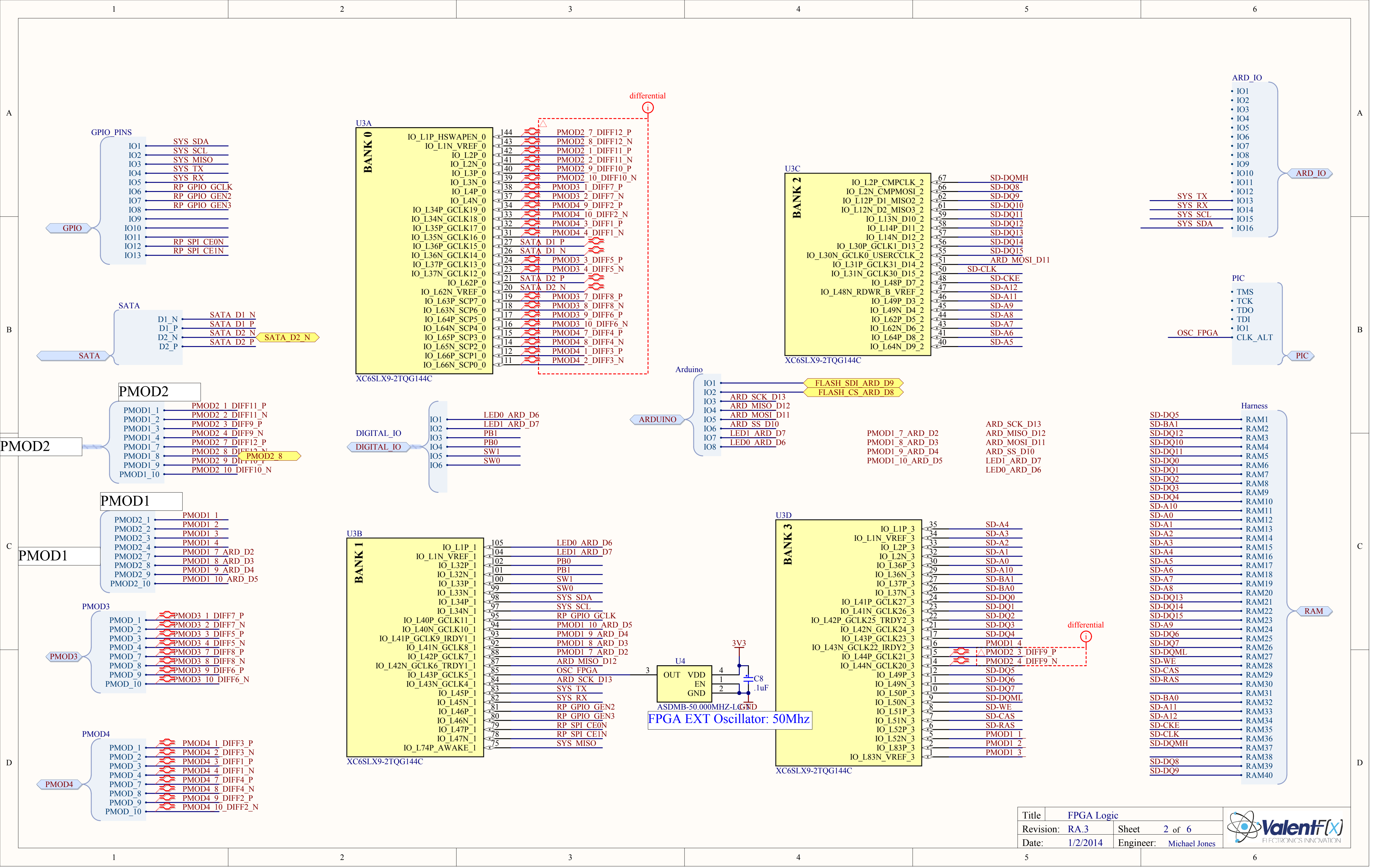


VALENTFX-LOGO-500
Logo2

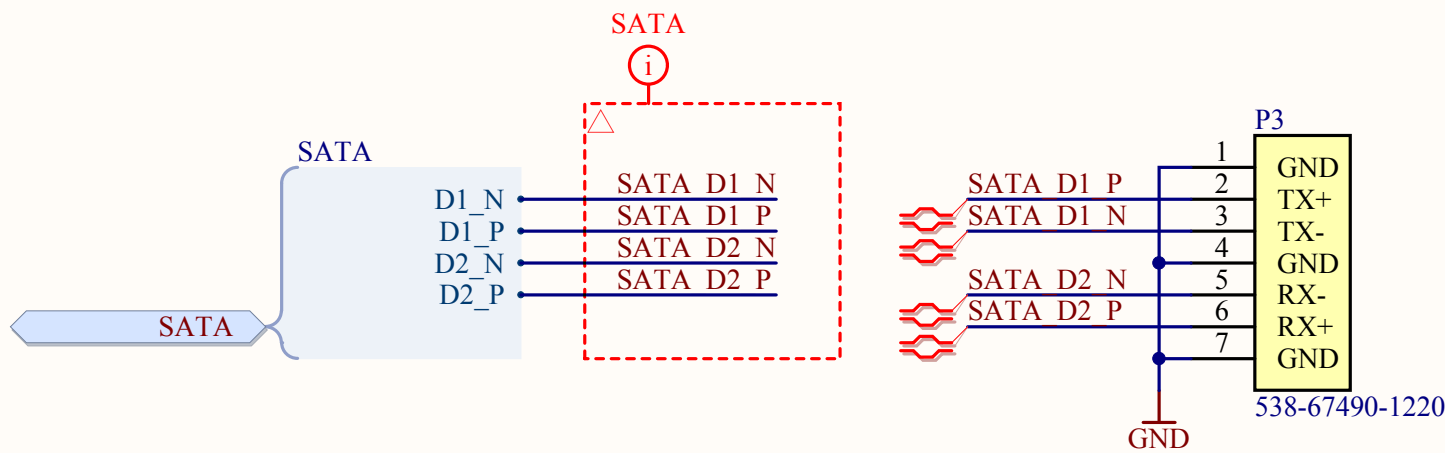
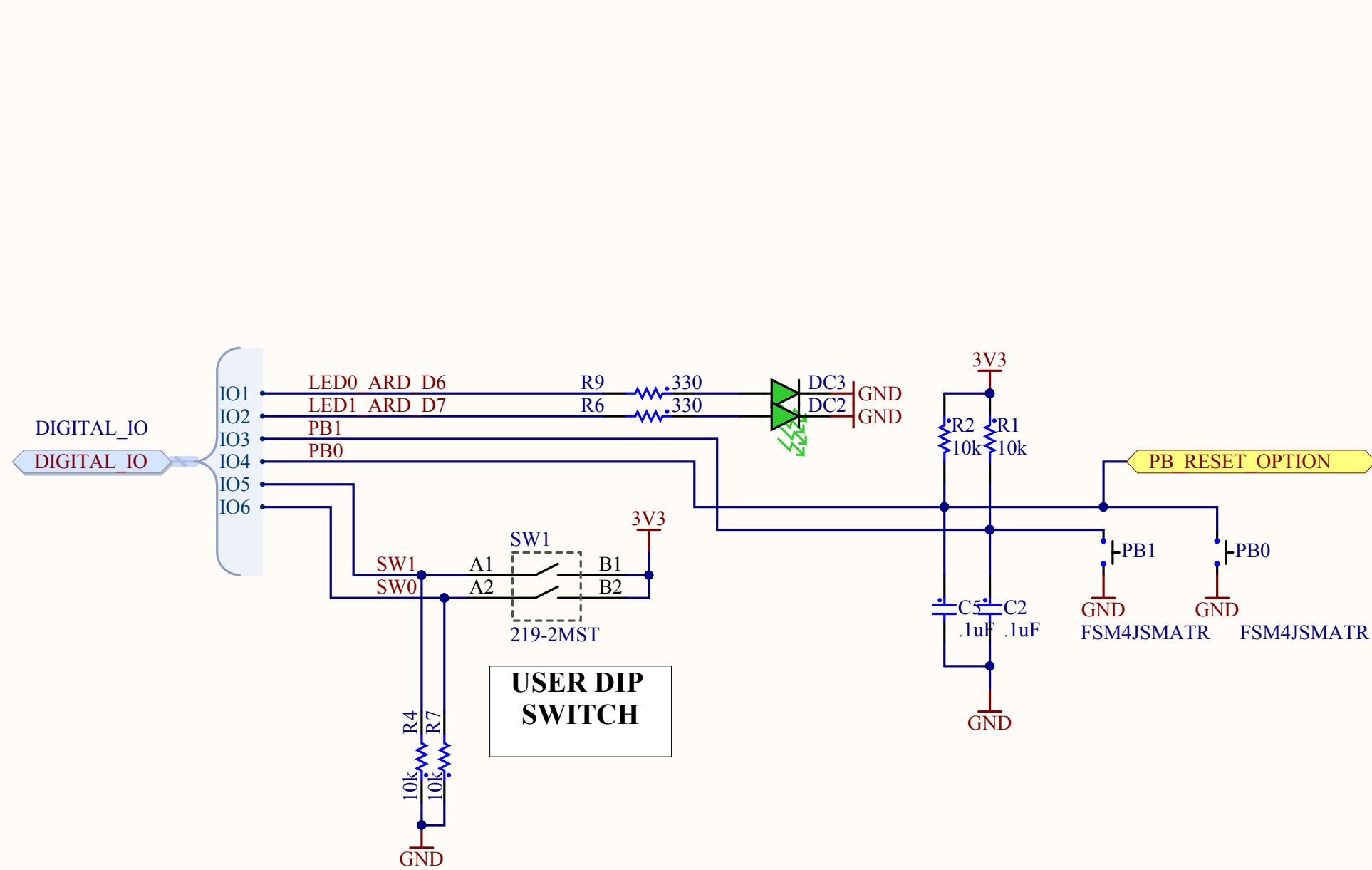
VALENTFX-LOGO-750
Logo?

LOGI-LOGO-750
LOGO4

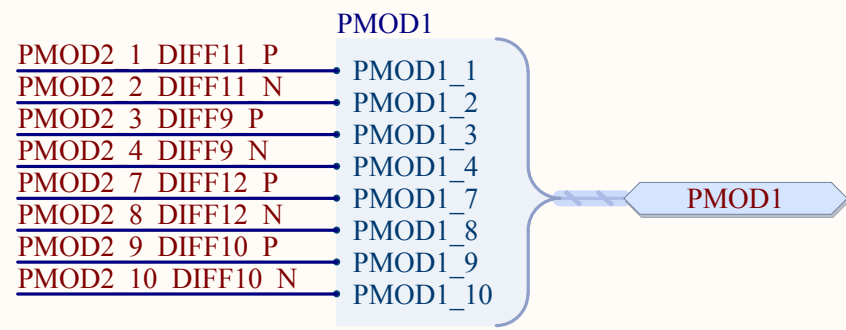
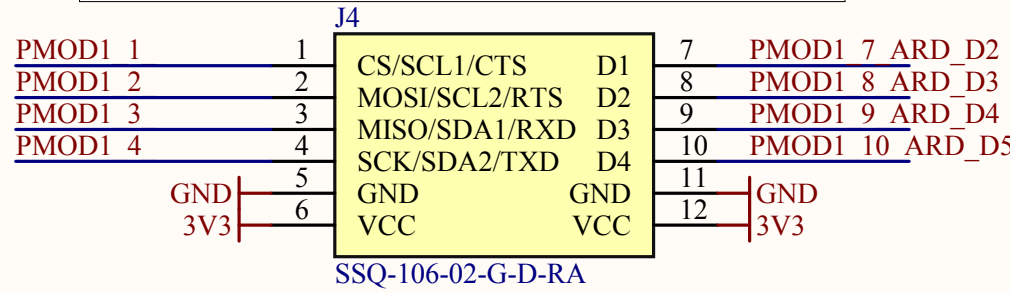
LOGI-LOGO-1000
LOGO1



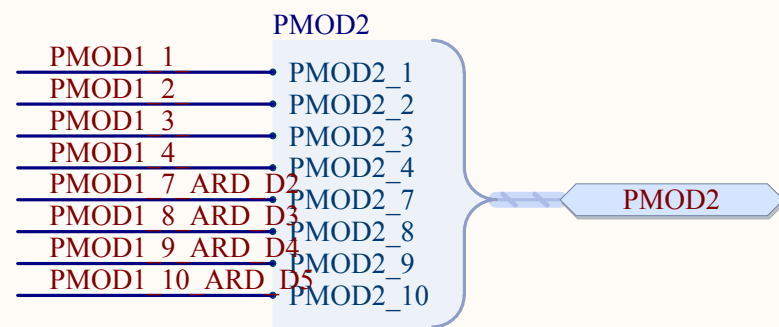
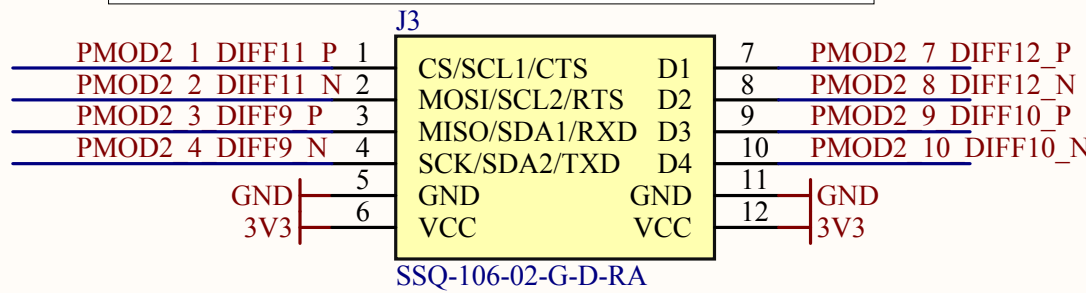
DIGITAL IO



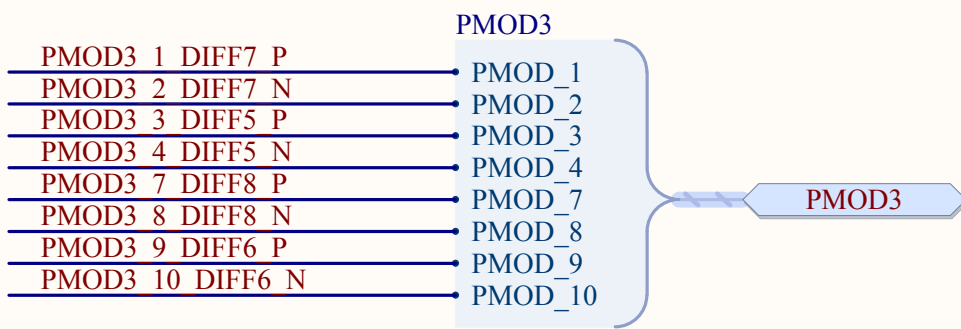
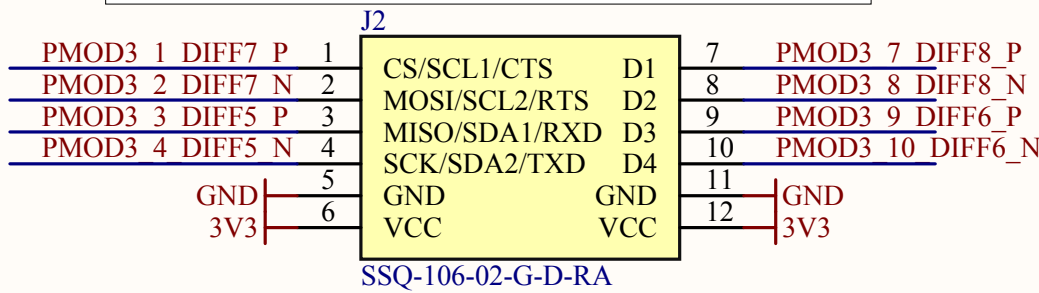
PMOD1



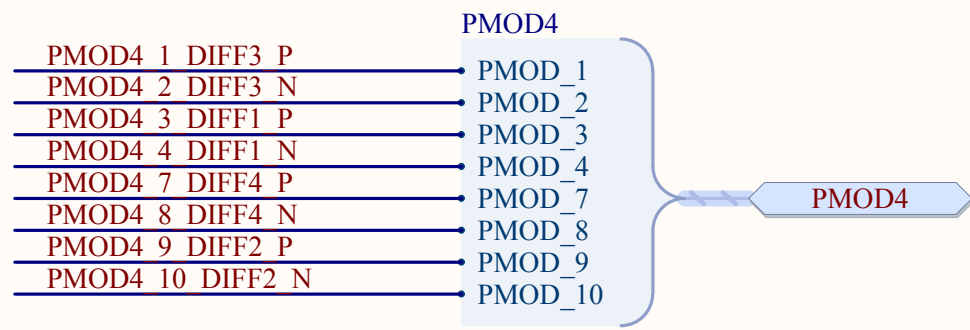
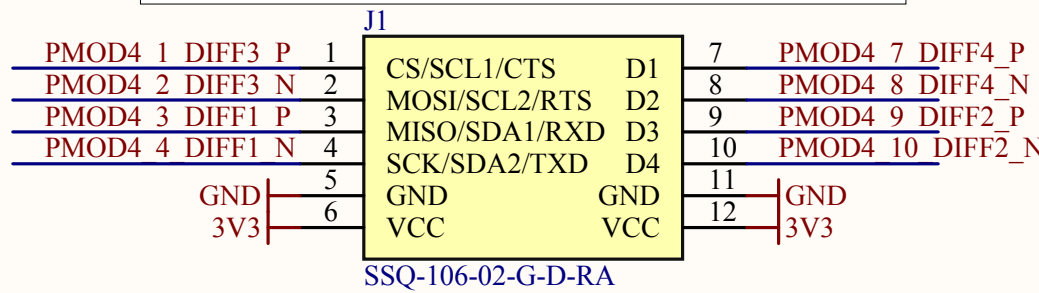
PMOD2



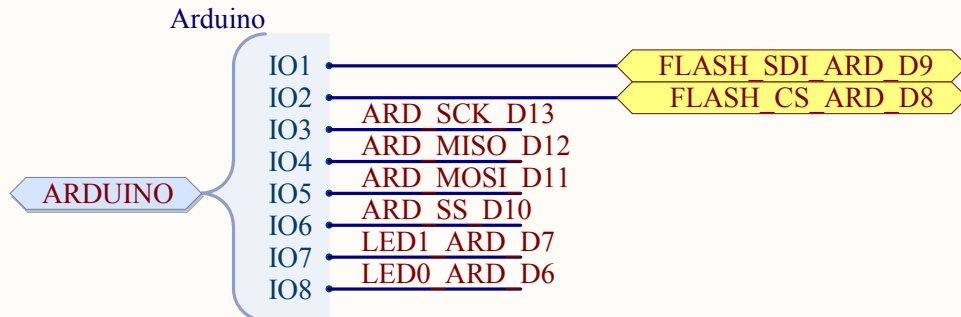
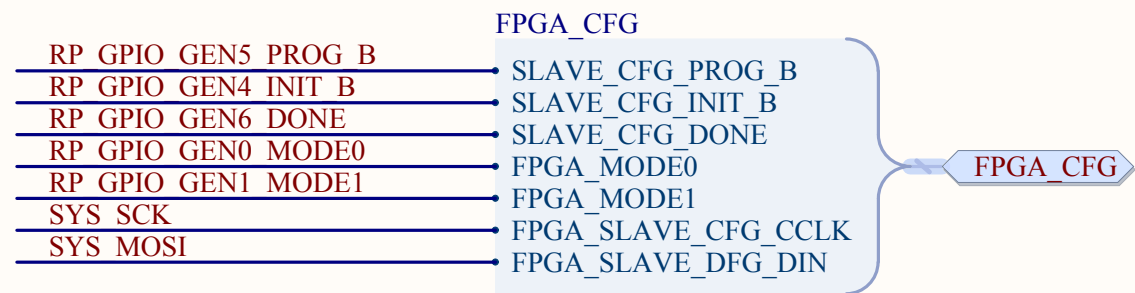
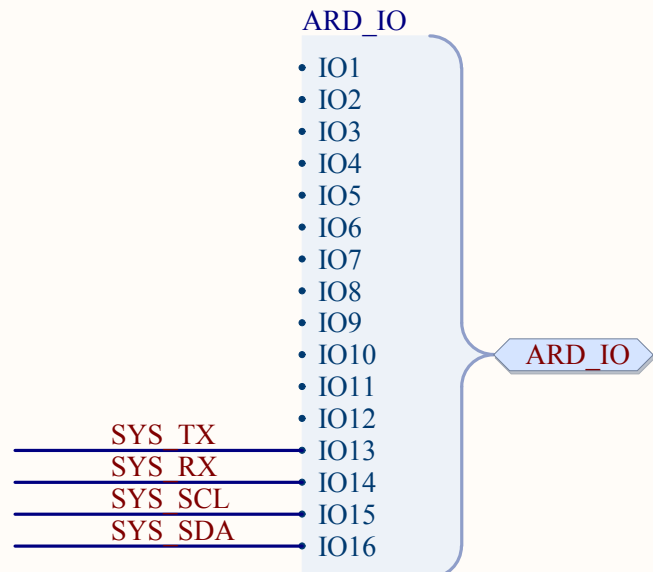
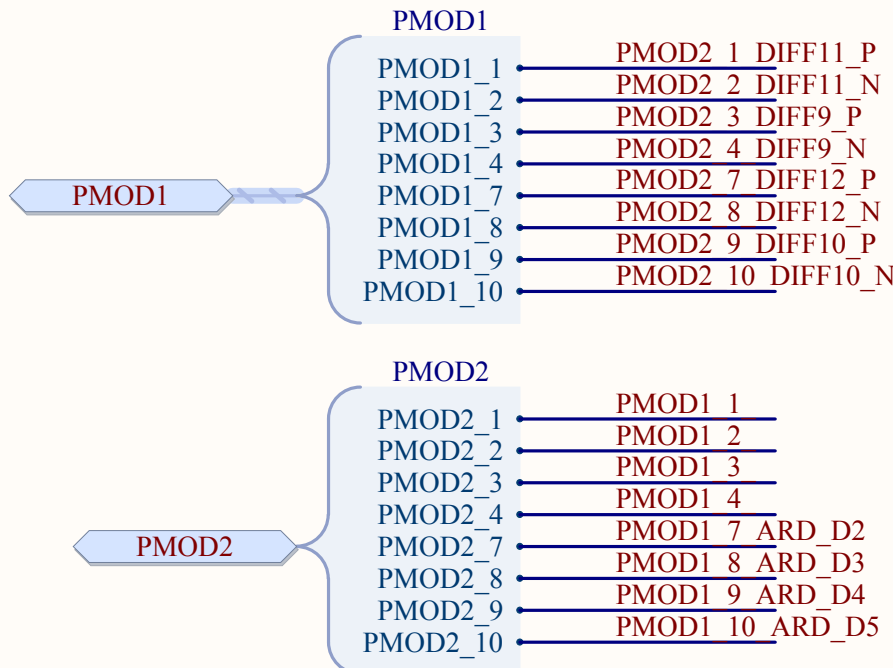
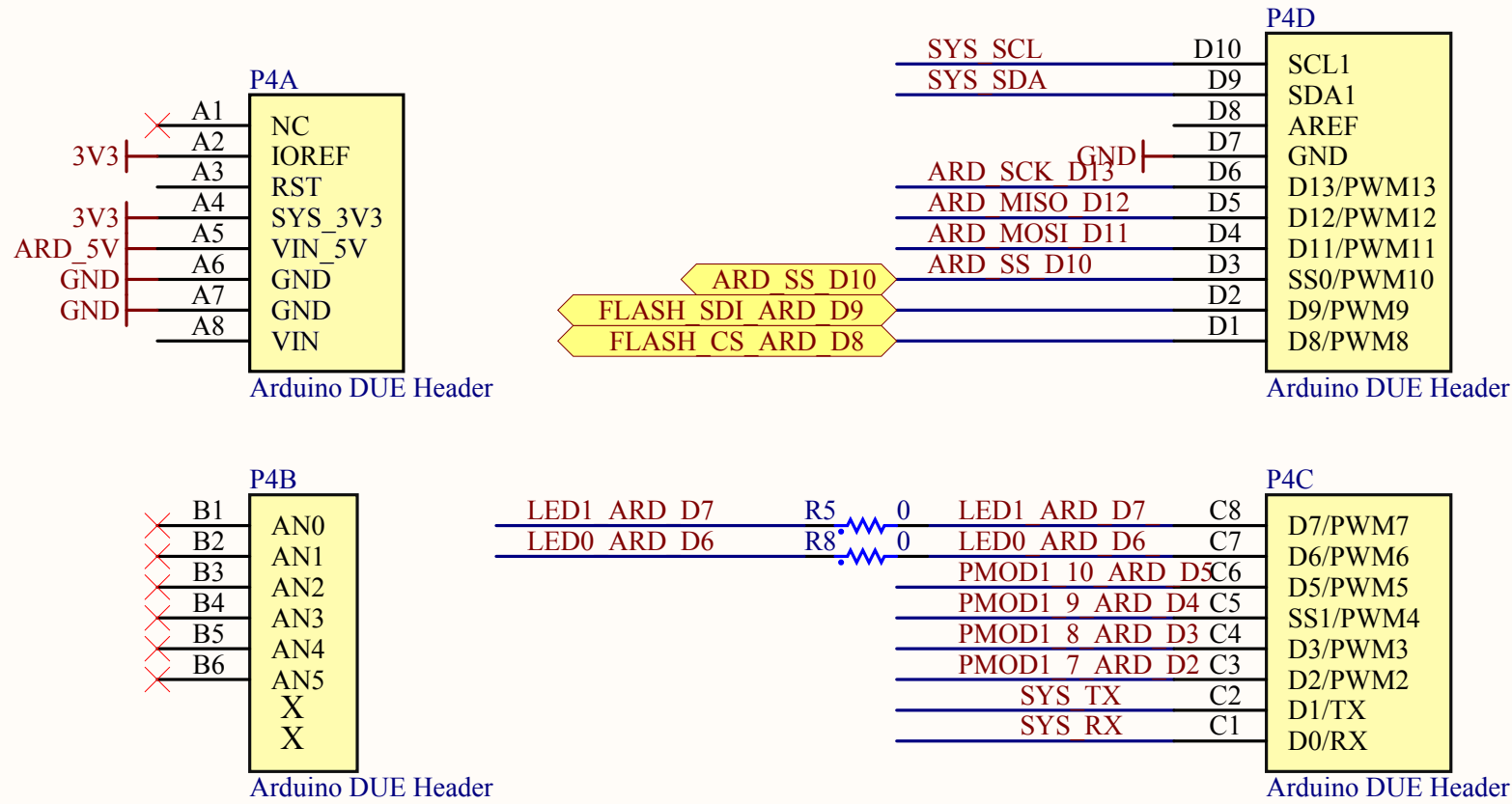
PMOD3



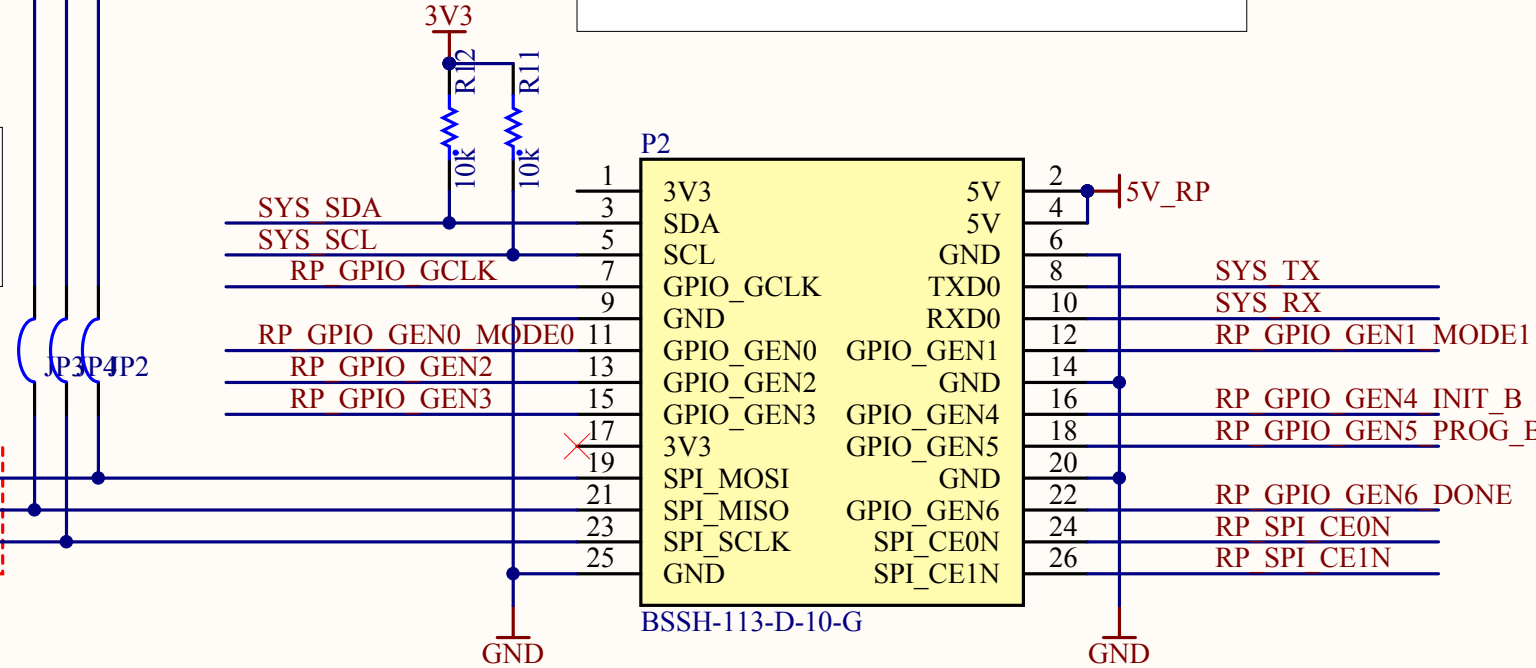
PMOD4



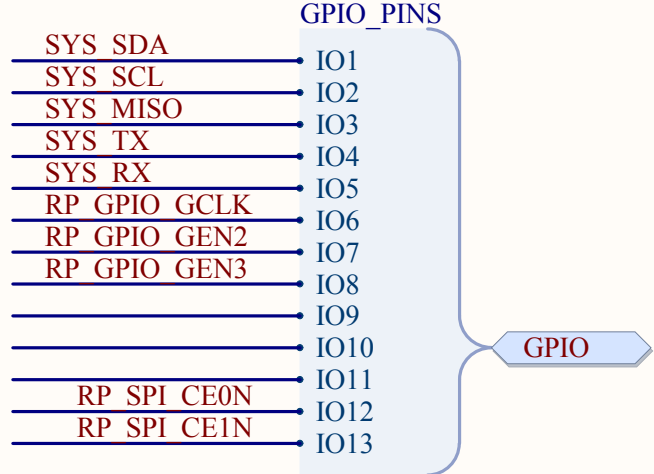
Arduino IO



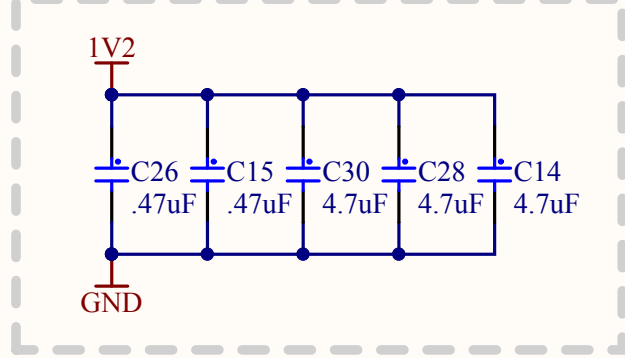
Raspberry Pi IO



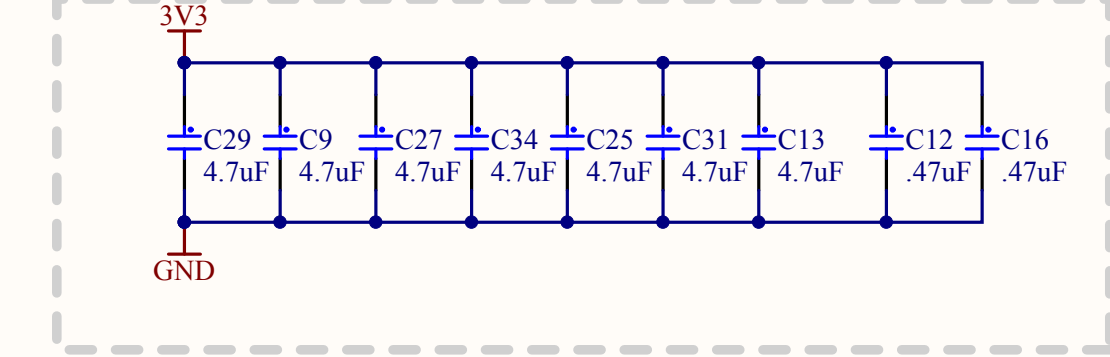
0 ohm resistors to allow removal of Arduino SPI pins. This will minimize reflections and optimize performance for RPI SPI operation



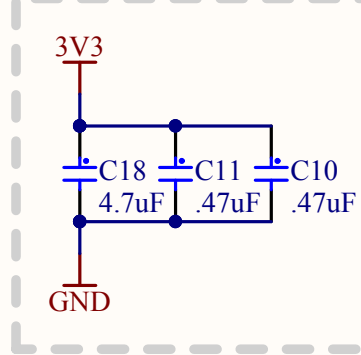
VCC_INT



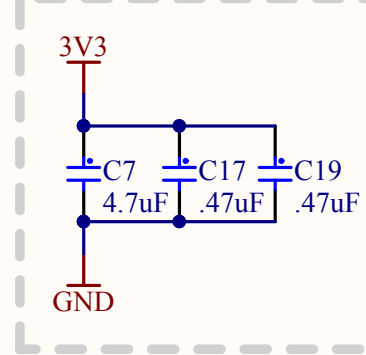
VCC_AUX



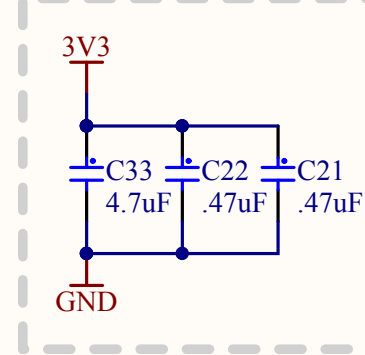
VCCO



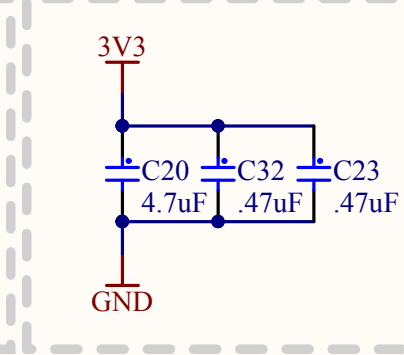
VCC1



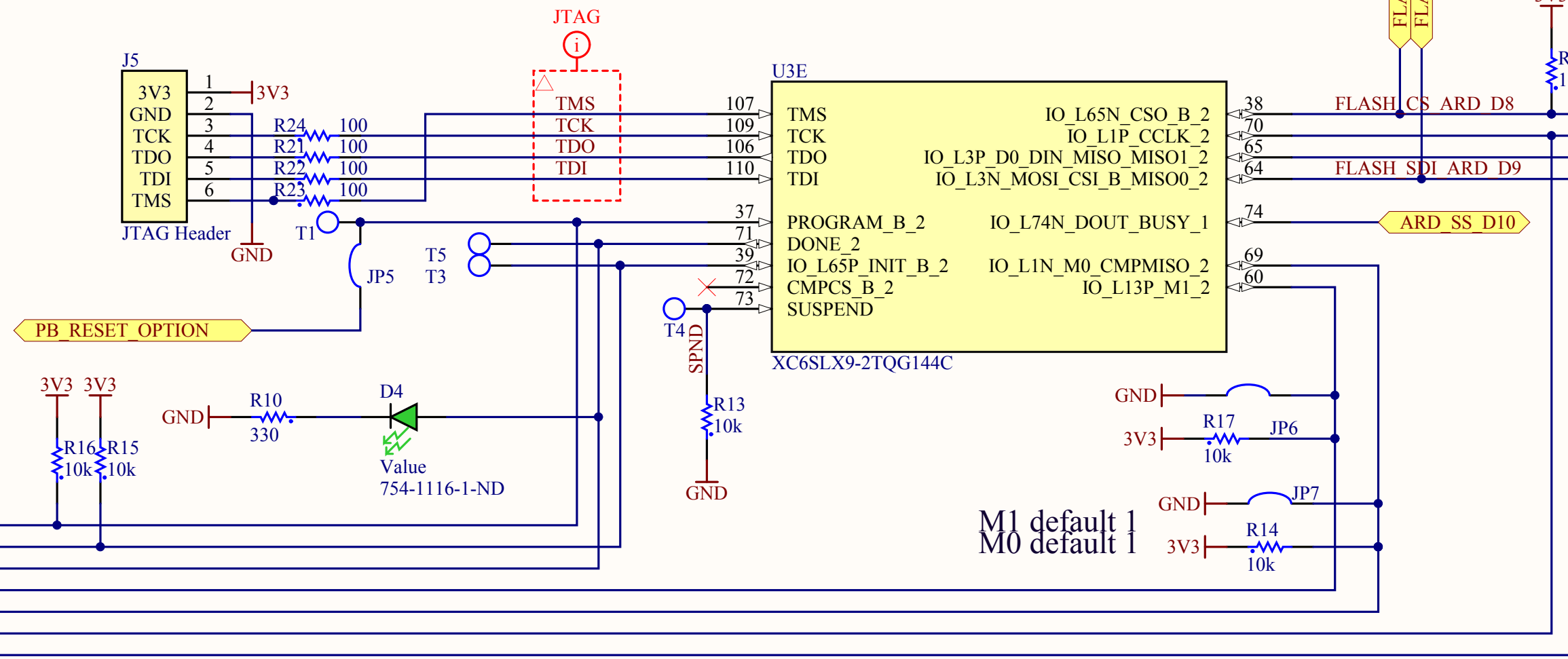
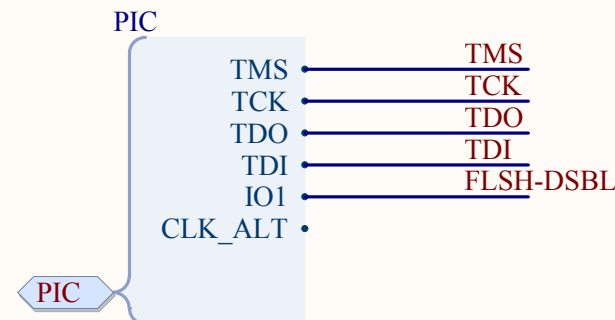
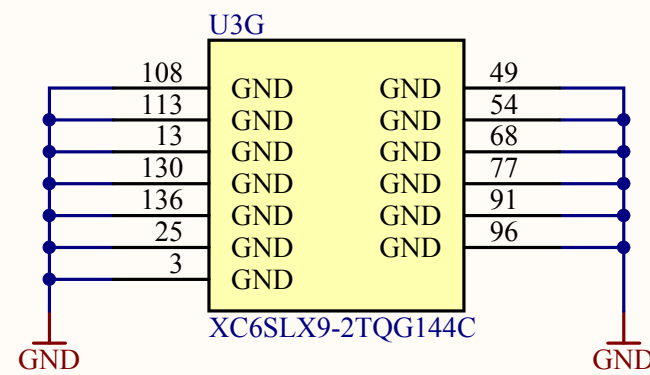
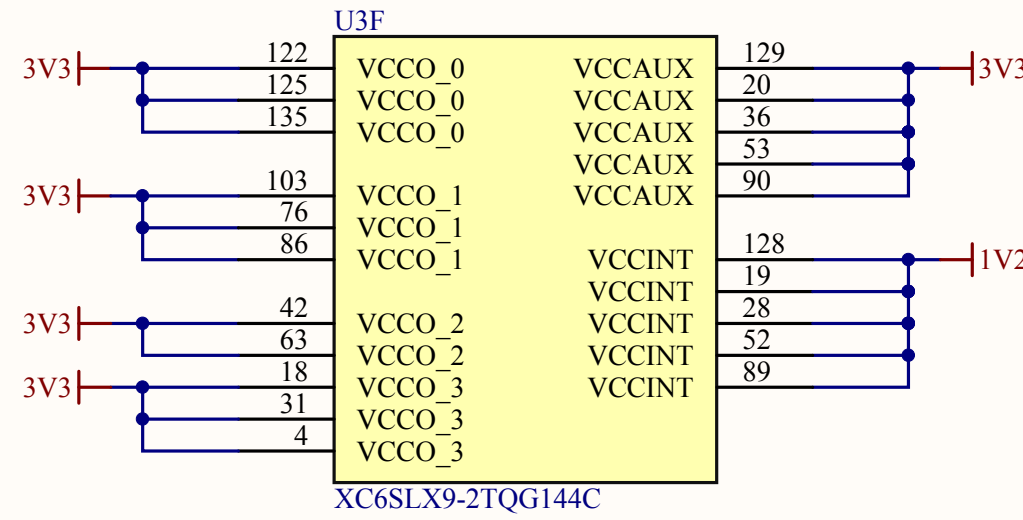
VCC2



VCC3



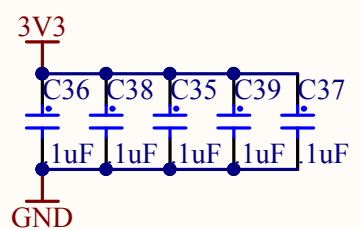
Decoupling cap configuration recommended by Xilinx UG393 (V1.2) for Spartan 6 power distribution



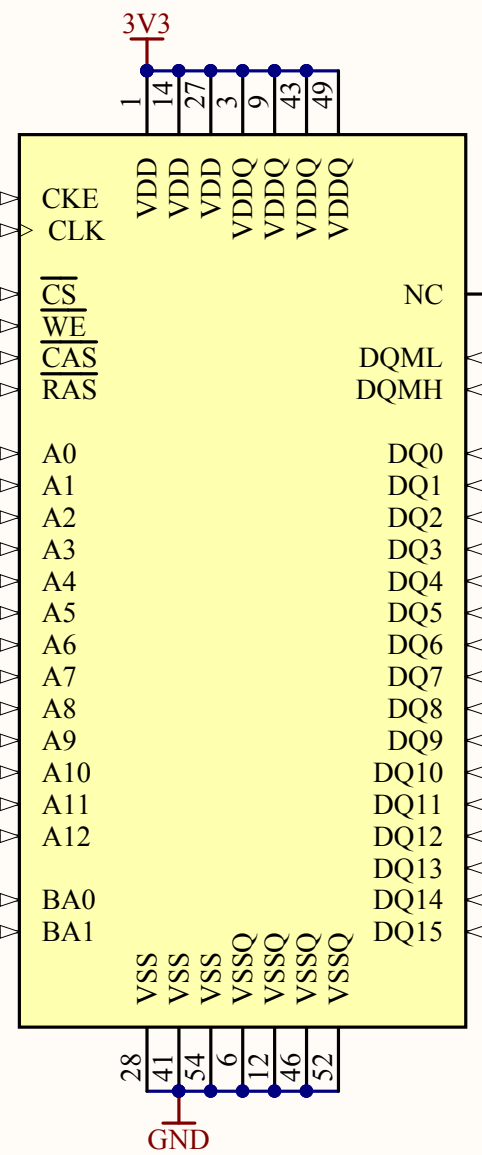
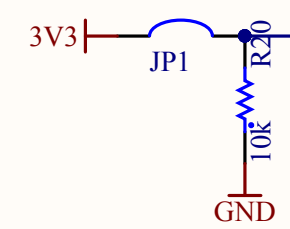
CFG_FLASH_RST will force all flash pins into HiZ removing any conflict while the BB is configuring FPGA

M[1:0] pull ups are set to select Self config by default. See UG380 p40 for pin descriptions.
01 = SelfConfig
11 = SlaveConfig

SDRAM



▲ A solder jumper was added to use the fpga pin elsewhere
- The CS is enabled by default
- Use the solder jumper to disable the SDRAM to reduce power if not used.



U6
MT48LC16M16A2P-7E

