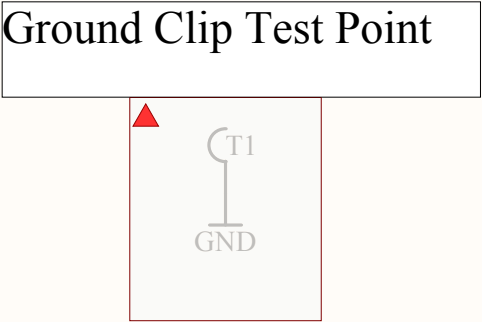
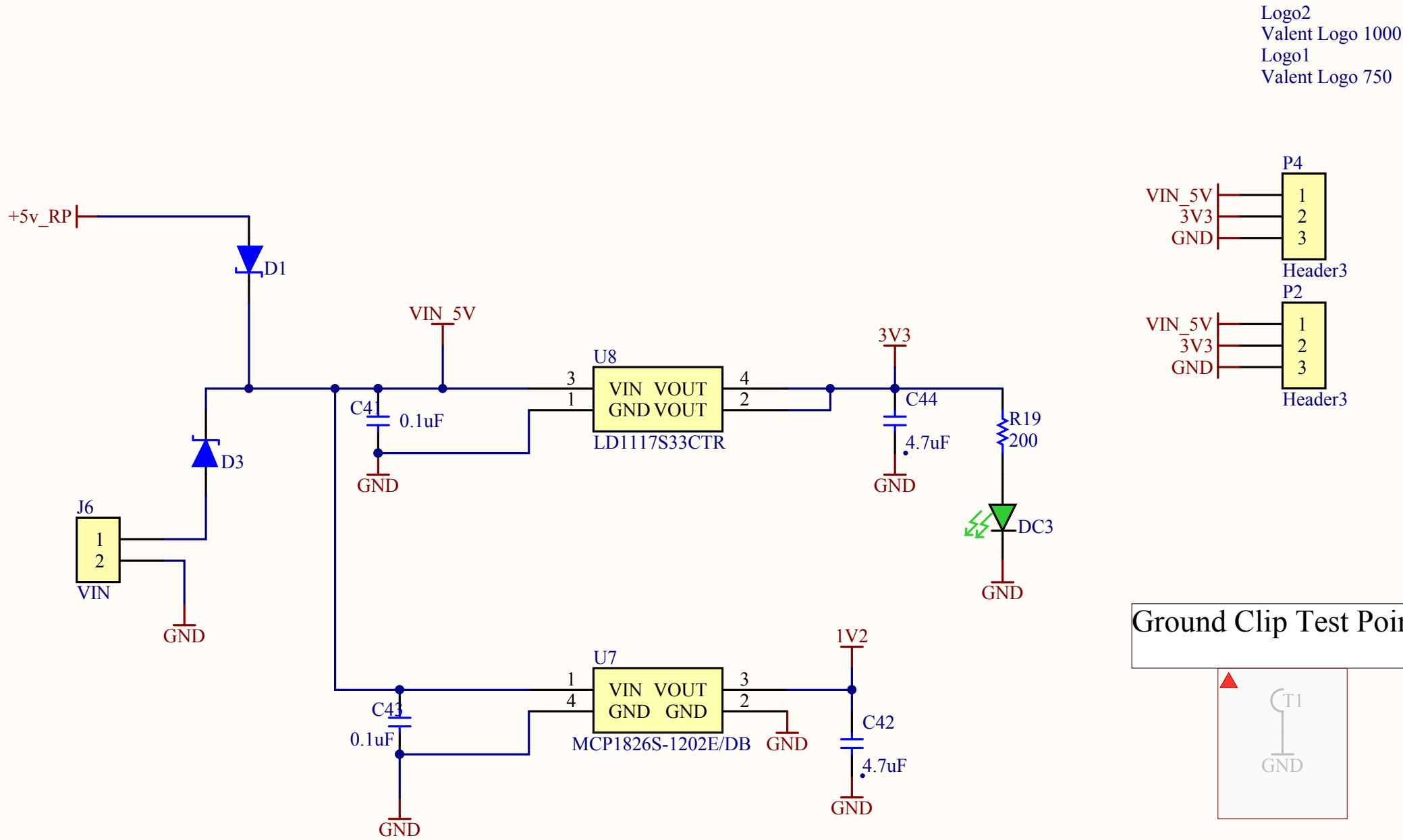
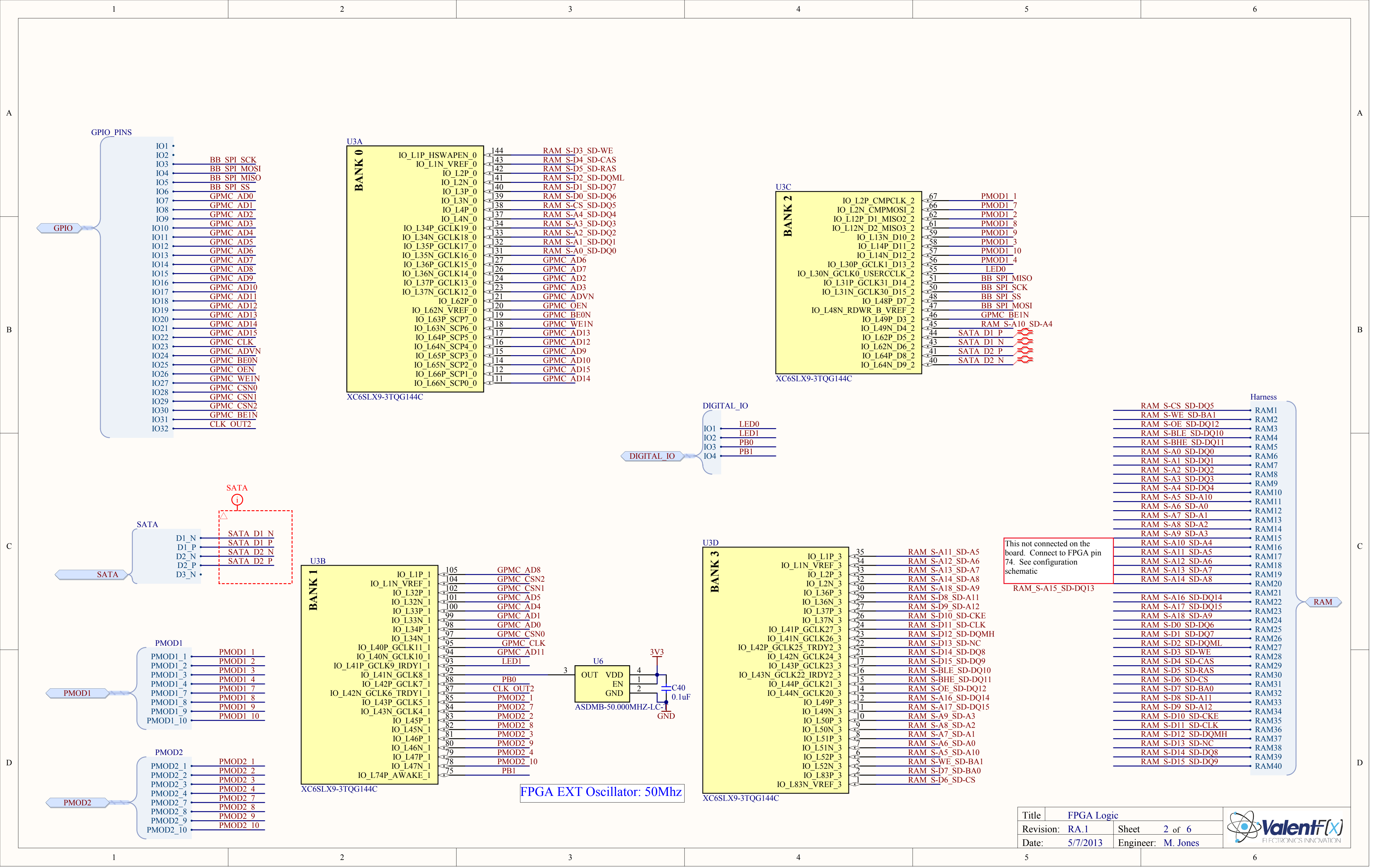




Power: The FPGA MARK-1 can be powered from a number of external sources including:

- 1.) Power Terminal (screw terminal) Not populated by default to reduce cost. (5V External Supply)
- 2.) MCU USB connector (5V Supply)
- 3.) FTDI USB connector (5V Supply)
- 4.) Arduino Master device (5V Supply)
- 5.) Raspberry Pi (5V supply)







A

B

C

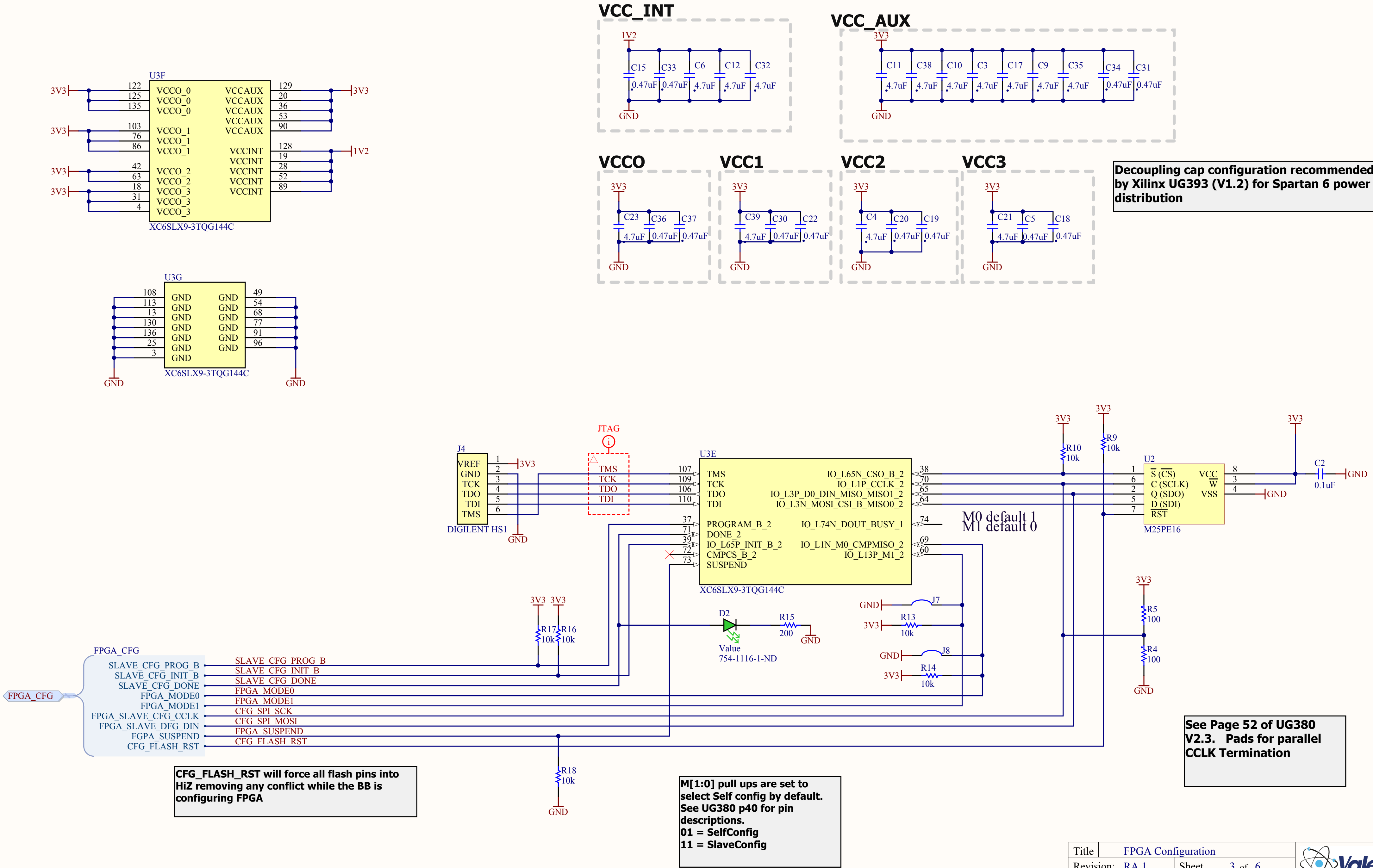
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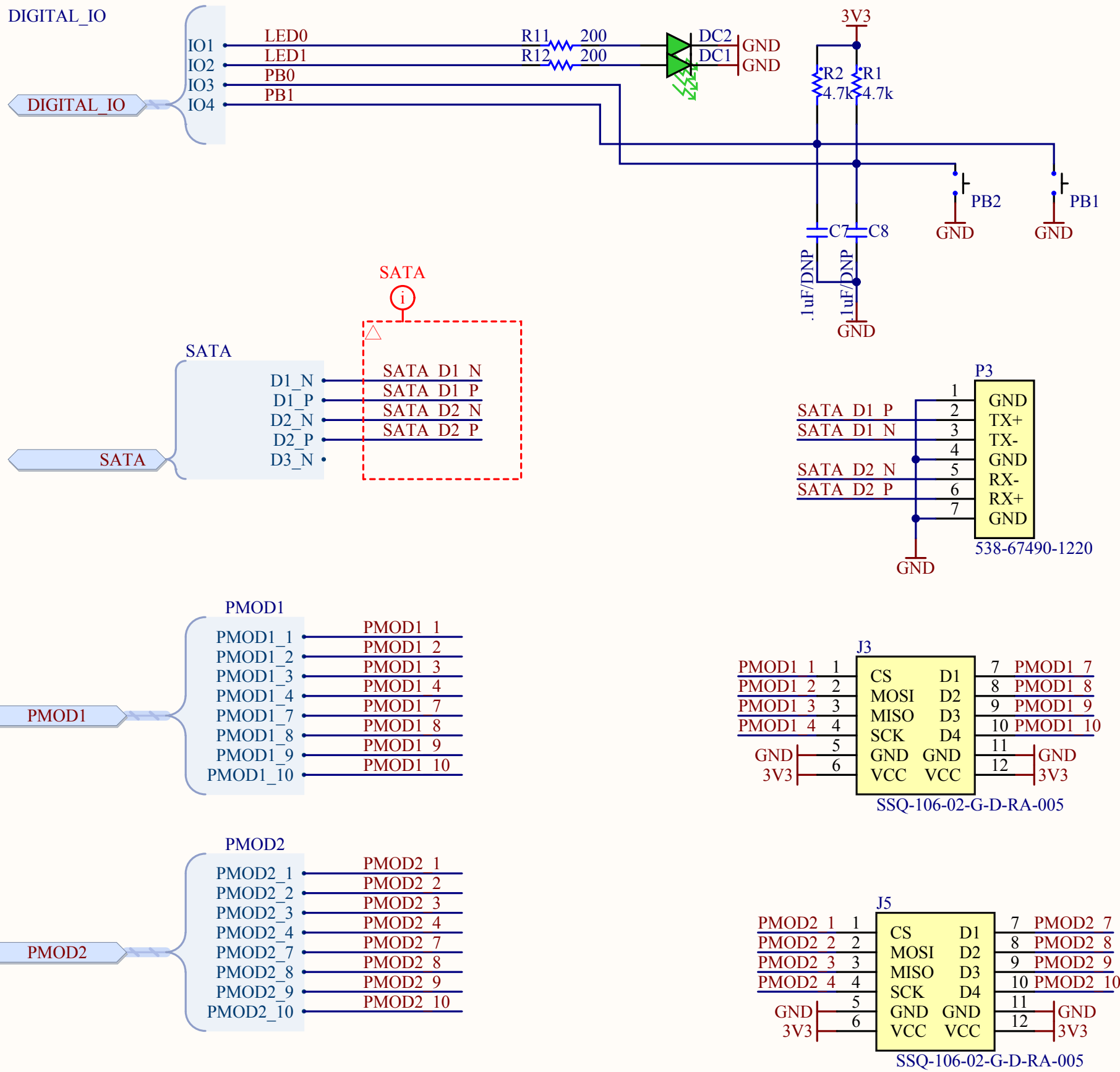
A

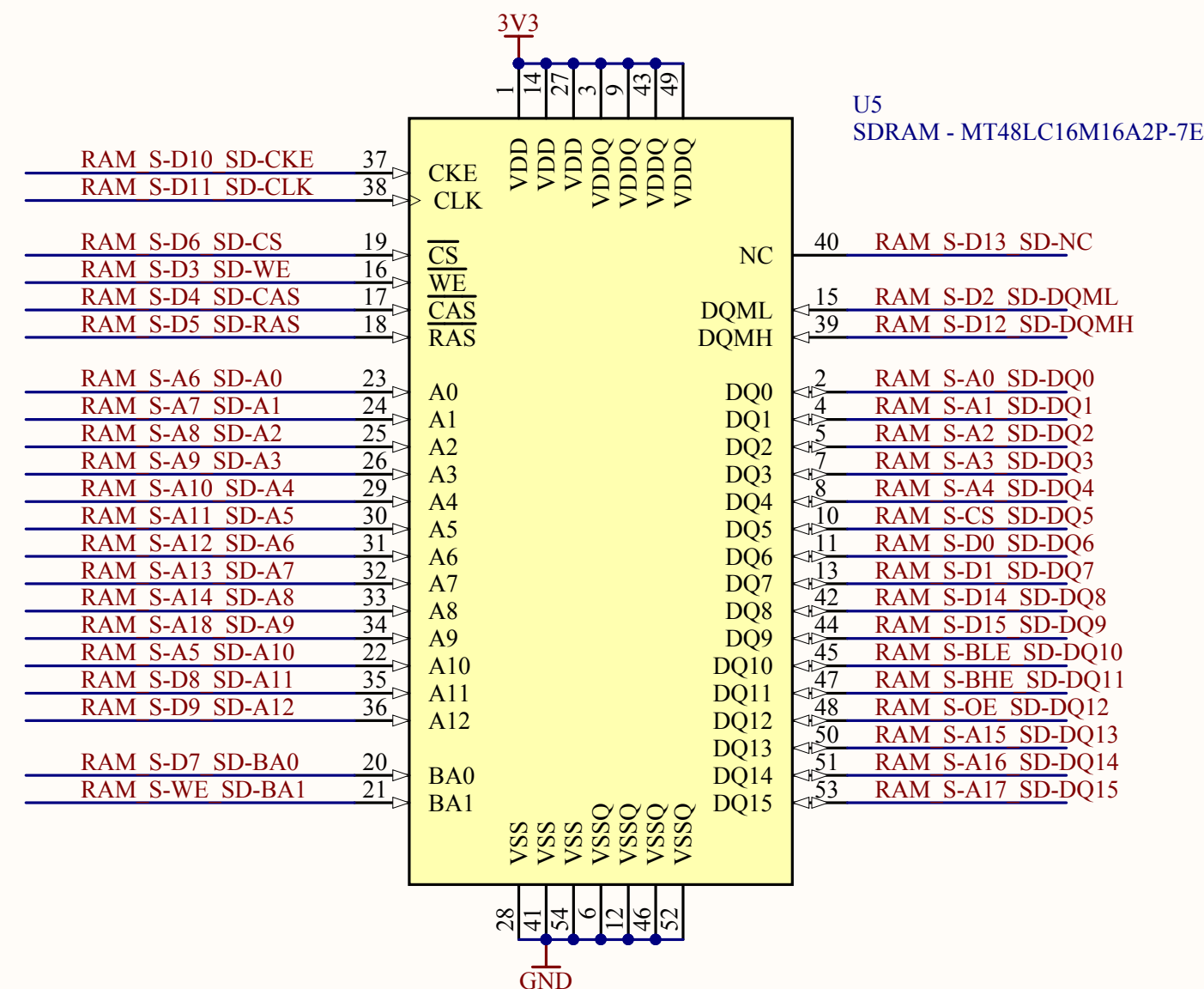
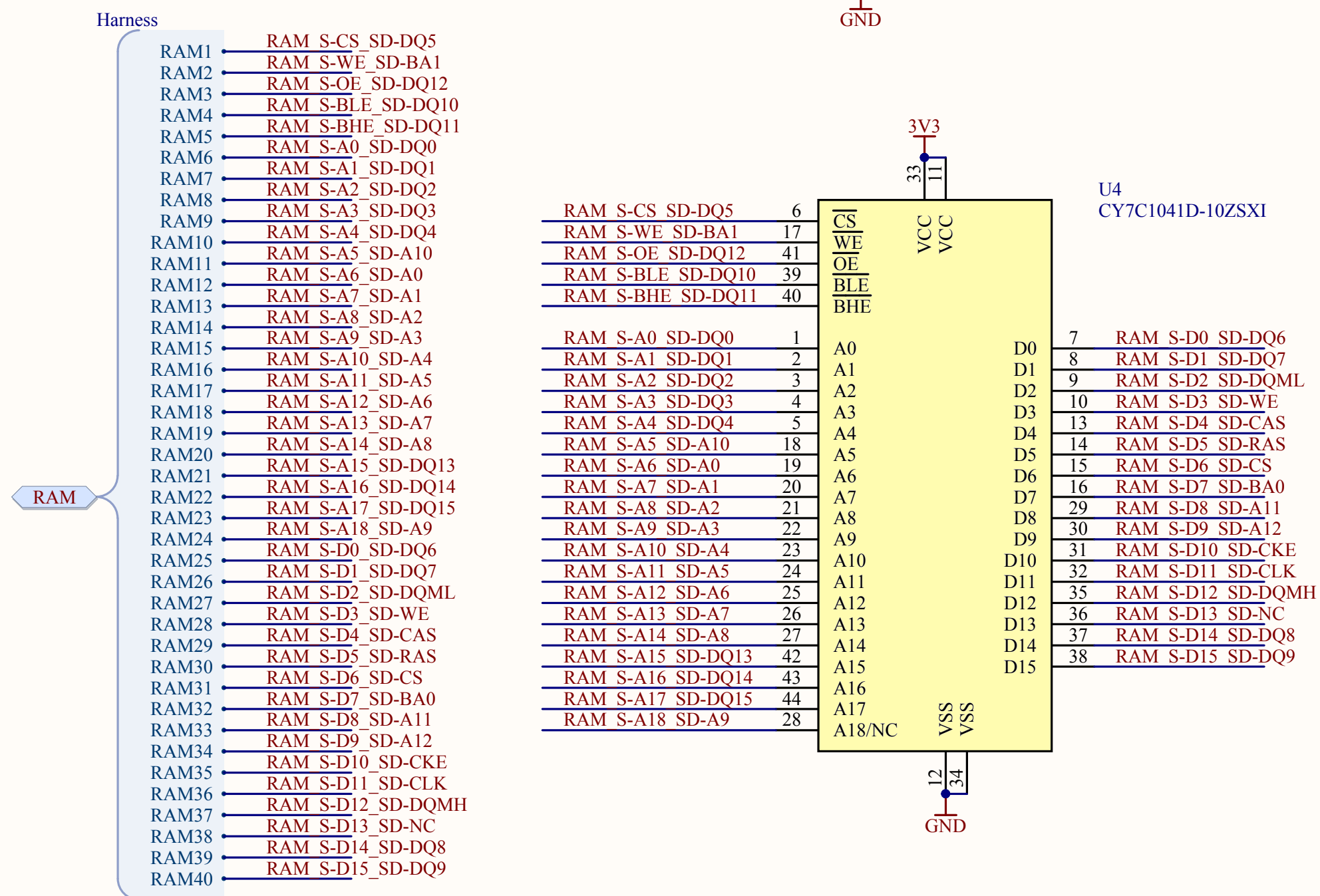
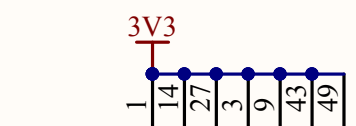
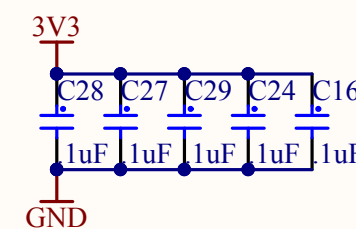
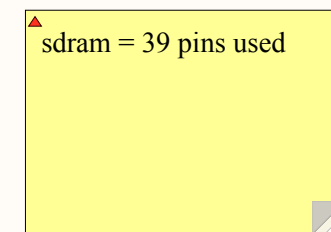
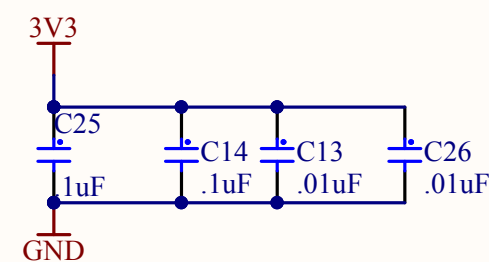
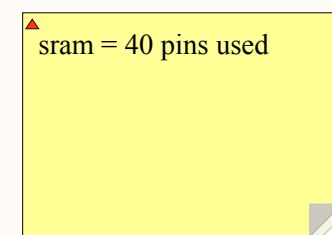
B

C

D





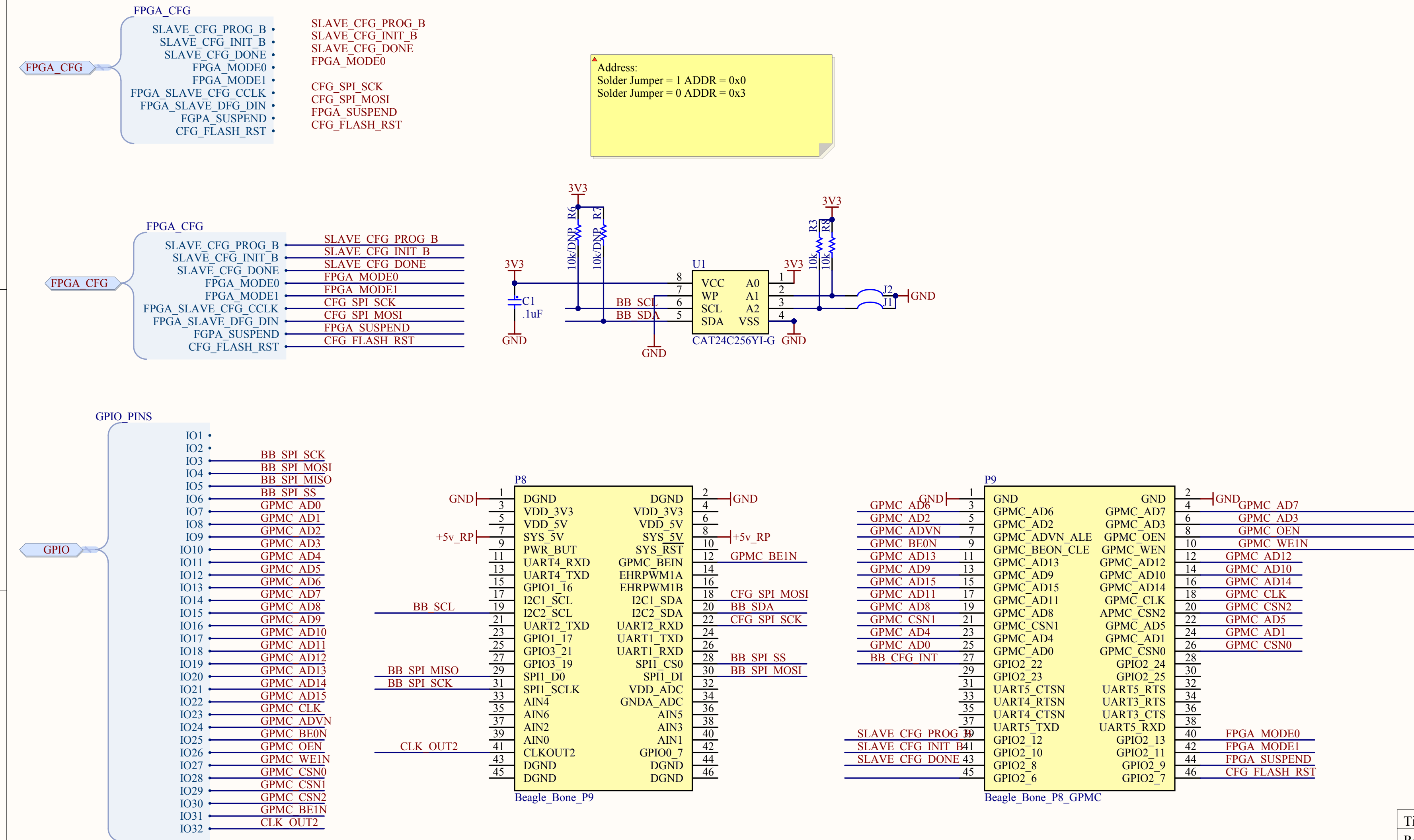


A

B

C

D



Title

Peripheral Port IO

Revision:

RA.1

Sheet

6 of 6

Date:

5/7/2013

Engineer:

M. Jones



Valent F(x)

ELECTRONICS INNOVATION

1 2 3 4 5 6