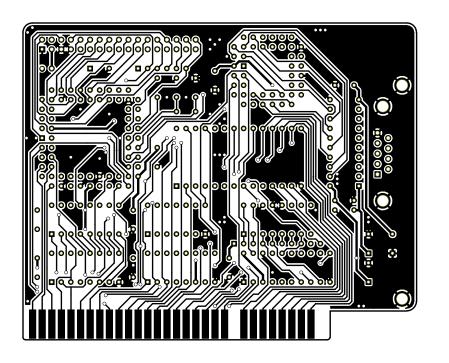


| File: isa_fdc.brd | | | |
|-------------------|-------------------------------------|---------|--|
| Sheet: 1/1 | | | |
| Title: | | | |
| Size: User | Date: 28 nov 2012 | Rev: | |
| KiCad E.D.A. | pcbnew (2012-01-19 BZR 3256)-stable | ld: 1/1 | |



| File: isa_fdc.brd | | | |
|-------------------|-------------------------------------|---------|--|
| Sheet: 1/1 | | | |
| Title: | | | |
| Size: User | Date: 28 nov 2012 | Rev: | |
| KiCad E.D.A. | pcbnew (2012-01-19 BZR 3256)-stable | ld: 1/1 | |

SW1.1 - ON: Set UART address to 0x2E8 (COM*)
SW1.2 - ON: Set UART address to 0x3E8 (COM3)
SW1.3 - ON: Set UART address to 0x3E8 (COM3)
SW1.4 - ON: Set UART address to 0x3E8 (COM1)
SW1.5 - ON: USE UART address to 0x3E8 (COM1)
SW1.5 - ON: USE (ROA' TO UART (COM2, COM4)
SW1.5 - ON: USE (ROA' FOR UART (COM1, COM3)
SW1.8 - ON: USE (ROA' FOR UART)
SW1.8 - ON: USE (ROA' FOR UART) SW2.1 - ON: Enable EEPROM SW2.2 - ON: Enable EEPROM write SW2.3 - ON: ROM A17 = 0; OFF: ROM A17 = 1 SW2.4 - ON: ROM A16 = 0; OFF: ROM A16 = 1 SW2.5 - ON: ROM A15 = 0; OFF: ROM A15 = 1 SW2.6 - ON: ROM A14 = 0; OFF: ROM A14 = 1 SW2.7 - ON: ROM A13 = 0; OFF: ROM A13 = 1 SW2.8 - NOT USED File: isa_fdc.brd Sheet: 1/1 Title: Size: User Date: 28 nov 2012 Rev: KiCad E.D.A. pcbnew (2012-01-19 BZR 3256)-stable ld: 1/1

