

Project
Variant: [No Variations]

31/05/2017
VersionRevision

RELEASED

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DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational
design notes.

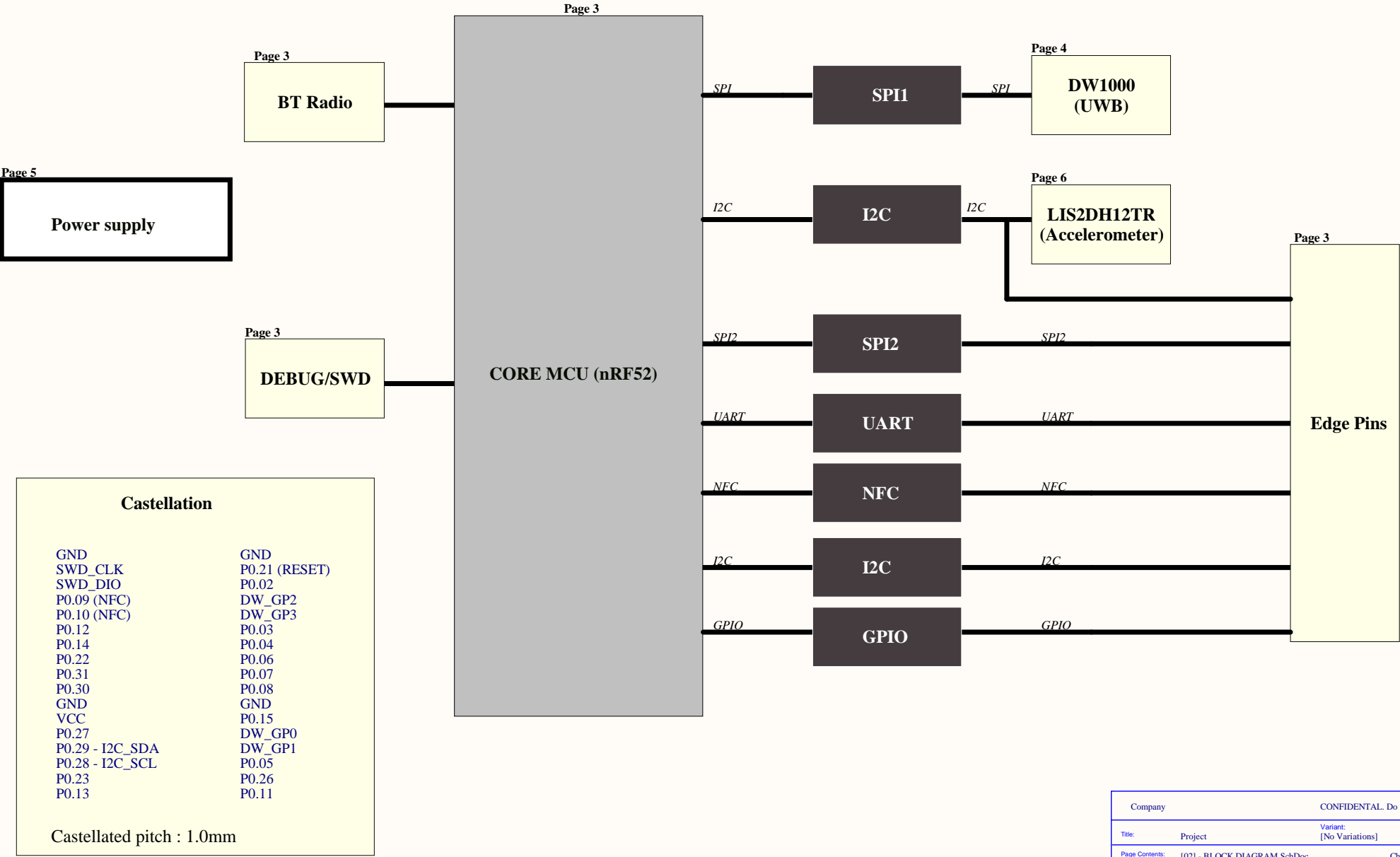
DESIGN NOTE:
Example text for critical
design notes.

DESIGN NOTE:
Example text for cautionary
design notes.

LAYOUT NOTE:
Example text for critical
layout guidelines.

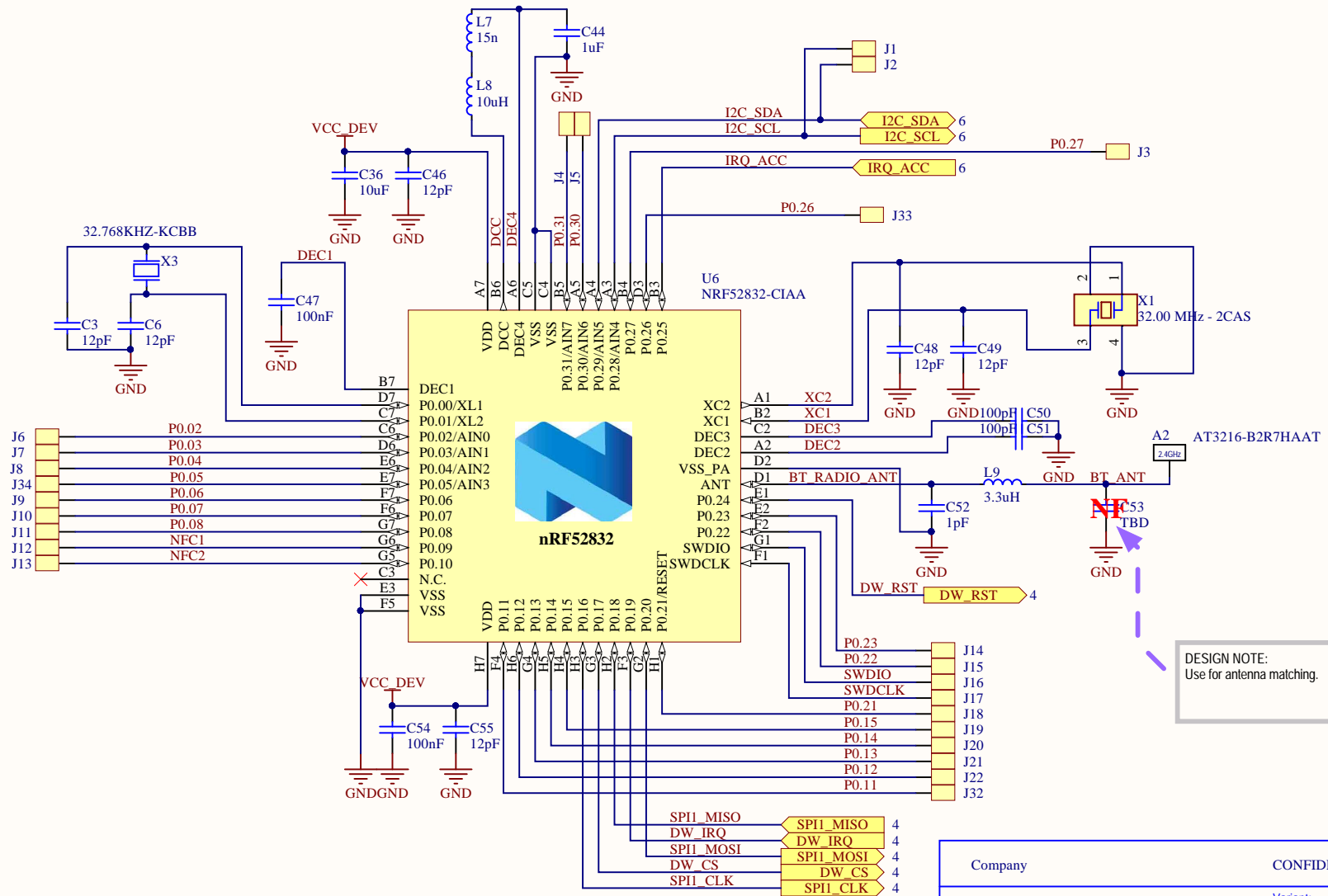
Company		CONFIDENTIAL. Do not distribute.	
Title:	Project	Variant:	[No Variations]
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DM1
(Block Diagram)



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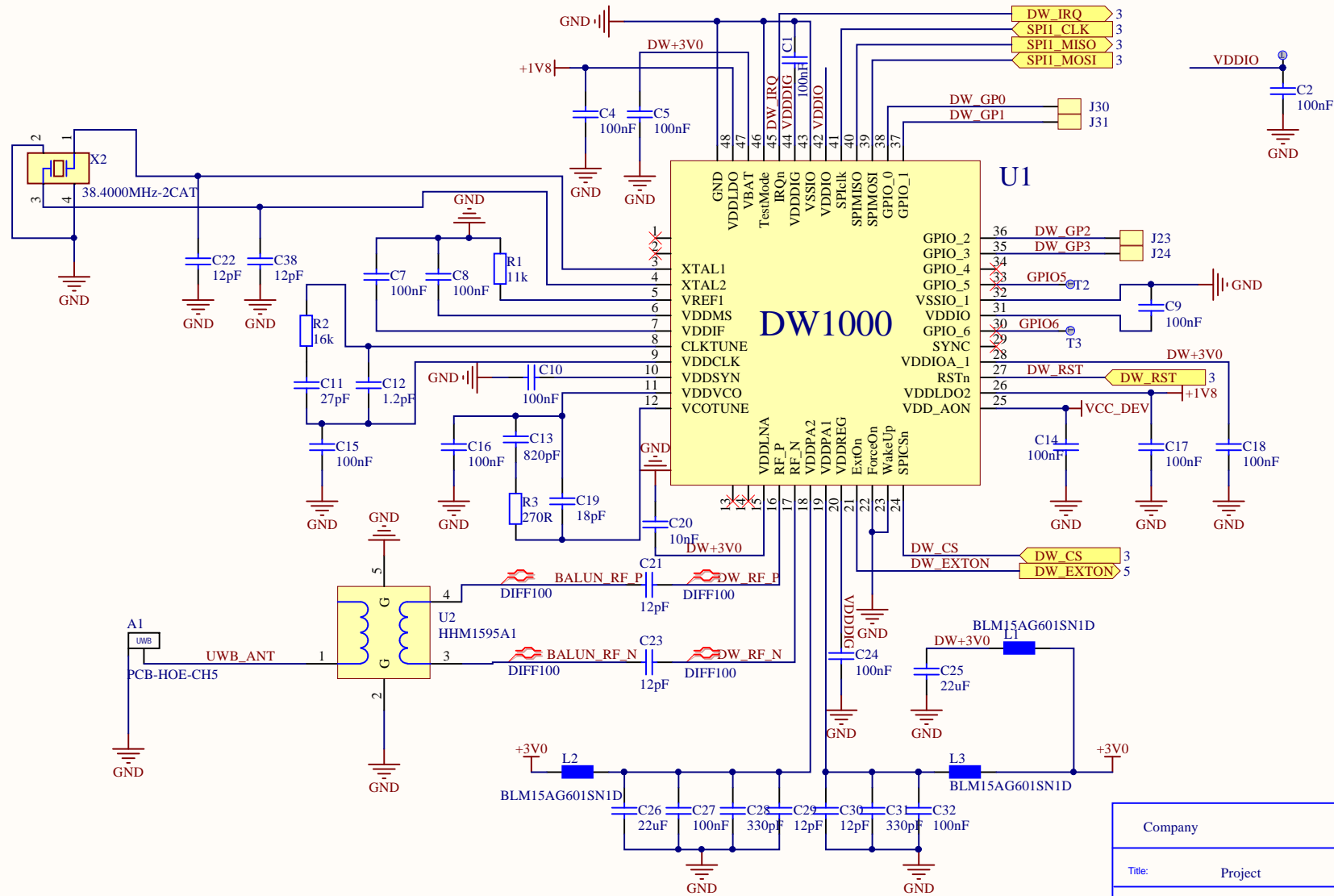
Core MCU



DESIGN NOTE:
Use for antenna matching.

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DW1000

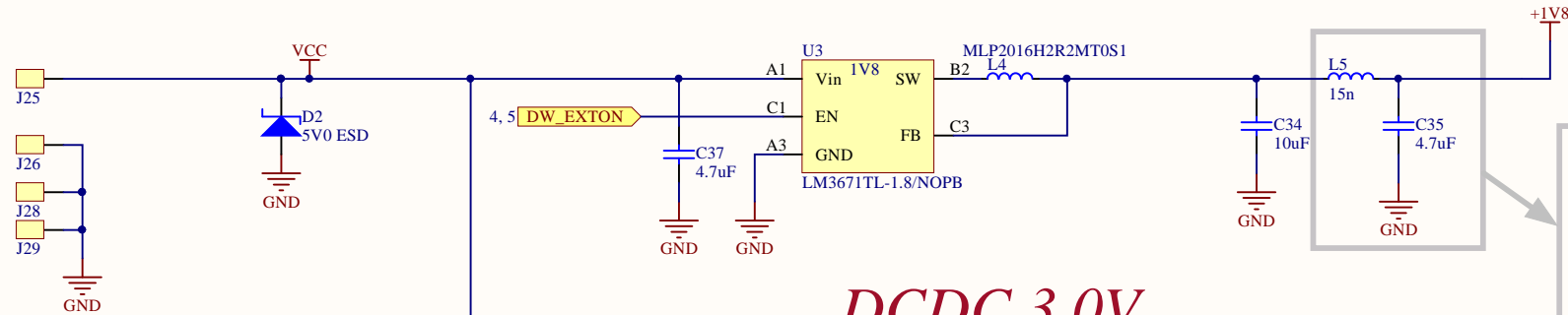


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Page Contents: [04] - DW radio.SchDoc		Checked by JK.TDK.AI	
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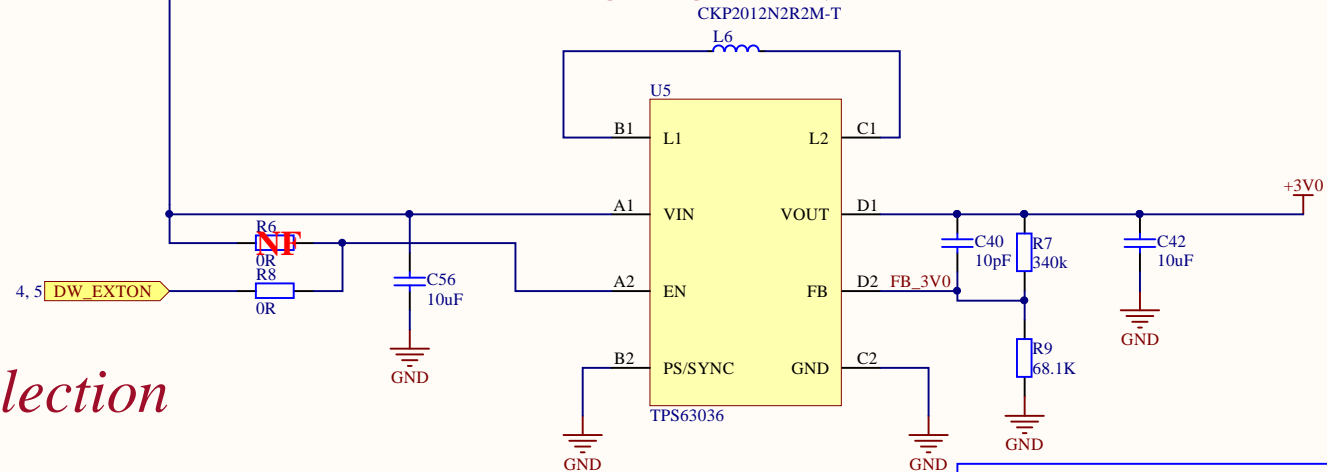


Power supply

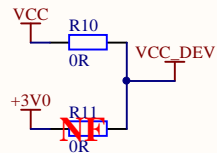
DCDC 1.8V



DCDC 3.0V



Power supply selection



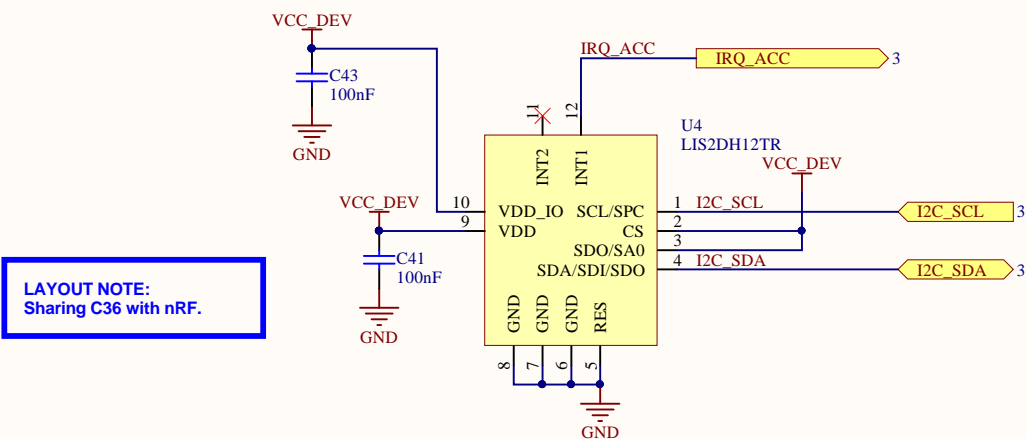
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I2C/SPI devices

LIS2DH12

3-Axes Accelerometer



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DOC: REVISION HISTORY

A1

- Inital version

B1

- DW_CS path corrcted
- NetC30_2 - Via connection fixed
- P0.5 path corrected
- Chnaged 32MHz, 38.4MHz xtals to ABM10 footprint (2.0 x 2.5 mm)

C1

- Changed BT antenna A2 to ACX AT3216-B2R7HAA
- Changed all 0.1UF capacitor to Murata GRM033C81E0104KE14D
- Changed C56 to 10uF
- Changed C40 to 10pF
- Changed R7 to 340K 1%
- Changed R9 to 68.1K 1%
- Changed Balun U2 to TDK HHM1595A1
- Changed U5 to TI TPS63036YFGR
- Changed L4 to Taiyo Yuden CKP20162R2M-T
- Changed L6 to Taiyo Yuden CKP2012N2R2M-T
- Changed 32 MHz crystal X1 to Commutek 2CAS (2520-size)
- Changed 38.4MHz crystal X2 to Commutek 2CAT (2520-size)
- Changed 32.768KHz crystal X3 to Commutek KCBB (3215-size)
- Changed C35 and C37 to 4.7uF 6.3V X5R 0603

CLOCKS, CPU & PCIe

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Designator
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Designator
[02] - BLOCK DIAGRAM.SchDoc

Designator
[03] - CORE MCU.SchDoc

Designator
[04] - DW radio.SchDoc

Designator
[05] - Power supply.SchDoc

Designator
[06] - SPI I2C devices.SchDoc

Designator
[07] - DOC REVISION HISTORY.SchDoc

TEMPLATE NOTES

Set Project Parameters

1) Go to Project -> Project Options -> Parameters

2) Set Company, Project and VersionRevision

Mark Not Fitted Components as


NF

Net Class Example

① Net Class

ClassName: PCIe

Differential signal example



TITLE Examples (You can change the color to reflect your company color)

PAGE TITLE

Peripheral / Group of component title

Smaller Title

Schematic Status Explanation

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There should not be any mistakes. Tell the engineer if you find one.

RELEASED - A board with this schematic has been sent to production.

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